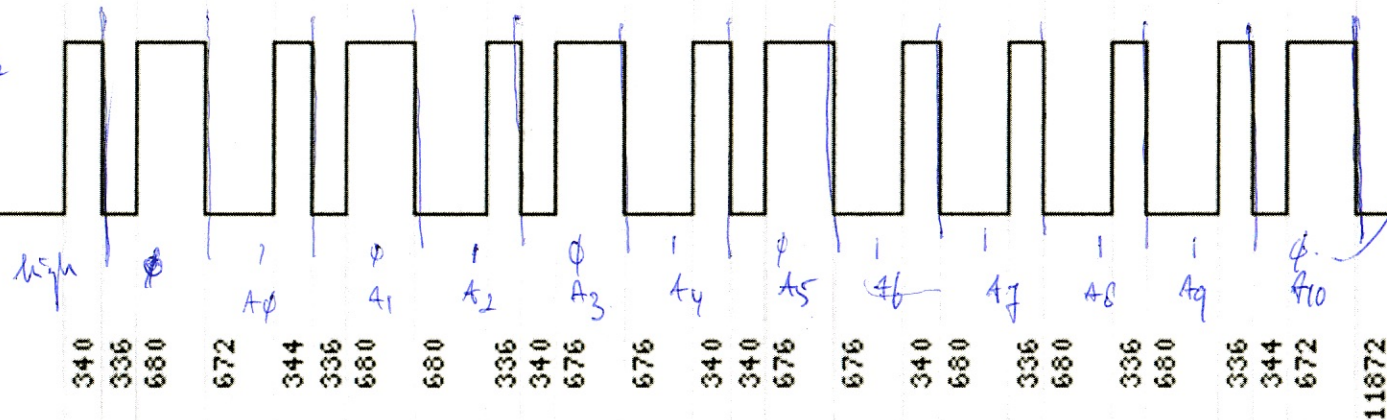


336/340



decimal: 1374

---

12 bits

eye: 35 low  
high.

4 repeats

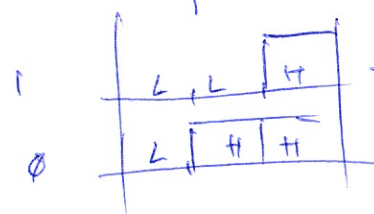
Switch  
setting

$\phi \phi \phi / \phi \phi \phi$   
16 GFs

$\frac{4 \times \text{word}}{\text{TA cycle}}$

Preceding Sync bit.

Data sheet: 6 cycles: osc. = 3kHz:



↑ appears inverted above!