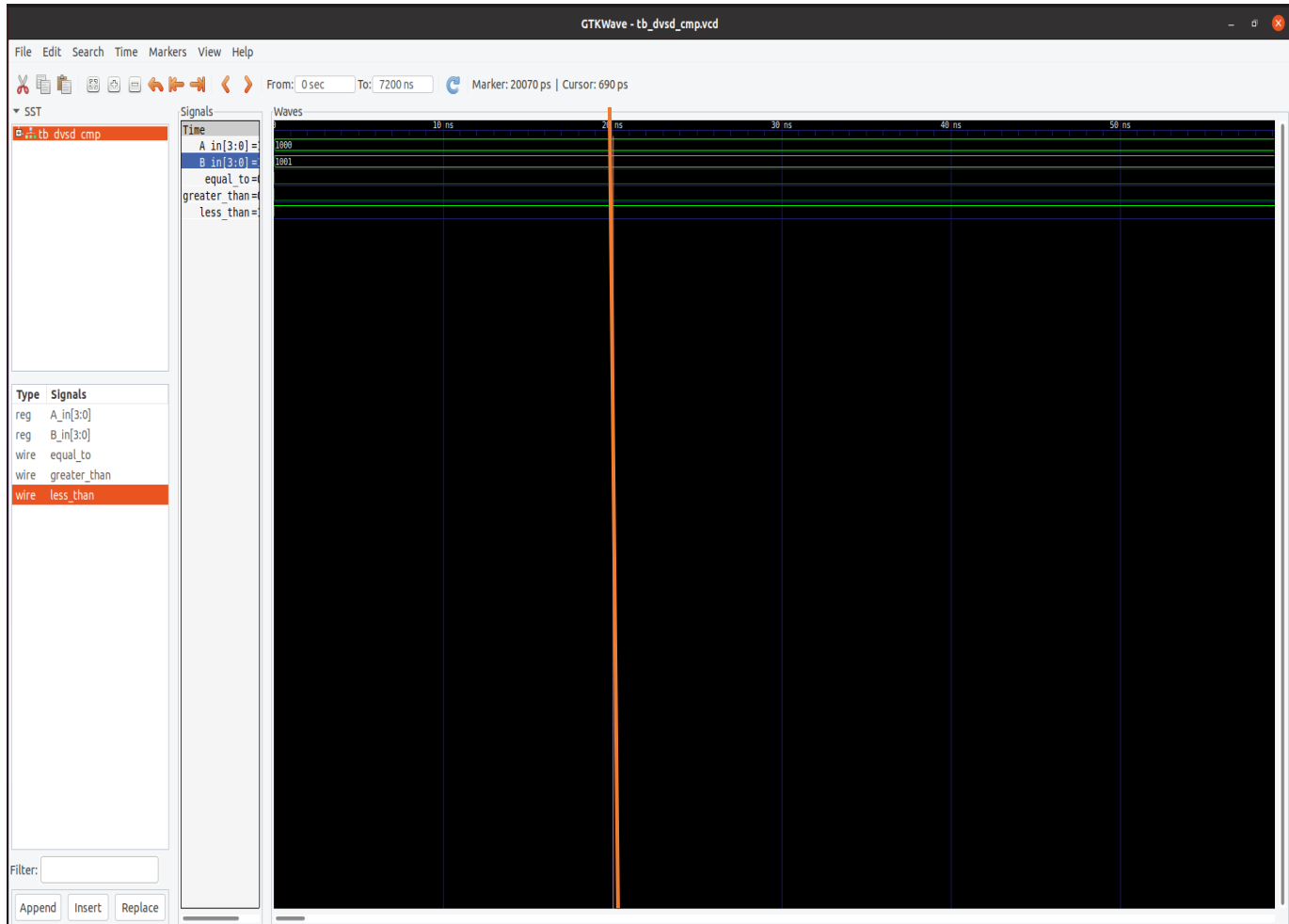


PRE LAYOUT SIMULATION OF 4 BIT MAGNITUDE COMPARATOR



V_{oh}

V_{OL}

