Revision History

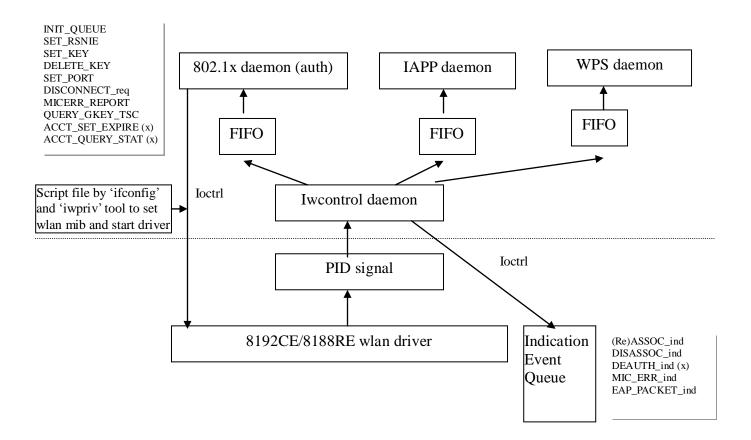
Revision	Release date	comment
1.0	2009/11/17	First issue
1.1	2010/1/14	Add comments
1.2	2010/1/29	Add mib of WAPI
1.3	2010/2/24	Add new configuration API support
1.4	2010/4/7	Add mib
		Add configuration file support
1.5	2010/5/4	Correct explanation of mib
1.6	2010/5/31	Add mib of manual WMM
1.7	2011/3/14	Add dual-band configuration & DFS
1.8	2011/7/27	Add multiple AP profile support
1.9	2011/9/14	Add comments for proc/stats
1.10	2011/9/29	Add LED type
1.11	2011/10/19	Add mib for band priority and special
		channel plan
1.12	2012/3/1	Add support for 8188RE
1.13	2012/5/2	Correct aggregation mib
1.14	2012/5/10	Delete the description for band priority
		and special channel plan. Add
		description for multiple profile.
1.15	2012/5/30	Modify comments for mibs "led_type"
		and "wifi_specific"
1.16	2012/7/31	Add 2.4G channel plan table. Add new
		regdomain value (14,15) for global and
		world-wide.
1.17	2012/9/10	Add mib for 802.11ac
1.18	2012/9/26	Add mib for VHT rate classify
1.19	2012/11/21	Modify the usage of deny_legacy

Features

- 802.11 a/b/g/n compatible
- AP mode and client mode support
- Security support 64/128 bits WEP, WPA, and WPA2 (TKIP and AES-CCMP)
- Auto rate adaptive
- Wireless MAC address filter
- Broadcast SSID control
- IAPP (802.11f) support
- Auto channel selection
- Driver based MP functions
- WDS function support
- Universal repeater mode support
- WMM supported for AP mode
- Support WLAN ASIC of 8192CE, 8188RE, 8192DE, and 8188ER
- WPS function support

- WAPI function support Set WMM parameters manually

System Architecture



WLAN Driver Configuration, IOCTL and PROC

Set mac address:

"ifconfig wlan0 hw ether xxxxxxxxxxxx"

Set wlan MIB:

"iwpriv wlan0 set_mib name=value1[,value2,value3...]"

Note 1: value can be a single field or multiple fields separated by ',' without any space between fields. Detail parameter may be referred the following table.

Note 2: if the value is the type of byte array, the format of value will be a string of ASCII of 0~f, which using 2 ASCII standing for one byte. For example, when set Tx power of CCK, it will be

"iwpriv wlan0 set_mib TxPowerCCK=08080909090a0a0a0a0b0b0b0c0c"

Up driver:

"ifconfig wlan0 up"

Close driver:

"ifconfig wlan0 down"

MIB command table:

	Meaning	Value	Default	Comment
channel	Operation frequency used	0 for auto channel, 1-14 for		
		11b/11g, 36-165 for 11a		
ch_low	The lowest channel to scan and use	1-14 for 11b/11g, 36-165 for 11a		
ch_hi	The highest channel to scan and use	1-14 for 11b/11g, 36-165 for 11a		
pwrlevelCCK_A	CCK Tx power level for 14	RF module dependent		Type of byte array
-	channels (28 hex digits) for path A	-		
pwrlevelCCK_B	CCK Tx power level for 14	RF module dependent		Type of byte array
_	channels (28 hex digits) for path B	•		
pwrlevelHT40_1S	40MHz mode HT OFDM 1 spatial	RF module dependent		Type of byte array
_A	stream Tx power level for 14	-		
	channels (28 hex digits) for path A			
pwrlevelHT40_1S	40MHz mode HT OFDM 1 spatial	RF module dependent		Type of byte array
_B	stream Tx power level for 14	_		
	channels (28 hex digits) for path B			
pwrdiffHT40_2S	40MHz mode HT OFDM 2 spatial	RF module dependent		Type of byte array
	stream Tx power difference between	_		
	HT40_1S for 14 channels (28 hex			
	digits). Bit[3:0] for path A. Bit[7:4]			
	for path B.			
pwrdiffHT20	20MHz mode HT OFDM Tx power	RF module dependent		Type of byte array
	difference between HT40_1S for 14			
	channels (28 hex digits). Bit[3:0] for			
	path A. Bit[7:4] for path B.			
pwrdiffOFDM	Legacy OFDM Tx power difference	RF module dependent		Type of byte array
	between HT40_1S for 14 channels			
	(28 hex digits). Bit[3:0] for path A.			
	Bit[7:4] for path B.			
Pwrlevel5GHT40_	40MHz mode HT OFDM 1 spatial	RF module dependent		Type of byte array
1S_A	stream Tx power level for 5G 196			
	channels (28 hex digits) for path A			
	40MHz mode HT OFDM 1 spatial	RF module dependent		Type of byte array
1S_B	stream Tx power level for 5G 196			
	channels (28 hex digits) for path B			
Pwrdiff5GHT40_2	40MHz mode HT OFDM 2 spatial	RF module dependent		Type of byte array

HT40 IS for \$6 196 channels (28 hex digits), Bil(30] for path A. Bil(74] for path B. Bil(74) for path B. 20MHz mode htt OfDM Tx power difference between HT40 IS for \$G\$ 196 channels (28 hex digits). Bil(30] for path A. Bil(74) for path B. Bil(30) for path A. Bil(74) for path B. Bil(30) for path A. Bil(74) for path B. Bil(30) for path A. Bil(74) for path B. Poworer index Difference between HT40 IS for \$G\$ 196 channels (28 hex digits). Bil(30) for path A. Bil(74) for path B. Poworer index Difference between BW20-IS and BW40-IS. Bil(74) for path A 2G Offset, Range -8-7. Poworer Index Difference between BW40-2S and BW40-IS. Bil(30) for path A 2G Offset, Range -8-7. Poworer Index Difference between BW20-2S and BW40-IS. Bil(30) for path A 2G Offset, Range -8-7. Poworer Index Difference between BW20-1S and BW40-IS. Bil(30) for path A 2G Offset, Range -8-7. Poworer Index Difference between BW20-1S and BW40-IS. Bil(30) for path A 2G Offset, Range -8-7. Poworer Index Difference between BW20-1S and BW40-IS. Bil(74) for path A 2G Offset, Range -8-7. Poworer Index Difference between BW20-1S and BW40-IS. Bil(30) for path A 2G Offset, Range -8-7. Poworer Index Difference between BW20-1S and BW40-IS. Bil(30) for path A 3G Offset, Range -8-7. Poworer Index Difference between BW20-1S and BW40-IS. Bil(34) for path A 3G Offset, Range -8-7. Poworer Index Difference between BW20-2S and BW40-IS. Bil(34) for path A 3G Offset, Range -8-7. Poworer Index Difference between BW20-2S and BW30-IS and BW40-IS (UpSide Ch L LowSide Ch)/2. Bil(34) for path A 3G Offset, Range -8-7. Poworer Index Difference between BW30-IS and BW40-IS (UpSide Ch L LowSide Ch)/2. Bil(34) for path A 3G Offset, Range -8-7. Poworer Index Difference between BW30-IS and BW40-IS (UpSide Ch L LowSide Ch)/2. Bil(34) for path A 3G Offset, Range -8-7. Poworer Index Difference between BW30-IS and BW40-IS (UpSide Ch L LowSide Ch)/2. Bil(34) for path A 3G Offset, Range -8-7. Poworer Index Difference between BW30-IS and BW40-IS (UpSide Ch L LowSide Ch)/2. Bil(34) for pat	C	1:00 1. d		
hex digits), Bit[330] for path A. 20MHz mode HT OFDM Tx power difference between HT40_1S for 5G 196 channels (28 hex digits), Bit[310] for path A. Bit[74] for path B. PwrdiffSGFDM Leavy OFDM Tx power difference between HT40_1S for 5G 196 channels (28 hex digits), Bit[310] for path A. Bit[74] for path B. PwrdiffSGFDM Leavy OFDM Tx power difference between HT40_1S for 5G 196 channels (28 hex digits), Bit[30] for path A. Bit[74] for path B. BW20-1S and BW40-1S. Bit[74] for path B. BW20-1S and BW40-1S. Bit[330] Path A 2G Offset, Range -8-7. Powover Index Difference between BW20-2S and BW40-1S. Bit[73] Path A 3G Offset, Range -8-7. Powover Index Difference between BW20-1S and BW40-1S. Bit[74] Path A 3G Offset, Range -8-7. Powover Index Difference between BW20-2S and BW40-1S. Bit[74] Path A 3G Offset, Range -8-7. Powover Index Difference between OrDM-Tx and BW40-1S. Bit[74] Path A 3G Offset, Range -8-7. Powover Index Difference between BW20-2S and BW40-1S. Bit[74] Path A 3G Offset, Range -8-7. Powover Index Difference between OrDM-Tx and BW40-1S. Bit[74] Path A 3G Offset, Range -8-7. Powover Index Difference between OrDM-Tx and BW40-1S. Bit[74] Path A 3G Offset, Range -8-7. Powover Index Difference between OrDM-Tx and BW40-1S. Bit[74] Path A 3G Offset, Range -8-7. Powover Index Difference between BW20-2S and BW40-1S. Bit[74] Path A 3G Offset, Range -8-7. Powover Index Difference between BW20-2S and BW40-1S. Bit[74] Path A 3G Offset, Range -8-7. Powover Index Difference between BW20-2S and BW40-1S. Bit[74] Path A 3G Offset, Range -8-7. Powover Index Difference between BW20-1S and BW40-1S. Bit[74] Path A 3G Offset, Range -8-7. Powover Index Difference between BW30-1S and BW40-1S. Bit[74] Path A 3G Offset, Range -8-7. Powover Index Difference between BW10-1S and BW40-1S (Difference betwee	S	stream Tx power difference between		
PwrdiffSGHT20 20MHz mode HT OFDM Tx power difference between HT40_1S for 5G 196 channels (28 hex digits). Bit[3:0] for path A. Bit[7:4] for path B. Bit[7:4				
Pwrdiff5GHT20 20MHz mode HT OFDM Tx power difference between HT40_1S for 5G 196 channels (28 hex digits). Bit[7:4] for path B. Bit[7:4] Fath A2G Offset, Range. 8-7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A2G Offset, Range. 8-7. Pwower Index Difference between BW20.2S and BW40-1S. Bit[7:4]: Path A2G Offset, Range. 8-7. Pwower Index Difference between BW20.2S and BW40-1S. Bit[7:4]: Path A2G Offset, Range. 8-7. Pwower Index Difference between BW20.2S and BW40-1S. Bit[7:4]: Path A2G Offset, Range. 8-7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A3G Offset, Range. 8-7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A3G Offset, Range. 8-7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A3G Offset, Range. 8-7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A3G Offset, Range. 8-7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A3G Offset, Range. 8-7. Pwower Index Difference between BW20.2S and BW40-1S. Bit[7:4]: Path A3G Offset, Range. 8-7. Pwower Index Difference between BW20.2S and BW40-1S. UpSide Ch 1- LowSide Ch)/2. Bit[7:4]: Path A5G Offset, Range. 8-7. Pwower Index Difference between BW16-1S and BW30-1S (UpSide Ch 1- LowSide Ch)/2. Bit[7:4]: Path A5G Offset, Range. 8-7. Pwower Index Difference between BW16-1S and BW30-1S offset, Range. 8-7. Pwower Index Difference between BW16-1S and BW30-1S offset, Range. 8-7. Pwower Index Difference between BW16-1S and BW30-1S offset, Range. 8-7. Pwower Index Difference between BW16-1S and BW30-1S offset, Range. 8-7. Pwower Index Difference between BW16-1S and BW30-				
difference between HT40_1S for 5G 196 channets (28 hex digits). Bit[3:0] for path A. Bit[7:4] for path B. Pwrdiff5GOFDM Legacy OFDM Try power difference between HT40_1S for 5G 196 channets (28 hex digits). Bit[3:0] for path A. Bit[7:4] for path B. Pwrdiff_20BW1S_ DPMIT_A Bit[7:4] For path B. Bit[7:4] For path B. Bit[7:4] For path B. Bit[7:4] For path A. Groffset, Range -8-7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[3:0]: Path A. Groffset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A. Groffset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A. Groffset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A. Groffset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A. Groffset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A. Groffset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A. Groffset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A. Groffset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A. Groffset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A. Groffset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A. Groffset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A. Groffset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A. Groffset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A. Groffset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A. Groffset, Range -8-7. Pwower Index Difference between BW20-2S and BW30-1S (UpSide Ch + LowSide Ch)2. Bit[7:4]: Path A. Groffset, Range -8-7. Pwower Index Difference between BW30-2S and BW30-1S. Bit[7:4]: Path A. Groffset, Range -8-7. Pwower Index Difference between BW30-2S and BW30-1S. Bit[7	D 1:005 CHIEGO		DE 11 1 1	T. C1 .
196 channels (28 hex digits) Bit[7:4] for path Bit[7:4] for pa	Pwrdiff5GHT20			Type of byte array
Bit[3:0] for path A. Bit[7:4] for path B. R. PwrdiffSGOFDM Legacy OFDM Tx power difference between HT40 Sf or SG 196 channels (28 hex digits). Bit[3:0] for path A. Bit[7:4] for path B. Pwower Index Difference between BW20-15 and BW40-1S. Bit[7:4]: Path A. GOffset, Range -8-7. Pwower Index Difference between OFDM-17x and BW40-1S. Bit[7:4]: Path A. GOffset, Range -8-7. Pwower Index Difference between OFDM-17x and BW40-1S. Bit[7:4]: Path A. GO Offset, Range -8-7. Pwower Index Difference between BW40-2S and BW40-1S. Bit[7:4]: Path A. GO Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[3:0]: Path A. GO Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A. GO Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A. GO Offset, Range -8-7. Pwower Index Difference between OFDM-17x and BW40-1S. Bit[7:4]: Path A. GO Offset, Range -8-7. Pwower Index Difference between OFDM-17x and BW40-1S. Bit[7:4]: Path A. GO Offset, Range -8-7. Pwower Index Difference between OFDM-17x and BW40-1S. Bit[7:4]: Path A. GO Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A. GO Offset, Range -8-7. Pwower Index Difference between BW30-2S and BW40-1S. Bit[7:4]: Path A. GO Offset, Range -8-7. Pwower Index Difference between BW30-2S and BW40-1S. Bit[7:4]: Path A. GO Offset, Range -8-7. Pwower Index Difference between BW30-2S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A. GO Offset, Range -8-7. Pwower Index Difference between BW10-18 and BW40-18 (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A. GO Offset, Range -8-7. Pwower Index Difference between BW10-18 and BW40-18 (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A. GO Offset, Range -8-7. Pwower Index Difference between BW10-18 and BW40-18 (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A. GO Offset, Range -8-7. Pwower Index Difference between BW30-2S and BW30-1S. Bit[7:4]: Path A. GO Offset, Range -8-7. Pwower Index Difference between BW30-2S and BW30-1S. Bit[7		_		
B. Legacy OFDM Tx power difference between HT40_1S for 5G 196 Channels (28 bex digits). Bit[3:0] for path A. Bit[7:4] for path B.				
PewdiffSGOFDM Legacy OFDM Tx power difference between HT40_1S for SG 196 channels (28 hex digits), Bit[3:0] for path B. pwrdiff_20BWIS, Bit[7:4] for path B. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4] For phath 2G Offset, Range -8-7. Pewder Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Pewder Index Difference between BW20_2B and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Pewder Index Difference between BW20_2B and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Pewder Index Difference between BW20_2B and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Pewder Index Difference between BW30_0B and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Pewder Index Difference between BW30_0B and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Pewder Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Pewder Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Pewder Index Difference between BW30_0B and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Pewder Index Difference between BW30_0B and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Pewder Index Difference between BW30_0B and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Pewder Index Difference between BW30_0B and BW40_1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Pewder Index Difference between BW30_0B and BW40_1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Pewder Index Difference between BW30_0B and BW40_1S. (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 3G Offset, Range -8-7. Pewder Index Difference between BW30_0B and BW40_1S. (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 2G Offset, Range -8-7. Pewder Index Difference between BW30_0B and BW30_1S. GlpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 2G Offset, Range -8-7. Pewder Index Difference between BW30_0B and BW30_1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Pewder Index Difference between BW30_0B and BW30_1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Pewder Index		Bit[3:0] for path A. Bit[7:4] for path		
between HT40. IS for 5G 196 channels C28 hex digits). Bit[3:0] for path A. Bit[7:4] for path B. Powdiff_20BWIS. Powore Index Difference between BW20-1S and BW40-1S. Bit[7:4]: Path A 2G Offset, Range_8-8-7. Powower Index Difference between OFDM-1Tx and BW40-1S. Bit[3:0]: Path A 2G Offset, Range_8-8-7. Powower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 2G Offset, Range_8-8-7. Powower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 2G Offset, Range_8-8-7. Powower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 2G Offset, Range_8-8-7. Powore Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 2G Offset, Range_8-8-7. Powore Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A 3G Offset, Range_8-8-7. Powore Index Difference between OFDM-1Tx and BW40-1S. Bit[3:0]: Path A 3G Offset, Range_8-8-7. Powore Index Difference between OFDM-1Tx and BW40-1S. Bit[3:0]: Path A 3G Offset, Range_8-8-7. Poworer Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range_8-8-7. Poworer Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range_8-8-7. Poworer Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range_8-8-7. Poworer Index Difference between BW20-2S and BW30-1S. Bit[7:4]: Path A 5G Offset, Range_8-8-7. Poworer Index Difference between BW20-2S and BW30-1S. Bit[7:4]: Path A 5G Offset, Range_8-8-7. Poworer Index Difference between BW30-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range_8-8-7. Poworer Index Difference between BW30-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range_8-8-7. Poworer Index Difference between BW30-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range_8-8-7. Poworer Index Difference between BW30-1S and BW30-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range_8-8-7. Poworer Index Difference between BW30-1S and BW30-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range_8				
channels (28 hex digits), Bit[3:0] for path A. Bit[7:4] for path B. Bit[7:4] for path B. Pwower Index Difference between BW20-1S and BW40-1S. Bit[7:4] path A 2G Offset, Range -8-7. Pwower Index Difference between OFDM-ITx and BW40-1S. Bit[3:0]: Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[3:0]: Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between OFDM-ITx and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between OFDM-ITx and BW40-1S. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-1S and BW30-1S and BW40-1S (UpSide Ch + LowSide Ch) ² . Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW80-1S and BW40-1S (UpSide Ch + LowSide Ch) ² . Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW80-1S and BW40-1S (UpSide Ch + LowSide Ch) ² . Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW80-1S and BW80-1S (UpSide Ch + LowSide Ch) ² . Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW80-1S and BW80-1S and BW80-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW80-1S and BW80-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW80-1S and BW80-1S. Bit[7:4]: P	Pwrdiff5GOFDM		RF module dependent	Type of byte array
pwrdiff_20BW1S Perower Index Difference between BW20-1S and BW40-1S. Bit[7-4]: Path A 2G Offset, Range -8-7. pwrdiff_40BW2S pwrdiff_40BW2S pwrdiff_40BW2S pwrdiff_40BW2S pwrdiff_40BW2S pwrdiff_50_20B pwrdiff_50_20B pwrdiff_50_20B pwrdiff_50_20B pwrdiff_50_30B BW20-1S and BW40-1S. Bit[7-4]: Path A 2G Offset, Range -8-7. pwrdiff_50_20B pwrdiff_50_30B BW20-1S and BW40-1S. Bit[7-4]: Path A 2G Offset, Range -8-7. pwrdiff_50_30B BW20-1S and BW40-1S. Bit[7-4]: Path A 2G Offset, Range -8-7. pwrdiff_50_30B BW20-1S and BW40-1S. Bit[7-4]: Path A 2G Offset, Range -8-7. pwrdiff_50_30B BW20-1S and BW40-1S. Bit[7-4]: Path A 3G Offset, Range -8-7. pwrdiff_50_30B BW20-1S and BW40-1S. Bit[7-4]: Path A 3G Offset, Range -8-7. pwrdiff_50_40B BW20-1S and BW40-1S. Bit[7-4]: Path A 3G Offset, Range -8-7. pwrdiff_50_40B BW20-2S and BW20-1S. Bit[7-4]: Path A 3G Offset, Range -8-7. pwrdiff_50_40B BW20-2S and BW20-1S. Bit[7-4]: Path A 3G Offset, Range -8-7. pwrdiff_50_40B BW20-2S and BW20-1S. Bit[7-4]: Path A 3G Offset, Range -8-7. pwrdiff_50_40B BW20-1S and BW40-1S. Bit[7-4]: Path A 3G Offset, Range -8-7. pwrdiff_50_40B BW20-1S and BW30-1S (UpSide Ch + LowSide Ch)/2. Bit[7-4]: Path A 3G Offset, Range -8-7. Pwower Index Difference between BW30-3B and BW30-1S (UpSide Ch + LowSide Ch)/2. Bit[7-4]: Path A 3G Offset, Range -8-7. Pwower Index Difference between BW30-3B and BW30-1S (UpSide Ch + LowSide Ch)/2. Bit[7-4]: Path A 3G Offset, Range -8-7. Pwower Index Difference between BW30-3B and BW30-1S (UpSide Ch + LowSide Ch)/2. Bit[7-4]: Path A 3G Offset, Range -8-7. Pwower Index Difference between BW30-3B and BW30-1S (UpSide Ch + LowSide Ch)/2. Bit[7-4]: Path A 3G Offset, Range -8-7. Pwower Index Difference between BW30-3B and BW30-1S (UpSide Ch + LowSide Ch)/2. Bit[7-4]: Path A 3G Offset, Range -8-7. Pwower Index Difference between BW30-3B and BW30-1S (UpSide Ch + LowSide Ch)/2. Bit[7-4]: Path A 3G Offset, Range -8-7. Pwower Index Difference between BW30-3B and BW30-3B and BW30-3B and BW30-3B and BW30-3				
DWOMEN TO BY A CONTRICT OF STATE OF STA		channels (28 hex digits). Bit[3:0] for		
DFDMIT_A BW20-IS and BW40-IS. Bit[7-4]: Path A 2G Offset, Range -8-7. Pwower Index Difference between DFDM-ITX and BW40-IS. Bit[3-0]: Path A 2G Offset, Range -8-7. Pwower Index Difference between BW40-2S and BW40-IS. Bit[7-1]: Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-IS. Bit[3-0]: Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW20-IS. Bit[3-0]: Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW20-IS. Bit[7-4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between OFDM-ITX and BW40-IS. Bit[3-0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between OFDM-ITX and BW40-IS. Bit[7-4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-IS. Bit[7-4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-IS. Bit[7-4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW40-IS and BW40-IS (UpSide Ch + LowSide Ch)/2. Bit[7-4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW60-IS and BW40-IS (UpSide Ch + LowSide Ch)/2. Bit[7-4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-IS and BW80-IS (UpSide Ch + LowSide Ch)/2. Bit[7-4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-IS and BW80-IS (UpSide Ch + LowSide Ch)/2. Bit[7-4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-IS and BW80-IS (UpSide Ch + LowSide Ch)/2. Bit[7-4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW80-2S and BW80-IS. Bit[7-4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW80-2S and BW80-IS. Bit[7-4]: Path A 5G Offset, Range -8-7. BW80-2S and BW80-IS. Bit[7-4]: Path A 5G Offset, Range -8-7.		path A. Bit[7:4] for path B.		
Bit[7:4]: Path A 2G Offset, Range -8-7. Pwower Index Difference between OFDM-ITx and BW40-1S. Bit[3:0]: Path A 2G Offset, Range -8-7. Pwower Index Difference between BW40-2S and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-1S and BW40-1S. Bit[3:0]: Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-1S and BW40-1S. Bit[7:4]: Path A 3G Offset, Range -8-7. Pwower Index Difference between OFDM-ITx and BW40-1S. Bit[7:4]: Path A 3G Offset, Range -8-7. Pwower Index Difference between OFDM-ITx and BW40-1S. Bit[7:4]: Path A 3G Offset, Range -8-7. Pwower Index Difference between BW2S_20BW2S_A BW40-1S. Bit[7:4]: Path A 3G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 3G Offset, Range -8-7. Pwower Index Difference between BW30-1S and BW40-1S. Bit[7:4]: Path A 3G Offset, Range -8-7. Pwower Index Difference between BW30-1S and BW40-1S. Bit[3:0]: Path A 3G Offset, Range -8-7. Pwower Index Difference between BW30-1S and BW40-1S. Bit[3:0]: Path A 3G Offset, Range -8-7. Pwower Index Difference between BW30-1S and BW40-1S. Bit[3:0]: Path A 3G Offset, Range -8-7. Pwower Index Difference between BW30-1S and BW40-1S. BW40-1S and BW40-1S. BW40	pwrdiff_20BW1S_	Pwower Index Difference between	RF module dependent (for 8812)	Type of byte array
Range –8-7. Pwower Index Difference between OFDM-ITx and BW40-IS. Bit[3:0]: Path A 2G Offset, Range –8-7. Pwower Index Difference between BW40-2S and BW40-1S. Bit[7:4]: Path A 2G Offset, Range –8-7. Pwower Index Difference between BW20-2S and BW20-IS. Bit[7:4]: Path A 2G Offset, Range –8-7. Pwower Index Difference between BW20-2S and BW40-IS. Bit[7:4]: Path A 5G Offset, Range –8-7. Pwower Index Difference between BW20-1S. Bit[7:4]: Path A 5G Offset, Range –8-7. Pwower Index Difference between OFDM-ITx and BW40-IS. Bit[7:4]: Path A 5G Offset, Range –8-7. Pwower Index Difference between OFDM-ITx and BW40-IS. Bit[7:4]: Path A 5G Offset, Range –8-7. Pwower Index Difference between BW20-2S and BW40-IS. Bit[7:4]: Path A 5G Offset, Range –8-7. Pwower Index Difference between BW20-2S and BW40-IS. Bit[7:4]: Path A 5G Offset, Range –8-7. Pwower Index Difference between BW20-2S and BW40-IS. Bit[7:4]: Path A 5G Offset, Range –8-7. Pwower Index Difference between BW20-2S and BW40-IS (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range –8-7. Pwower Index Difference between BW160-IS and BW80-IS (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range –8-7. Pwower Index Difference between BW160-IS and BW80-IS (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range –8-7. Pwower Index Difference between BW160-IS and BW80-IS (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range –8-7. Pwower Index Difference between BW160-IS and BW80-IS (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range –8-7. Pwower Index Difference between BW160-IS and BW80-IS (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range –8-7. Pwower Index Difference between BW160-IS and BW80-IS (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range –8-7. Pwower Index Difference between BW160-IS and BW80-IS (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range –8-7. Pwower Index Difference between BW80-IS (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range –8-7. BY80-IS AND BW80-IS (UpSide Ch)/2. Bit	OFDM1T_A	BW20-1S and BW40-1S.	_	
Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit(30): Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit(7-4): Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW20-1S. Bit(30): Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-1S and BW20-1S. Bit(7-4): Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-1S and BW40-1S. Bit(7-4): Path A 5G Offset, Range -8-7. Pwower Index Difference between OFDM-1Tx and Bw40-1S. Bit(30): Path A 5G Offset, Range -8-7. Pwower Index Difference between OFDM-1Tx and Bw40-1S. Bit(7-4): Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW20-1S. Bit(7-4): Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW20-1S. Bit(30): Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW20-1S. Bit(7-4): Path A 5G Offset, Range -8-7. Pwower Index Difference between BW30-1S and BW40-1S (UpSide Ch + LowSide Ch)-2. Bit(7-4): Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)-2. Bit(7-4): Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)-2. Bit(7-4): Path A 5G Offset, Range -8-7. Pwordiff_5G_80B Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)-2. Bit(7-4): Path A 5G Offset, Range -8-7. Pwordiff_5G_80B Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)-2. Bit(7-4): Path A 5G Offset, Range -8-7. Pwordiff_5G_80B Pwower Index Difference between BW80-2S and BW80-1S. Bit(7-4): Path A 5G Offset, Range -8-7. Pwordiff_5G_80B Pwower Index Difference between BW80-2S and BW80-1S. Bit(7-4): Path A 5G Offset, Range -8-7. Pwordiff_5G_80B Pwower Index Difference between BW80-1S. Bit(7-4): Path A 5G Offset, Range -8-7. Pwordiff_5G_80B Pwower Index Difference between BW80-1S. Bit(7-4): Path A 5G Offset, Range -8-7. Pwordiff_5G_80B Pwower Index Difference betwee		Bit[7:4]: Path A 2G Offset,		
OFDM-1Tx and BW40-1S. Bit[3:0]: Path A 2G Offset, Range -8-7. Power Index Difference between BW40-2S and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Power Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Power Index Difference between BW20-1S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Power Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Power Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Power Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Power Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Power Index Difference between BW40-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Power Index Difference between BW40-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Power Index Difference between BW40-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Power Index Difference between BW160-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Power Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Power Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Power Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Power Index Difference between BW80-2S and BW80-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Power Index Difference between BW80-2S and BW80-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. BY80-2S and BW80-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. BY80-2S and BW80-1S. BY80-2S and BW80-1		Range $-8\sim7$.		
Bit[3:0]: Path A 2G Offset, Range -8-7. pwrdiff_40BW2S_A Bit[7:4]: Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-1S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between OFDM-1Tx and Bw40-1S. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between OFDM-1Tx and Bw40-1S. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW40-2S and Bw40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and Bw20-1S. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and Bw40-1S. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW30-2S and Bw40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + Lowide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + Lowide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-2S and BW80-1S (UpSide Ch + Lowide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwore Index Difference between BW160-1S and BW80-1S (UpSide Ch + Lowide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwore Index Difference between BW80-2S and BW80-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwore Index Difference between BW80-2S and BW80-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwore Index Difference between BW80-2S and BW80-1S. Bit[7:4]: Path A 5G Offset, Range -8-7.		Pwower Index Difference between		
Pwordiff_40BW2S_A Range -8-7. Pwower Index Difference between BW40-2S and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[7:4]: Path A 3G Offset, Range -8-7. Pwower Index Difference between BW20-1S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW30-1S and BW40-1S (UpSide Ch LowSide Ch)2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW30-1S (UpSide Ch LowSide Ch)2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW30-1S (UpSide Ch LowSide Ch)2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW30-1S (UpSide Ch LowSide Ch)2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW30-1S (UpSide Ch LowSide Ch)2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW30-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW30-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW30-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW30-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW30-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW30-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW30-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Bit[7:4]: Path A 5G Offset, Range -8-7. Bit[7:4]: Path A 5G Offset, Range -8-7. Bit[7:				
Pwordiff_40BW2S_A Range -8-7. Pwower Index Difference between BW40-2S and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[7:4]: Path A 3G Offset, Range -8-7. Pwower Index Difference between BW20-1S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW30-1S and BW40-1S (UpSide Ch LowSide Ch)2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW30-1S (UpSide Ch LowSide Ch)2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW30-1S (UpSide Ch LowSide Ch)2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW30-1S (UpSide Ch LowSide Ch)2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW30-1S (UpSide Ch LowSide Ch)2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW30-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW30-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW30-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW30-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW30-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW30-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW30-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Bit[7:4]: Path A 5G Offset, Range -8-7. Bit[7:4]: Path A 5G Offset, Range -8-7. Bit[7:		Bit[3:0]: Path A 2G Offset,		
pwrdiff_40BW2S_A BW40-2S and BW40-1S. Bit[7-4]: Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-1S and BW40-1S. Bit[7-4]: Path A 3G Offset, Range -8-7. Pwower Index Difference between BW1S_OFDM1T_ BW20-1S and BW40-1S. Bit[7-4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[7-4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7-4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7-4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7-4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[7-4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW40-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7-4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7-4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7-4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7-4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW2S_160BW2S_A				
BW40-2S and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[3:0]: Path A 2G Offset, Range -8-7. pwrdiff_5G_20B WIS_OFDM1T_ A Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW15_160BW1S_ BW80-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW80-1S. BW80-2S and BW80-1S.	pwrdiff 40BW2S		RF module dependent (for 8812)	Type of byte array
Bit(7:4): Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-1S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Prowower Index Difference between BW25_160BW25_A BW30-2S and BW30-1S. BW30-2S and B			r	J1
Range -8-7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 2G Offset, Range -8-7. pwrdiff_5G_20B W1S_OFDM1T_ A BW20-1S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW10-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW10-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW16-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW16-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW16-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference Between BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference Between BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference Between BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7.	_			
Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 2G Offset, Range –8–7. Pwower Index Difference between BW15_OFDM1T_BW20-1S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range –8–7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[3:0]: Path A 5G Offset, Range –8–7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[7:4]: Path A 5G Offset, Range –8–7. Pwower Index Difference between BW40-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range –8–7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range –8–7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[3:0]: Path A 5G Offset, Range –8–7. Pwower Index Difference between BW20-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range –8–7. Pwower Index Difference between BW160-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range –8–7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range –8–7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range –8–7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range –8–7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range –8–7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range –8–7. Pwower Index Difference between BW2S_160BW2S_A BW80-2S and BW80-1S.				
BW20-2S and BW20-1S. Bit[3:0]: Path A 2G Offset, Range -8-7. Pwower Index Difference between BW20-1S and BW40-1S. Bit[1:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW25_20BW2S_A Bw40-2S and BW40-1S. Bit[1:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[1:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[1:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[1:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW80-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[1:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW30-1S (UpSide Ch + LowSide Ch)/2. Bit[0:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW30-1S (UpSide Ch + LowSide Ch)/2. Bit[0:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW30-1S (UpSide Ch + LowSide Ch)/2. Bit[1:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW30-1S (UpSide Ch + LowSide Ch)/2. Bit[1:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW30-1S (UpSide Ch + LowSide Ch)/2. Bit[1:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW30-1S (UpSide Ch + LowSide Ch)/2. Bit[1:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between RW160-1S and BW30-1S (UpSide Ch + LowSide Ch)/2. Bit[1:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between RW160-1S and BW30-1S (UpSide Ch + LowSide Ch)/2. BW30-2S and BW30-1S (UpSide Ch + LowSide Ch)/3. BW30-2S and BW30-1S (UpSide Ch + LowSid				
Bit[3:0]: Path A 2G Offset, Range -8~7. Pwower Index Difference between W1S_OFDM1T_ A Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW40-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW80-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW80-2S and BW80-1S. BW80-2S				
Range –8~7. Pwower Index Difference between W1S_OFDM1T_B BW20-1S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range –8~7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[3:0]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW40-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW30-2S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW30-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW30-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW30-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW30-1S and BW30-1S. Bit[7:4]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW30-1S and BW30-1S. Bit[7:4]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW30-1S. Bit[7:4]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW30-1S. Bit[7:4]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW30-1S. Bit[7:4]: Path A 5G Offset, Range –8~7.				
pwrdiff_5G_20B W1S_OFDMIT_ A BW20.1S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between W2S_20BW2S_A Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW20-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW28_160BW2S_A				
W1S_OFDM1T_ A BW20-1S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between W2S_20BW2S_A Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW80-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S. BW80-2S and BW80-1S. A Bit[7:4]: Path A 5G Offset, Range -8-7. RF module dependent (for 8812) Type of byte array	pwrdiff 5G 20B		RF module dependent (for 8812)	Type of byte array
A Bit[7:4]: Path A 5G Offset, Range = 8~7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[3:0]: Path A 5G Offset, Range = 8~7. Pwower Index Difference between BW2S_20BW2S_A Bit[7:4]: Path A 5G Offset, Range = 8~7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range = 8~7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range = 8~7. Pwower Index Difference between BW80-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range = 8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range = 8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range = 8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range = 8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range = 8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range = 8~7. Pwower Index Difference between BW80-2S and BW80-1S. BW80-2S and BW80-1S. Bit[7:4]: Path A 5G Offset, Range = 8~7.			iti modere dependent (for 6612)	Type of Syle array
Range –8~7. Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[3:0]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW40-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[7:4]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW80-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW160-1S and BW80-1S. Bit[7:4]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW80-1S. Bit[7:4]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW80-1S. Bit[7:4]: Path A 5G Offset, Range –8~7.				
Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[3:0]: Path A 5G Offset, Range -8-7. pwrdiff_5G_40B W2S_20BW2S_A Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW40-1S. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range -8-7. pwrdiff_5G_80B W1S_160BW1S_A BW80-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8-7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Sh/2). Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Sh/2). Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Sh/2). Pwower Index Difference between BW80-2S and BW80-1S. RF module dependent (for 8812) Type of byte array Type of byte array				
OFDM-1Tx and BW40-1S. Bit[3:0]: Path A 5G Offset, Range –8~7. Power Index Difference between BW40-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range –8~7. Pwowler Index Difference between BW80-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW160-2S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW2S_160BW2S_ BW80-2S and BW80-1S. BW80-2S and BW80-				
Bit[3:0]: Path A 5G Offset, Range –8~7. pwrdiff_5G_40B W2S_20BW2S_A BW40-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range –8~7. pwrdiff_5G_80B W1S_160BW1S_A BW80-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW160-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range –8~7. pwrdiff_5G_80B W2S_160BW2S_BW80-2S and BW80-1S. A BW80-2S and BW80-1S. BW80-				
Range -8~7. pwrdiff_5G_40B W2S_20BW2S_A BW40-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW80-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW80-2S and BW80-1S. BW80-2S and BW80-1S. BW80-2S and BW80-1S. Bit[7:4]: Path A 5G Offset, Range -8~7. Produle dependent (for 8812) Type of byte array Type of byte array Type of byte array				
pwrdiff_5G_40B W2S_20BW2S_A BW40-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW80-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. pwrdiff_5G_80B W2S_160BW2S_ A Bit[7:4]: Path A 5G Offset, Range -8~7. RF module dependent (for 8812) Type of byte array Type of byte array				
W2S_20BW2S_A BW40-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range -8~7. pwrdiff_5G_80B W1S_160BW1S_BW80-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8~7. RF module dependent (for 8812) Type of byte array Type of byte array BW80-2S and BW80-1S. RF module dependent (for 8812)	purdiff 5G 40B		PE module dependent (for 8812)	Type of byte array
Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range -8~7. pwrdiff_5G_80B W1S_160BW1S_A A RF module dependent (for 8812) Type of byte array RF module dependent (for 8812) Type of byte array RF module dependent (for 8812) Type of byte array RF module dependent (for 8812) Type of byte array RF module dependent (for 8812) Type of byte array RF module dependent (for 8812) Type of byte array RF module dependent (for 8812) Type of byte array RF module dependent (for 8812) RF module dependent (for 8812) RF module dependent (for 8812) Type of byte array RF module dependent (for 8812) RF module dependent (for 8812) Type of byte array RF module dependent (for 8812) RF module dependent (for 8812)			KI module dependent (for 8812)	Type of byte array
Range =8~7. Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range =8~7. Pwower Index Difference between BW80-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range =8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range =8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range =8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range =8~7. Pwower Index Difference between BW80-2S and BW80-1S. Bit[7:4]: Path A 5G Offset, Range =8~7.	W 23_20D W 23_A			
Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range -8~7. pwrdiff_5G_80B W1S_160BW1S_A Pwower Index Difference between BW80-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. pwrdiff_5G_80B W2S_160BW2S_A Pwower Index Difference between BW80-1S. Bit[7:4]: Path A 5G Offset, Range -8~7. RF module dependent (for 8812) Type of byte array RF module dependent (for 8812) Type of byte array				
BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range –8~7. pwrdiff_5G_80B W1S_160BW1S_ A Pwower Index Difference between BW80-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range –8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range –8~7. pwrdiff_5G_80B W2S_160BW2S_ A BW80-2S and BW80-1S. Bit[7:4]: Path A 5G Offset, Range –8~7. Produle dependent (for 8812) Type of byte array FF module dependent (for 8812) Type of byte array				
Bit[3:0]: Path A 5G Offset, Range -8~7. pwrdiff_5G_80B W1S_160BW1S_A A Pwower Index Difference between BW80-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. pwrdiff_5G_80B W2S_160BW2S_A Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW80-2S and BW80-1S. Bit[7:4]: Path A 5G Offset, Range -8~7. Type of byte array Type of byte array				
Range -8~7. pwrdiff_5G_80B W1S_160BW1S_ A Pwower Index Difference between BW80-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. pwrdiff_5G_80B W2S_160BW2S_ A RF module dependent (for 8812) Type of byte array RF module dependent (for 8812) Type of byte array Type of byte array RF module dependent (for 8812) Type of byte array Type of byte array				
pwrdiff_5G_80B W1S_160BW1S_ BW80-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. pwrdiff_5G_80B W2S_160BW2S_ A Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW80-1S. Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BF module dependent (for 8812) Type of byte array Type of byte array Type of byte array				
W1S_160BW1S_ BW80-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. pwrdiff_5G_80B W2S_160BW2S_ BW80-2S and BW80-1S. A Bit[7:4]: Path A 5G Offset, Range -8~7. RF module dependent (for 8812) Type of byte array Type of byte array	numdiff FC OOD		DE modulo dependent (for 9912)	Time of bota assess
Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. pwrdiff_5G_80B W2S_160BW2S_ A Bit[7:4]: Path A 5G Offset, Range -8~7. RF module dependent (for 8812) Type of byte array Type of byte array BW80-2S and BW80-1S. Bit[7:4]: Path A 5G Offset, Range -8~7.			Kr module dependent (for 8812)	Type of byte array
Bit[7:4]: Path A 5G Offset, Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. pwrdiff_5G_80B W2S_160BW2S_ A BW80-2S and BW80-1S. Bit[7:4]: Path A 5G Offset, Range -8~7. RF module dependent (for 8812) Type of byte array Type of byte array Type of byte array				
Range -8~7. Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. pwrdiff_5G_80B W2S_160BW2S_ A Pwower Index Difference between BW80-2S and BW80-1S. Bit[7:4]: Path A 5G Offset, Range -8~7. RF module dependent (for 8812) Type of byte array Type of byte array	A			
Pwower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. pwrdiff_5G_80B W2S_160BW2S_ A BW80-2S and BW80-1S. Bit[7:4]: Path A 5G Offset, Range -8~7. RF module dependent (for 8812) Type of byte array Type of byte array RF module dependent (for 8812)				
BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. pwrdiff_5G_80B W2S_160BW2S_ A BW80-2S and BW80-1S. Bit[7:4]: Path A 5G Offset, Range -8~7. RF module dependent (for 8812) Type of byte array Type of byte array				
Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7. pwrdiff_5G_80B W2S_160BW2S_ A Pwower Index Difference between BW80-2S and BW80-1S. Bit[7:4]: Path A 5G Offset, Range -8~7. Type of byte array Type of byte array				
Bit[3:0]: Path A 5G Offset, Range -8~7. pwrdiff_5G_80B W2S_160BW2S_ A Bit[7:4]: Path A 5G Offset, Range -8~7. RF module dependent (for 8812) Type of byte array Type of byte array Type of byte array RF module dependent (for 8812)				
Range -8~7. pwrdiff_5G_80B W2S_160BW2S_ A Refrection between BW80-1S. Bit[7:4]: Path A 5G Offset, Range -8~7. Refrection between BW80-1S. Bit[7:4]: Path A 5G Offset, Range -8~7.		· ·		
pwrdiff_5G_80B W2S_160BW2S_ A BW80-2S and BW80-1S. Bit[7:4]: Path A 5G Offset, Range -8~7. RF module dependent (for 8812) Type of byte array				
W2S_160BW2S_ BW80-2S and BW80-1S. A Bit[7:4]: Path A 5G Offset, Range -8~7.				
A Bit[7:4]: Path A 5G Offset, Range -8~7.			RF module dependent (for 8812)	Type of byte array
Range –8~7.	W2S_160BW2S_			
	A			
Pwower Index Difference between		- C		
		Pwower Index Difference between		

	DW4160.20 1.DW4160.10		
	BW160-2S and BW160-1S.		
	Bit[3:0]: Path A 5G Offset,		
1, cc 00DM11	Range –8~7.	DE 11 1 1 (6 0012)	TD C1
pwrdiff_20BW1S_		RF module dependent (for 8812)	Type of byte array
OFDM1T_B	BW20-1S and BW40-1S.		
	Bit[7:4]: Path B 2G Offset,		
	Range –8~7.		
	Pwower Index Difference between		
	OFDM-1Tx and BW40-1S.		
	Bit[3:0]: Path B 2G Offset,		
	Range –8~7.		
	Pwower Index Difference between	RF module dependent (for 8812)	Type of byte array
20BW2S_B	BW40-2S and BW40-1S.		
	Bit[7:4]: Path B 2G Offset,		
	Range –8~7.		
	Pwower Index Difference between		
	BW20-2S and BW20-1S.		
	Bit[3:0]: Path B 2G Offset,		
	Range –8~7.		
pwrdiff_5G_20B	Pwower Index Difference between	RF module dependent (for 8812)	Type of byte array
W1S_OFDM1T_	BW20-1S and BW40-1S.		
В	Bit[7:4]: Path B 5G Offset,		
	Range –8~7.		
	Pwower Index Difference between		
	OFDM-1Tx and BW40-1S.		
	Bit[3:0]: Path B 5G Offset,		
	Range –8~7.		
pwrdiff_5G_40B	Pwower Index Difference between	RF module dependent (for 8812)	Type of byte array
W2S_20BW2S_B	BW40-2S and BW40-1S.		
	Bit[7:4]: Path B 5G Offset,		
	Range –8~7.		
	Pwower Index Difference between		
	BW20-2S and BW20-1S.		
	Bit[3:0]: Path B 5G Offset,		
	Range –8~7.		
pwrdiff_5G_80B	Pwower Index Difference between	RF module dependent (for 8812)	Type of byte array
W1S_160BW1S_	BW80-1S and BW40-1S (UpSide		
В	Ch + LowSide Ch)/2.		
	Bit[7:4]: Path B 5G Offset,		
	Range –8~7.		
	Pwower Index Difference between		
	BW160-1S and BW80-1S (UpSide		
	Ch + LowSide Ch)/2.		
	Bit[3:0]: Path B 5G Offset,		
1:00 F.C. 00B	Range –8~7.	DE In I (6 0010)	Thursday C.L.
pwrdiff_5G_80B	Pwower Index Difference between	RF module dependent (for 8812)	Type of byte array
W2S_160BW2S_	BW80-2S and BW80-1S.		
В	Bit[7:4]: Path B 5G Offset,		
	Range –8~7.		
	Pwower Index Difference between		
	BW160-2S and BW160-1S.		
	Bit[3:0]: Path B 5G Offset,		
	Range –8~7.	0. language. 1.1. 1. 1.	
preamble	CCK preamble type	0 – long preamble, 1 – short	
4	Enghla T/D accident	preamble	
trswitch	Enable T/R switch	0 – disable, 1 – enable	
disable_ch14_ofd	Disable OFDM sending and	0 – enable, 1 – disable	
m	receiving in channel 14	0. 255	0.4 1.4 1.1
xcap	Crystal Capacitor value	0 – 255	0 stands the value is
			not calibrated yet.

tssi1	Tx signal strength value of path A	0 – 255		0 stands the value is not calibrated yet.
tssi2	Tx signal strength value of path B	0 – 255		0 stands the value is not calibrated yet.
ther	Thermal value	0 – 255		0 stands the value is not calibrated yet.
MIMO_TR_mode	MIMO mode assignment	1 – 1T2R, 3 – 2T2R, 4 – 1T1R	3	,
ssid	SSID	"string_value", SSID with 32		
		characters in max		
defssid	If don't give SSID in Ad-hoc client mode and no IBSS available, it will start an IBSS with SSID given here.	"string_value", SSID with 32 chars in max	"defaultS SID"	
bssid2join	Besides SSID, designate target BSSID to join	xxxxxxxxxxx (12 digits mac address)		Type of byte array
benint	Beacon interval in ms	20-1024	100	
dtimperiod	DTIM period	1-255	1	Suggest to set 1
				because patent issue
swcrypto	S/w encryption enabled/disabled	0 – disable, 1 – enable		
aclmode	Access control mode	0 - disable, 1 - accept, 2 - deny		
aclnum	Set number of ACL	Suggest set '0' whenever driver is re-initialized		
acladdr	Set access control address	xxxxxxxxxxx (12 digits mac address)		When acl is added, the aclnum will be increased automatically.
oprates	Operational rates	Bit0-bit11 for 1,2,5.5,11,6,9,12,18,24,,36,48,54M	0xfff	
basicrates	Basic rates	Bit0-bit11 for 1,2,5.5,11,6,9,12,18,24,,36,48,54M	0xf	
regdomain	Regulation domain	1-15 (FCC, IC, ETSI, SPAIN, FRANCE, MKK, ISREAL, MKK1, MKK2, MKK3, NCC, RUSSIAN, CN, GLOBAL, WORLD-WIDE)	1	Please refer the 2.4G channel plan table in detail.
autorate	Auto rate adaptive	0 – disable, 1 – enable	1	
fixrate	Fixed Tx rate	Bit0-bit11 for 1,2,5.5,11,6,9,12,18,24,,36,48,54M Bit12-Bit27 for MCS0,MCS1,,MCS15 (Bit31 + 0) for NSS1-MCS0 (Bit31 + 1) for NSS1-MCS1 (Bit31 + 2) for NSS1-MCS2 (Bit31 + 10) for NSS2-MCS0 (Bit31 + 11) for NSS2-MCS1 (Bit31 + 12) for NSS2-MCS1 (Bit31 + 12) for NSS2-MCS2 etc		Refer when auto rate is disabled
disable_protection disable_olbc	Forcedly disable protection mode Forcedly OLBC detection	0 – auto, 1 – disable protection 0 – auto, 1 – disable protection		Normally when 11g is used, driver will auto detect if legacy (11b) device is existed. When 11n is used, driver will auto detect if legacy (11b/g) device is existed. If yes, it will enable protection mode automatically. Normally 11g AP

				should detect OLBC. If disabled, AP will enter protection mode only when legacy device associated.
deny_legacy	Deny the association from legacy STA for corresponding band	1 – 11b, 2 – 11g, 4 – 11a, 8 – 11n		Set the corresponding legacy band of STA to deny
fast_roaming lowestMlcstRate	Client mode fast roaming Use lowest basic rate to send multicast and broadcast	0 - disable, 1 - enable 0 - disable Bit0-bit11 for 1,2,5.5,11,6,9,12,18,24,,36,48,54M Bit12-Bit27 for MCS0,MCS1,,MCS15		
stanum	Limit max associated sta number	0-32. 0 – disable (not limit).		
authtype	802.11 Authentication type	0 – open system, 1 – shared key, 2 – auto	2	
encmode	Encryption mode	0 – disabled, 1 – WEP64, 2 – TKIP, 4 – AES(CCMP), 5 – WEP128		Set to 2 always under WPA/WPA2 mode
wepdkeyid psk_enable	WEP default Tx key PSK mode	0-3 0 - disable, 1 - WPA, 2 - WPA2, 3 - WPA/WPA2 mixed		
wpa_cipher	WPA PSK cipher suite	2 –TKIP, 8 – AES(CCMP), 10 – TKIP/AES mixed		
wpa2_cipher	WPA2 PSK cipher suite	2 –TKIP, 8 – AES(CCMP), 10 – TKIP/AES mixed		
passphrase	PSK key	32 characters or 64 hex digits		
gk_rekey 802_1x	Group key update time Flag of using 802.1x	0 – disable, >1 – enable 0 – disable, 1 – enable		Time unit is second When 802.1x is enabled, the Auth daemon must be invoked
default_port	Default state of 802.1x control port	0 – data packet is not allowed to pass through until 802.1x authentication is ok 1 – data packet is allowed pass through even 802.1x authentication is not ok		Refer when 802_1x is set to 1
wepkey1	WEP key1	10 hex digits for WEP64, 26 hex digits for WEP128		Type of byte array
wepkey2	WEP key2	10 hex digits for WEP64, 26 hex digits for WEP128		Type of byte array
wepkey3	WEP key3	10 hex digits for WEP64, 26 hex digits for WEP128		Type of byte array
wepkey4	WEP key4	10 hex digits for WEP64, 26 hex digits for WEP128		Type of byte array
opmode	Operation mode (AP or client)	16 – AP, 8 – Infrastructure client, 32 – Ad-hoc client	16	
hiddenAP	Hidden AP enable/disable	0 – disabled, 1 – enabled		
rtsthres	RTS threshold	0-2347	2347	
fragthres	Fragment threshold	256-2346	2346	
shortretry	Short retry limit	1-255	3	
longretry	Long retry limit	1-255	3	mr
expired_time	Client inactivity time in 10ms	>100	30000	Time unit is 10 ms.
led_type	WLAN LED type	For 8188RE, 8192CE, 8192DR	4	
		LED0 LED1 0 tx rx	<u> </u> 	

			П	
		2 link tx/rx (d,m)		
		3 link/tx/rx (d,m) n/a 4 link tx/rx (d)	1	
		4		
		6 enable tx/rx (d)		
		7 enable/tx/rx (d) n/a		
		8 11a tx/rx (d) 11g tx/rx (d)		
		0-1 – hw control	<u>'</u>	
		2-8 – sw control		
		d – count data frames		
		m – count data frames m – count management frames		
		LED2 (GPIO8)		
		11 link/tx/rx (d,m)		
		15 link/tx/rx(d)		
		12 enable/tx/rx (d)	<u> </u>	
		LED2 (GPIO10)	<u> </u>	
		13 link/tx/rx (d,m)	<u> </u>	
		LED1 (GPIO10) (RTL8192D)		
		14 link/tx/rx (d,m)		
		50 enable/tx/rx (d)	<u> </u>	
		11-15, 50 – sw control		
		d – count data frames		
		m – count management frames		
		in count management frames		
		For 8188ER		
		LED0 (GPIO5)		
		3 link/tx/rx (d,m)		
		5 link/tx/rx (d)		
		7 enable/tx/rx (d)		
		All sw control		
		d – count data frames		
		m – count management frames		
iapp_enable	IAPP enable/disable	0 – disable, 1 - enable		
block_relay	Block packet relaying between	0 - relay, $1 - block relay$ and drop,		
	associated clients	2 – block relay and indicate to		
		bridge		
deny_any	Deny the association SSID of "any"	0 – disable, 1 – enable		
	including upper and lower cases			
crc_log	Calculate CRC error packets	0 – disable, 1 – enable		
wifi_specific	Do WiFi logo test specific check	0 – disable, 1 – enable, 2 – auto	2	0 for performance
				mode; 1 for WiFi
				mode, 2 for auto
				mode. PS. For
				8192DR 1x1
				concurrent mode and
				8188E, please set 1 to
				pass WiFi logo test.
disable_txsc	Tx shortcut enable/disable	0 – enable, 1 – enable		
disable_rxsc	Rx shortcut enable/disable	0 – enable, 1 – enable		
disable_brsc	Bridge shortcut enable/disable	0 – enable, 1 – enable		
keep_rsnie	Don't clean RSN IE while	0 - erase, 1 - keep		
1 1	reinitialize the interface	1 111 0 11 1 1 1	2	
band	Band selection	1 - 11b, $2 - 11g$, $4 - 11a$, $8 - 11n$	3	
2422221C	Han at 20 alf for a rest of	64 – 11ac	1	
cts2self	Use cts2Self for protection mode	0 – no, 1 – yes	1	
wds_enable	WDS enable/disable	0 – disable, 1 – enable		
wds_pure	Flag to enable pure WDS mode that	0 – disable, 1 – enable		
	don't broadcast beacon and don't accept any station			

wds_num wds_add	Give WDS packets higher priority Set number of WDS Set mac address of peer WDS AP	Suggest set '0' whenever driver is re-initialized xxxxxxxxxxx (12 digits mac		
		vvvvvvvvvvv (12 digita mag		
	and the rate sent to the peer WDS AP	address). The max entry could be added is 8 in default configuration. After mac address, there is a 32-bit variable to give the rate. Bit0-bit11 for 1,2,5.5,11,6,9,12,18,24,,36,48,54M Bit12-Bit27 for MCS0,MCS1,,MCS15		When mac address is added, the wds_num will be increased automatically.
wds_encrypt	WDS encryption mode	0 – disabled, 1 – WEP64, 2 –		
	,	TKIP, 4 – AES (CCMP), 5 – WEP128		
wds_wepkey	WDS WEP default key	10 hex digits for WEP64, 26 hex digits for WEP128		Type of byte array
_	WDS PSK key Disable NAT2.5 transformation in client mode	32 characters or 64 hex digits 0 – enable, 1 – disable		
_	Enable MAC clone from the first incoming packet	0 – disable, 1 – enable		
	Flag of adding broadcast flag into	0 – enable, 1 – disable		
	DHCP request Add extra tag in PPPoE packets by NAT2.5	0 – disable, 1 – enable	1	When set to 0, NAT2.5 can only support one session buildup at the same time.
clone_mac_addr	Assign the target MAC to clone	xxxxxxxxxxx (12 digits mac address)		Type of byte array
	NAT2.5 shortcut enable/disable	0 – enable, 1 – disable		
	Show hidden BSS in site survey	0 – disable, 1 – enable		
ack_timeout	Set ACK timeout value	0-255		0 means using standard value. In unit of us.
private_ie	Send and get private IE	At most 64 hex digits byte array		
groupID	Group ID of virtual AP (multiple SSID)	0-65535		When AP (including root and virtual) set the same group ID, the wlan traffics could be relayed. Root interface: wlan0 Virtual interface: wlan0-va0~wlan0-va3.
vap_enable	Tell driver if multiple AP function is enabled or disabled	0 – disable, 1 – enable		If multiple AP is enabled, this mib must be set to 1.
apsd_enable	Temporary disable wlan function Support WMM and QoS Support WMM APSD function Support WiFi Protection Setup	0 – normal, 1 – wlan off 0 – disable, 1 – enable 0 – disable, 1 – enable Bit0 for client mode, Bit1 for AP		00 Set to 1.
	PIN setting for WPS	mode "string_value" with 8 characters in max		
-				
supportedmcs	Supported MCS rates Basic MCS rates	Bit 0-15 for MCS0,, MCS15 Bit 0-15 for MCS0,, MCS15	0xffff	

	1 403.6		
mode			
C		1	
Control sideband offset		1	
	_ - -		
Support short GL in 20M bandwidth			
<u> </u>			
	,		
mode			
Flag of DEBUG_ERR() macro		fffffff	
Flag of DEBUG_INFO() macro		0	
Flag of DEBUG_WARN() macro		U	
Chenic En Con		0	
Flag of DEBUG_TRACE() macro		U	
Multiple of July LED LUGLIC		1	This realise could be
	1~100	1	This value will be referred only when
requency.			mib value of
			'led_type' is greater
			than 1.
WAPI mode	0 - Disable	n	man 1.
WAI I Mode		U	
WAPI PSK			
	0~32		
	1 – Disable		This object selects a
	2 – Time based		mechanism for rekeying
	3 – Packet based		the unicast key.
	4 – Mix mode(Rekey when time or		
	,		
Timeout threshold of time-based	Unit: sec.		
unicast key update mechanism			
Multicast key update mode			This object selects a
			mechanism for rekeying the multicast key.
			me municast key.
Timeout threak ald aftime hand	,		
	Unit: sec.		
	0: disable 1: enable	0	
	o. disuote, 1. chaote		
	0: disable, 1: enable	0	It is useless in general
		-	case
	1~7	7	Its value in flash is
broadcasted by AP			sum of SIFS and total
•			slot time.
	Support 20M/40M coexistant mode Flag of DEBUG_ERR() macro Flag of DEBUG_INFO() macro Flag of DEBUG_WARN() macro Flag of DEBUG_TRACE() macro Multiple of wlan LED blinking frequency. WAPI PSK WAPI PSK length Unicast key update mode Fimeout threshold of time-based micast key update mechanism Packet number threshold of packet based unicast key update mechanism Multicast key update mode Fimeout threshold of time-based multicast key update mechanism Packet number threshold of packet based multicast key update mechanism Packet number threshold of packet based multicast key update mechanism Enable / disable EDCA use manual values Enable / disable AP broadcasting BK queue under ACM Set AIFS slot number for BK queue	2 - 80M 1 - secondary channel is below the primary channel, 2 - secondary channel is above the primary channel is able in the primary channel i	2 - 80M 1 - secondary channel is below the primary channel, 2 - secondary channel is above the primary channel is above the prim

				11a/b/g and 16 us when 11n. Slot time is 20 us when 11a/b and 9 us when 11g/n. For example, sta_bkq_aifsn=7 under
				11g/n, AIFS is 9*7+16 = 79 us.
sta_bkq_cwmin	Set minimal contention window period for BK queue broadcasted by AP	1~10	4	Slot time will be 2^n-1, 15, by default.
sta_bkq_cwmax	Set maximal contention window period for BK queue broadcasted by AP	1~10	10	Slot time will be 2^n-1, 1023, by default.
sta_bkq_txoplimit	Set TXOP limit for BK queue broadcasted by AP	0~256	0	
sta_beq_acm	Enable / disable AP broadcasting BE queue under ACM		0	
sta_beq_aifsn	Set AIFS slot number for BE queue broadcasted by AP	1~7	3	Its value in flash is sum of SIFS and total slot time. SIFS is 10 us when 11a/b/g and 16 us when 11n. Slot time is 20 us when 11g/n. For example, sta_beq_aifsn=3 under 11g/n, AIFS is 9*3+16 = 43 us.
sta_beq_cwmin	Set minimal contention window period for BE queue broadcasted by AP	1~10	4	Slot time will be 2^n-1, 15, by default.
sta_beq_cwmax	Set maximal contention window period for BE queue broadcasted by AP	1~10	10	Slot time will be 2^n-1, 1023, by default.
sta_beq_txoplimit	broadcasted by AP	0~256	0	
sta_viq_acm	Enable / disable AP broadcasting VI queue under ACM		0	
sta_viq_aifsn	Set AIFS slot number for VI queue broadcasted by AP	1~7	2	Its value in flash is sum of SIFS and total slot time. SIFS is 10 us when 11a/b/g and 16 us when 11n. Slot time is 20 us when 11g/n. For example, sta_viq_aifsn=2 under 11g/n, AIFS is 9*2+16 = 34 us.
sta_viq_cwmin	Set minimal contention window period for VI queue broadcasted by AP	1~10	4	Slot time will be 2^n-1, 15, by default.
sta_viq_cwmax	Set maximal contention window period for VI queue broadcasted by AP	1~10	3	Slot time will be 2^n-1, 7, by default.
sta_viq_txoplimit	Set TXOP limit for VI queue broadcasted by AP	0~256	188	Follow SPEC in 11b

sta_voq_acm	Enable / disable AP broadcasting VO queue under ACM	0: disable, 1: enable	0	
sta_voq_aifsn	Set AIFS slot number for VO queue broadcasted by AP	1~7	2	Its value in flash is sum of SIFS and total slot time. SIFS is 10 us when 11a/b/g and 16 us when 11n. Slot time is 20 us when 11a/b and 9 us when 11g/n. For example, sta_voq_aifsn=2 under 11g/n, AIFS is 9*2+16 = 34 us.
sta_voq_cwmin	Set minimal contention window period for VO queue broadcasted by AP	1~10	3	Slot time will be 2^n-1, 7, by default.
sta_voq_cwmax	Set maximal contention window period for VO queue broadcasted by AP	1~10	2	Slot time will be 2^n-1, 3, by default.
sta_voq_txoplimit	Set TXOP limit for VO queue broadcasted by AP	0~256	102	Follow SPEC in 11b
ap_bkq_aifsn	Set AIFS slot number for BK queue used by AP	1~7	7	Its value in flash is sum of SIFS and total slot time. SIFS is 10 us when 11a/b/g and 16 us when 11n. Slot time is 20 us when 11a/b and 9 us when 11g/n. For example, ap_bkq_aifsn=7 under 11g/n, AIFS is 9*7+16 = 79 us.
ap_bkq_cwmin	Set minimal contention window period for BK queue used by AP	1~10	4	Slot time will be 2^n-1, 15, by default.
ap_bkq_cwmax	Set maximal contention window period for BK queue used by AP	1~10	10	Slot time will be 2 ⁿ -1, 1023, by default.
ap_bkq_txoplimit	Set TXOP limit for BK queue used by AP	0~256	0	
ap_beq_aifsn	Set AIFS slot number for BE queue used by AP	1~7	3	Its value in flash is sum of SIFS and total slot time. SIFS is 10 us when 11a/b/g and 16 us when 11n. Slot time is 20 us when 11a/b and 9 us when 11g/n. For example, ap_beq_aifsn=3 under 11g/n, AIFS is 9*3+16 = 43 us.
ap_beq_cwmin	Set minimal contention window period for BE queue used by AP	1~10	4	Slot time will be 2^n-1, 15, by default.
ap_beq_cwmax	Set maximal contention window period for BE queue used by AP	1~10	6	Slot time will be 2^n-1, 63, by default.
ap_beq_txoplimit	Set TXOP limit for BE queue used by AP	0~256	0	
ap_viq_aifsn	Set AIFS slot number for VI queue	1~7	1	Its value in flash is

	11 45	T		COTTO 1
	used by AP			sum of SIFS and total
				slot time.
				SIFS is 10 us when
				11a/b/g and 16 us
				when 11n. Slot time is
				20 us when 11a/b and
				9 us when 11g/n.
				For example,
				ap_viq_aifsn=1 under
				11g/n, AIFS is 9*1+16
				= 25 us.
ap_viq_cwmin	Set minimal contention window	1~10	4	Slot time will be
	period for VI queue used by AP			2 ⁿ -1, 15, by default.
ap_viq_cwmax	Set maximal contention window	1~10	3	Slot time will be
	period for VI queue used by AP			2^n-1, 7, by default.
ap_viq_txoplimit	Set TXOP limit for VI queue used	0~256	188	Follow SPEC in 11b
	by AP			
ap_voq_aifsn	Set AIFS slot number for VO queue	1~7	1	Its value in flash is
	used by AP			sum of SIFS and total
				slot time.
				SIFS is 10 us when
				11a/b/g and 16 us
				when 11n. Slot time is
				20 us when 11a/b and
				9 us when 11g/n.
				For example,
				ap_voq_aifsn=1 under
				11g/n, AIFS is 9*1+16
				=25 us.
ap_voq_cwmin	Set minimal contention window	1~10	3	Slot time will be
	period for VO queue used by AP			2 ⁿ -1, 7, by default.
ap_voq_cwmax	Set maximal contention window	1~10	2	Slot time will be
1 - 1-	period for VO queue used by AP			2^n-1, 3, by default.
ap_voq_txoplimit	Set TXOP limit for VO queue used	0~256	102	Follow SPEC in 11b
1 - 1 - 1	by AP			
phyBandSelect	Set band mode for dual-band	1 - 2G, 2 - 5G	wlan0: 2	Please refer to section
			wlan1: 1	"Dual-band
				configuration"
macPhyMode	Set dual or single MAC/PHY mode	0 – Single MAC/PHY,	2	Please refer to section
		2 – Dual MAC/PHY		"Dual-band
				configuration"
supportedvht	Set Tx/Rx MCS map of VHT	0xfffa – Support 2SS MCS0~9,	0xfffa	1SS Support
T I	Capabilities element which carried	1SS MCS0~9		b[1:0] =
	in beacon, probe response,			0: MCS0~7
	association request, response.	0xfff5 - Support 2SS MCS0~8,		1: MCS0~8
		1SS MCS0~8		2: MCS0~9
	Definition follows Draft			3: not support
	P802.11ac_D3.0, 8.4.2.160.3 VHT	0xfffe - Support 1SS MCS0~9,		T.F.
	Supported MCS Set field	2SS not support		2SS Support
	11	T F		b[3:2] =
		0xfffc - Support 1SS MCS0~7,		0: MCS0~7
		2SS not support		1: MCS0~8
		11		2: MCS0~9
				3: not support
vht_tx map	Set VHT Tx rate map for rate	0xfffff – Support 2SS MCS9~0,	0xfffff	b[9:0]:
- T	adaptive algorithm	1SS MCS9~0	-	1SS MCS9~0
		0x7ffdf –Support 2SS MCS8~0,		b[19:10]:
		1SS MCS8~0		2SS MCS9~0,
				Í
·	+	•	-	

0x3fcff - Suppor 1SS MCS7~0	rt 2SS2 MCS7~0,
0xfcff: Support 1SS MCS7~0	2SS2 MCS5~0,

Note1: The default value of MIB will be '0' if it is not specified.

Note2: The values set to EDCA manually will be applied after driver close and up

Read wlan register command:

"iwpriv wlan0 read_reg type,offset"

- > type could be b for byte, w for word, dw for double word
- > offset indicates the register offset in hex

Write wlan register command:

"iwpriv wlan0 write_reg type,offset,value"

- ➤ type may be b for byte, w for word, dw for double word
- > offset indicates the register offset in hex
- > value for write in hex

Read memory command:

"iwpriv wlan0 read_mem type,start,len"

- ➤ type may be b for byte, w for word, dw for double word
- start indicates the memory start address in hex
- len is for read length in hex

Write memory command:

"iwpriv wlan0 write_mem type,start,len,value"

- ➤ type may be b for byte, w for word, dw for double word
- > start indicates the memory start address in hex
- len is for write length in hex
- > value for write in hex

Note:

The commands above take "wlan0" for example. One can replace "wlan0" with "wlan1" in each command when dual MAC/PHY is enabled.

Driver based MP function:

We supported Driver based MP functions controlled by "iwpriv" utility. Please refer to "8192C Linux Driver MP.doc" for detail explanation and usages.

Additional IOCTL commands (for web display):

id	Meaning	Input	output	comment
0x8b30	Get station info	None	64 array of sta_info_2_web (note1)	
0x8b31	Get associated station number	None	1 word (2 bytes)	
0x8b32	Get version information	None	2 byte of version infomation	
0x8b33	Issue scan request	None	1 byte of result (-1:fail, 0: success)	
0x8b34	Get scan result and scanned	1 byte flag	4 bytes of number of entries and array of	
	BSS database	(get BSS	bss_desc (note4) with flag set to 0	
		database or		
		not)		
0x8b35	Issue join request	bss_desc to	1 byte of result (0: success, 1: scanning, 2:	
		join	fail)	

0x8b36	Get join result	None	1 byte of result (note5)	
0x8b37	Get BSS info	None	Bss_info_2_web structure (note2)	This is used typically
				in client mode
0x8b38	Get WDS info	None	8 array of wds_info (note3)	

```
Note1:
typedef struct _sta_info_2_web {
      unsigned short
                       aid;
      unsigned char
                       addr[6];
      unsigned long
                       tx_packets;
      unsigned long
                       rx_packets;
      unsigned long
                       expired_time;
      unsigned short
                       flags; // bit2 indicate whether this entry is valid, bit3 indicates if sta is in sleeping
      unsigned char
                       TxOperaRate; // current used tx rate in 500 k bps (e.g., 108 for 55M)
     unsigned char
                       rssi; // received signal strength indication
     unsigned long
                       link_time; // 1 sec unit
     unsigned long
                       tx_fail;
     unsigned long
                       tx_bytes;
     unsigned long
                       rx_bytes;
      unsigned char
                       network;
      unsigned char
                       ht_info;
     unsigned char
                       resv[6];
} sta_info_2_web;
Note2:
typedef enum _wlan_mac_state {
     STATE_DISABLED=0, STATE_IDLE, STATE_SCANNING, STATE_STARTED, STATE_CONNECTED,
STATE_WAITFORKEY
} wlan_mac_state;
typedef struct _bss_info_2_web {
     unsigned char state;
                             // defined in wlan_mac_state
     unsigned char channel;
     unsigned char txRate;
     unsigned char bssid[6];
     unsigned char rssi, sq;
     unsigned char ssid[33];
} bss_info_2_web;
Note3:
typedef struct _wds_info {
      unsigned char
                       state;
      unsigned char
                       addr[6];
      unsigned long
                       tx_packets;
     unsigned long
                       rx_packets;
      unsigned long
                       tx_errors;
      unsigned char
                       TxOperaRate;
} wds_info;
Note4:
struct ibss_priv {
     unsigned short
                       atim_win; };
struct bss_desc {
      unsigned char
                       bssid[6];
      unsigned char
                       ssid[32];
      unsigned char
                       *ssidptr;
      unsigned short
                       ssidlen;
      unsigned char
                       meshid[32];
      unsigned char
                       *meshidptr;
      unsigned short
                       meshidlen;
```

```
unsigned int
                    bsstype;
     unsigned short
                    beacon_prd;
     unsigned char
                    dtim_prd;
     unsigned long
                    t_stamp[2];
     struct ibss_priv ibss_par;
     unsigned short
                    capability;
     unsigned char
                    channel;
     unsigned long
                    basicrate;
     unsigned long
                    supportrate;
     unsigned char
                    bdsa[6];
     unsigned char
                    rssi;
     unsigned char
                    sq;
     unsigned char
                    network;
};
Note5:
Oxff: pending
2-4: success
others: fail
Files under '/proc/wlan0':
cam_info - dump h/w encryption cam content
     mib_xxx – show mib info
sta_info – show all associated station info
sta_keyinfo – show the encryption keys of all associated station info
txdesc – show tx descriptor contents for queue 0 to queue 5 according to command
     rxdesc – show rx descriptor contents
     buf_info – show the internal buffer pointers and counts
     desc_info - show tx and rx descriptor pointers, indexes, and register contents
     stats – show Tx, Rx, and beacon statistics
          up_time - driver uptime
          tx_packets – total tx packet numbers
          tx_bytes – total tx byte counts
          tx_retrys – total tx retry counts
          tx_fails – total tx failed numbers
          tx_drops – total tx dropped counts
          rx_packets - total rx packet numbers
          rx_bytes – total rx byte counts
          rx_retrys – total rx retry counts
          rx_crc_errors - total rx CRC error packet numbers
          rx_errors – total rx error packet numbers (including CRC error, ICV error, etc.)
          rx_data_drops - total rx data dropped counts other than sequence number issue
          rx_decache - total rx data dropped counts due to sequence number duplicated
          rx_fifoO – total rx fifo overflow counts
          rx_rdu - total rx buffer under run counts
          beacon ok – total transmitted OK beacons
          beacon_er - total transmitted failed beacons
         freeskb_err - total error pointers of tx skb numbers
          dz_queue_len - total queued packet numbers for sleeping sta
          check_cnt_tx - internal tx status check counts
          check_cnt_rst - internal driver status check counts
          reused_skb - reused skb numbers
          skb_free_num - free skb numbers
```

- ✓ tx_average average of tx flow
- \checkmark rx_average average of rx flow
- ✓ cur_tx_rate current tx rate
- ➤ mib_EDCA show the EDCA parameters will be applied when enabled
- *.txt MAC and PHY parameter files

2.4G Channel Plan

regulation domain	supported channels
(mib regdomain value)	
FCC (1)	1,2,3,4,5,6,7,8,9,10,11
IC (2)	1,2,3,4,5,6,7,8,9,10,11
ETSI(3)	1,2,3,4,5,6,7,8,9,10,11,12,13
SPAIN (4)	10,11
FRANCE (5)	10,11,12,13
MKK (6)	1,2,3,4,5,6,7,8,9,10,11,12,13,14
ISREAL (7)	3,4,5,6,7,8,9,10,11,12,13
MKK1 (8)	1,2,3,4,5,6,7,8,9,10,11,12,13,14
MKK2 (9)	1,2,3,4,5,6,7,8,9,10,11,12,13,14
MKK3 (10)	1,2,3,4,5,6,7,8,9,10,11,12,13,14
NCC (11)	1,2,3,4,5,6,7,8,9,10,11
RUSSIAN (12)	1,2,3,4,5,6,7,8,9,10,11,12,13
CN (13)	1,2,3,4,5,6,7,8,9,10,11
GLOBAL (14)	1,2,3,4,5,6,7,8,9,10,11 passive scan: 12,13,14
WORLD-WIDE (15)	1,2,3,4,5,6,7,8,9,10,11 passive scan: 12,13

Note: When wifi is used as client mode, it will only listen AP Beacon during scanning in passive channel except it found hidden AP existed. When wifi is used as AP mode and configure to "auto" channel, it will not select the passive channel.

Dual-band Configuration

- Dual-band functions are only supported by RTL8192D series.
- Dual MAC/PHY mode is dependent on Linux kernel configuration "RTL8192D dual-MAC-dual-PHY mode"
- For Dual MAC/PHY mode, wlan0 is for 5G only, wlan1 is for 2G only.

Dual-band related mibs:

- > phyBandSelect: setting the wlan interface as either 5G or 2G
- *macPhyMode:* setting the wlan interface to be started as Dual MAC/PHY (1T1R Concurrent Mode) or Single MAC/PHY (2T2R Selective Mode)
- *band:* setting the band for wlan interfaces. For example: 5G: 12 (A+N), 2G: 11 (B+G+N)
- > channel: setting a correct channel according to the band setting.

phyBandSelect	Set band mode for dual-band	1 - 2G, $2 - 5G$	wlan0: 2	Please refer to section
			wlan1: 1	"Dual-band
				configuration"
macPhyMode	Set dual or single MAC/PHY mode	0 – Single MAC/PHY,	2	Please refer to section
		2 – Dual MAC/PHY		"Dual-band
				configuration"
channel	Operation frequency used	0 for auto channel, 1-14 for		
		11b/11g, 36-165 for 11a		
band	Band selection	1 - 11b, $2 - 11g$, $4 - 11a$, $8 - 11n$	wlan0:12	
			wlan1:11	

pwrlevel5GHT40	40MHz mode HT OFDM 1 spatial	RF module dependent	Type of byte array.
1S_A	stream Tx power level for 196	Transdate dependent	E.g. Channel 36
15_A	(channel 1~196) channels (392 hex		should use the 36'th
	digits) for path A		byte.
-	40MHz mode HT OFDM 1 spatial	RF module dependent	Type of byte array.
1S_B	stream Tx power level for 196		E.g. Channel 36
	(channel 1~196) channels (392 hex		should use the 36'th
	digits) for path B		byte.
pwrdiff5GHT40_2	40MHz mode HT OFDM 2 spatial	RF module dependent	Type of byte array.
S	stream Tx power difference between		E.g. Channel 36
	HT40_1S for 196 (channel 1~196)		should use the 36'th
	channels (392 hex digits). Bit[3:0]		byte.
	for path A. Bit[7:4] for path B.		
pwrdiff5GHT20	20MHz mode HT OFDM Tx power	RF module dependent	Type of byte array.
	difference between HT40_1S for		E.g. Channel 36
	196 (channel 1~196) channels (392		should use the 36'th
	hex digits). Bit[3:0] for path A.		byte.
	Bit[7:4] for path B.		
pwrdiff5GOFDM	Legacy OFDM Tx power difference	RF module dependent	Type of byte array.
	between HT40_1S for 196 (channel		E.g. Channel 36
	1~196) channels (392 hex digits).		should use the 36'th
	Bit[3:0] for path A. Bit[7:4] for path		byte.
	B.		

Note 1: if the value is the type of byte array, the format of value will be a string of ASCII of 0~f, which using 2 ASCII standing for one byte. For example, when set Tx power of pwrlevel5GHT40_1S_A, it will be

"iwpriv wlan0 set mib

Configuration by "iwpriv" Examples:

- I. Setting as 5G Single MAC/PHY selective mode
 - 1. disable all wlan interfaces
 - > ifconfig wlan0 down
 - 2. setting related mibs
 - a. setting single MAC/PHY
 - > iwpriv wlan0 set_mib macPhyMode=0
 - b. setting 5GHz band
 - > iwpriv wlan0 set_mib phyBandSelect=2
 - c. setting band as A+N mode
 - > iwpriv wlan0 set_mib band=12
 - d. setting channel, e.g channel 44
 - > iwpriv wlan0 set_mib channel=44
 - e. setting other mib if necessary, such as 40M bandwidth, encryption, etc.
 - 3. enable wlan interface
 - > ifconfig wlan0 up

II. Setting as 2G Single MAC/PHY selective mode

- 1. disable all wlan interfaces
 - > ifconfig wlan0 down

- 2. setting related mibs
 - a. setting single MAC/PHY
 - > iwpriv wlan0 set_mib macPhyMode=0
 - b. setting 2.4GHz band
 - > iwpriv wlan0 set_mib phyBandSelect=1
 - c. setting band as B+G+N mode
 - > iwpriv wlan0 set mib band=11
 - d. setting channel, e.g channel 6
 - > iwpriv wlan0 set_mib channel=6
 - e. setting other mib if necessary, such as 40M bandwidth, encryption, etc.
- 3. enable wlan interface
 - > ifconfig wlan0 up

III. Setting as the Dual MAC/PHY concurrent mode

- 1. disable all wlan interfaces
 - > ifconfig wlan0 down
 - > ifconfig wlan1 down
- 2. setting related mibs
 - a. setting dual MAC/PHY
 - > iwpriv wlan0 set_mib macPhyMode=2
 - > iwpriv wlan1 set_mib macPhyMode=2
 - b. setting wlan0 as 5GHz band, setting wlan1 as 2.4GHz band
 - > iwpriv wlan0 set_mib phyBandSelect=2
 - > iwpriv wlan1 set_mib phyBandSelect=1
 - c. setting wlan0 band as A+N mode, setting wlan1 band as B+G+N mode
 - > iwpriv wlan0 set_mib band=12
 - > iwpriv wlan1 set_mib band=11
 - d. setting channel, e.g 5G channel 44, 2G channel 6
 - > iwpriv wlan0 set_mib channel=44
 - > iwpriv wlan1 set_mib channel=6
 - e. setting other mib if necessary, such as 40M bandwidth, encryption, etc.
- 3. enable wlan interface
 - > ifconfig wlan0 up
 - >ifconfig wlan1 up

Dynamic Frequency Selection (DFS)

- I. DFS is enabled if Linux kernel configuration "DFS support" is enabled.
- II. To obey regulation, DFS channels can ONLY be selected by auto-channel selection. The user can see "Auto (DFS)" on the channel column on web UI.
- III. If the user want to force the DUT set in a DFS channel for evaluation purpose, one should set console command with "flash set WLAN0_CHANNEL <channel #>", and then reboot.

 Note: Alternatively, the user can use http://192.168.1.254/syscmd.asp to input the command.

5G Channel Plan

regulation domain	supported channels – DFS enabled	supported channels – DFS disabled
(mib regdomain value)		

FCC (1)	36,40,44,48,52,56,60,64,100,104,108,	36,40,44,48,149,153,157,161,165
	112,116, 136,140,149,153,157,161,165	
IC (2)	36,40,44,48,52,56,60,64,149,153,157,	36,40,44,48,149,153,157,161
	161	
ETSI(3)	36,40,44,48,52,56,60,64,100,104,108,	36,40,44,48
	112,116,120,124,128,132,136,140	
SPAIN (4)	36,40,44,48,52,56,60,64,100,104,108,	36,40,44,48
	112,116,120,124,128,132,136,140	
FRANCE (5)	36,40,44,48,52,56,60,64,100,104,108,	36,40,44,48
	112,116,120,124,128,132,136,140	
MKK (6)	36,40,44,48,52,56,60,64,100,104,108,	36,40,44,48
	112,116,120,124,128,132,136,140	
ISREAL (7)	36,40,44,48,52,56,60,64,100,104,108,	36,40,44,48
	112,116,120,124,128,132,136,140	
MKK1 (8)	34,38,42,46	34,38,42,46
MKK2 (9)	36,40,44,48	36,40,44,48
MKK3 (10)	36,40,44,48,52,56,60,64	36,40,44,48
NCC (11)	56,60,64,100,104,108,112,116,136,140,	56,60,64,149,153,157,161,165
	149,153,157,161,165	
RUSSIAN (12)	36,40,44,48	36,40,44,48
CN (13)	5149,153,157,161,165	149,153,157,161,165
GLOBAL(14)	36,40,44,48,52,56,60,64,100,104,108,11	36,40,44,48,149,153,157,161,165
	2,116,136,140,149,153,157,161,165	
WORLD-WIDE(15)	36,40,44,48,52,56,60,64,100,104,108,11	36,40,44,48,149,153,157,161,165
	2,116,136,140,149,153,157,161,165	

iwcontrol Daemon Configuration

Need start daemon when:

- 802.1x daemon is used
- IAPP daemon is used
- WPS daemon is used

Note: iwcontrol daemon should be started after 802.1x, IAPP, or WPS daemon is running

Start daemon:

"iwcontrol wlan_interface"

wlan_interface: wlan interface, e.g., wlan0

Note:

- 1. iwcontrol daemon will parse the pid files in "/var/run" and create FIFO files to do IPC with WPS, IAPP, and 1x daemon.
- 2. Multiple wireless interfaces can be supported in iwcontrol parameters.

802.1x Daemon Configuration

Need start daemon when:

- WPA/WPA2 is used
- WEP + 802.1x (authentication with radius server)
- No encryption + 802.1x (authentication with radius server)

Start 802.1x daemon:

"auth wlan_interface lan_interface auth wpa_conf &"

- wlan_interface: wlan interface, e.g., wlan0
- > lan_interface: lan interface, which connects to Radius server, e.g., br0

- > auth: denote to act as authenticator
- > wpa_conf: path of wpa config file, e.g., /var/wpa-wlan0.conf *Note:*
- 1. Multiple 802.1x daemons will be created for different wireless interfaces.
- 2. PID file "/var/run/auth-wlanx.pid" will be created for each 1x daemon

Parameter format in wpa config file:

"keyword = value"

table of wpa parameters

keyword	value	Comment
encryption	0 – disable, 1 – WEP, 2 – WPA, 4 – WPA2 only, 6 –	
	WPA2 mixed	
ssid	"string_value", 1-32 char	
enable1x	0/1 – disable/enable 1x Radius authentication	Refer when encryption is set to 0, 1
enableMacAuth	0/1 – disable/enable MAC authentication	
SupportNonWpaClient	0/1 – disable/enable none WPA client support when	This feature is not supported now
	WPA is set	
wepKey	1 – WEP64, 2 – WEP128	Refer when encryption is set 1 (wep)
wepGroupKey	set "" as default	No use
authentication	1 – Radius, 2 – PSK (pre-shared key)	
unicastCipher	1 – TKIP, 2 – AES	
wpa2UnicastCipher	1 – TKIP, 2 – AES	
usePassphrase	0 – use psk value as key in raw data, 1 – use passphrase	
_	algorithm to convert psk value	
psk	"string_value", if usePassphrase=0 (raw data), it should	
	be 64 hex digits. If usePassphrase=1, the string length	
	should be $>=8$ and $<=64$.	
groupRekeyTime	Group key re-key time	No use
rsPort	UDP Port number of radius server	Normally 1812 is used
rsIP	IP address of radius server (e.g., 192.168.1.1)	
rsPassword	"string_value", password of radius server with 31 char	
	in max	
rs2Port	UDP Port number of radius server set 2	Normally 1812 is used
rs2IP	IP address of radius server (e.g., 192.168.1.1) set 2	
rs2Password	"string_value", password of radius server with 31 char	
	in max set 2	
rsMaxReq	Max retry number of request packet with radius server	Set 3 as default
rsAWhile	Timeout time (in second) of waiting rsp packet of radius	Set 5 as default
	server	
accountRsEnabled	0/1 – disable/enable accounting radius server	
accountRsPort	UDP Port number of accounting radius server	
accountRsIP	IP address of accounting radius server	
accountRsPassword	"string value", password of accounting radius server	
	with 31 char in max	
accountRsUpdateEnabled	0/1 – disable/enable the feature of statistic update with	
_	accounting server	
accountRsUpdateTime	Update time in seconds	
accountMaxReq	Max retry number of request packet with accounting	
•	radius server	
accountAWhile	Timeout time (in second)of waiting rsp packet of	
	accounting radius server	

IAPP Configuration

Start IAPP daemon:

"iapp lan_interface wlan_interface ...&"

- lan_interface: interface name which IAPP daemon use to send IAPP packet (e.g., br0)
- wlan_interface: wlan interface, e.g., wlan0

Notes:

- 1. IAPP can support multiple wireless interfaces.
- 2. PID file "/var/run/iapp.pid" will be created for iapp deamon.

WPS Configuration

The driver has already supported WPS function, but it needs to cooperate with WPS daemon in user level. Please refer to "Realtek_WPS_user_guide.doc" for detail explanation and usages.

WAPI Configuration

The driver has already supported WAPI function. Please refer to "WAPI Porting Guide.doc" for detail explanation and usages.

Configuration File support

The driver can be configured via a *configuration file* each time an interface is up.

Kernel configuration:

Select "Network device support" ---> Wireless LAN (non-hamradio) ---> Config File support"; then rebuild kernel image.

Configuration file:

- > Path: /etc/Wireless/RTL8192CD.dat
- Sytax: 'wlan_interface'_'mib_command', e.g. wlan0_ssid=xxxx.

Notes:

- 1. Add '#' in front of comment lines.
- 2. Space is NOT allowed between 'wlan_interface' and 'mib_command'.
- 3. If the user needs to configure MIB values with special characters, e.g. '#', the value of 'mib_command' MUST be quoted E.g. wlan0_ssid="#XXXXX@##\$\$%%"
- 4. 'wlan_interface': wlan interface, e.g., wlan0, wlan0-va0. However, please **DO NOT** configure **WDS** interfaces because WDS is configured in wlan0 interface.
- 5. 'mib_command': MIB commands, e.g., ssid=xxxx, please refer to table "MIB command table" and following "Extended MIB command table"
- 6. MIB value should be also configured for each virtual interface separately.
- 7. Each time an interface is up, the configuration file will be loaded.

Extended MIB command table (available only if Config File support is turned on):

Name	Meaning	Value	Default	Comment
hwaddr	MAC address of WLAN interface	12 hex digits, e.g. 00e04c8192a1	0	
meshSilence	In AP+Mesh mode but not enable	0 – mesh enabled, 1 – mesh	0	Available if mesh is
	mesh function	disabled		built with kernel
				image

iwconfig/iwlist support

The driver has already supported iwconfig and iwlist (Wireless Tools v29) for getting or setting some configurations.

Kernel configuration:

Select "Network device support ---> Wireless LAN (non-hamradio) ---> Wireless Extensions v18 support" and "Network device support ---> Wireless LAN (non-hamradio) ---> Wireless Tools v29 support"; then rebuild kernel image.

iwconfig – configure a wireless network interface.

Notes: Because 'iwconfig' cannot fully cover all the configurations of the AP, we suggest the users using 'iwpriv' to setup the AP.

Synopsis of **iwconfig**:

- iwconfig [interface]
- iwconfig *interface* [essid X] [mode M] [freq F] [channel C] [ap A] [rate R] [rts RT] [frag FT] [enc E] [key K] [retry R]
- ➤ iwconfig --help
- iwconfig --version

Parameters of **iwconfig**

Name	Meaning	Value	Access	Comment
essid	ESSID	any string, e.g. iwconfig essid "My SSID"	GET/SET	
mode	operating mode of the device	Ad-Hoc, Managed (client mode), Master (AP mode), Repeater, Monitor	GET	
freq	operating frequency	frequency in GHz	GET/SET	
channel	operating channel value	channel value	GET/SET	
ap	MAC address	e.g. 00:e0:4c:01:23:45	GET	
rate/bit[rate]	maximum available bit rate	bit rate in Mb/s	GET	
rts[_threshold]	RTS threshold	packet size or off	GET/SET	
frag[mentation_thr eshold]	fragmentation threshold	packet size; off: based on driver setting	GET/SET	
key/enc[ryption]	WEP key settings	mode: open/restricted; keys in 10 or 32 hex-digit	GET	
retry	retry limits	number of retrys	GET	

Notes: for more detailed information, please refer to the manual of iwconfig.

iwlist – Get more detailed wireless information from a wireless interface

Notes: Because '*iwlist*' cannot fully cover all the configurations of the AP, we suggest the users using *ioctl* to access settings of the AP.

Synopsis of **iwlist**:

- ➤ iwlist [interface] [keyword]
- ➤ iwlist --help
- > iwlist --version

keywords of iwlist

Name	Meaning	Value	Comment
scanning	site survey of neighboring WLAN	list of Access Points and Ad-Hoc	
	devices	cells in range.	
channel/frequency	supported channel and frequency	frequencies in GHz corresponding	varied as domain
		to the channels	region changed
bitrate/rate	supported rate and extended	supported bit-rates in Mb/s	HT rates are not
	supported rate announced in beacon		listed by iwlist
keys/encryption	WEP encryption information	key sizes, list of available keys and	
		current transmit key	
ap/accsspoints/pee	Associated peer list	list of associated peers	
rs			
auth	Authentication capabilities	WPA, WPA2, CIPHER-TKIP,	
		CIPHER-CCMP	

Notes: for more detailed information, please refer to the manual of iwlist.

Multiple AP profile support

In wireless client mode, our SDK could provide the feature to set multiple AP profiles (e.g., SSID and security setting) into driver. When booting up, wlan driver will look for AP according to these profiles. If any one AP is found, it will associate to it with the configured security.

How to enable it in SDK

Run kernel menuconfig in SDK. Enable AP profile support as follows:

Network device support --->
Wireless LAN (non-hamradio) --->
[*] RTL8192C/D 802.11b/g/n support
[*] Client Mode support
[] Repeater Mode support
[*] Multiple AP profile Support

Before enabling multiple AP profile, you must enable "Client mode support" first. Then, save the kernel config and rebuild the image.

How to config in wlan driver

There are 3 new added mib for multiple AP profile as:

Name	Meaning	Value	Default	Comment
ap_profile_enable	Enable/Disable multiple AP profile	0 – disable, 1 - enable	0	
	support			
ap_profile_num	Set profile number	Number of profile to set	0	When "ap_profile_add" is
				called, the "ap_profile_num"
				will be increased by 1
				automatically. So, suggest to
				set this number to '0' first, and
				then issue command
				"ap_profile_add" to add profile

			subsequently
ap_profile_add	Add AP profile	*Note	

Note:

If wireless security is open system, its value format is:

ssid,sec_type,auth_type

ssid - SSID of associated AP, which length is from 1~32 bytes

sec_type - Security type. 0 - open, 1 - wep64, 2- wep128, 3 - wpa, 4 - wpa2

auth_type - Authentication type. 0 - open, 1 - shared key, 2 - auto. Please note, only in wep mode, you could set auth_type in 1 or 2.

Example:

Add AP profile with SSID="open-ssid" and open authentication:

iwpriv wlan0 set_mib ap_profile_add="open-ssid",0,0

If wireless security is wep 64-bit encryption, its value format is:

$ssid, sec_type, auth_type, default_key, key 1, key 2, key 3, key 4$

default_key - Default Tx key id (0~4).

key1~key4: WEP key in 10 bytes hex string.

Example

If wireless security is wep 128-bit encryption, its value format is:

ssid,sec_type,auth_type,default_key,key1,key2,key3,key4

key1~key4: WEP key in 13 bytes hex string.

Example:

If wireless security is wpa psk, its value format is:

ssid,sec type,auth type,cipher,psk

cipher: WPA cipher. 2 - TKIP, 8 - AES.

psk - WPA pre-shared-key in ASCII string (length 8~63). When length is set to 64, it will be thought as hex value, and its binary value will be used directly without converting PSK in wifi driver.

Example:

```
Add AP profile with SSID="wpa-ssid", open authentication, TKIP with PSK in "1234567890" iwpriv wlan0 set_mib ap_profile_add="wpa-ssid",3,0,2,"1234567890"
```

If wireless security is wpa2 psk, its value format is:

ssid,sec type,auth type,cipher,psk

Example:

Add AP profile with SSID="wpa2-ssid", open authentication, AES with PSK in "1234567890" iwpriv wlan0 set_mib ap_profile_add="wpa2-ssid",4,0,8,"1234567890"

Note1: The maximum number of profile is set to 5.

Note2: In our SDK, the wlan profile mib is configured by ioctl (0x8b43) directly through Flash utility. Not by iwpriv command described above.

How to debug

You may issue the following command from console to dump the all profile information and show which profile is used now as:

cat /proc/wlan0/mib_ap_profile

Limitation

- H/W encryption CAM size is 32
- Multiple BSSID CAM size is 8
- Tx SKB buffer must have 8 bytes space in tail for TKIP MIC
- Support 31 wlan clients in current configuration for 8192CE/8188RE/8192DE and 63 wlan clients for 8188ER
- Support 8 WDS number in current configuration