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# Tufts University

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School of Engineering  
Department of Electrical and Computer Engineering

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**EE22-Electronics 2**  
Fall 2012 – Prof. Sonkusale  
TA: Robbie D'Angelo

Final Project:  
Colpitt Oscillator and FM Transmitter  
Due: Tuesday, December 18, 2012

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Lab Partners: Gerard Denoyer

Date Submitted: 12/18/12

## Introduction:

The purpose of this experiment was to implement a Colpitt Oscillator and then use that oscillator to create a modulator to transmit an RF signal. We also gained further practice with LTSpice, bread boarding, and soldered our final projects.

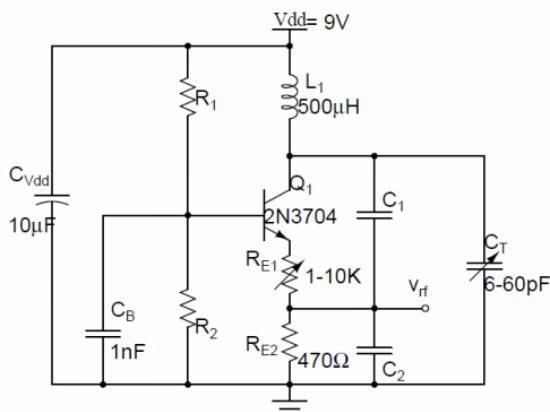
This lab also taught us how to create a new type of oscillator using an inductor, which made it a type of oscillator that we had not yet designed or used in lab. This type of oscillator uses resistances to bias a BJT transistor and an inductor and two capacitances in series to determine the frequency. The equation for the frequency is  $w_o = ((C1+C2)/(C1*C2*L))^{.5}$ , where  $w_o = 2\pi f$ . The feedback circuit that causes the circuit to oscillate is the combination of the inductor with the two series connected capacitors.

This oscillator was then used as a carrier frequency for the modulator and transmitter that was built in the second part of the lab. The modulator that we built in lab uses a transistor to add the signals coming from oscillator to the audio signal coming in. The signal from the oscillator comes into the base of the first transistor (biased in common emitter configuration) to modulate the collector current. However, the collector current was also being varied by the second transistor which is wired below the first. This second transistor used the incoming audio signal to modulate the current going through both transistors. Because the current in the first transistor is being modified by the second transistor and by the oscillating voltage at the gate, the current at the collector of the first transistor represents the sum of the two currents. The sum of these two signals is then sent to an antennae circuit so that the signal could be transmitted. The transmitted signal was then listened to using an ordinary radio tuned to the same frequency as the carrier frequency.

## Materials:

The materials used to create the oscillator were a breadboard, two 1 mH inductors (which are put together to form a 500 uH inductor), a 10 uF, 1 nF, 95 pF, 475 pF capacitors, a 9-90 pF variable capacitor, 1-10 K ohm potentiometer, 470 ohm, 39 ohm, 16 ohm resistor, 2N3704 NPN transistor, prototype board and, soldering iron, solder, oscilloscope, function generator, and power source.

The materials used to create the modulator and transmitter were a breadboard, two 1 mH inductors (which are put together to form a 500 uH inductor), 400 uF capacitor with a ferrite core, a 10 uF, 90 pF, 1.6 uF, 1 uF, 790 uF, 53 nF capacitors, 9-90 pF capacitor, two 100k ohm potentiometers, 18 k, 23 k, 29k, 15k, 1k, 2.1k ohm resistors, two 2N3704 NPN transistors, prototype board, solder, iron, oscilloscope, function generator, and power source.

Procedure:Part 1: Oscillator**Figure 1: Colpitt Oscillator Circuit**

Before the circuit in *Figure 1* could be implemented, a number of values had to be calculated. The collector current had to be 1 mA and the parallel combination of R1 and R2 had to be 12 k ohm. Details about these calculations can be found in *Figure 2 of Appendix A*. Then a small signal analysis was conducted on the circuit in order to determine the value of C1 and C2. Details about these calculations can be found in *Figure 1 and 2 of Appendix A*.

Once the calculations were complete, the circuit was simulated in LTSpice. After building the schematic, various analyses were conducted on the circuit. The DC operating point of the circuit was found. A transient simulation was run while monitoring the collector voltage to determine the operation frequency of the oscillator. Then an FFT analysis was conducted on that voltage. Another FFT analysis was then conducted on the current through the inductor. Then the frequency of operation was determined by tuning the variable capacitor.

After the spice results were found, the circuit was implemented on a breadboard. After the circuit was built, the oscillation frequency was determined by monitoring the voltage at base node. Then an FFT analysis was run on that voltage. Finally the frequency of operation was found by tuning the variable capacitor. Finally, in order to make our circuits more permanent and reliable they were soldered to a prototype board. Measurements were then performed on the soldered circuit to ensure it worked properly.

### Part 2: Modulator and transmitter

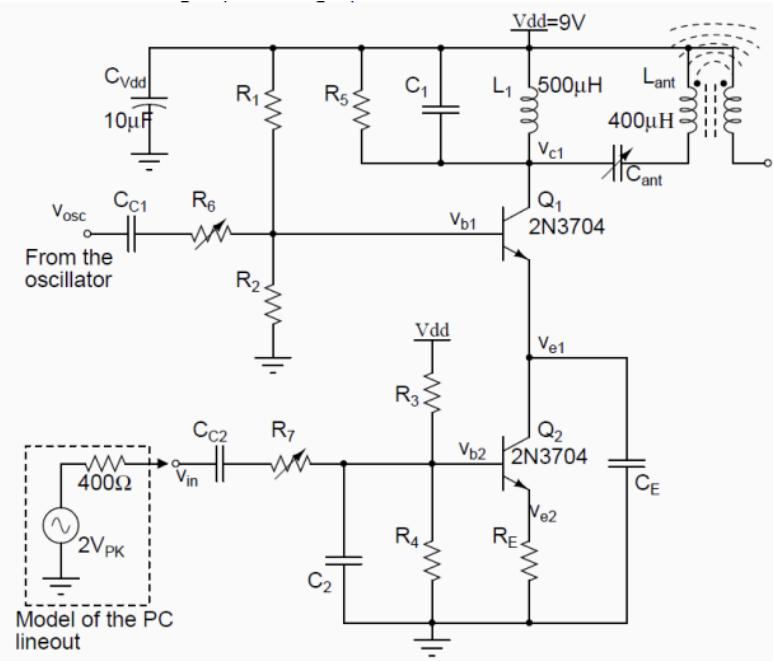


Figure 2: Circuit Diagram of Modulator and Transmitter

In the second part of the lab, more hand calculations were conducted to find the values of the necessary components from the circuit in *Figure 2*. Details about all of these calculations can be found in *Figure 3 of Appendix A*.

Once the calculations were completed, the circuit was simulated in LTSpice. A 5 kHz signal was used as the input audio tone in the simulation. The current in Q1 was monitored to ensure the current was not too large. Then an FFT analysis was done on the collector current of the first transistor. Then the voltage at the collector of Q1 was measured to ensure the small signal amplitude was small enough to avoid distorting the signal. Finally, the current in Lant was measured.

After simulating in LTSpice the circuit was implemented on the breadboard. After the circuit was built, the modulated frequency was determined by monitoring the voltage at Lant. Then an FFT analysis was run on that voltage. Finally, in order to make our circuits more permanent and reliable they were soldered to a prototype board. Measurements were then performed on the soldered circuit to ensure it worked properly.

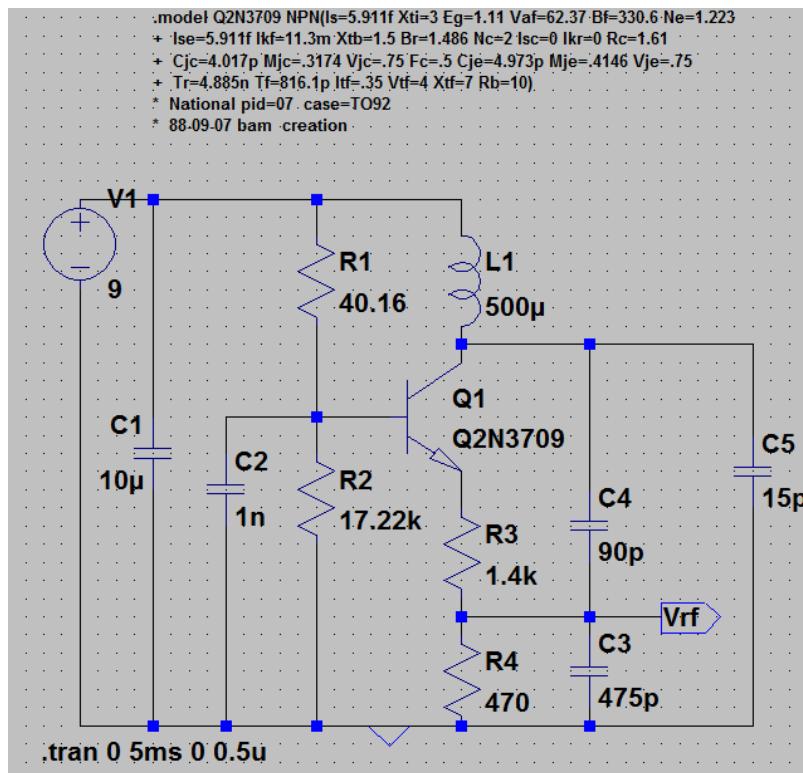
With the real world implementation of the circuit, the carrier frequency input was fed a 1V peak 750 Hz sine wave and the audio input was fed a 2 V peak sine wave that varied from a frequency of 400 Hz to about 15 KHz. That was the range that we could audibly hear a signal being transmitted over the radio. When the audio and carrier signal were connected, two signal generators were used, one for each signal. The oscillator from part 1 was not used because the final version did not yield the exact desired value. Furthermore, the transmitter circuit was optimized for a carrier frequency of 750 kHz, so a 750 KHz wave was fed from one signal generator and a changing frequency signal was fed from the other input.

**Results:**Part 1: Oscillator

The inductors were measured and the first inductor values were: L<sub>1</sub>=1.0295mH, Q<sub>1</sub>=2.8098. For the second inductor, the values were: L<sub>2</sub>=1.0315mH, Q<sub>2</sub>= 2.8075. The results from the hand calculations for the missing values can be found below in *Table 1*.

Calculated Values Part 1:	
R1	40.166 k ohm
R2	17.122 k ohm
C1	95 pF
C2	475 pF

**Table 1: Calculated values for oscillator circuit**



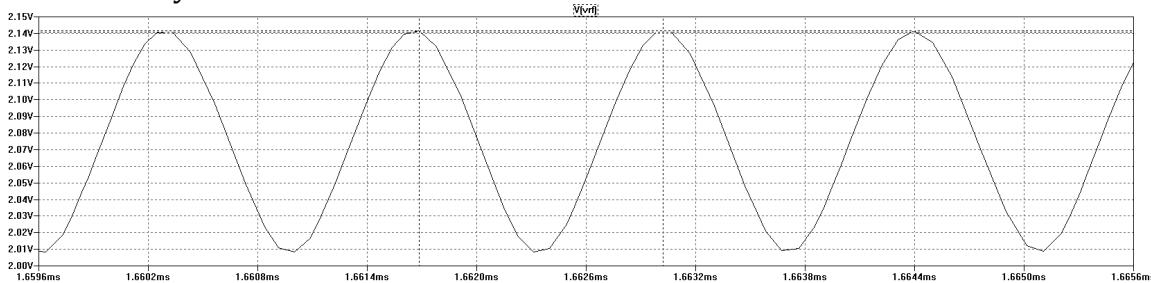
**Figure 3: LTSpice Oscillator Circuit**

The circuit in *figure 3* was simulated in LTSpice. Its operating point can be found below in *figure 4*. It shows that the transistors were biased properly so that they are both in the active region of operation.

```
--- Operating Point ---
V(n001):      9          voltage < vTQ src
V(n003):    2.65182      voltage < NPN Base
V(n002):      9          voltage < btw L & BJT
V(n004):   1.99773      voltage < NPN Emitter
V(vrf):  0.476616      voltage < middle RC Divider
Ic(Q1):  0.0010109     device_current
Ib(Q1):  3.17602e-006   device_current
Ie(Q1): -0.00101408    device_current
I(C5):   2.7e-022       device_current
I(C4):  1.26146e-018   device_current
I(C3):  3.52696e-019   device_current
I(C2):  2.65182e-021   device_current
I(C1):  -9e-017        device_current
I(L1):  0.0010109       device_current
I(R4):  0.00101408     device_current
I(R3):  0.00101408     device_current
I(R2):  0.000154896    device_current
I(R1):  0.000158072    device_current
I(V1): -0.00116897    device_current
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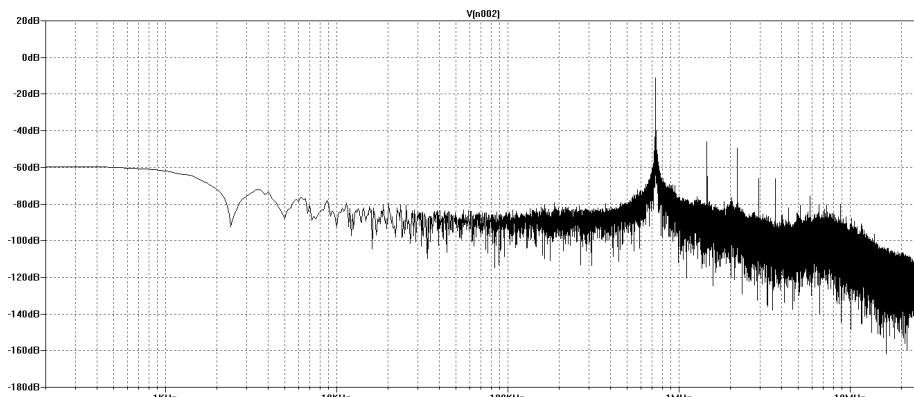
**Figure 4: Operating Point Information from LTSpice simulation of oscillator**

After finding the DC operating point of the circuit, a transient analysis was performed to determine what the output signal frequency was. A graph of the signal can be found in *figure 5*. The output frequency of the signal was found to be 748 KHz which is very close to the desired value of 800 KHz.



**Figure 5: Output signal from LTSpice signal from oscillator**

Then an FFT analysis was run on the voltage to find the magnitude of harmonic frequencies in the signal. This FFT analysis can be found in *Figure 6*, and the information regarding the magnitude of the peaks of the harmonics can be found in *Table 2*.

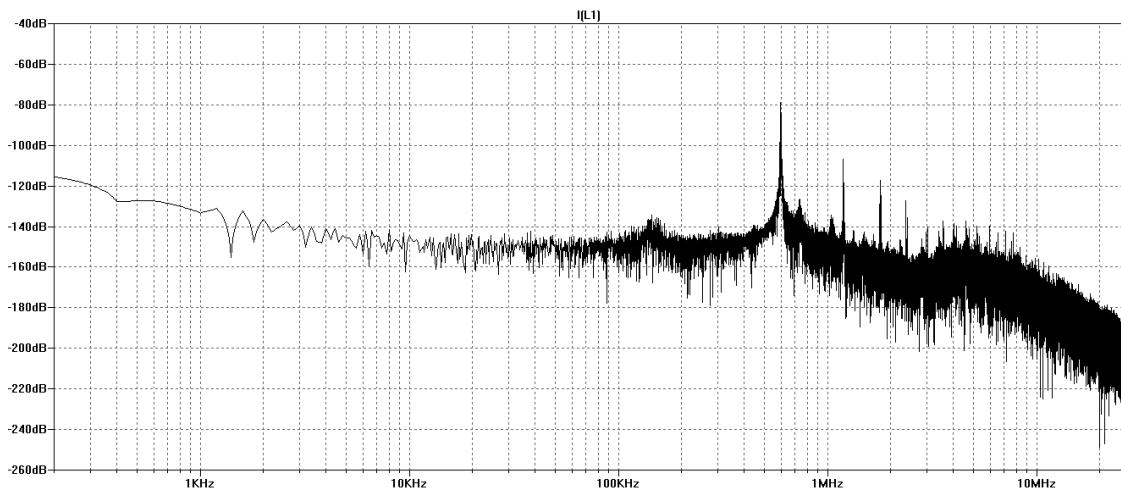


**Figure 6: FFT Analysis of output signal of oscillator in LTSpice**

Harmonics	Frequency (Hz)	Magnitude (dB)
1	731.437 K	-11.20
2	1.463 M	-46.68
3	2.194 M	-49.50
4	2.926 M	-66.22

**Table 2:** Peak harmonic frequencies from FFT and magnitudes at output voltage in LTSpice

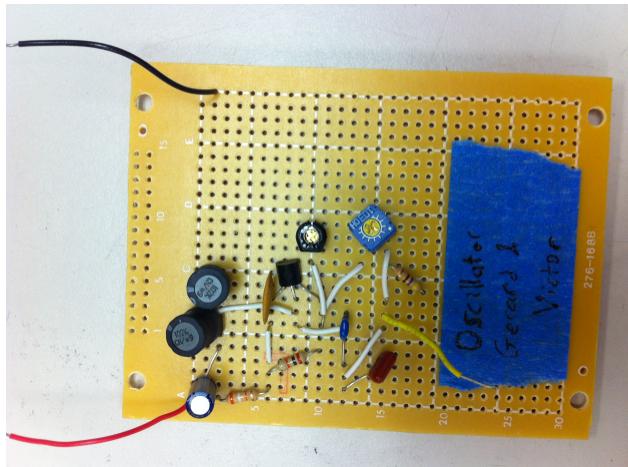
Then an FFT analysis was run on the current flowing through the inductor and the peaks of the harmonics were found. This FFT analysis can be found in *Figure 7*, and data regarding the peaks of the harmonics can be found in *table 3*.

**Figure 7:** FFT analysis of inductor current of LTSpice oscillator circuit

Harmonics	Frequency (Hz)	Magnitude (dB)
1	731.53 K	-79.200
2	1.463 M	-112.498
3	2.194 M	-118.079
4	2.926 M	-129.382

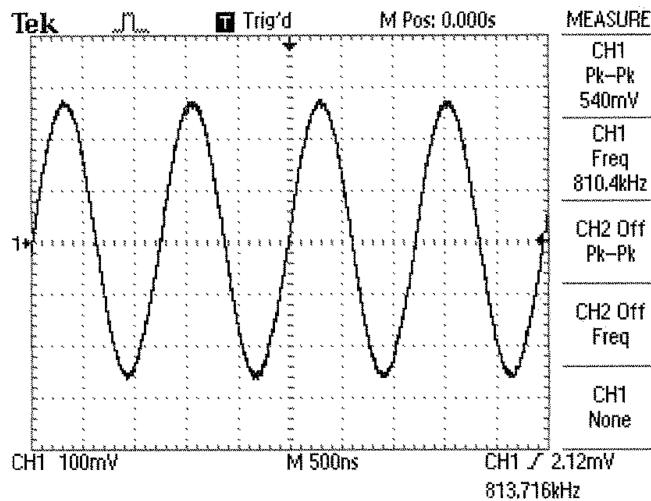
**Table 3:** Peak harmonic frequencies from FFT and magnitudes from current in inductor

Then the variable capacitor was tuned from 9 to 90 pF (the value of the actual capacitor from the lab) and the frequency range was found to be from 748 KHz at 9pF to 544 KHz at 90 pF. This shows that the bandwidth of the signal was about 200 KHz using the tuning capacitor.



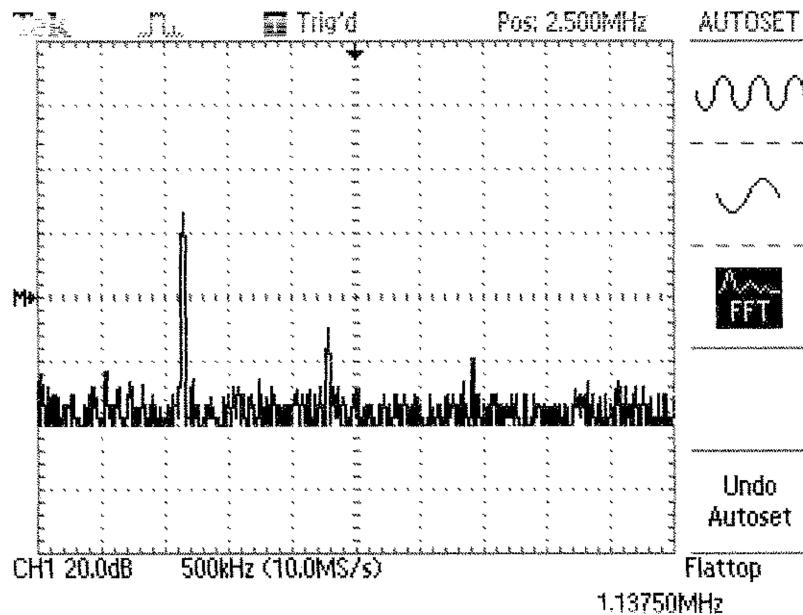
**Figure 8: Picture of oscillator on proto-board**

After performing the LTSpice analysis, the circuit was implemented on a breadboard, and then on a prototype board. An image of the circuit of the oscillator on the proto-board can be found in *Figure 8* above. The output signal was measured on the oscilloscope and a graph of this can be found in *Figure 9*.



**Figure 9: Output signal of oscillator on breadboard**

Then an FFT analysis was run on the signal to determine the frequency of the harmonics. A graph of this analysis can be found in *figure 10* and a table describing the data regarding the magnitude of the peak harmonic frequencies can be found in *table 4*. The FFT analysis shows a slightly higher frequency than on the breadboard because the capacitances were optimized for the breadboard and not the proto-board which had different cross capacitances.



**Figure 10:** FFT analysis of output signal of oscillator on the proto-board

Harmonics	Frequency (MHz)	Magnitude (dB)
1	1.15	-14.6
2	2.3	-51.4
3	3.4	-63.4
4	4.5	-69.0

**Table 4:** Peak harmonic frequencies and magnitudes from FFT of protoboard circuit of oscillator

The variable capacitor did not cause the oscillator to have a range of frequencies. Instead it would go from being in a state where there was no output at all to a state where a signal with a 810 KHz frequency was present. There was unfortunately no ability to tune the circuit frequency using the variable capacitor. Instead the frequency was gotten to 810 KHz on the breadboard because one capacitance, C1 was changed to optimize the frequency on the breadboard.

## Part 2: Modulator and transmitter

Before creating the circuit, the values of various components had to be calculated. The values of these components can be found in *table 5*. The value of CC1 was calculated to be 159 pF, but that is the minimum value for the capacitor and coupling caps are more effective at blocking out DC voltages when they are around 1 uF or greater. The values of R6 and R7 were both set to about the same level in order to get the clearest signal across the radio and it was about 25 K ohm for both.

Calculated Values Part 2:			
R1	18 k ohm	C1	90 pF
R2	22.5 k ohm	C2	796 pF
R3	29.0 k ohm	Ce	53 nF
R4	14.5 k ohm	CC1	159 pF (1uF used)
R5	.94 k ohm	CC2	1.59 uF
R6	100 k pots (set to ~25K)	Cant	113 pF
R7	100 k pots (set to ~25K)		
Re	2.1 k ohm		

Table 5: Calculated values for transmitter circuit

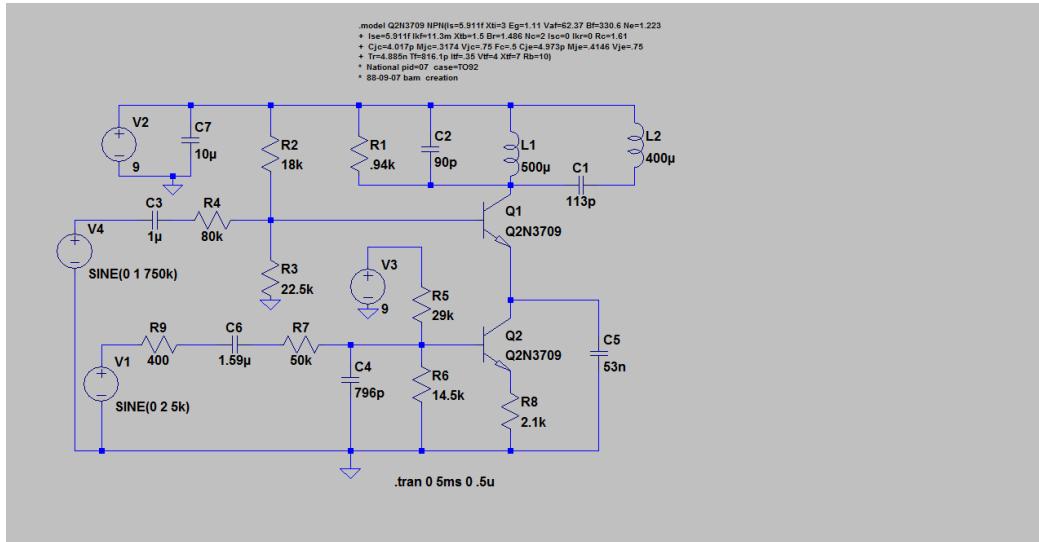


Figure 11: LTSpice circuit of modulator and transmitter

The oscillator was implemented in LTSpice using the circuit schematic above in *Figure 11*. In order to simulate the circuit, a carrier frequency of 750 kHz was used and a 2V input audio signal was also used. *Figure 12* below shows the current going through the collector of Q1. It shows that the modulating signal is at the right level.

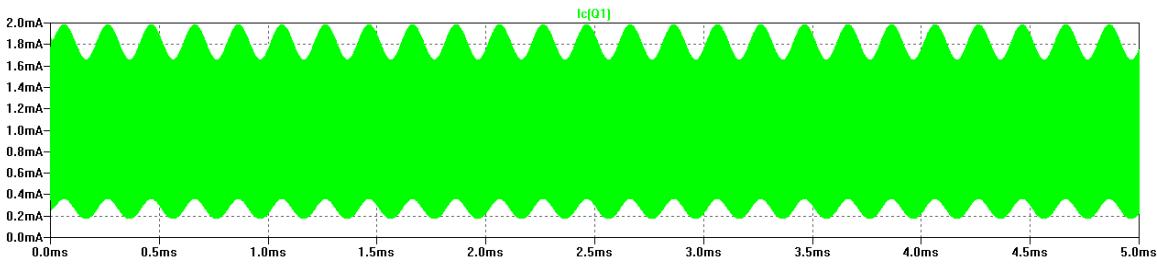
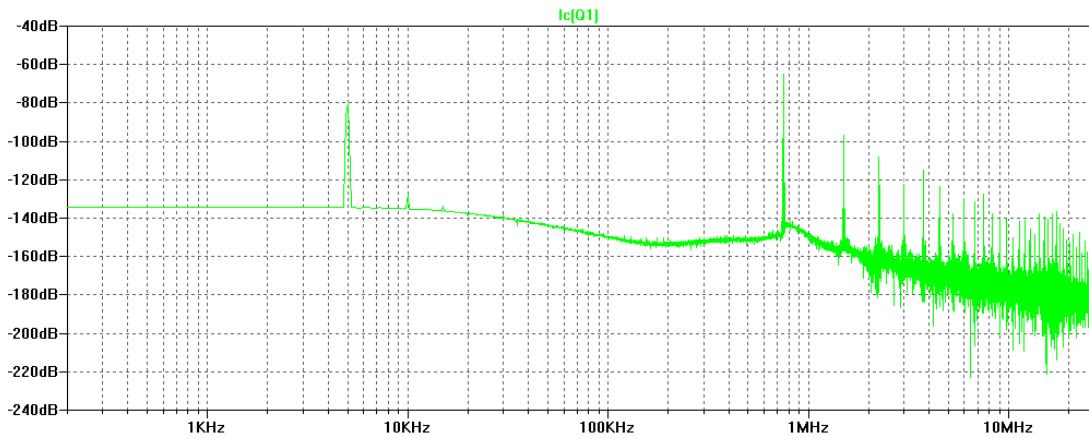


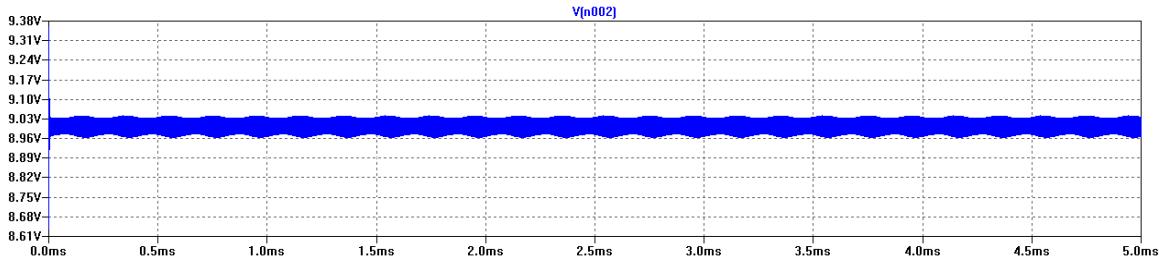
Figure 12: Graph of current at collector of Q1 in Spice simulation of modulator

Then an FFT analysis was conducted which can be found below in *figure 13*. It shows that there are two major peaks to this signal which are as expected. One at 5 KHz (from the audio input) and another at 750 KHz (the carrier frequency). The subsequent peaks are harmonics of the carrier frequency.



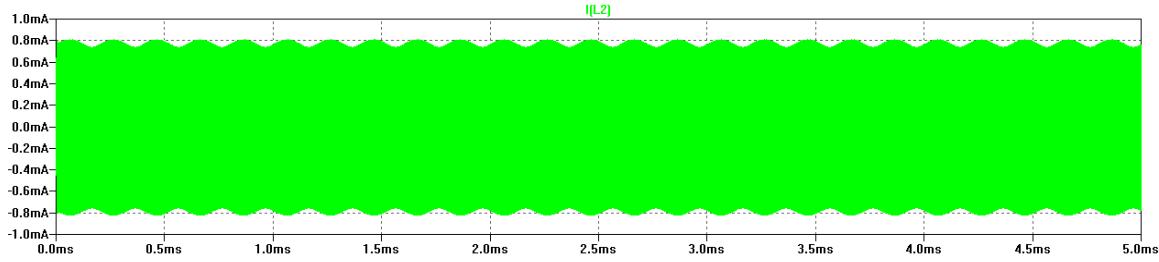
**Figure 13:** FFT of current at collector in Q1 in LTSpice simulation of modulator

Then the voltage at the collector of Q1 was measured to ensure that the small signal was small enough to avoid distorting the signal by changing the biasing of the transistors. The graph in *figure 14* shows that the small signal was not very big and would not distort the signal because it varies between 9.05 and 8.96 volts.



**Figure 14:** LTSpice graph of voltage at collector of Q1 of modulator

Then the current in Lant was measured and it can be found in *figure 15*. It shows a healthy current because it varies from -.8 mA to .8 mA.



**Figure 15:** LTSpice graph of current in Lant of transmitter

An FFT analysis was run on the current in Lant which can be found in *figure 16*. This graph shows a peak at 750 KHz as expected due to the carrier frequency. However, it also shows an inverse peak at 5 KHz. A peak was expected at 5 KHz, but it was not expected that it would be an inverse peak. However, the fact that there is a peak indicates that the signal is present.

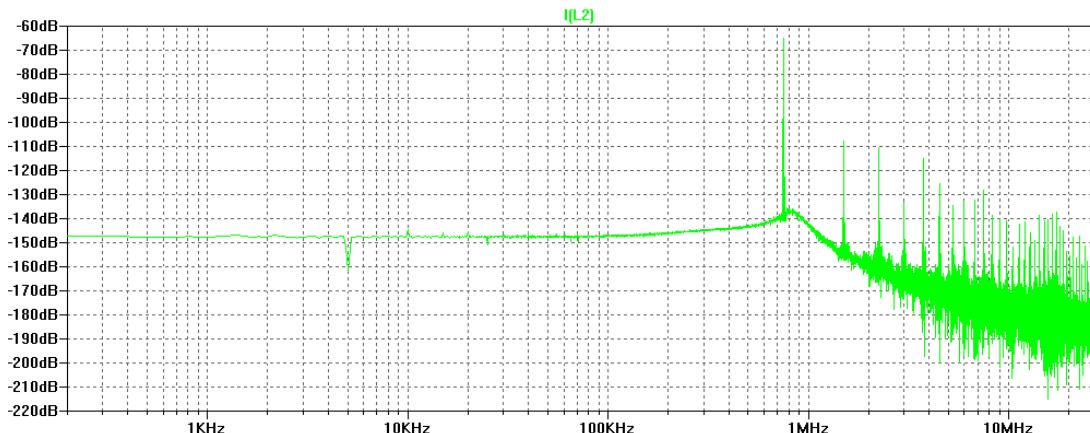


Figure 16: LTSpice FFT of current in Lant of transmitter

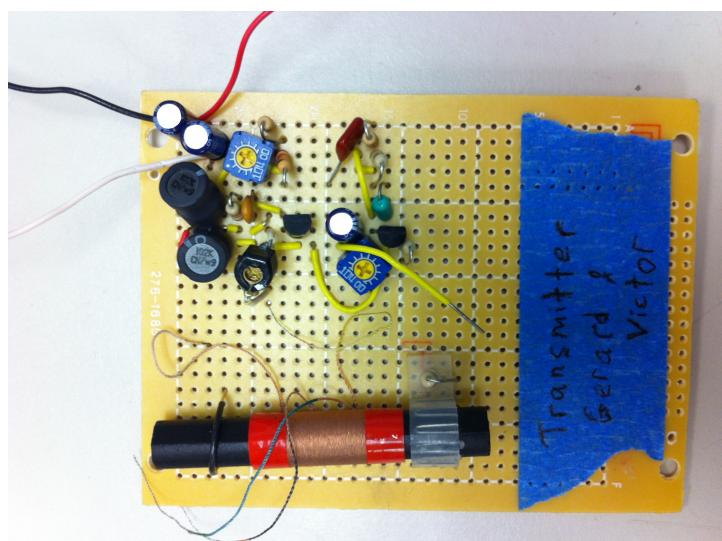


Figure 17: Picture of transmitter circuit on proto-board

After simulating the transmitter/modulator in LTSpice it was implemented on a breadboard to make sure it worked, and then it was implemented on a proto-board. A picture of the circuit on the proto-board can be found in figure 17 above. Then the voltage at the node between Cant and Lant was measured and can be found in figure 18. Figure 19 shows a graph of the same signal, but with a zoom on the frequency to better show the 750 KHz carrier frequency. The waveforms in both graphs show that the amplitude of the 750 KHz signal is being modulated by the 5 KHz input audio signal.

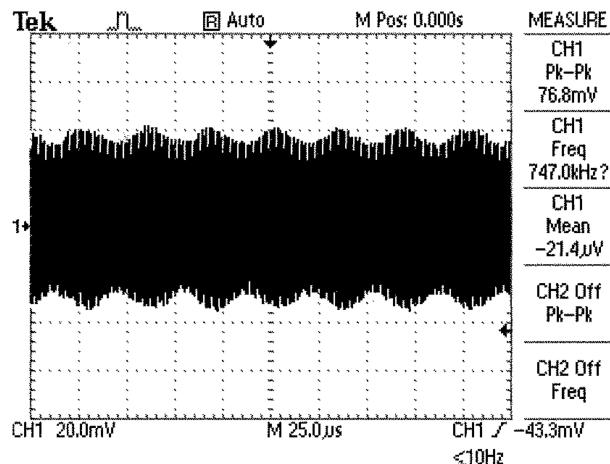


Figure 18: Graph of voltage at Cant to Lant node of transmitter on proto-board

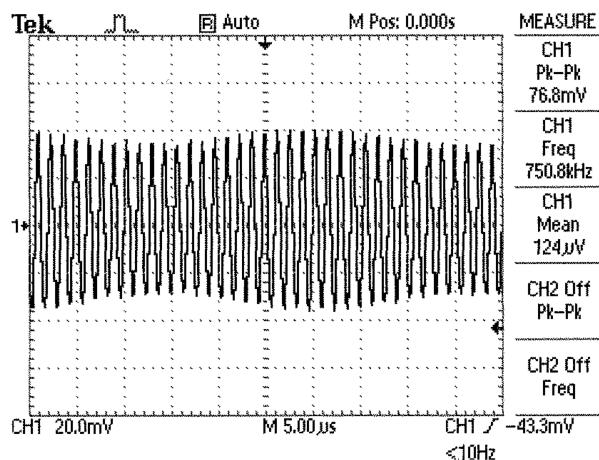


Figure 19: Zoomed in graph of voltage at Cant to Lant node of transmitter on proto-board

Then an FFT analysis was conducted on this signal and it can be found in figure 20. It shows that there is a major peak at 750 KHz and another at its harmonic of 1.5 MHz.

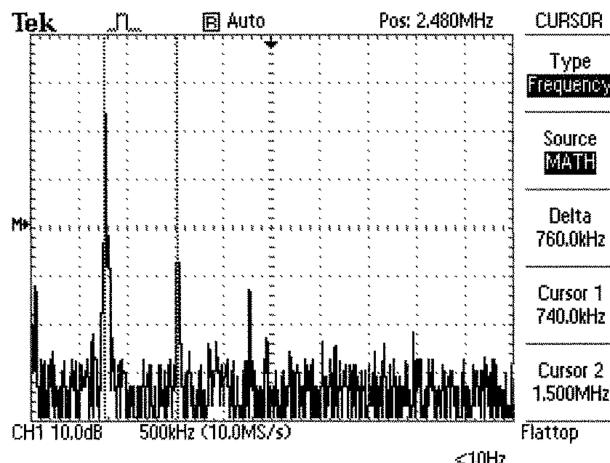


Figure 20: FFT of voltage at Cant to Lant node of transmitter on proto-board

After verifying that all measured results made sense, we turned on a radio and set it to 750 KHz to see if we could hear the signal that we were transmitting through Lant. When the radio was close enough to the transmitter circuit, the input tones were clearly audible. The audio signal was also clearer when we listened to our transmitter when it was soldered to the proto-board rather than on the breadboard. Overall, we were able to make a working radio transmitter.

## Discussion:

### Part 1:

The results from the LTSpice simulation showed that the oscillator was functioning correctly. When we ran the circuit with very similar values to what we obtained with the hand calculations, we were able to get the circuit to oscillate with a frequency of 748 kHz. This value is close to the desired value of 800 KHz, so we can assume that the results from the hand calculations were valid. When this circuit was implemented on the breadboard, it was not oscillating at the correct frequency. In order to get the oscillation close to 800 KHz, the value of C1 had to be altered. In order to get the oscillation to the desired value, a 27 pF capacitor was used for C1. Using this value for C1 allowed the circuit to oscillate at 810 kHz which is very close to the desired value of 800 kHz. When the circuit was implemented on the breadboard and on the proto-board, tuning the capacitor Ct had very little impact on the frequency. When Ct was set correctly, the circuit oscillated at a constant 800 KHz. When Ct was further turned, the oscillation frequency changed very little, and then the amplitude of the signal decreased until the output signal disappeared. Because of this, the only way to change the oscillation of the circuit was to change the value of C1 to 27 pF. One of the reasons why this had to take place is because all of the components used in the lab have a tolerance and do not behave exactly as they do in the models or the calculations. It is not exactly sure why tuning the capacitor Ct did not have an impact on frequency and caused the signal to disappear if turned too much. However, changing the value of C1 worked. And the rest of the circuit used very similar values to the LTSpice circuit and hand calculations. There is always a small discrepancy between the results obtained using LTSpice, the results obtained from calculations, and the values used in the real world because both the calculations and LTSpice uses models to approximate how real the real circuit will operate.

Another thing to note regarding the oscillator is that when it was implemented on the proto-board, the output frequency was not exactly the same as on the breadboard. On the proto-board, the output frequency was about 1.13 MHz which is a little higher than the desired value of 800 KHz. However, it is still only about 300 KHz away, so it is not that significant of a difference. This difference can be associated to the fact the when the circuit was assembled on the proto-board, it was assembled differently and on a different board, so the cross capacitances between the various components on the proto-board were different from the ones when the circuit was assembled on the breadboard. Furthermore, to get the oscillation on the breadboard at 810 kHz, various capacitances were tried for C1 until the right one worked. It would have been very difficult to change out the

capacitance on the soldered board until we achieved the correct frequency, so we left the board as is. However, we were able to create an oscillator that had a constant amplitude and frequency on a breadboard and proto-board that were not that far off from 800 kHz.

### Part 2:

The LTSpice showed that the results from the calculations were valid because the circuit had the same or very similar values to the ones obtained in calculations, and the circuit was able to modulate a signal successfully. We could clearly see on the output a 750 KHz sine wave whose amplitude was being modulated by a 5 KHz wave. This proved that the circuit was functioning as desired in LTSpice. When the circuit was implemented on the breadboard, similar values were used as in the calculation and LTSpice simulation. When the circuit was powered on with Cant tuned to the correct capacitance of 113 pF, a constant tone was heard over the radio when the circuit was tuned to 750 KHz. This showed that the transmitter was modulating the signal and transmitting properly. Furthermore, when the voltage at Lant was measured, it was found to have a 750 KHz frequency and to have its amplitude modulated by the frequency that was being fed to the audio input. This shows that the breadboard implementation of the circuit was working correctly.

One thing that we noticed when the soldered the board was that the output signal was much clearer. The tone was much louder and there was less static which seemed to indicate that the signal was being broadcast more efficiently. Furthermore, on the breadboard, when a 3 KHz signal was fed to the input, there seemed to be a beat to the tone created. When the circuit was soldered, this problem disappeared. The fact that the soldered circuit performed better was because all the connections were more reliable and much shorter connections were used to connect Lant and Cant. The soldering increased the reliability to the entire circuit and allowed it to function better. It is also now in a package that can be carried around without fear of a wire becoming disconnected. Overall, it seems that the results from the real world implementation, LTSpice, and calculations were correct because the values from the calculations were used in both and the circuit functioned correctly in both cases.

### Conclusion:

Overall, it seems that the results from the lab were valid. For the first part of the lab, the results obtained from the LTSpice were very similar to those from the calculations. When the circuit was implemented in the real world, the value of C1 had to be changed to allow for variations between the real world and the modeled world. But we were able to get an output signal with frequency of 810 KHz. On the final soldered version, this frequency changed a little, but the results are still valid. The values from the calculations led to an oscillator that had a frequency relatively close to 800 kHz. Since the oscillator functions as desired, part 1 of the experiment can be considered a success.

In the second part of the lab, the results obtained from LTSpice showed that the calculations were correct because we were able to observe an amplitude-modulated signal. When the circuit was implemented on the breadboard, we also saw the same amplitude modulated signal and were able to hear it on a radio. When the circuit was finally soldered to a proto-board, we were still able to hear it on the radio and the signal was of even better quality. Because the transmitter was able to successfully modulate the carrier frequency's amplitude and transmit the signal to a radio where it was successfully demodulated, we can consider that the transmitter functioned correctly. Overall, the entire experiment can be considered a success because both the oscillator and transmitter functioned as desired.

# APPENDIX:

## APPENDIX PART A: Hand Calculations

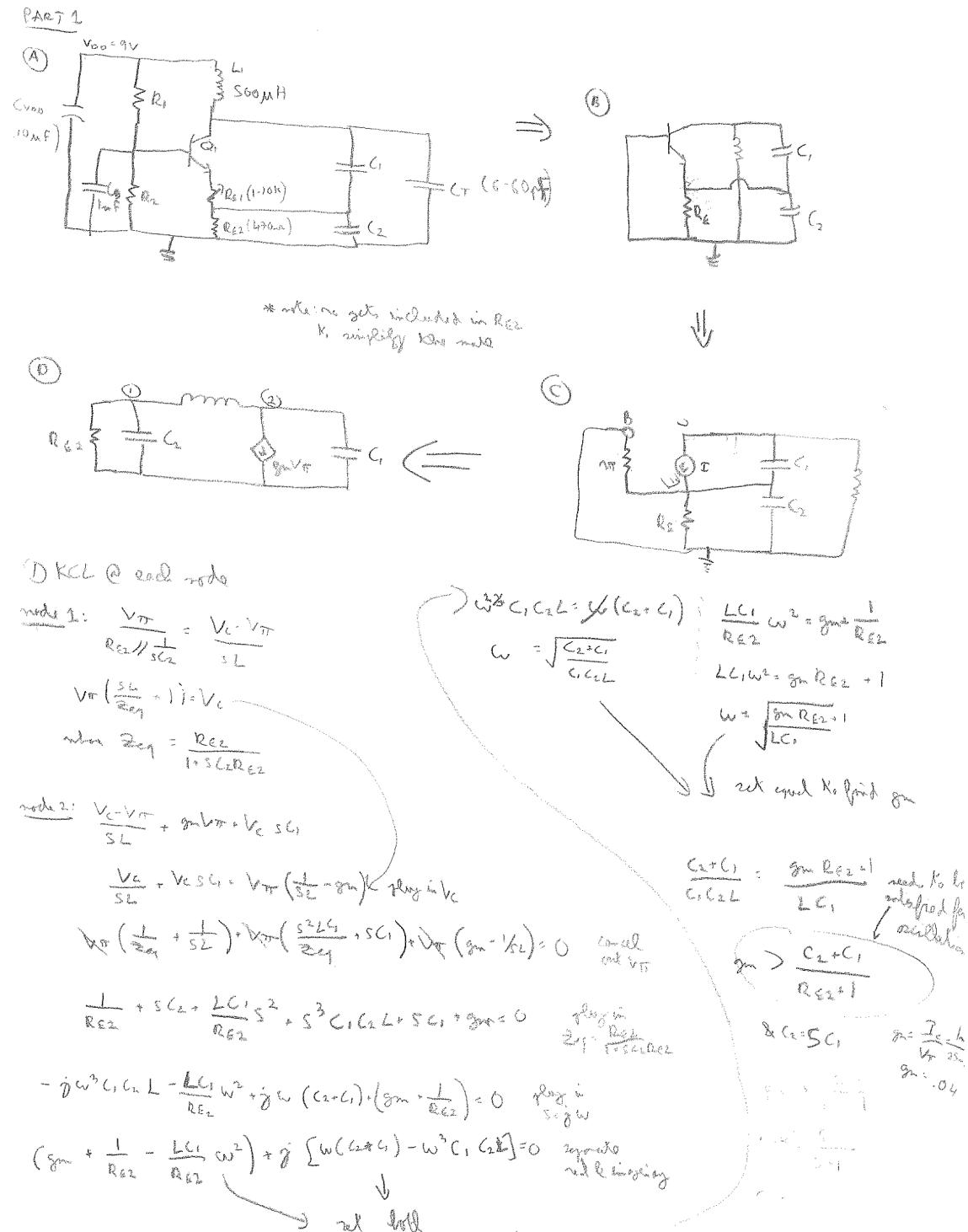


Figure 21: Hand calculations from part 1 showing derivation of small signal in section 1, and the derivation of the frequency equations and stability conditions.

PART 1

$$1) I_E = \frac{V_{BE} - V_{CE}}{R_E + \frac{R_B}{B+1}} = 1mA$$

$$R_B = R_1 // R_2 = 12K\Omega = \frac{R_1 R_2}{R_1 + R_2}$$

$$V_{BE} = V_{CE} + \frac{R_E}{R_1 + R_2} \leftarrow \text{from 1st eq}$$

$$I_E = 1mA = \frac{9V \left( \frac{12K}{R_1} \right)}{(1.5K\Omega + 470\Omega) \cdot \frac{12K}{101}}$$

$$R_1 = 40.168 \Omega$$

$$R_2 = 12.122 \Omega$$

$$2) \omega_0 = \sqrt{\frac{C_2 + C_1}{C_1 C_2 L}} \quad C_2 = 5C_1$$

$$2\pi \cdot 500K = \sqrt{\frac{5C_1 + C_1}{C_1 (5C_1)L}}$$

$$5,026,548.25^2 = \frac{6C_1}{5C_1^2 L}$$

$$5 \cdot 2.526 \times 10^{12} C_1^2 L = 6C_1$$

$$(0.263 \times 10^{-12}) C_1 (500mA) = C_1$$

$$6.32 \times 10^{-10} C_1 = 6$$

$$C_1 = 9.5 pF$$

$$C_2 = 5(9.5 pF) = 47.5 pF$$

**Figure 22: Hand calculations to find the missing values in the oscillator circuit in part 1**

PART 2

$$1) \frac{R_2}{R_1+R_2} = 5$$

$$\frac{R_1 R_2}{R_1 + R_2} = 10 \text{ k}$$

$$4R_2 = SR_1 + SR_2$$

$$4R_2 = SR_1$$

$$R_1 = 0.5 R_2$$

$$\frac{4SR_1 R_2}{R_1 + R_2} = 10 \text{ k}$$

$$4SR_2 = SR_2$$

$$4S R_2^2 = \frac{10 \text{ k}}{5} R_2 + 0.5 R_2$$

$$R_2 = 22,500 \text{ }\Omega$$

$$(R_2 \approx 22.5 \text{ k}\Omega)$$

$$R_1 = 4/5(22,500) \approx 18,000 \text{ }\Omega$$

$$(R_1 \approx 18 \text{ k}\Omega)$$

$$⑤ \omega \leq \frac{1}{RC} \quad \text{where } R = R_3 // R_4 = 10 \text{ k}$$

$$\& C = C_2$$

$$2\pi (20 \text{ k}) \leq \frac{1}{(10 \text{ k}) C_2}$$

$$(C_2) 1.256 \times 10^9 \leq 1$$

$$C_2 = 7.95 \times 10^{-10}$$

$$(C_2 = 7.95 \text{ pF})$$

$$⑥ \frac{1}{WC} = 3 \text{ k} \cdot 10 @ 500 \text{ kHz}$$

$$\frac{1}{(500 \text{ kHz}) \cdot C_2} = 6$$

$$1 = 1.88 \times 10^9 C_2$$

$$C_2 = 5.3 \times 10^{-10}$$

$$(C_2 = 5.3 \text{ pF})$$

$$⑦ (R_6 = R_7 = 10 \text{ k}) \text{ potentiometer}$$

$$⑧ \omega = \frac{1}{RC} \quad \text{where } R = R_3 // R_4 \& R_6 \text{ is ignored.}$$

$$10 \text{ k} \cdot 2.9 \cdot \frac{1}{(10 \text{ k}) C_1}$$

$$(C_1 = 159 \text{ pF}) \quad \text{check for } 1 \text{ nF}$$

$$⑨ \omega = \frac{1}{RC} \quad \text{where } R = R_3 // R_4 \& R_7 \text{ is ignored}$$

$$10 \cdot 2\pi = \frac{1}{(10 \text{ k}) C_2}$$

$$C_2 = 1.59 \times 10^{-6}$$

$$(C_2 = 1.59 \mu\text{F})$$

$$⑩ \omega_{\text{low}} @ \text{BW} = \omega_{\text{3dB HIGH}} - \omega_{\text{3dB LOW}} = \frac{R}{C}$$

$$2\pi \cdot 300 \text{ k} = \frac{R}{500 \text{ M}}$$

$$R = 942 \text{ }\Omega$$

$$(R \approx 942 \text{ k}\Omega)$$

$$⑪ \omega_0 = \frac{1}{\sqrt{LC}}$$

$$250 \text{ k} \cdot 2\pi = \frac{1}{\sqrt{C_1 \cdot 500 \text{ M}}}$$

$$\sqrt{C_1 \cdot 500 \text{ M}} = \frac{1}{2\pi \cdot 250 \text{ k}}$$

$$500 \text{ M} C_1 \approx 4.5 \times 10^{-14}$$

$$C_1 = 9.00 \times 10^{-14} \text{ F}$$

$$(C_1 = 90 \text{ pF})$$

$$⑫ \omega_0 = \frac{1}{\sqrt{LC}}$$

$$250 \text{ k} \cdot 2\pi = \frac{1}{\sqrt{C_2 \cdot 500 \text{ M}}}$$

$$200 \text{ p} \cdot 4C_2 = 4.5 \times 10^{-14}$$

$$C_2 = 1.125 \times 10^{-10}$$

$$(C_2 = 113 \text{ pF})$$

$$3) I_E = \frac{V_{BB} - V_{BE}}{R_E + \frac{R_E}{B+1}} = 1 \text{ mA} \quad V_{BB} = 9 \text{ V}, \frac{14.5 \text{ k}\Omega}{14.5 \text{ k} + 2.9 \text{ k}}$$

$$V_{BE} = 2.8 \text{ V}$$

$$I_E = \frac{2.8 \times 6}{R_E + \frac{2.9}{B+1}} = 1 \text{ mA}$$

$$\frac{2.2}{R_E + 99.0099} = 1 \text{ mA}$$

$$2.2 = .001 R_E + .099$$

$$.001 R_E = 2.1$$

$$R_E = 2.1 \text{ k}\Omega$$

$$(R_E = 2.1 \text{ k}\Omega)$$

Figure 23: Hand calculations from part 2 to find the missing component values for the transmitter circuit