Communication portocols
. Spud of both data transmission & reception must be similar, there will be proper hardstake & no los of
data
reveived must be known that the data is being forwarded to it & after completion to guarantee it's necestion an act nowledge ment is
u needed. 2 type of Communication protocol.
· Synchronous -> Similar clock.  · Synchronous -> does not have similar clock.
Syn chronous:-  transmitter Receiver
CLP [PO] G: SPI 12C
· Common clk for synchronization  After the fransmitter sending all data it sends a stop be

- After the transmires arrangem rura it serds a stop best forform all date is sent, then it serds Ack.

I not verered-the Askit sends the date again. 2. Asynchronigation He fin the time unterval of the transmission

g packets ( Eg: 28 boatranger) MoAck. 1011001 D 9 o data packets finthe Baudrate G: WART CAH. Receiver Frankroutter), ( Ilmi versal Asynchronous Deciver V.Y.

Deciver V.Y.

Deciver V.Y. Device 1 (churk of Syter 1 by 1) usuels by 1 byte (8648). . Bus (CAH mosty used). · Star (Mother, Stave). (2 devices). . AD Hoe / peer topeer topology. Just

Bit frame - sequence of site inform State & Bop. DO \_\_\_\_\_ DT panely Stop (\*) 10 bit dete frame + parity There use to be 2 Stop hits to have somedely Egel reedy for the nent date fame. \* This is used for Short distance communication only because for long distance there might selection against wither vertical . Herefor long distance we moved for (RS232) Comm protocol. Leg, 9pin fordy Hard Shake veg. TX Eq. R. communicate veg their Status. Leady to be Leady to hansmit

Leady to hansmit

Leady to hansmit

Leady to hansmit

Seady to hansmit

Dis - 34 to 254. J diff 264 the system (an seasoly communicate with 1's &0's Hence cue use level Crifter. Low to High (16) High to law (15) . Computer . · printer Bogramming of WART · Band Late · Bogramming methods · CART peripheral PROS, COHS And App. of WART 1600 } Both transmitter Excueirer must 115200 & configure the some Budrole to be handowed - Bit Barging. A- 01000001 Bandvale - 1 bit/sec. Device 1

Device 2

Device 2 Fort bit 0 Ther date 1 bel / 1 clock cycle. The Stop- 2.

VART peripherals Parallel in semal out ab lonjiguration gr U AR1 (Ace to Bound sock). · Clock configuration . Data Loading · Pata transmission. - looping method vloritoring data (Adv.). - Interrupt method. disady. Adv. Of WART . Synchro. og Band Rate . NOAK. Esy towlerface only 2 degice can be connected. Less Handware Less Eglevare complications GSM, bhatath,