

RZ/V2H Group

Handbook for RZ/V2H

Introduction

This document compiles useful information for each stage of device selection, development, and Mass production. You can also select what you need for your application from our rich selection of application notes that describe how to use a peripheral function, example applications, how to create a program, and more.

Please utilize these information, materials and application notes as a handbook when developing.

Target Device

RZ/V2H Group

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1. The table of information and materials needed for Device Selection, Development and Mass production.

1.1 Step1: Device Selection

This section summarizes the information that is useful for the preliminary survey phase (Step1-1) and for the evaluation phase for device performance and features (Step1-2) when selecting the device.

1.1.1 Step1-1: Preliminary survey phase

#	Item	Contents	Link
1	Hardware information	Datasheet	Doc
2		RZ/V2H Group Flyer	Doc
3		RZ Family Brochure	Doc
4		Video	Web site
5		Blog	Web site
6	Product & Solutions	Reference designs (Winning Combination)	
		- Smart Robot Vacuum Cleaner	Web site
		- High-Performance Vision AI System	Web site
		- Single Board Computer with Vision AI	Web site
7	Product Specification Comparison	RZ Family Product Selector	Web site
8		White Paper	Web site
9	Partner information	Preferred Partner Program (System solutions provider)	Web site
10		RZ Family Partner Ecosystem	Web site

1.1.2 Step1-2: Evaluation phase for device performance and features

#	Item	Contents	Link
User's Manual / Documentation			
1	Document	RZ/V2H Group User's manual: Hardware	Doc
2		RZ/V2H Group User's manual: Hardware (Additional Document)	Doc ^{*2}
3		Technical update (errata information)	Web site
4		Product change notice (PCN)	Web site
5		RZ Family Product Part Number Guide (the meaning of character in part number)	Doc
6		Semiconductor reliability handbook	Doc
Evaluation Board			
7	Evaluation Board (for General purpose)	RZ/V2H-EVK Vision AI MPU Evaluation Kit	Web site
Evaluation environment (set up method)			
8	Hardware (Set up EVK)	RZ/V2H-EVK Hardware Manual	Doc
9		Camera module for EVK/e-CAM22_CURZH information (*) External link	Web site
10	AI SDK	RZ/V AI Web page - <i>Getting Started</i> - <i>How to build RZ/V2H AI SDK Source Code</i>	Web site Web site Web site
11		RZ/V2H AI SDK Overview	Web site
12		RZ/V2H AI SDK Release Note	Doc
13		RZ/V2H AI SDK	File ^{*1}
14	Linux (Manual set)	RZ/V2H AI SDK Source Code	File ^{*1}
15		BSP (RTK0EF0045Z94001AZJ-v1.0.3.zip)	File
16		Linux Interface Specification GStreamer User Manual: Software	Doc ^{*1}
17	Linux (Security Package)	Security Solution Overview	Doc
18		Security Package	File ^{*2}
19	Multi-OS	RZ/V Multi-OS Package Overview	Web site
20		RZ/V Multi-OS Package Release Note	Doc
21		RZ/V Multi-OS Package Compressed file	File
22		AWO (Always On) Startup Guide	Doc
23		FSP (Flexible Software Package)	File
24		FSP Getting Started Guide	Doc
25	ISP Support Package	ISP Support Package Overview	Web site
26		Release Note	Doc ^{*2}
27		ISP Support Package (supported IMX415)	File ^{*2}
28	Various tools	DRP-AI TVM (GitHub)	Web site
29		DRP-AI Translator i8 Installer	File ^{*1}
30		DRP-AI Translator i8 Release Note	Doc
31		DRP-AI Extension Pack (Pruning tool) Manual	Doc
32		DRP-AI Extension Pack (Pruning tool)	File ^{*1}
33		DRP-AI Pruning Guideline (GitHub)	Web site
34		e ² studio Installer for Windows	File ^{*1}
35		e ² studio Installer for Linux	File ^{*1}
36		Smart Configurator for RZ	File ^{*1}
37		Smart Configurator for RZ Release Note	Doc

*1: To access contents of software packages, My Renesas account is required.

*2: NDA required for access to secure site.

1.2 Step2: Product Design, Development

This section summarizes useful information for product design and development.
(Note: To access contents of board design data, My Renesas account is required.)

#	Item	Contents	Link
1	Board Design	LPDDR4/4X Controller Setting guide - Setting parameters generation tool (Gen_tool)	Doc*
2		PCB Design guide	Doc*
3		Thermal design Guide	Doc
4		LSI Design Model (IBIS) - 1 chip IBIS model	Model*
5		Interface Design Model (Spara, IBIS) - SI simulation models for high-speed interfaces - PI simulation models	Model*
6		CPU Board Design Data	Data
7		EXP Board Design Data	Data
8		Packaging Information	Web site
9		Package Search (pkg_20143/FBGA 840)	Web site

* NDA required for access to secure site.

1.3 Step3: Mass Production

#	Item	Contents	Link
	Writing a program (Programmer)	TBD	-
	Writing a program (Tool)	TBD	-

1.4 Supportive Information

#		Link
1	FAQ (frequently asked inquiries)	Web site
2	RZ Family Renesas Wiki	Web site
3	Technical support	Web site

2. Summary of information by category

This part shows the information about application notes by the category.

(Note: To access contents of sample code, My Renesas account is required.)

2.1 Overview

#	Category	Description
1	Standard	Hardware Design / Software for start-up / Clock / Voltage / Memory/Others

2.2 RZ/V2H Application Note [Standard]

#	Title	Contents	Sample code
1	RZ/V Getting Started with Flexible Software Package	This note describes how to create an application for the RZ/V using the Renesas Flexible Software Package (FSP).	-
2	RZ/V available partner camera module list	List of camera modules that can be connected to the RZ/V series.	-
3	RZ/V2H Group Lifetime Guideline	This note describes the guidelines for the lifetime of the RZ/V2H group.	-
4	RZ/V2H Group Thermal Design Guide Application Note	This note describes the guidelines for the thermal design of the RZ/V2H group.	-
5	RZ/V2H Group Reference power consumption guide(typ.) for use case	This note describes calculation results of power consumption for several use cases.	-
6	[NDA]DDR4TOP Application Note	This note describes the DRAM access configuration procedure for the LPDDR4/4X controller (DDR), and how to use Gen_tool.	-
7	[NDA]PCB Design Guidelines	This note describes the notices of PCB design for RZ/V2H group.	-

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Apr.25.2025	-	The first edition issued

This handbook reflects information available as of April 25, 2025. For the latest information, please also refer to the product pages on our website ([RZ/V2H - Quad-core Vision AI MPU with DRP-AI3 Accelerator and High-Performance Real-time Processor | Renesas](#)).