

SRI CHARAN VASANTHA

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EDUCATION

Aditya Engineering College , Surampalem	2023 - 2027
B.Tech in Electronic and Communication Engineering	CGPA : 7.94
Gamyam Intermediate College , G.Mamidada	2021 - 2023
Intermediate in MPC	Marks : 886

TECHNICAL SKILLS

Languages : C, C++, Python, HTML, CSS, JavaScript(Basic)
HDL : Verilog, SystemVerilog, UVM, FPGA, RTL Design
Tools : VS Code, GitHub, Vercel, Cadence
Simulation Software : MATLAB, PSpice, AutoCAD
Assembly : 8051, 8086
Technologies : VLSI

INTERNSHIP EXPERIENCE

- **TechnicalHub (VLSI)** May 25 – April 26
Design and Verification Intern Onsite
 - Gained hands-on experience in digital electronics and Verilog through practical implementation.
 - Worked on designing, simulating, and optimizing digital circuits.
 - Developed Verilog-based modules for hardware development and testing
- **GenZ Educate Wing, Bengaluru, India** May 25 – July 25
Web Development Intern Remote
 - Completed a 1-month training in HTML, CSS, and JavaScript fundamentals.
 - Developed 2 web development projects as part of the internship program.
 - Applying frontend development practices to build responsive, user-friendly web interfaces.

PROJECTS

- Personal Portfolio Website – Built responsive UI and deployed using Vercel.->
- Real-Time Weather Application – Integrated API to fetch live weather data.->
- Real Estate Listing Platform – Search and filter interface for properties.->
- Digital Calculator – Built using HTML, CSS, JavaScript for basic operations.
- Traffic Light Controller Simulation – Designed using digital logic concepts.

CERTIFICATIONS

- Semiconductor 101 v1.0, Behavioral Modeling with verilog-AMS v23.03 (Cadence).
- Digital IC design fundamentals v2.0, SystemVerilog and Verilog (Cadence).
- HTML and CSS (Hackerrank).
- Python,C++,C.

ACHIEVEMENTS

- NMMS Scholarship Awardee for academic excellence.
- Selected for an educational visit to ISRO.
- Earned 5-Star Badge in C, Cpp, and Python on HackerRank.->
- Solved 300+ coding problems on CodeChef ->, 100+ on LeetCode.->