



QUANTUM COMPUTING INTRODUCTION

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NOVEMBER, 2020

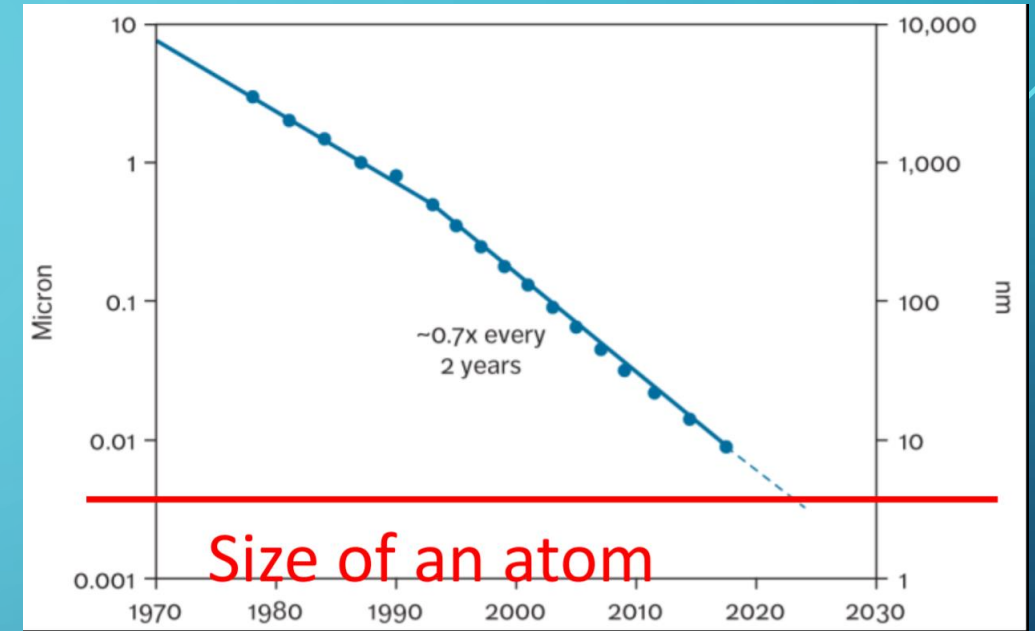
BRIEF HISTORICAL OVERVIEW

- **Quantum systems** evolve in a **state space exponentially larger than the number of parameters** require to define each state
- This **exponential complexity** hinders the simulation of large quantum system using classical computers but simultaneously **enables quantum parallelism**
- *“**Nature isn’t classical**, goddamn it! And if you want to make a simulation of Nature, you’d better make it quantum mechanical, and by golly it’s a wonderful problem, because it doesn’t look so easy.”*

[Richard Feynman, 1981]

BRIEF HISTORICAL OVERVIEW

- Moore's Law: since 1960 semiconductor size has halved every two years;
- By 2020 circuits will be dominated by quantum effects
- By 2050 circuits will reach the minimum scale at which information can be physically represented
- Is Quantum Computing a natural consequence of Moore's law?



BRIEF HISTORICAL OVERVIEW

- In **1985** Deutsch developed a model of a **quantum Turing machine**, a theoretical basis for quantum computing
- In **1994** Shor has shown that efficient ($O(\log^3(N))$) **factorization of prime numbers** is possible on quantum computers;
It hasn't been shown that classical polylogarithmic algorithms for factorization don't exist, although none is known
- In **1996** Grover proposed a **search** algorithm on **unstructured databases** with complexity $O(\sqrt{N})$, quadratically better than classical searches ($O(N)$)

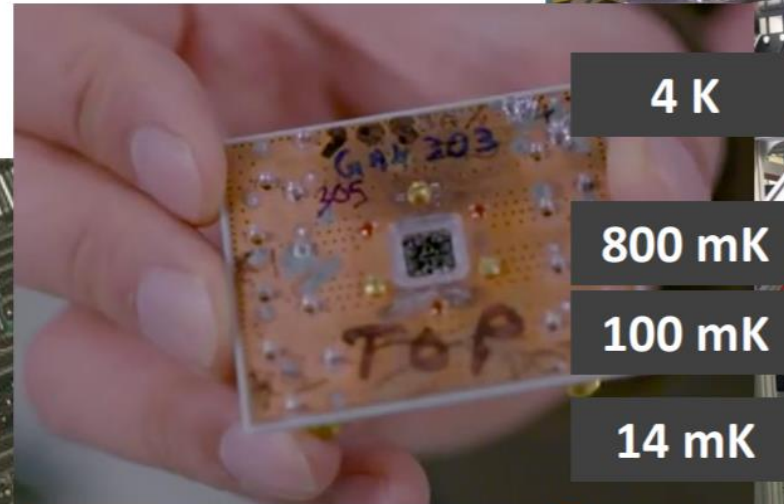
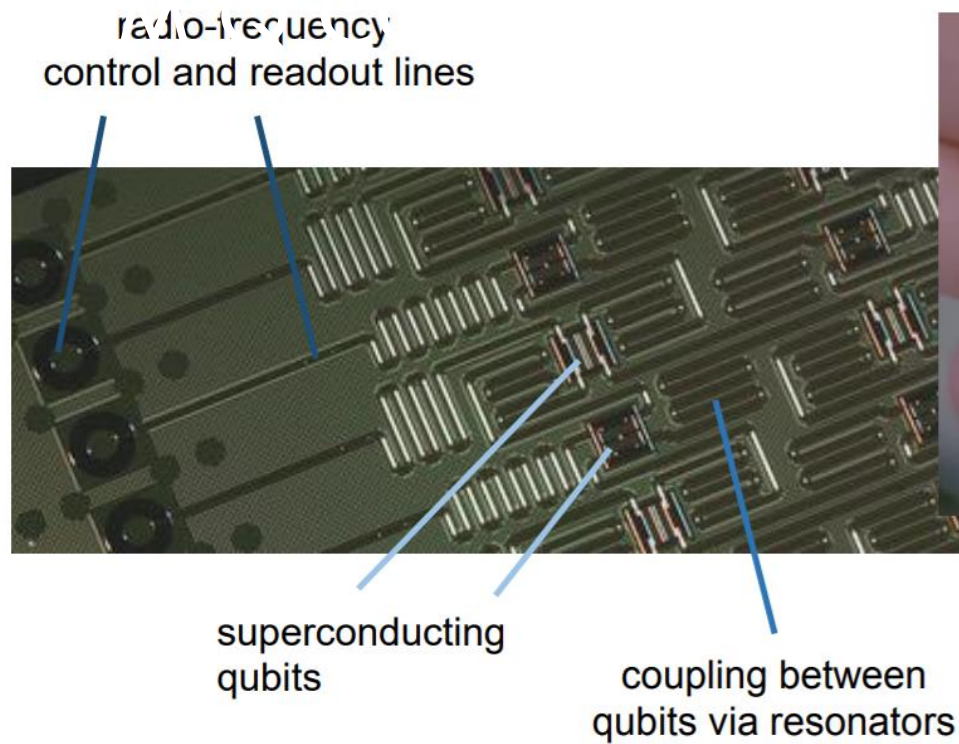
BRIEF HISTORICAL OVERVIEW

- NISQ (Noisy Intermediate Scale Quantum) era:

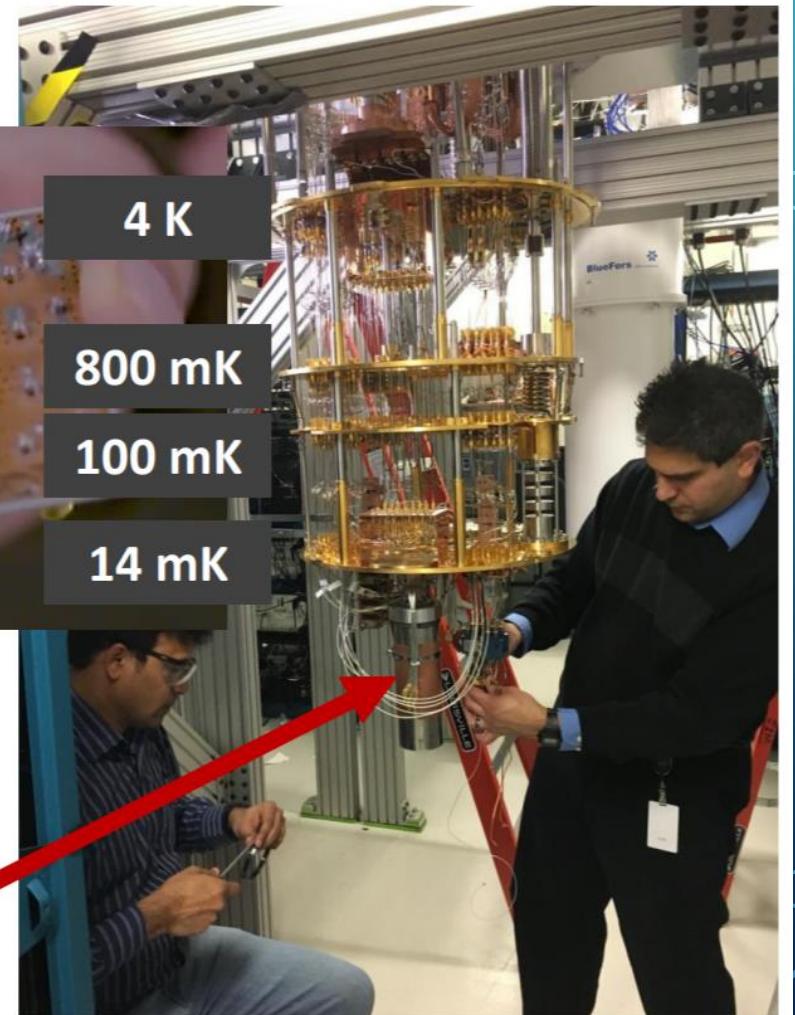
- Noisy qubits
- Noisy q-gates
- 20 .. 50 qubits (100 seem feasible)¹
- Limited connectivity among qubits
- Limited coherence time (~ 100 usec)

¹ Adiabatic quantum computers can reach 2000 qubits (D-Wave 2000Q System), but operate based on the simulated annealing algorithm and the adiabatic theorem, requiring the modelling of optimization problems as physical Hamiltonians





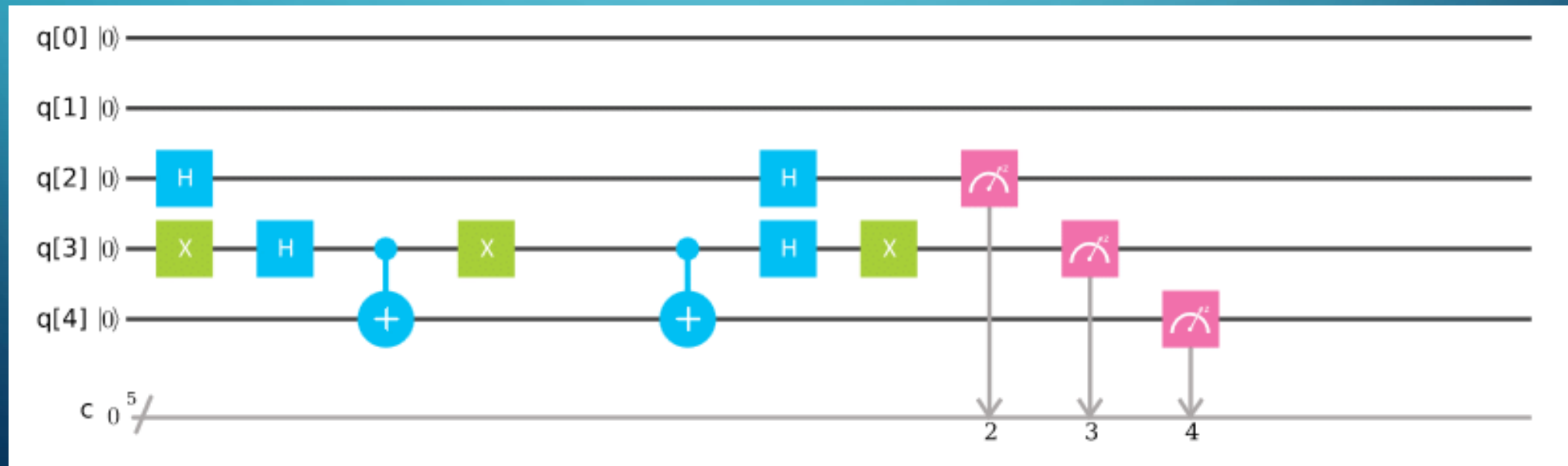
cryostat temperature
0.014 K



"Demonstration of a quantum error detection code using a square lattice of four superconducting qubits", A.D. Córcoles et al., **Nat. Comm.**, 6:6979 (2015)

QUANTUM CIRCUIT MODEL

- Quantum computers can represent an **exponentially large number of states** due to **quantum parallelism**
- The **quantum circuit model** generalizes the **binary logic gates model** used in classical computers: **quantum gates operate on quantum states**



QUANTUM COMPUTING PROPERTIES

#1 Qubit

#2 Measurement

#3 Unitary Transformations

#4 Quantum Parallelism

#5 No-Cloning Theorem

#6 Initial State

#1 - QUBIT

- A classical bit's value is uniquely and deterministically either 0 or 1

$$b \in \{0,1\}$$

- A **quantum state** is a linear combination (**superposition**) of the **basis states**:

$$|q\rangle = \alpha_0|0\rangle + \alpha_1|1\rangle; \alpha_0, \alpha_1 \in \mathbb{C}, \sum_{i=0}^1 |\alpha_i|^2 = 1$$

- A qubit can be in both basis states simultaneously, and **any quantum operation** on the qubit **operates over both states**
- A qubit can behave like a classical bit by setting one of the weights α_i to 1 and the quantum machine can behave as a classical computer

#1 - QUBIT

- A superposition of n qubits is a linear combination of 2^n states:

$$|q^{\otimes n}\rangle \equiv |\Psi\rangle = \sum_{i=0}^{2^n-1} \alpha_i |i\rangle, \quad \sum_{i=0}^{2^n-1} |\alpha_i|^2 = 1$$

- **any quantum operation** on the n qubits superposition **operates over all 2^n states**

#1 - QUBIT

- Example: 2-qubits superposition

$$|q^{\otimes 2}\rangle = \alpha_0|00\rangle + \alpha_1|01\rangle + \alpha_2|10\rangle + \alpha_3|11\rangle, \quad \sum_{i=0}^3 |\alpha_i|^2 = 1$$

- Only n qubits are required to represent $N=2^n$ states

A classical machine requires $N*n$ bits to represent N states

Example: 3 qubits can simultaneously represent 8 states
 24 = 8*3 bits are required to represent the 8 states

#2 - MEASUREMENT

- Measurement of a quantum register **yields a classic state**

$$\text{measurement}(|\Psi\rangle = \sum_{i=0}^{2^n-1} \alpha_i |i\rangle) = |i\rangle, \text{ with probability } |\alpha_i|^2$$

- The **quantum superposition collapses into the measured state**, losing all information on the α_i 's

any further reading will return the same state $|i\rangle$

- No intermediate result can be accessed (debugging has to be rethought)
- The α_i 's cannot be accessed directly, i.e., they cannot be measured

#3 – UNITARY TRANSFORMATIONS

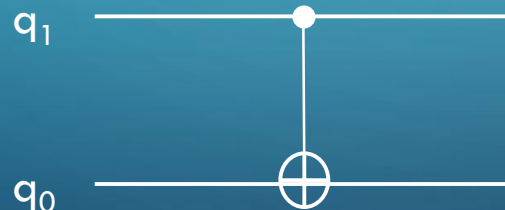
- Physical laws require all **quantum transitions** to be **unitary**:

$$|\Psi'\rangle = U|\Psi\rangle \Rightarrow U^{-1} = U^\dagger, U^\dagger U = I$$

- This also implies means that the **transformation is reversible**:
given the outputs the inputs can be known!

Example: CNOT gate (invert qubit q_0 if control qubit q_1 is 1):

q_1	q_0	q_1	q_0
0	0	0	0
0	1	0	1
1	0	1	1
1	1	1	0



$$|\Psi\rangle = \alpha_0|00\rangle + \alpha_1|01\rangle + \alpha_2|10\rangle + \alpha_3|11\rangle$$

$$\begin{bmatrix} \alpha_0 \\ \alpha_1 \\ \alpha_3 \\ \alpha_2 \end{bmatrix} = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix} \begin{bmatrix} \alpha_0 \\ \alpha_1 \\ \alpha_2 \\ \alpha_3 \end{bmatrix}$$

$$|\Psi'\rangle = \alpha_0|00\rangle + \alpha_1|01\rangle + \alpha_3|10\rangle + \alpha_2|11\rangle$$

#3 – UNITARY TRANSFORMATIONS

- Under unitary transformations the **Euclidean norm** of the coefficients is **preserved** to be unity – probabilistic model

$$|\Psi\rangle = \sum_{i=0}^{2^n-1} \alpha_i |i\rangle, \sum_{i=0}^{2^n-1} |\alpha_i|^2 = 1 \Rightarrow |\Psi'\rangle = U|\Psi\rangle = \sum_{i=0}^{2^n-1} \alpha_i' |i\rangle, \sum_{i=0}^{2^n-1} |\alpha_i'|^2 = 1$$

- While classical circuits are seen as operating over the state, quantum circuits are thought as operating over the coefficients



quantum

$$|\Psi\rangle = \alpha_0|0\rangle + \alpha_1|1\rangle$$

Diagram illustrating a quantum NOT gate. The input state is ψ , and the output state is ψ' .

$$|\Psi'\rangle = \alpha_1|0\rangle + \alpha_0|1\rangle$$

#3 – UNITARY TRANSFORMATIONS

- Unitary transformations have a **number of outputs equal to the number of inputs**
- **Classical** boolean **gates are not reversible**
- Quantum gates:

- NOT (X gate): $\begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix}$

- Rotation (phase shift): $\begin{bmatrix} 1 & 0 \\ 0 & e^{i\theta} \end{bmatrix}$

- CNOT: $\begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix}$

Toffoli (CCNOT):

$$\begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix}$$

#3 – UNITARY TRANSFORMATIONS (HADAMARD)

- The Hadamard gate is often used to prepare uniform superpositions

- Hadamard: $\frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 1 \\ 1 & -1 \end{bmatrix}$



$$|q_0\rangle = \frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 1 \\ 1 & -1 \end{bmatrix} \begin{bmatrix} 1 \\ 0 \end{bmatrix} = \begin{bmatrix} 1/\sqrt{2} \\ 1/\sqrt{2} \end{bmatrix} = \frac{1}{\sqrt{2}} [|0\rangle + |1\rangle]$$

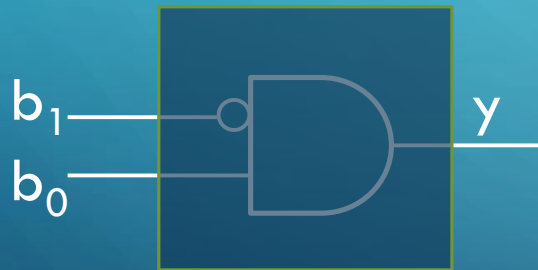


$$|q_1 q_0\rangle = \left(\frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 1 \\ 1 & -1 \end{bmatrix} \right) \otimes \left(\frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 1 \\ 1 & -1 \end{bmatrix} \right) \begin{bmatrix} 1 \\ 0 \\ 0 \\ 0 \end{bmatrix} = \begin{bmatrix} 1/2 \\ 1/2 \\ 1/2 \\ 1/2 \end{bmatrix} = \frac{1}{2} [|00\rangle + |01\rangle + |10\rangle + |11\rangle]$$

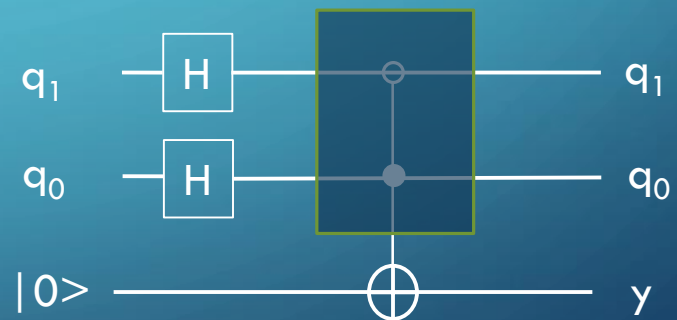
#4 - QUANTUM PARALLELISM

- An n -qubits register represents $N=2^n$ states simultaneously
- A quantum algorithm operates over the N states simultaneously
- Quantum parallelism is exponential on the number of qubits

Example: what is the key encoded in the circuit?

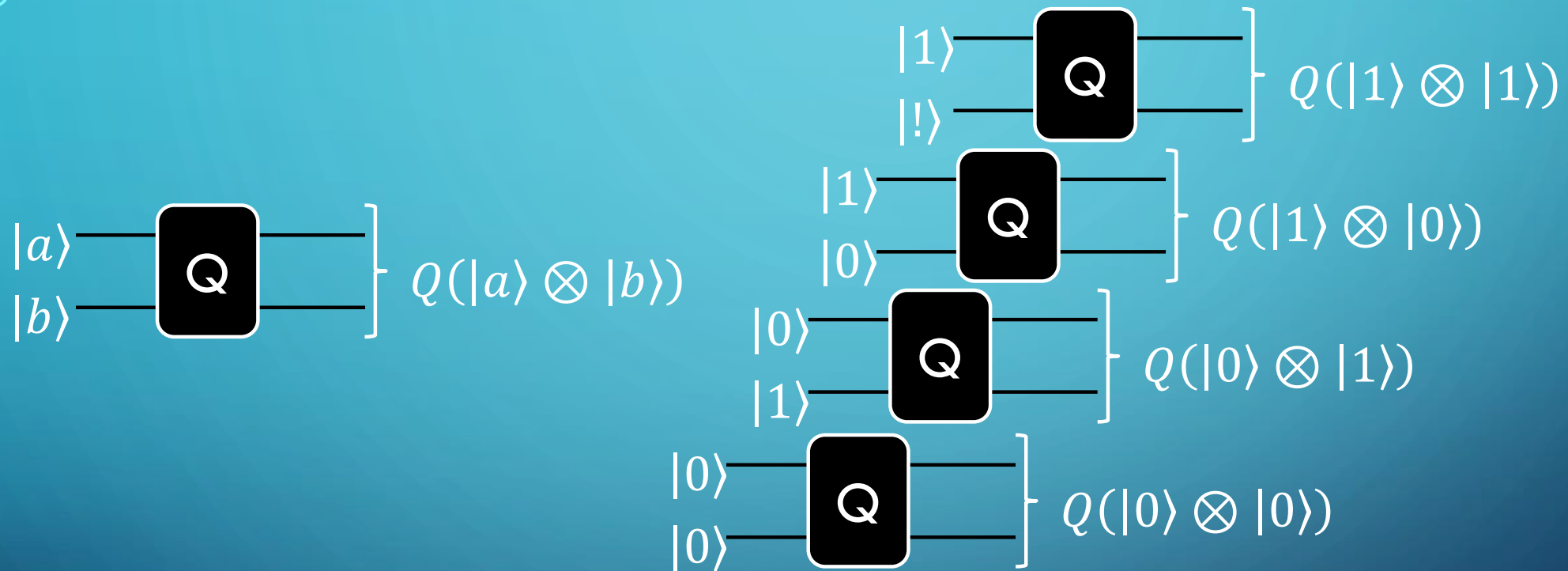


4 executions are required to iterate over the 4 possible candidates



1 execution is enough to encode the solution in $|q_1 q_0 y\rangle$, but ...

#4 - QUANTUM PARALLELISM



$$|\Psi\rangle = \sum_{i=0}^{2^n-1} \alpha_i |i\rangle \Rightarrow \hat{Q}|\Psi\rangle = \hat{Q} \left(\sum_{i=0}^{2^n-1} \alpha_i |i\rangle \right)$$

#4 - QUANTUM PARALLELISM

- Resembles data parallelism: **the same algorithm is simultaneously applied to all possible states**, but **without replication of resources**
- Caveat: when a **measurement** is performed to access the result, only **a single state is read**, and this is **stochastically selected**
- **Information on all other states is lost**
- This irreversible loss of information means that even though the **computation evolves on an exponentially large state space**, we only have **access to a very limited portion of it**

#5 - NO-CLONING THEOREM

- **Quantum information cannot be copied!**
- There is no unitary transformation that copies one arbitrary quantum superposition in one register to another register:
$$|R\rangle|Q\rangle \rightarrow U|R\rangle|Q\rangle = |R\rangle|R\rangle$$
- **Copying intermediate results** into temporary storage (variables) is thus **impossible**

#6 – INITIAL STATE

- Quantum algorithms require that **quantum registers are initialized to some known state**
- This **initial state** is referred to as the **ground state** and usually made to be the **basis state $|0\rangle$**
- **Loading data** to the quantum registers may in many cases require a number of gates (computation) larger than the number of gates necessary to execute the intended algorithm, **offseting the quantum advantage**

EXAMPLE CIRCUIT

A quantum circuit diagram with two horizontal lines representing qubits. Both lines are labeled $|0\rangle$ on the left. The top line has a control dot connected to a target circle on the bottom line. The bottom line has a control dot connected to a target circle on the top line. This represents a CNOT gate with the top qubit as control and the bottom qubit as target, and vice versa.

$$|\psi\rangle = |00\rangle$$

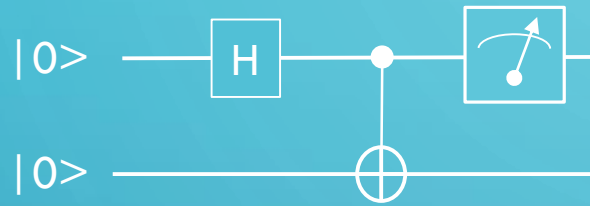
A quantum circuit diagram with two horizontal lines representing qubits. Both lines are labeled $|0\rangle$ on the left. The top line has a blue square box labeled 'X' followed by a control dot connected to a target circle on the bottom line. The bottom line has a control dot connected to a target circle on the top line. This represents a CNOT gate with the top qubit as control and the bottom qubit as target, and vice versa, after an X gate on the top qubit.

$$|\psi\rangle = |11\rangle$$

A quantum circuit diagram with two horizontal lines representing qubits. Both lines are labeled $|0\rangle$ on the left. The top line has a blue square box labeled 'H' followed by a control dot connected to a target circle on the bottom line. The bottom line has a control dot connected to a target circle on the top line. This represents a CNOT gate with the top qubit as control and the bottom qubit as target, and vice versa, after an H gate on the top qubit.

$$|\psi\rangle = \frac{1}{\sqrt{2}}(|00\rangle + |11\rangle)$$

BELL STATE AND ENTANGLEMENT



$$|\psi\rangle = \frac{1}{\sqrt{2}}(|00\rangle + |11\rangle)$$

Suppose the upper qubit is measured and it reads 0.

What are the probabilities of (afterwards) measuring 0 and 1 on the lower qubit?

MEASUREMENTS ON A SIMULATOR AND A REAL SYSTEM

