


CV

Vasilis Garvelatos

 Edinburgh, United Kindom
+44 7763961626

 vasigavr1@gmail.com

 vasigavr1.github.io

 linkedin.com/in/vasigavr1

 github.com/vasigavr1

Interests & Skills

Distributed Systems

- Consensus, Replication,
- Fault tolerance, Availability,
- Key-Value-Stores, RDMA

Computer Architecture

- Consistency, Persistency,
- Parallelism, Memory Systems

Parallel Programming

- Language-level memory models

Software Engineering

- C, C++, Python, Linux

Hardware Design

- VHDL, Verilog, SystemC,
- Modelsim, Quartus



Education

Since 2017
University of
Edinburgh

Ph.D. in Computer Science

- Thesis: Enforcing Consistency in the Datacenter
- Advisor: Vijay Nagarajan; Secondary Advisor: Boris Grot
- Sponsored by ARM Centre of Excellence

2016-2017
University of
Edinburgh

M.Sc by research in Computer Science,

- Thesis: An RDMA-based skew-aware Key-Value Store
- Advisor: Vijay Nagarajan; Secondary Advisor: Boris Grot

2009-2015
DUTH University

B.Sc. & M.Sc. in Electrical & Computer Engineering

- Thesis: Heterogeneous links in VC-based Network-on-Chips
- Advisor: George Dimitrakopoulos

Projects & Publications

EuroSys '21
(to appear)

Odyssey: The Impact of Modern Hardware on Strongly-Consistent Replication Protocols

-
-
-

PPoPP '20
Best paper
nominee

Kite: Efficient and Available Release Consistency for the Datacenter

-
-
-

EuroSys '18

Scale-out ccNUMA: Exploiting Skew with Strongly-Consistent Caching.

-
-
-

ASPLOS '20
Micro Top Picks
Honorable
Mention

Hermes: Fast and Reliable Data Replication with Linearizability

-
-
-

ASPLOS '20

Lazy Release Persistency

-
-
-

ATC '21
(submitted)

Avocado: A Secure In-Memory Distributed Storage System

-
-

Work Experience

Since 2017

Teaching at the University of Edinburgh,

- TA / Tutor / Marker & covering Lectures
- Classes: Computer Architecture, Introduction to Computer Systems
Parallel Architectures

2016

DSP engineer, Akya Limited, UK

- EDA tool design in C++