



Vasilis Garvielatos

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Technical skills

C, C+, Python
Linux
VHDL, Verilog

Reasearch Interests

Distributed Systems

-- Consensus, Replication, Fault tolerance, Availability, Key-Value-Stores, RDMA

Computer Architecture

-- Consistency, Persistency, Parallelism, Memory Systems

Parallel Programming

-- Language-level memory models

Education

Ph.D. in Computer Science

-- Thesis: Enforcing Consistency in the Datacenter
-- Advisor: Vijay Nagarajan
-- Secondary Advisor: Boris Grot
-- Sponsored by ARM centre of excellence

M.Sc by research in Computer Science,

-- Thesis: An RDMA-based skew-aware Key-Value Store
-- Advisor: Vijay Nagarajan

B.Sc. & M.Sc. in Electrical & Computer Engineering

Thesis: Heterogeneous links in VC-based Network-on-Chips
Advisor: George Dimitrakopoulos

Publications

EuroSys '21

Odyssey: The Impact of Modern Hardware on Strongly-Consistent Replication Protocols
V. Gavrielatos, A. Katsarakis, V. Nagarajan

ASPLOS '20
Micro Top Picks
Honorable
Mention

Hermes: Fast and Reliable Data Replication with Linearizability
A. Katsarakis, V. Gavrielatos, A. Joshi, S. Katebzadeh,
B. Grot, V. Nagarajan, A. Dragojevic

ASPLOS '20

Lazy Release Persistency
M. Dananjaya, V. Gavrielatos, A. Joshi, V. Nagarajan.

PPoPP '20
Best paper
nominee

Kite: Efficient and Available Release Consistency for the Datacenter
V. Gavrielatos, A. Katsarakis, V. Nagarajan, B. Grot, A. Joshi.

EuroSys '18

Scale-out ccNUMA: Exploiting Skew with Strongly-Consistent Caching
V. Gavrielatos, A. Katsarakis, A. Joshi, N. Oswald, B. Grot, V. Nagarajan.

Work Experience

since 2017

Teaching at University of Edinburgh,
TA / Tutor / Marker & covering Lectures
for Computer Architecture, Introduction to Computer Systems
Parallel Architectures

2016

DSP engineer , Akya Limited, UK
--EDA tool design