CS268 Project Proposal - Synchronization of Clocks in Wireless Networks

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The "Tactile Internet" promises to introduce several new emerging technology market opportunities as well as new kinds of public services. Immersive and interactive applications such as robotics, gaming, virtual & augmented reality, smart-healthcare, and smart grid all could involve precise human-to-machine and machine-to-machine interaction. The Internet of Things (IoT) also envisions a heterogeneous network where devices are connected seamlessly via the technology best suited for their particular needs. To enable these Tactile Internet and high-performance IoT applications, ultra-reliable communication with latencies of about 1ms is crucial. This domain is largely unexplored and the techniques used by existing standards are fundamentally ill suited for low-latency and high-reliability.

An existing domain which parallels these requirements is high performance industrial control systems. As no existing wireless technology can support these requirements, wired communication protocols (Fieldbus) are used. They support short messages (10s of bytes) to/from closeby sensor/actuators (10s - 100s) delivered regularly (100s - 1000s of times per second). The protocols are ultra-reliable (probability of a single packet delivery failure of 10^{-8}) with very low latency (a couple of ms). Synchronized cooperative communication based wireless protocols proposed in [1], [2] achieves QoS (high-reliability and low latency) similar to wired fieldbus systems by exploiting multi-user diversity and distributed space-time coding to achieve. One of the main requirements for protocols aiming at high-performance IoT applications is tightly synchronized clocks. We aim to address this problem in our project.

The goal of this project is development and analysis of protocols to achieve clock synchronization in a wireless network while tolerating a small (predefined) amount of error. We model the time at each node n (T_n) to have two components: the time from the oscillator $(T_{n,o})$ and the correction time $(T_{n,c})$ such that $T_n = T_{n,o} + T_{n,c}$. We call T_n to be the virtual clock time of node n. We want the virtual clock time differences between any two nodes m and n to be within a tolerable limit say T_ϵ i.e., $|T_m - T_n| \leqslant T_\epsilon$. The target T_ϵ is in the order of 100ns.

We envision three ways of approaching this problem: a centralized (beacon oriented) solution, a distributed solution, and a hierarchical solution. Each solution is well suited for different scenarios. For

example, a centralized (and hierarchical) solution works better when most clocks are not synchronized. But when few nodes are not synchronized, a distributed or hierarchical synchronization would be more suitable. We will consider various jitter and drift models for clocks and address both the transient as well as steady state dynamics of the clocks. We will analyze these methods both theoretically as well as through simulations. We expect to have a few fundamental theorems under suitable assumptions and model based simulation results, for example number of rounds of communication it took for the network to reach equilibrium and how the network responded to different clock events (jitter vs drift). **If time permits**, we will try to build a protocol that piggybacks over WiFi.

REFERENCES

[1] V. Narasimha Swamy, S. Suri, P. Rigge, M. Weiner, G. Ranade, , A. Sahai, and B. Nikoli, "Cooperative communication for high-reliability low-latency wireless control," in *Communications (ICC), 2015 IEEE International Conference on*. IEEE, 2015, pp. 4380–4386.

[2] V. Narasimha Swamy, P. Rigge, G. Ranade, A. Sahai, and B. Nikoli, "Network coding for High-Reliability Low-Latency wireless control," in *2016 IEEE Wireless Communications and Networking Conference WS 6 : IEEE WCNC'2016 Workshop on 5G & Vertical Industry (IEEEWCNC2016-5G&VI)*, Doha, Qatar, Apr. 2016.