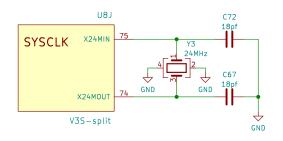
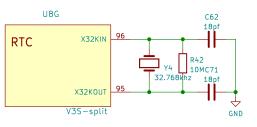
1 2 3	4 5	6 7 8
Clocks Buttons — lights	Memory	Audio Video
<i>y</i>		774410 77460
File: Clocks_Buttons.kicad_sch	File: Memory.kicad_sch	File: Audio_Video.kicad_sch
CTMZO		D
STM32	Connections	Power
	File: Connections.kicad_sch	
File: STM32.kicad_sch		File: Power.kicad_sch
		Sheet: / File:
		Title: OzzyBoard Rev B
		ViCad E.D.A. Mand (6.0.9)

V3s Clocks





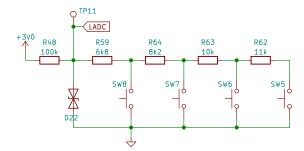
Activity led



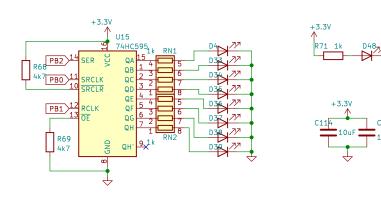
Mounting holes

- J12 H1 MountingHole
- J13 MountingHole tooling_hole H3
- J14 tooling_hole H4 MountingHole

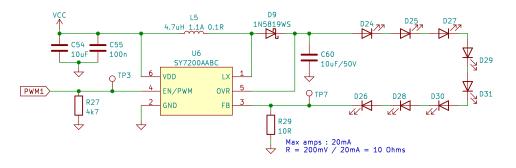
Buttons



GPIO



Backleds



Sheet: /Clocks Buttons — lights/ File: Clocks_Buttons.kicad_sch

Title: OzzyBoard Rev B

 Size: A4
 Date: 2024-01-15
 Rev: 0.92

 KiCad E.D.A. kicad (6.0.8)
 Id: 2/7

12C/PWM/Serial **USB** Host USB VCC-USB 109 USB-DP 111 USB-D+ UART2 USB-DM 110 USB-D-PB3/UART2_CTS/PB_EINT3 PB2/UART2_RTS/PB_EINT2 41 PB1/UART2_RX/PB_EINT1 40 39 V3S-split PB1 PB0 PWM0/1 PB5/PWM1/PB_EINT5 PB4/PWM0/PB_EINT4 43 PWM0 CH340N TWI0/1 PB9/TWI1_SDA/UARTO_RX/PB_EINT9 PB8/TWI1_SCK/UARTO_TX/PB_EINT8 48 2 UD-PB7/TWI0_SDA/PB_EINT7 46 PB6/TWI0_SCK/PB_EINT6 45 GND V3S-split 0.91" OLED sda Eth PHY R32 49.9 GND U8L V3S-split RJ45_LED_Shielded EPHY-TXP 92 EPHY-TXP **EPHY** SPD_LED EPHY-TXN 91 EPHY-TXN EPHY-RXP 90 EPHY-RXP LNK_LED SSD1306_conn EPHY-RXN 89 EPHY-RXN EPHY-SPD-LED 78 SPD_LED 77 LNK_LED 04 EPHY-RTX 94 R43 6k8 Sheet: /Connections/ GND File: Connections.kicad_sch Title: OzzyBoard Rev B

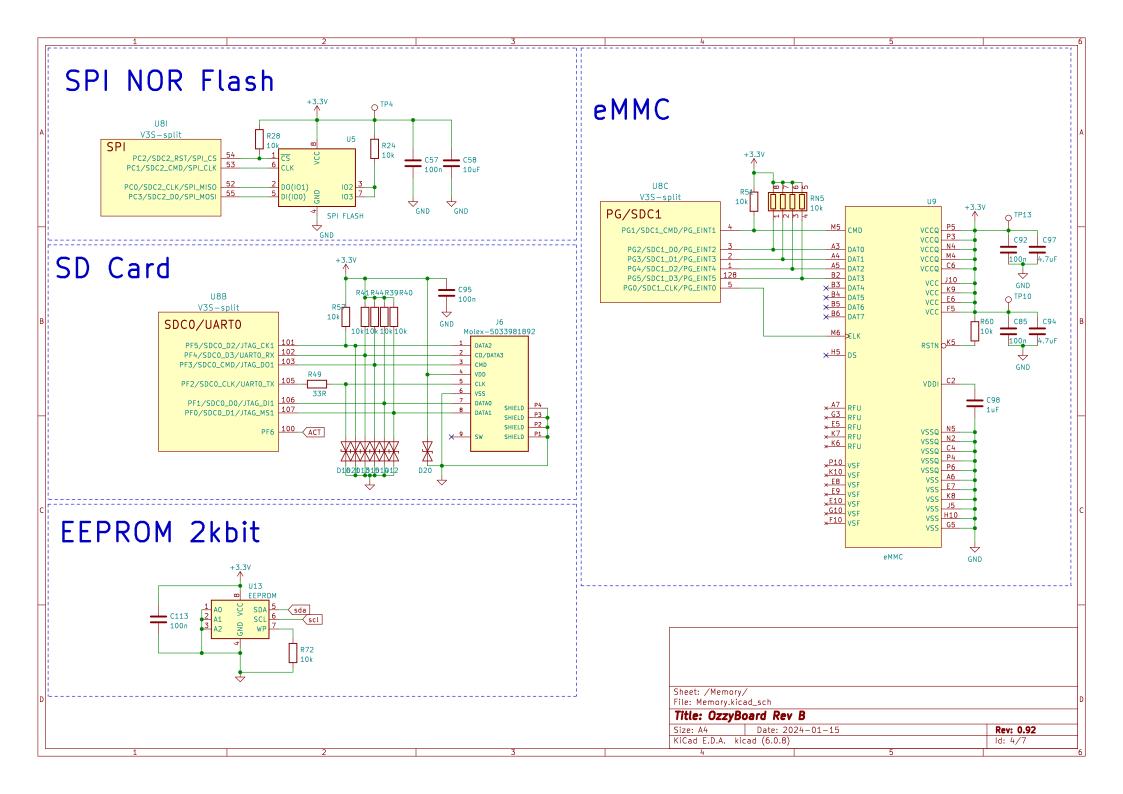
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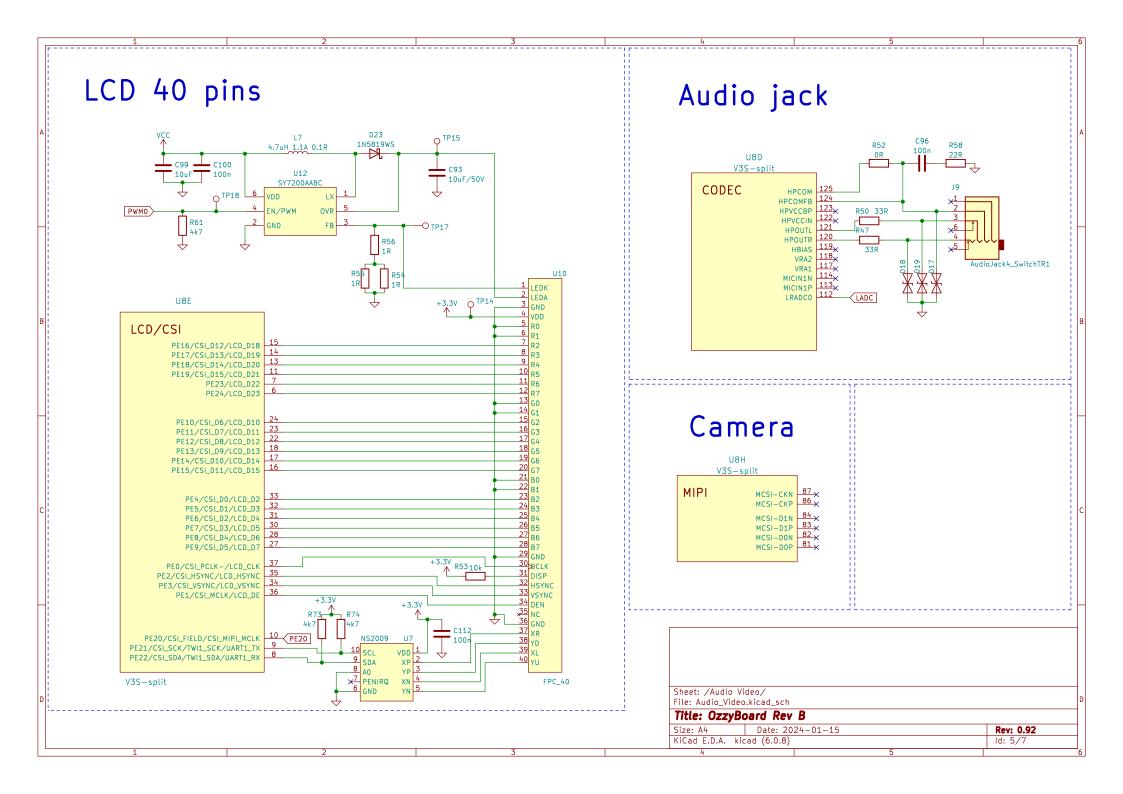
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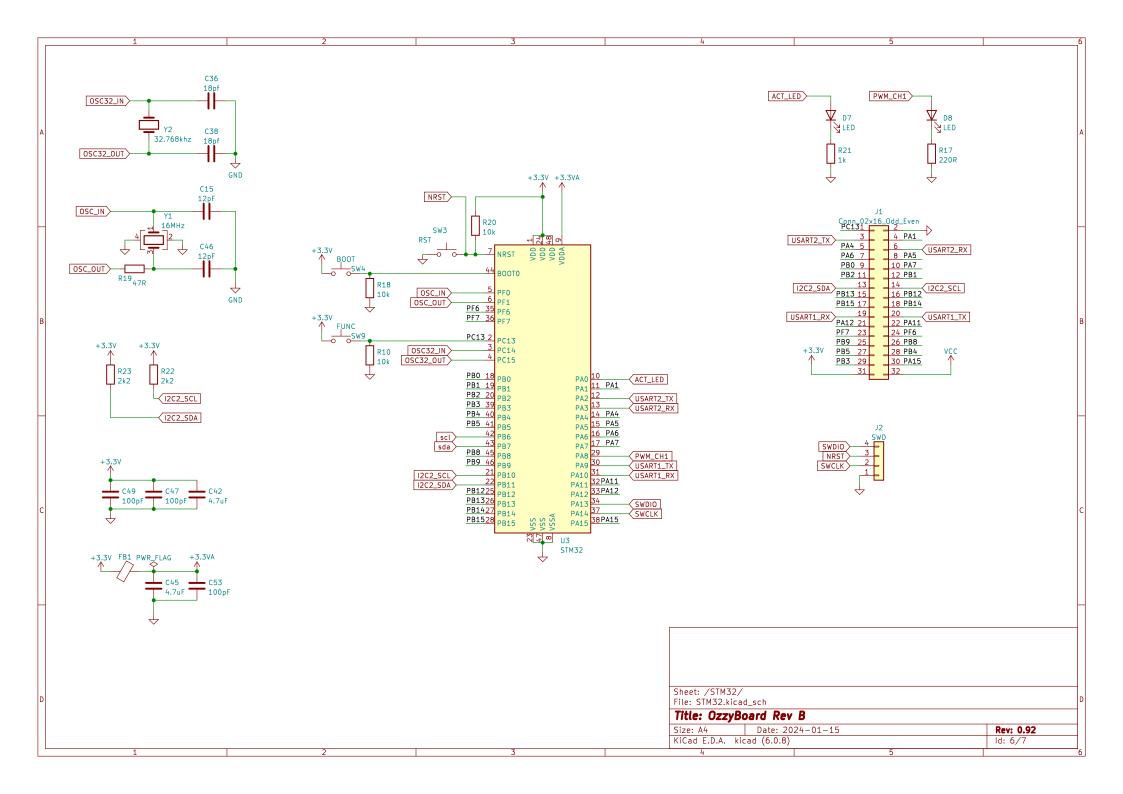
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Rev: 0.92

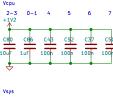
ld: 3/7

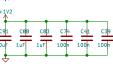




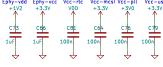


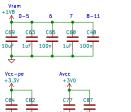
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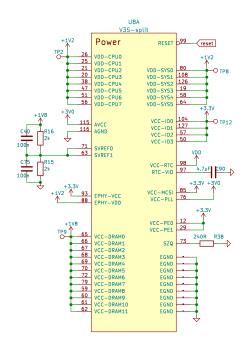




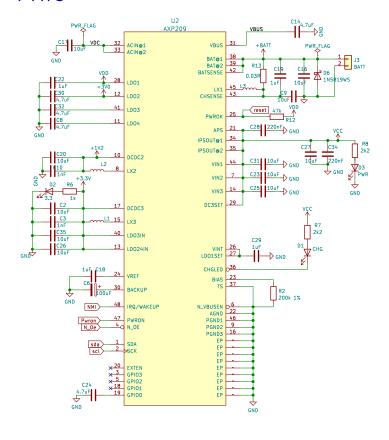




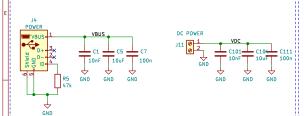
CPU



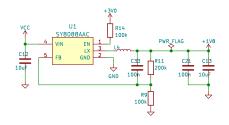
PMIC



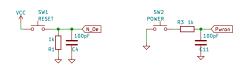
DC IN



DRAM



Reset & Power buttons



Sheet: /Power		
File: Power.kic	ad_sch	
Title: Ozzy	Board Rev B	
Size: A3	Date: 2024-01-15	Rev: 0.92
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	7	8