DIR	NO.	PIN NAME	PIN NAME	Default	Fuction1	Fuction2
	1	VR_PA	VR PA	VR PA		
•	2	VDD_IN	VDD IN	VDD IN		
	3	GND_PLL	GND PLL	GND PLL		
	4	XTA	XTA	XTA		
	5	XTB	XTB	XTB		
 	6	NC	NC	NC		
	7	DIO3	DI03	DI03		
	8	VREG	VREG	VREG		
LEFT	9	GND_DCC	GND_DCC	GND_DCC		
	10	DCC_SW	DCC_SW	DCC_SW		
	11	VBAT_RF	VBAT_RF	VBAT_RF		
	12	VBAT_DIO	VBAT_DIO	VBAT_DIO		
	13	DIO2	DI02	DI02		
	14	SWIM	PAO	SWIM		
	15	NRST	PA1	NRST		
	16	LCD_COM0	PA4	LCD_COMO	ADC1_IN2	COMP1_INP
	17	LCD_COM1	PA5	LCD_COM1	ADC1_IN1	COMP1_INP
•	18	LCD_COM2	PA6	LCD_COM2	ADC1_INO	COMP1_INP
	19	VDDA	VDD1	VDD1/VDDA		
	20	VREFP	VREFP	VREFP		
	21	UART1_RX	PG0	UART3_RX	LCD_SEG28	
	22	UART1_TX	PG1	UART3_TX	LCD_SEG29	
•	23	VLCD	VLCD	VLCD	HADWO DV	
	24	LCD_SEG0	PE3	LCD_SEG4	UART2_RX	DAC TRIC1
роттом	<u>25</u>	LCD_SEG1 LCD SEG2	PE4	LCD_SEG5	UART2_TX	DAC_TRIG1
BOTTOM	26 27	LCD_SEG2	PE5 PD0	LCD_SEG6 LCD_SEG7	ADC1_IN23 ADC1_IN22	COMP1_INP
•	28	LCD_SLG3	PD0 PD1	LCD_SEG7	COMP1 INP	COMP2 INP
	29	LCD SEG4	PD2	LCD_COM3	ADC1 IN20	COMP1 INP
•	30	LCD_SEG5	PD3	LCD_SEG9	ADC1_IN19	COMP1 INP
	31	UARTO RX	PH4	UART2 RX	MDC1_INTO	
	32	UART0_TX	PH5	UART2 TX		
•	33	VDDD1	VDD3	VDD3		
	34	LCD SEG6	PB4	LCD SEG14	SPI1 NSS	ADC1 IN14
	35	LCD SEG7	PB5	LCD_SEG15	SPI1 SCK	ADC1 IN13
	36	LCD_SEG8	PB6	LCD SEG16	SPI1 MOSI	ADC1 IN12
RIGHT	37	LCD_SEG9	PB7	LCD_SEG17	SPI1_MISO	ADC1_IN11
	38	VDDD2	VDD4	VDD4		
	39	SPI_MISO	PF0	SPI1_MISO		ADC1_IN24
	40	SPI_MOSI	PF1	SPI1_MOSI		ADC1_IN25
	41	SPI_SCK	PF2	SPI1_SCK		ADC1_IN26
	42	SPI_NSS	PF3	SPI1_NSS		ADC1_IN27
	43	LCD_SEG10	PF4	LCD_SEG40	LCD_COM4	
	44	LCD_SEG11	PF5	LCD_SEG41	LCD_COM5	
	45	LCD_SEG12	PF6	LCD_SEG42	LCD_COM6	
	46	LCD_SEG13	PF7	LCD_SEG43	LCD_COM7	: -:
	47	LCD_SEG14	PD4	LCD_SEG18		ADC1_IN10
	48	LCD_SEG15	PD5	LCD_SEG19	ODTO 00-	ADC1_IN9
	49	LCD_SEG16	PD6	LCD_SEG20	SPI2_SCK	ADC1_IN8
	50	GPIO1	PD7	LCD_SEG21	SPI2_NSS	ADC1_IN7
	51	GPIO0	PG6	GPI00	SPI2_MOSI	I OD ODGOS
	<u>52</u>	LCD_SEG17	PG7	GPIO1	SPI2_MISO	LCD_SEG35
<u> </u>	53	I2C_SDA	PC0	I2C1_SDA		

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	54	I2C_SCL	PC1	I2C1_SCL		
	55	VDDD3	VDD2	VDD2		
	56	ADC1_IN0	PC2	ADC1_IN6	UART1_RX	LCD_SEG22
	57	ADC1_IN1	PC3	ADC1_IN5	UART1_TX	LCD_SEG23
	58	GPIO2	PC4	GPI02	LCD_COM4	ADC1_IN4
TOP	59	OSC32K_IN	PC5	OSC32_IN	SPI1_NSS	UART1_TX
	60	OSC32K_OUT	PC6	OSC32_OUT	SPI1_SCK	UART1_RX
	61	ADC1_IN2	PC7	ADC1_IN3	LCD_COM5	LCD_SEG25
	62	GPIO3	PE6	GPI03	LCD_COM6	LCD_SEG26
	63	GPIO4	PE7	GPI04	LCD_COM7	LCD_SEG27
	64	NSS	NSS	NSS		
	65	SCAN	SCAN	SCAN		
	66	RFI_P	RFI_P			
	67	RFI_N	RFI_N			_
	68	RFO	RFO			

Fuction3	Fuction4
ADC1_TRIG	
IDOI_INIO	
DAC_TRIG2	
COMP2 INP	
COMP2_INP	ADC1_TRIG
ADC1_IN21	
COMD1 TND	DAC OUTO
COMP1_INP	DAC_OUT2
COMP1_INP	DAC_OUT2
COMP1_INP	DAC_OUT2
COMP1_INP	
	DAC_OUT1
	DAC_OUT2
COMD1 TND	
COMP1_INP	
COMP1_INP	
COMP1_INP	VREF_INT
COMP1_INP	VREF_INT
L	I

LoRa	17	17
	17	11
SWIM	1	1
NRST	1	1
VDD	5	5
VREFP	1	1
VLCD	1	1
SPI	1	4
UART	2	4
I2C	1	2
GPIO	5	5
ADC	3	3
LCD_COM	4	4
LCD_SEG	18	18
OSC32K	2	2
Total	62	68

LoRa	MCU	
SPI_NSS	PIO	
SPI_SCK	PI1	
SPI_MOSI	PI2	
SPI_MISO	PI3	
SPI_NRESET	PH0	
SPI BUSY	PH1	DIOO
DIO1	PH2	

COMP1_INP	
COMP1_INP	COMP2_INM
COMP1_INP	COMP2_INM
COMP1_INP	COMP2_INM
UART3_TX	
UART3_RX	