

[FEA] Utilize TypeAdaption and TypeNegotiation feature of ROS2 Humble to accelerate cuda/tensorrt-pipeline performance. #5396



Answered by knzo25) ZhenshengLee asked this question in Feature requests



ZhenshengLee 2 weeks ago

edited -

description

In 2022, ros2 humble release brings nvidia hardware support, https://www.openrobotics.org/blog/2022/5/24/ros-2-humble-hawksbillrelease based on type adaptation (REP-2007) and type negotiation (REP-2009) which gives cuda/tensorrt ros2 pipeline huge performance improvement(on Jetson Devices, theorytically any devices with cuda/tensorrt too)

From autoware docs, https://autowarefoundation.github.io/autowaredocumentation/main/reference-hw/ad-computers/, that the cuda/tensorrt is supported by autoware reference HW(including Jetson and other PC with Nvidia GPUs), so the ros2 humble feature can be used to accelerate the ros2 cuda/tensorrt pipeline.

consideration

there are official REP-2007 and REP-2009 official example maintained by

https://github.com/ros2/examples/blob/rolling/rclcpp/topics/minimal_publishe r/member_function_with_type_adapter.cpp https://github.com/osrf/negotiated

Nvidia has launched ISAAC ROS https://developer.nvidia.com/isaac/ros and there is isaac_ros_nitros https://github.com/NVIDIA-ISAAC-ROS/isaac_ros_nitros base class to use for Jetson Devices.

extra content

I think there is possibility to port isaac_ros_nitros to x86 dGPU platform, which could reuse much work of nvidia.

EDIT: there is official support to x86 platform from ISAAC ROS, https://nvidia-isaac-ros.github.io/getting_started/index.html#systemrequirements



Category



Feature requests

Labels

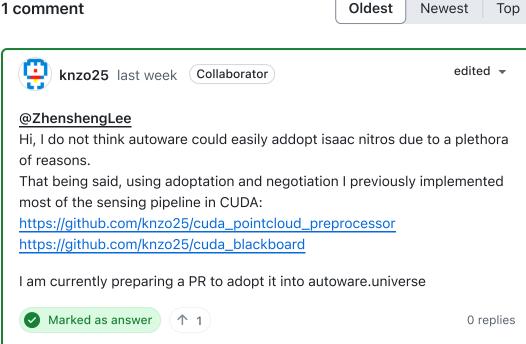
None yet

2 participants









Answer selected by ZhenshengLee