

CS 380 - GPU and GPGPU Programming Lecture 3: GPU Architecture 1

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Reading Assignment #2 (until Sep 14)



Read (required):

- Orange book (GLSL), chapter 4 (*The OpenGL Programmable Pipeline*)
- Brief GLSL overview:

https://en.wikipedia.org/wiki/OpenGL_Shading_Language

• GPU Gems 2 book, chapter 30 (*The GeForce 6 Series GPU Architecture*) available online:

http://download.nvidia.com/developer/GPU Gems 2/GPU Gems2 ch30.pdf

What is in a GPU?



Lots of floating point processing power

Stream processing cores
 different names:
 stream processors,
 CUDA cores, ...





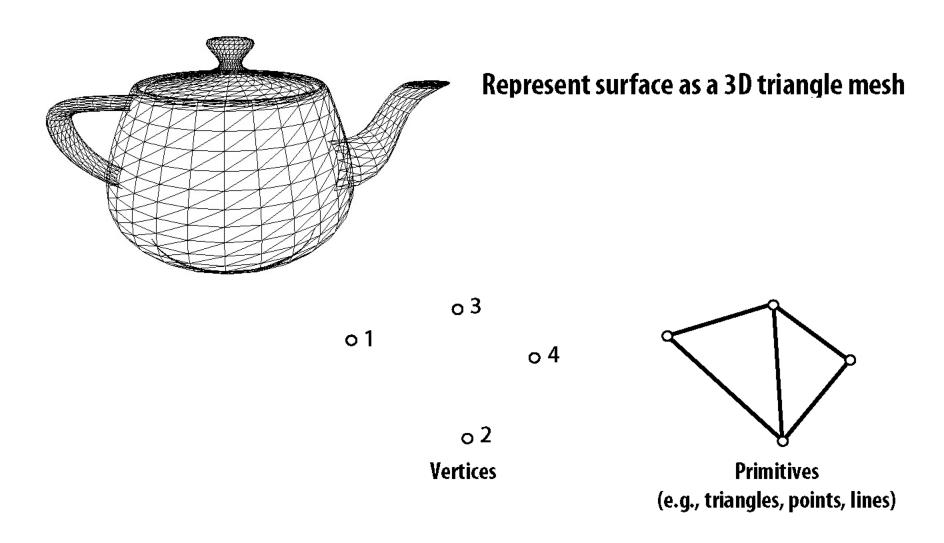
Was vector processing, now scalar cores!

Still lots of fixed graphics functionality

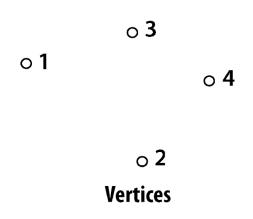
- Attribute interpolation (per-vertex -> per-fragment)
- Rasterization (turning triangles into fragments/pixels)
- Texture samping and filtering
- Depth buffering (per-pixel visibility)
- Blending/compositing (semi-transparent geometry, ...)
- Frame buffers

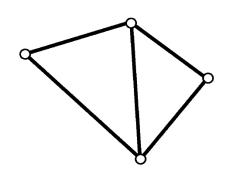


Real-time graphics primitives (entities)

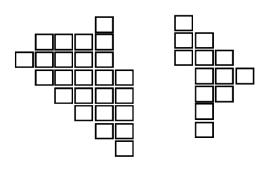


Real-time graphics primitives (entities)

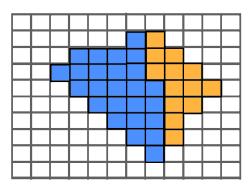




Primitives (e.g., triangles, points, lines)



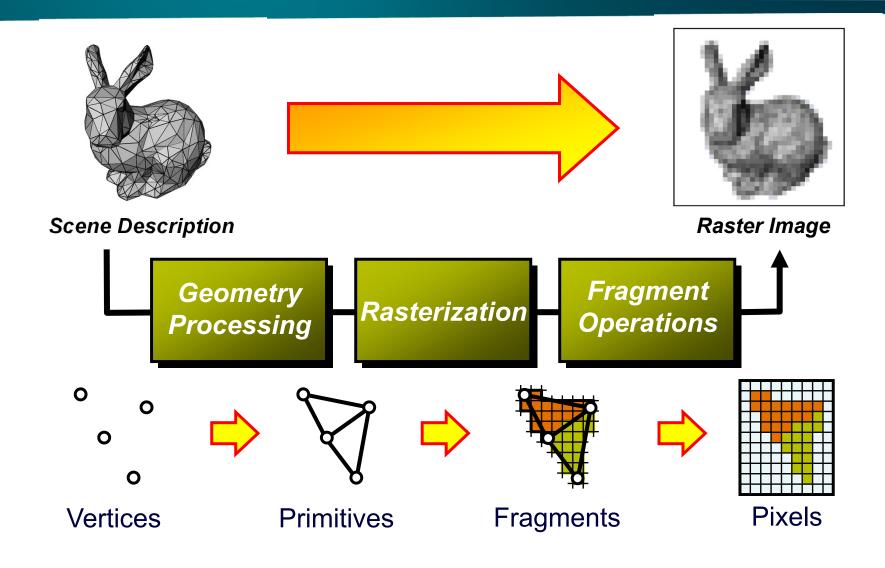




Pixels (in an image)

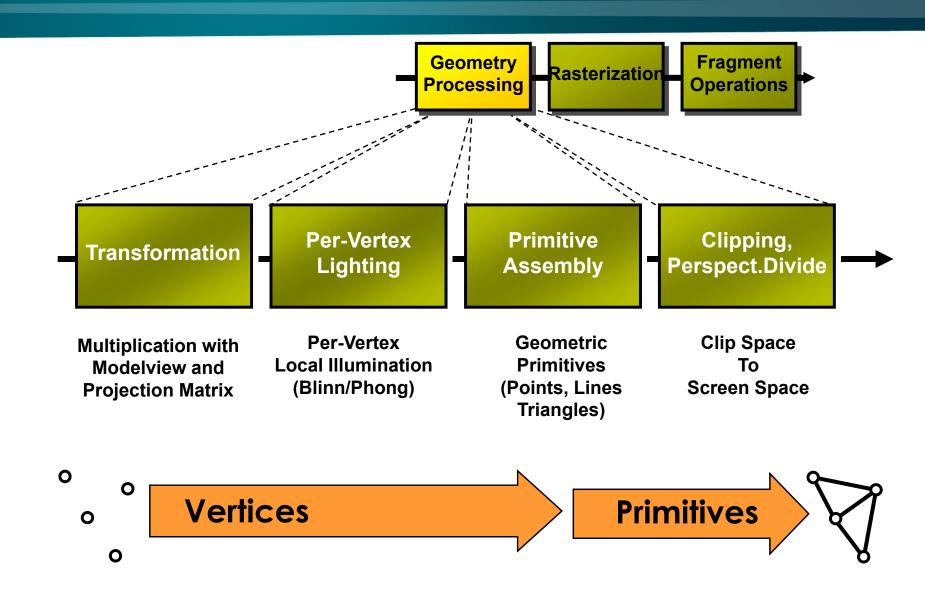
Graphics Pipeline





Geometry Processing

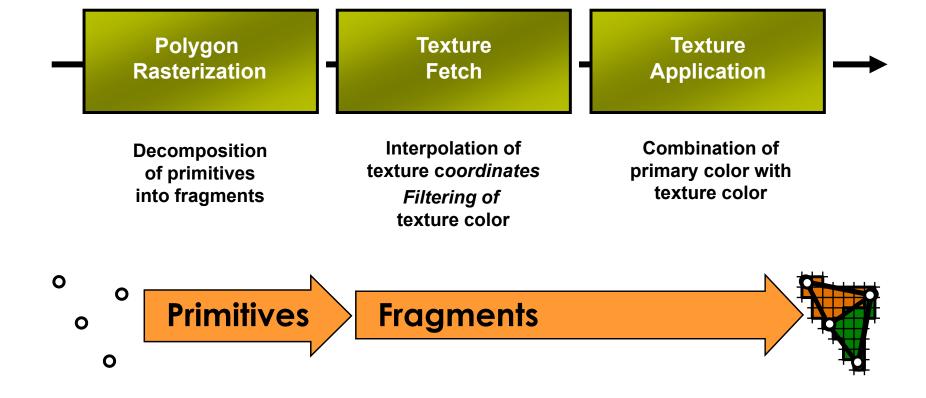




Rasterization

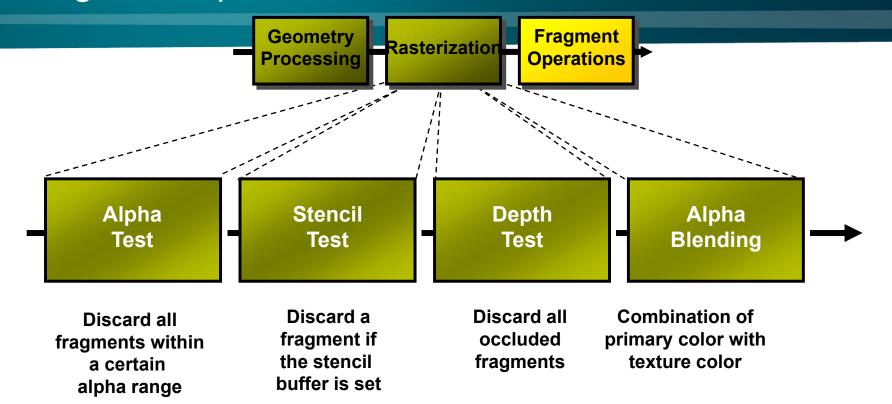






Fragment Operations

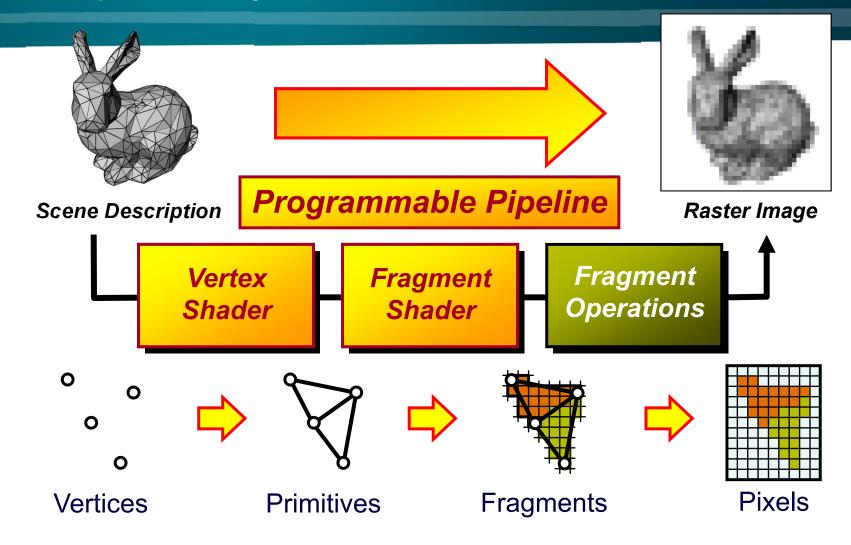






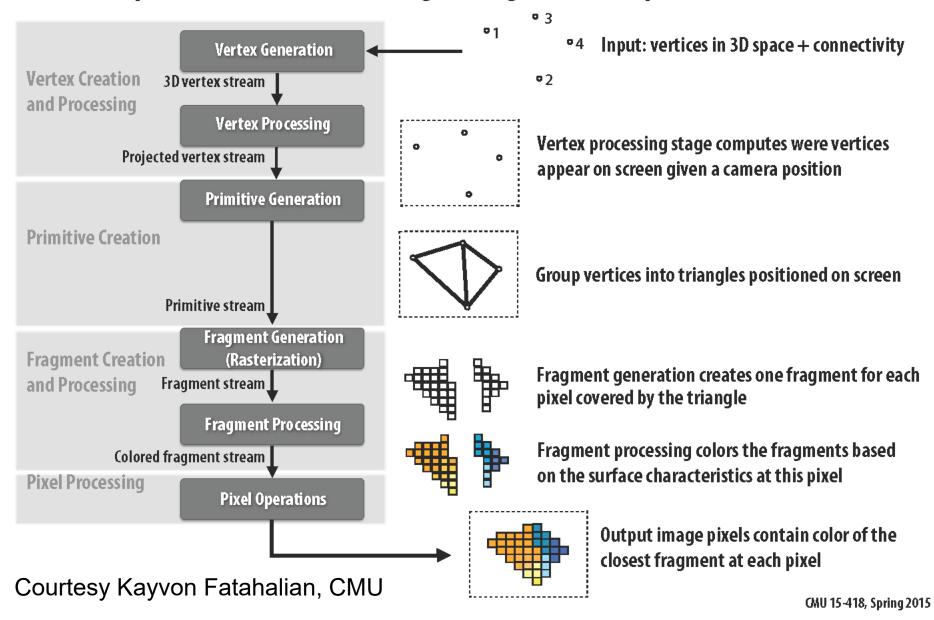
Graphics Pipeline





Graphics pipeline architecture

Performs operations on vertices, triangles, fragments, and pixels



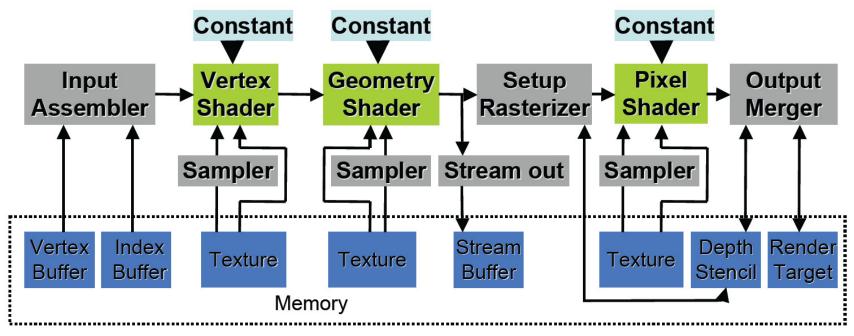
Direct3D 10 Pipeline (~OpenGL 3.2)



New geometry shader stage:

- Vertex -> geometry -> pixel shaders
- Stream output after geometry shader





Direct3D 11 Pipeline (~OpenGL 4.x)



New tessellation stages

Hull shader

(OpenGL: tessellation control)

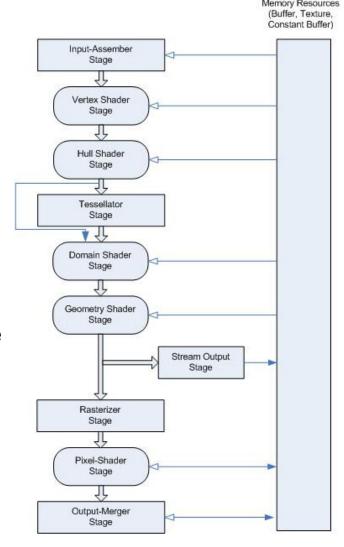
Tessellator

(OpenGL: tessellation primitive generator)

Domain shader

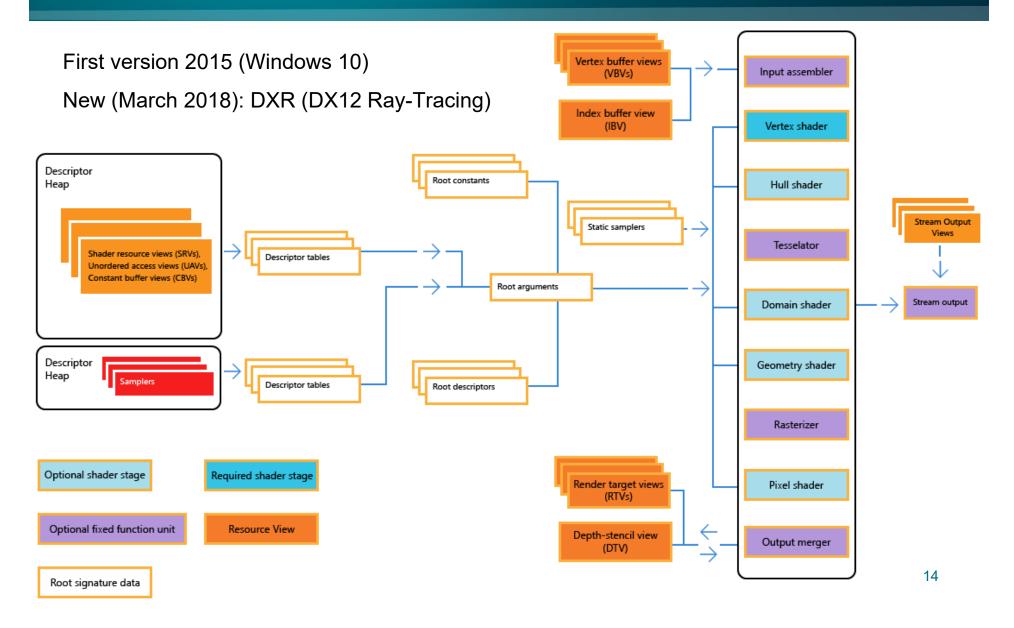
(OpenGL: tessellation evaluation)

- In future versions, there might be yet more stages, but for some time now all additions were outside this pipeline:
 - Compute shaders
 - Vulkan
 - Ray tracing cores



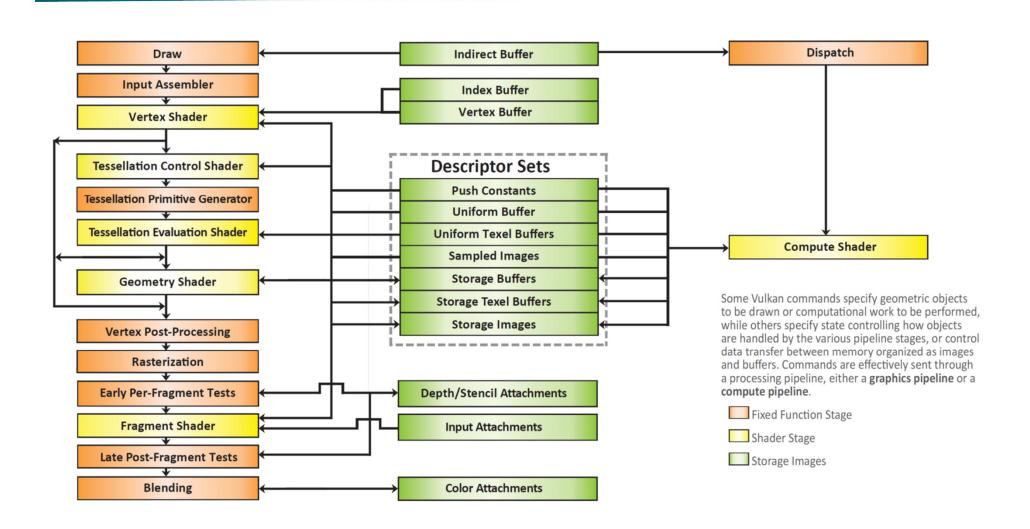


Direct3D 12 Pipeline (and Later Updates)

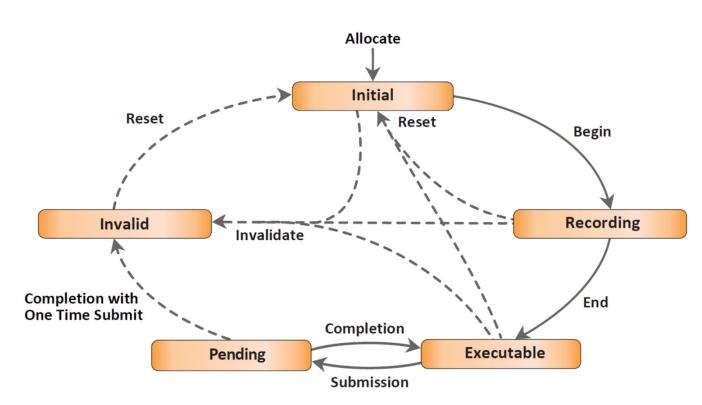


Vulkan (1.1) Pipeline









Initial state

The state when a command buffer is first allocated. The command buffer may be reset back to this state from any of the executable, recording, or invalid states. Command buffers in the initial state can only be moved to recording, or freed.

Recording state

vkBeginCommandBuffer changes the state from initial to recording. Once in the recording state, vkCmd* commands can be used to record to the command buffer.

Executable state

vkEndCommandBuffer moves a command buffer state from recording to executable. Executable command buffers can be submitted, reset, or recorded to another command buffer.

Pending state

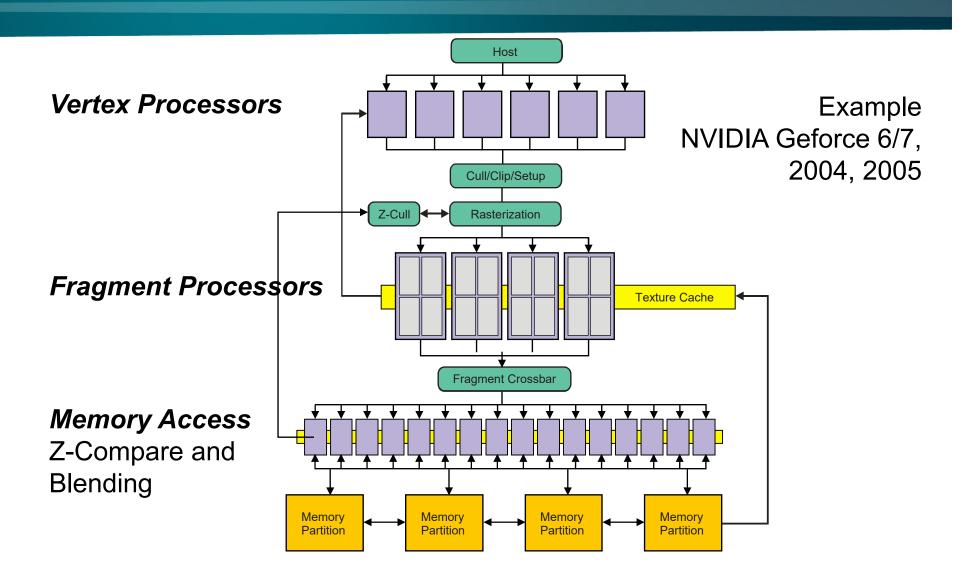
Queue submission changes the state from executable to pending, in which applications must not attempt to modify the command buffer in any way. The state reverts back to executable when current executions complete, or to invalid.

Invalid state

Some operations will transition the command buffer into the invalid state, in which it can only be reset or freed.

GPU Structure Before Unified Shaders



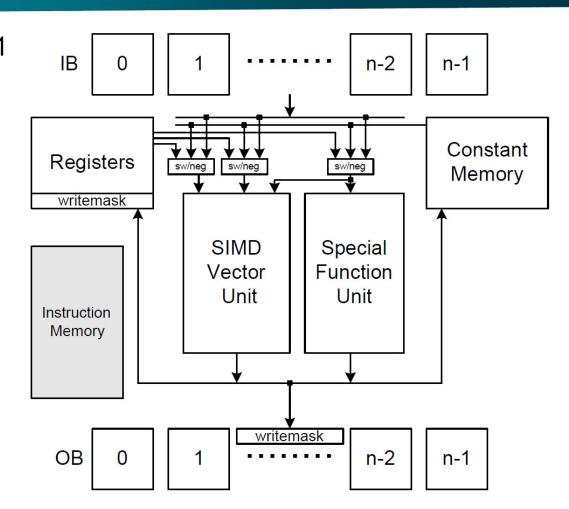


Legacy Vertex Shading Unit (1)



Geforce 3 (NV20), 2001

- floating point 4-vector vertex engine
- still very instructive for understanding GPUs in general



Lindholm et al., A User-Programmable Vertex Engine, SIGGRAPH 2001

Legacy Vertex Shading Unit (2)



Input attributes

Vertex Attribute Register	Conventional Per-vertex Parameter	Conventional Per-vertex Parameter Command	Conventional Component Mapping
0	Vertex position	glVertex	x,y,z,w
1	Vertex weights	glVertexWeightEXT	w,0,0,1
2	Normal	glNormal	
3	Primary color	glColor	r,g,b,a
4	Secondary color	glSecondaryColorEXT	r,g,b,1
5	Fog coordinate	glFogCoordEXT	f,0,0,1
6	-	-	-
7	-1	-	-
8	Texture coord 0	<pre>glMultiTexCoordARB(GL_TEXTURE0)</pre>	s,t,r,q
9	Texture coord 1	<pre>glMultiTexCoordARB(GL_TEXTURE1)</pre>	s,t,r,q
10	Texture coord 2	<pre>glMultiTexCoordARB(GL_TEXTURE2)</pre>	s,t,r,q
11	Texture coord 3	<pre>glMultiTexCoordARB(GL_TEXTURE3)</pre>	s,t,r,q
12	Texture coord 4	<pre>glMultiTexCoordARB(GL_TEXTUER4)</pre>	s,t,r,q
13	Texture coord 5	<pre>glMultiTexCoordARB(GL_TEXTUER5)</pre>	s,t,r,q
14	Texture coord 6	<pre>glMultiTexCoordARB(GL_TEXTUER6)</pre>	s,t,r,q
15	Texture coord 7	glMultiTexCoordARB(GL_TEXTUER7)	s,t,r,q

```
Code examples
```

DP4 o[HPOS].x, c[0], v[OPOS];

MUL R1, R0.zxyw, R2.yzxw;

MAD R1, R0.yzxw, R2.zxyw, -R1; swizzling!

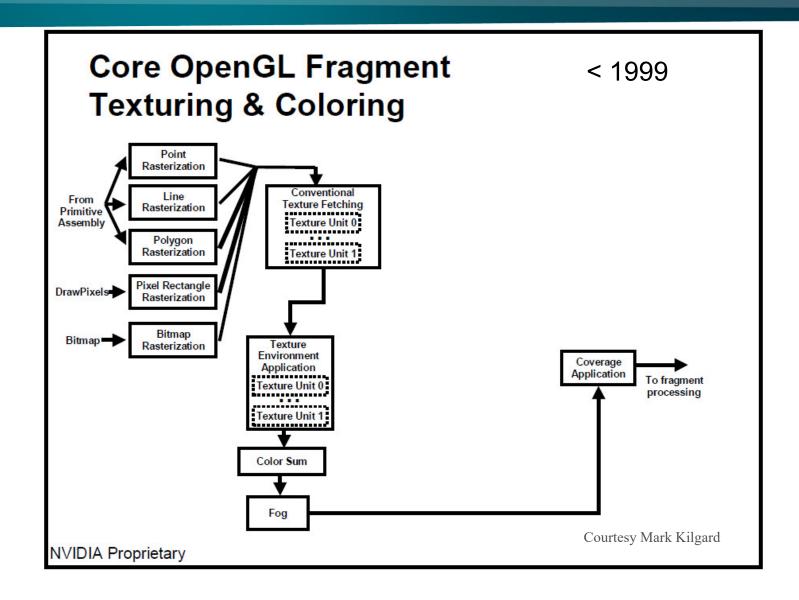




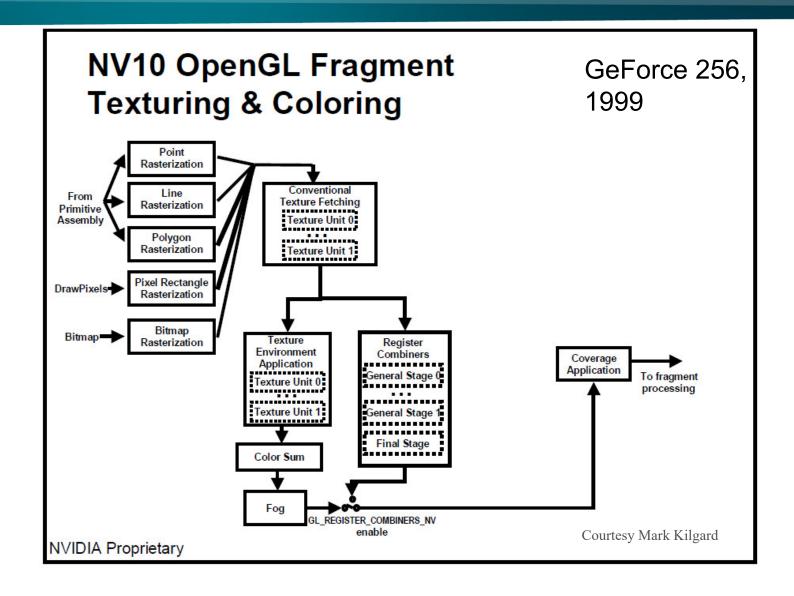
Vector instruction set, very few instructions; no branching yet!

OpCode	Full Name	Description
MOV	Move	vector -> vector
MUL	Multiply	vector -> vector
ADD	Add	vector -> vector
MAD	Multiply and add	vector -> vector
DST	Distance	vector -> vector
MIN	Minimum	vector -> vector
MAX	Maximum	vector -> vector
SLT	Set on less than	vector -> vector
SGE	Set on greater or equal	vector -> vector
RCP	Reciprocal	scalar-> replicated scalar
RSQ	Reciprocal square root	scalar-> replicated scalar
DP3	3 term dot product	vector-> replicated scalar
DP4	4 term dot product	vector-> replicated scalar
LOG	Log base 2	miscellaneous
EXP	Exp base 2	miscellaneous
LIT	Phong lighting	miscellaneous
ARL	Address register load	miscellaneous

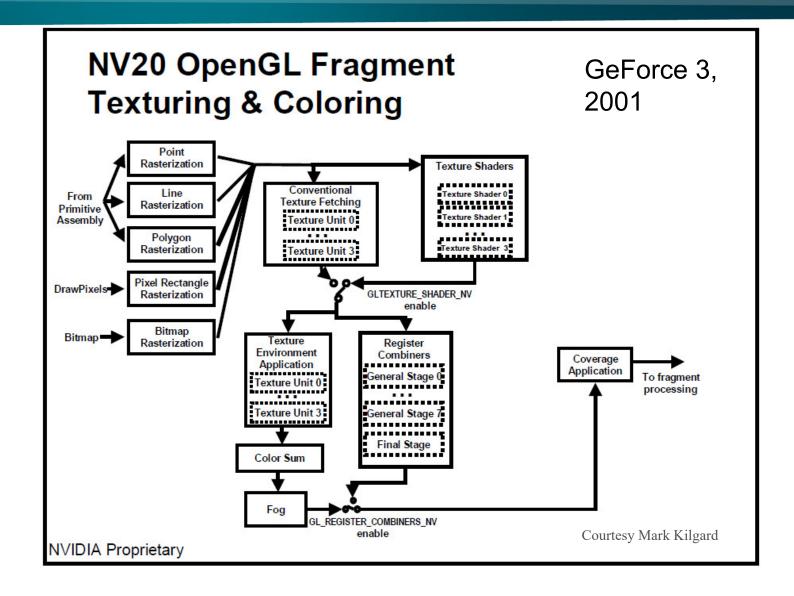




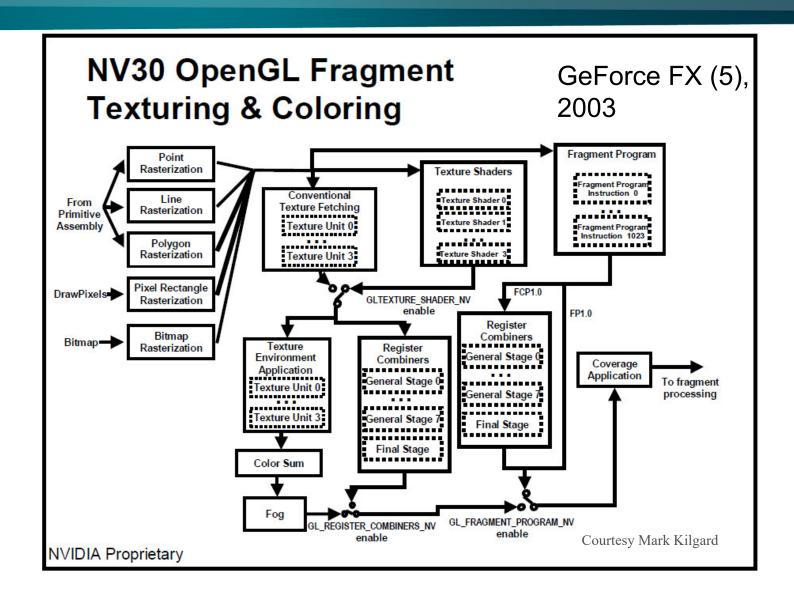






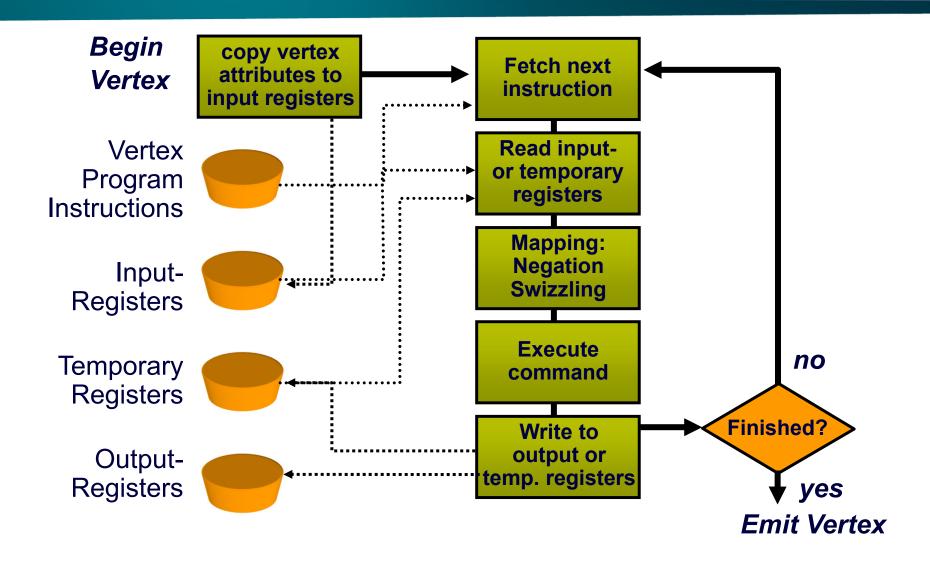






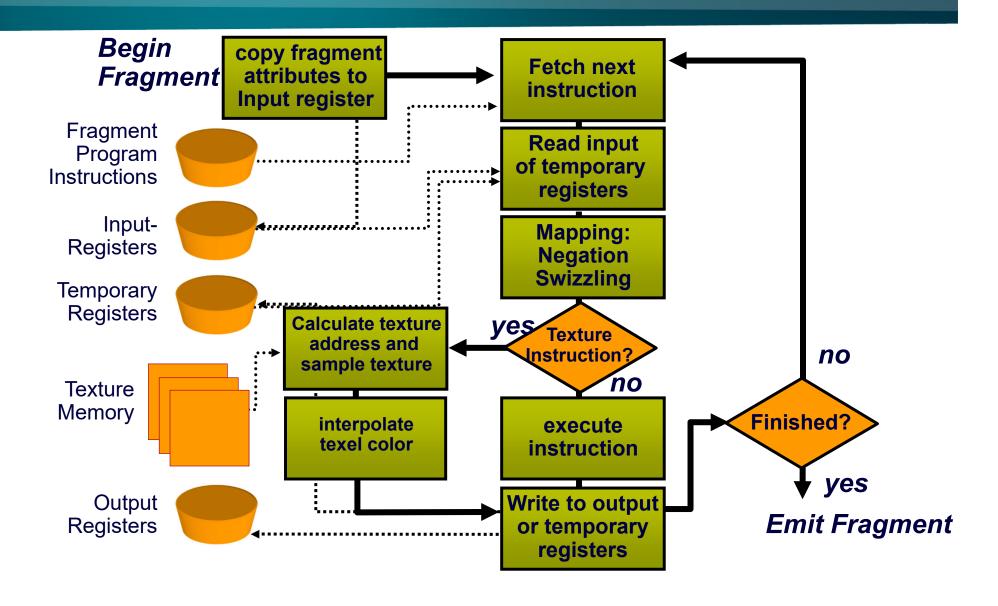
Vertex Processor





Fragment Processor





A diffuse reflectance shader

```
sampler mySamp;
Texture2D<float3> myTex;
float3 lightDir;
float4 diffuseShader(float3 norm, float2 uv)
{
  float3 kd;
  kd = myTex.Sample(mySamp, uv);
  kd *= clamp( dot(lightDir, norm), 0.0, 1.0);
  return float4(kd, 1.0);
}
```

Independent, but no explicit parallelism

