

# STM32L4 Series Ultra-low-power and performance



# STM32™ ultra-low-power at 100 DMIPS with DSP and FPU

# **ULTRA-LOW-POWER EXCELLENCE**

The STM32L4 microcontroller is based on a new ultra-low-power platform featuring FlexPowerControl which extends flexibility to reach optimized power consumptions: With an EEMBC ULPBench score of 253 ULPBench<sup>TM</sup>-CP, the STM32L4 outperforms the market in the ultra-low-power domain.

# WITH PERFORMANCE

Offering up to 1 Mbyte of Flash (dual bank) memory and up to 320 Kbytes of SRAM, the STM32L4 unleashes the ARM® Cortex®-M4 power efficiency with floating point unit (FPU) and DSP instructions.

It delivers 100 DMIPS / 273 CoreMark thanks to the ST ART Accelerator  $^{\text{TM}}$  at 80 MHz. The entire system performance is optimized using a multi-AHB bus matrix and DMA controllers.

# **OUTSTANDING LOW-POWER MODES**

| Wake-up time | VBAT 2 nA / 200 nA*     |                   |
|--------------|-------------------------|-------------------|
| 250 μs       | Shutdown 8 n            | A / 200 nA*       |
| 14 µs        | Standby                 | 28 nA / 280 nA*   |
| 14 µs        | Standby + 16-Kbytes RAM | 200 nA / 450 nA*  |
| <b>5 μs</b>  | Stop 2 (full retention) | 1.0 μΑ / 1.28 μΑ* |
| 4 μs         | Stop 1 (full retention) | 4.3 μΑ / 4.7 μΑ*  |
| 6 cycles     | Sleep                   | 10 μA / MHz**     |
|              | Run at 24 MHz           | 36 μA / MHz**     |
|              | Run at 80 MHz           | 38 μA / MHz**     |

<sup>\*</sup> without RTC / with RTC \*\* ext SMPS



# STM32L4A6 BLOCK DIAGRAM

# Connectivity

**USB OTG Crystal less,** 1x SD/SDIO/MMC, 3x SPI, 4x I2C. 2x CAN, 1x Quad SPI (Dual Flash), 5x USART + 1 x ULP UART

# **Digital**

AES (256-bit), SHA (256-bit), TRNG, 2 x SAI, DFSDM (8 channels), Camera I/F, Chrom-ART

### **Analog**

3x 16-bit ADC, 2 x DAC, 2 x comparators, 2 x Op amps 1 x Temperature sensor ARM® Cortex®-M4 CPU 80 MHz **FPU** MPU **ETM** 

**DMA** 

ART Accelerator™

Up to 1-Mbyte Flash with ECC **Dual Bank** 

320-Kbyte **RAM** 

# **Display**

LCD driver 8 x 40

### **Timers**

17 timers including: 2 x 16-bit advanced motor control timers 2 x ULP timers 7 x 16-bit-timers 2 x 32-bit timers

# I/Os

Up to 136 I/Os **Touch-sensing controller** 

## **Parallel Interface**

FSMC 8-/16-bit (TFT-LCD, SRAM, NOR, NAND)

# **HARDWARE TOOLS**



A full set of evaluation boards enables flexible prototyping as well as full STM32L4 evaluation. Commercial part numbers:

NUCLEO-L432KC (32 pins); NUCLEO-L476RG, NUCLEO-L452RE. NUCLEO-L452RE-P (64 pins):

NUCLEO-L496ZG (144 pins), NUCLEO-L496ZG-P (144 pins) and

STM32L496G-DISCO. STM32L476G-DISCO. B-I 475F-IOT01A and STM32I 476G-FVAL

# **SOFTWARE TOOLS**

STM32CubeMX enables fast development thanks to its MCU clock configurator, power consumption calculator and code generation tools.

# ST COMMUNITY

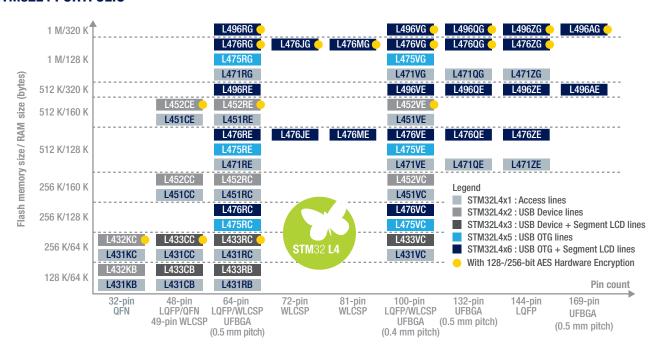


Ask, learn, share, discuss, become famous and engage with the community of STM32 enthusiasts on community.st.com

# **SMART PERIPHERALS**

- Low-power UART and I<sup>2</sup>C communication interfaces
- Low-power time counter (16-bit low-power timers)
- Up to 7 SPIs including Quad-SPI supporting software execution
- Independent peripheral communication clock separate from main system clock
- Digital filters for sigma-delta modulators supporting digital microphone (PDM to PCM) conversion w/ HW filter)

# STM32L4 PORTFOLIO



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