



Change DB9 mounting holes to pated/GND. Move ETH jack outward.
v1.3 Fix C14/C20 Silkscreen. Change solder jumpers to open. Change TOT jumper to solder
v1.2 Delete 3.3V Reg. Wire out GPIOA14/A16 to DB9
v1.1 Add built-in Dallas sensor

FET Technologies

Sheet: /
File: Duo2_Direwolf.kicad_sch

Title: NanoPi Duo2 Direwolf Carrier Board

Size: USLedger	Date: 2022-02-05	Rev: 1.3
KiCad E.D.A.	kicad (6.0.0)	Id: 1/1