Savitribai Phule Pune University
(formerly University of Pune)

TRANSCRIPT

This is to certify that MR./MS. **BHOJ VEDANT RAJAY** Mother's Name :SAAYALI (SEAT NO. B80393024) has appeared for the **BACHELOR OF ENGINEERING (ELECTRONICS & TELECOMMU.) (2008 PATTERN)** examination of this University held in MAY,2015 and is declared to have passed the said examination in **FIRST CLASS WITH DISTINCTION.**

He/She has obtained following marks at the said examination (P.R.NO.71214628J)

SUBJECT		MARKS OBTAINED
F.E. SEAT NO. F8390075 MAY, 2012		
ENGINEERING MATHEMATICS I	PP	64/100
APPLIED SCIENCE I	PP	77/100
APPLIED SCIENCE I	TW	22/25
FUNDA.OF PROGRAMMING LANGUAGES	PR	26/50
BASIC ELECTRICAL ENGINEERING	PP	58/100
BASIC ELECTRICAL ENGINEERING	TW	23/25
BASIC CIVIL & ENVIRONMENTAL ENGG.	PP	61/100
BASIC CIVIL & ENVIRONMENTAL ENGG.	TW	22/25
ENGINEERING GRAPHICS I	PP	60/100
MANUFACTURING PRACTICES	TW	21/25
ENGINEERING MATHEMATICS II	PP	55/100
APPLIED SCIENCE II	PP	71/100
APPLIED SCIENCE II	TW	22/25
ENGINEERING MECHANICS	PP	59/100
ENGINEERING MECHANICS	TW	19/25
BASIC ELECTRONICS ENGINEERING	PP	62/100
BASIC ELECTRONICS ENGINEERING	TW	22/25
ENGINEERING GRAPHICS II	TW	42/50
BASIC MECHANICAL ENGINEERING	PP	72/100
BASIC MECHANICAL ENGINEERING	TW	22/25

GRAND TOTAL: 880/1300
RESULT: FIRST CLASS WITH DISTINCTION.

SUBJECT		MARKS OBTAINED
S.E. SEAT NO. S80393027 MAY, 2013		
SIGNAL AND SYSTEMS	PP	74/100
SIGNAL AND SYSTEMS	OR	40/50
SOLID STATES DEVICES AND CIRCUITS	PP	40/100
SOLID STATES DEVICES AND CIRCUITS	PR	37/50
NETWORK ANALYSIS	PP	48/100
DIGITAL LOGIC DESIGN	PP	46/100
DIGITAL LOGIC DESIGN	PR	40/50
POWER DEVICES AND MACHINES	PP	61/100
NETWORK AND POWER LAB.	TW	42/50
ELECTRONIC INSTRUMENTS AND TOOLS	TW	41/50
ENGINEERING MATHEMATICS III	PP	93/100
ENGINEERING MATHEMATICS III	TW	23/25
INTEGRATED CIRCUITS APPLICATIONS	PP	57/100
INTEGRATED CIRCUITS APPLICATIONS	PR	43/50
ELECTROMAGNETIC	PP	60/100
ELECTROMAGNETIC	TW	23/25
DATA STRUCTURES	PP	74/100
DATA STRUCTURES	PR	30/50
COMMUNICATION THEORY	PP	51/100
COMMUNICATION THEORY	OR	41/50
CIRCUIT SIMULATION AND TOOLS	TW	45/50

GRAND TOTAL: 1009/1500
RESULT: FIRST CLASS WITH DISTINCTION.

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Telephone Nos.
020 - 25601221
020 - 25601207
020 - 25601217
Fax : 020 - 25691341

Ganeshkhind,
PUNE-411007.
Web : *www.unipune.ac.in*
e-mail : *certificate@unipune.ac.in*

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T.E. SEAT NO. T80393027 MAY, 2014

Subject	Type	Marks
CONTROL SYSTEMS	PP	64/100
DIGITAL COMMUNICATION	PP	40/100
DIGITAL COMMUNICATION	PR	44/50
NETWORK SYNTHESIS & FILTER DESIGN	PP	70/100
NETWORK SYNTHESIS & FILTER DESIGN	TW	45/50
MICROCONTROLLERS & APPLICATION	PP	60/100
MICROCONTROLLERS & APPLICATION	PR	43/50
DIGITAL SIGNAL PROCESSING	PP	68/100
DIGITAL SIGNAL PROCESSING	OR	32/50
ELECTRONIC DESIGN PRACTICE	OR	42/50
SIGNAL CODING & ESTIMATION THEORY	PP	67/100
SIGNAL CODING & ESTIMATION THEORY	PR	34/50
SYSTEM PROGRA.& OPERATING SYS.	PP	70/100
SYSTEM PROGRA.& OPERATING SYS.	TW	40/50
COMPUTER ORGANIZATION & ARCHITEC	PP	55/100
INDUSTRIAL MANAGEMENT	PP	63/100
WAVE THEORY & ANTENNA	PP	66/100
WAVE THEORY & ANTENNA	PR	38/50
MINI PROJECT & SEMINAR	OR	46/50
TEST & MEASUREMENT TECHNIQUES	OR	39/50

GRAND TOTAL: 1026/1500

RESULT: FIRST CLASS WITH DISTINCTION.

B.E. SEAT NO. B80393024 MAY, 2015

Subject	Type	Marks
ELCTRONICS PRODUCT DESIGN	PP	60/100
ELCTRONICS PRODUCT DESIGN	TW	23/25
VLSI DESIGN & TECHNOLOGY	PP	58/100
VLSI DESIGN & TECHNOLOGY	PR	41/50
COMPUTER NETWORK	PP	67/100
COMPUTER NETWORK	OR	41/50
DIGITAL IMAGE PROCESSING	PP	66/100
DIGITAL IMAGE PROCESSING	TW	23/25
DIGITAL IMAGE PROCESSING	PR	43/50
MOBILE COMMUNICATION	PP	53/100
PROJECT (PART-1)	TW	44/50
TELECOMM. & SWITCHING SYSTEM	PP	54/100
TELECOMM. & SWITCHING SYSTEM	OR	38/50
OPTICAL FIBER COMMUNICATION	PP	73/100
OPTICAL FIBER COMMUNICATION	TW	23/25
OPTICAL FIBER COMMUNICATION	PR	42/50
SOFT COMPUTING	PP	85/100
SOFT COMPUTING	TW	24/25
SOFT COMPUTING	PR	41/50
ADV SATELLITE SYS & APPLICATION	PP	72/100
PROJECT II	TW	97/100
PROJECT II	OR	48/50

GRAND TOTAL: 1116/1500

RESULT: FIRST CLASS WITH DISTINCTION.

MEDIUM OF INSTRUCTION : ENGLISH

PP-PAPER, PR-PRACTICAL,TW-TERM WORK, OR-ORAL

REF.NO.:EXAM./CERTI./2016/6181
DATE:03/06/2016

for CONTROLLER OF EXAMINATION.