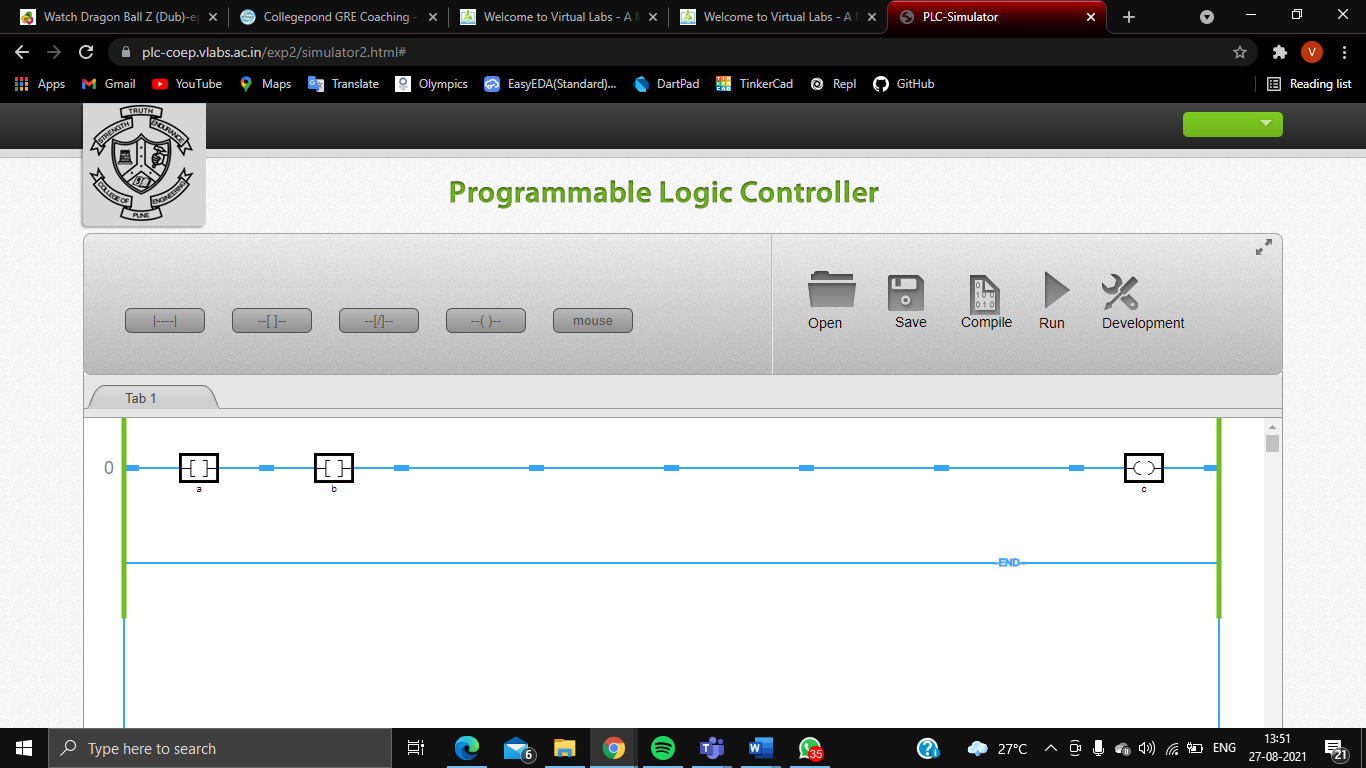
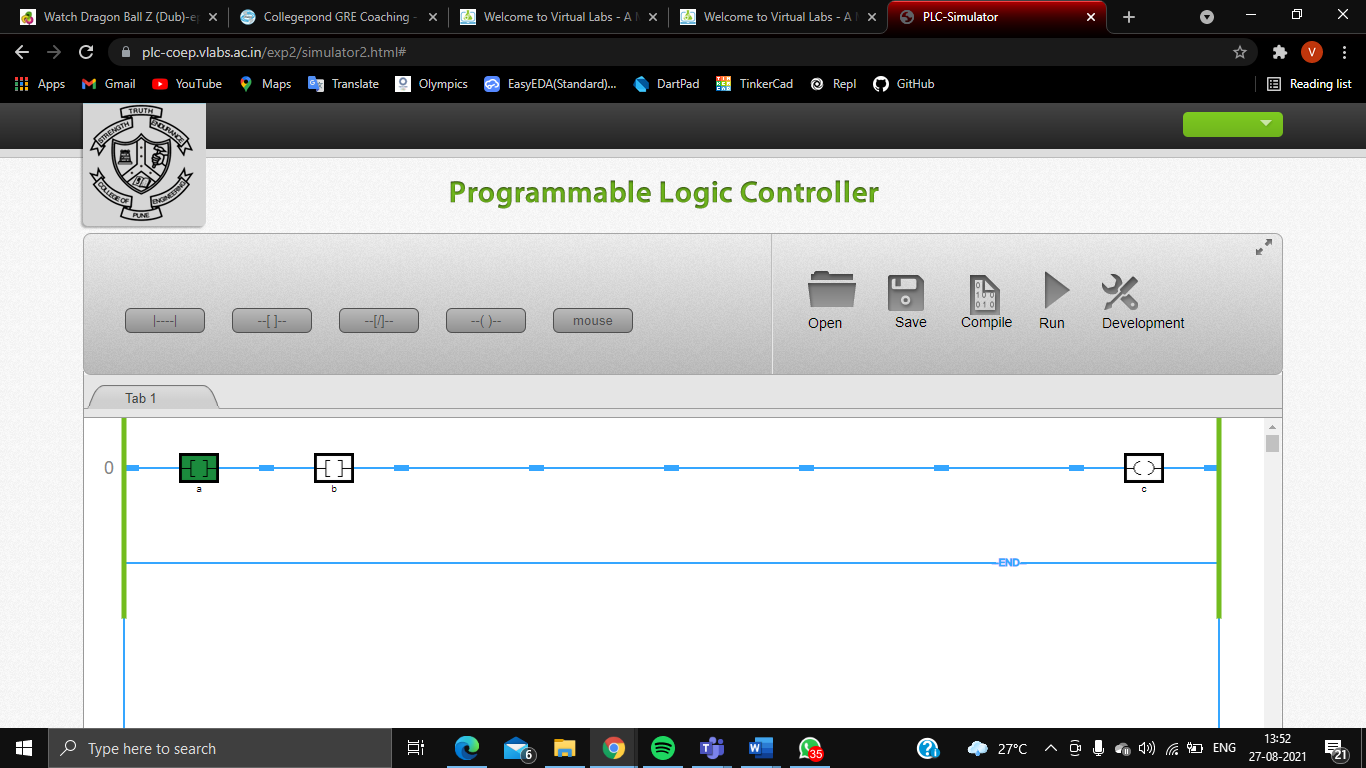
AND

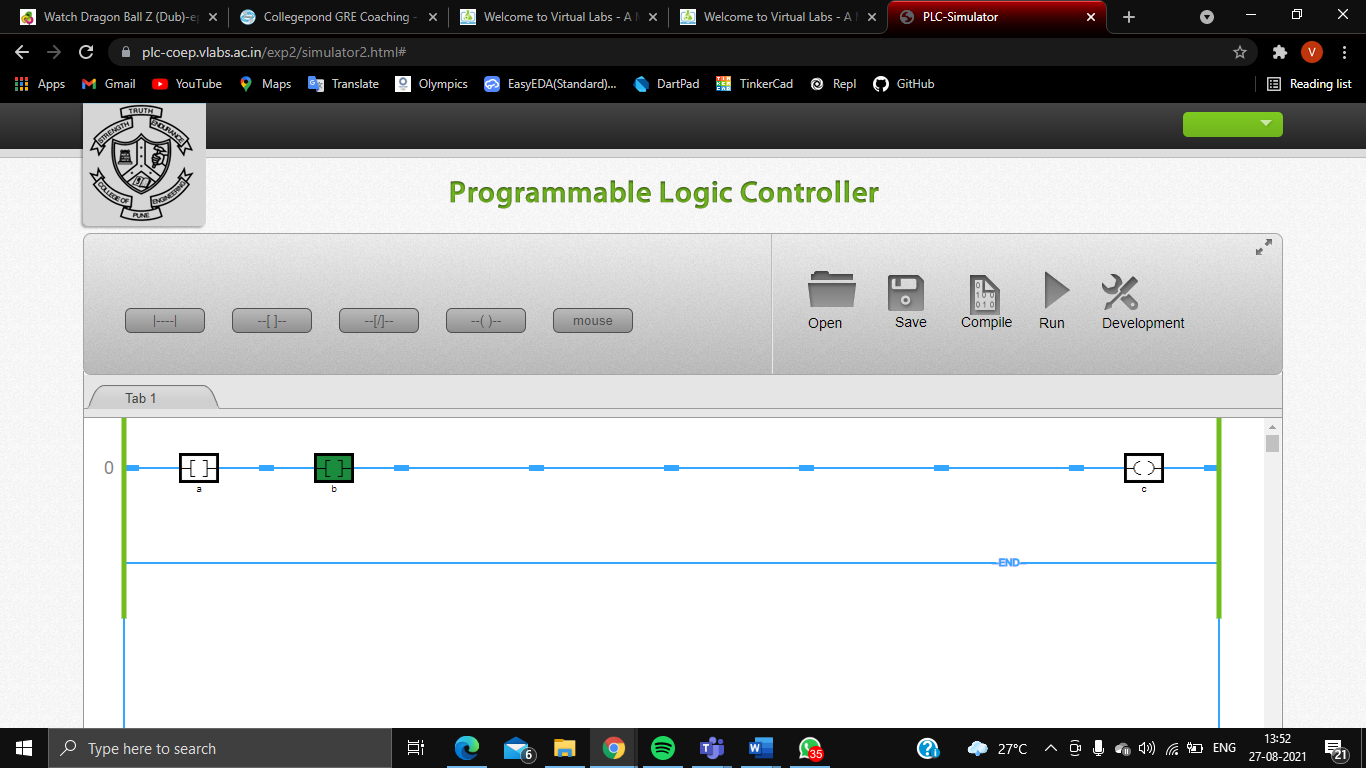
(0,0)

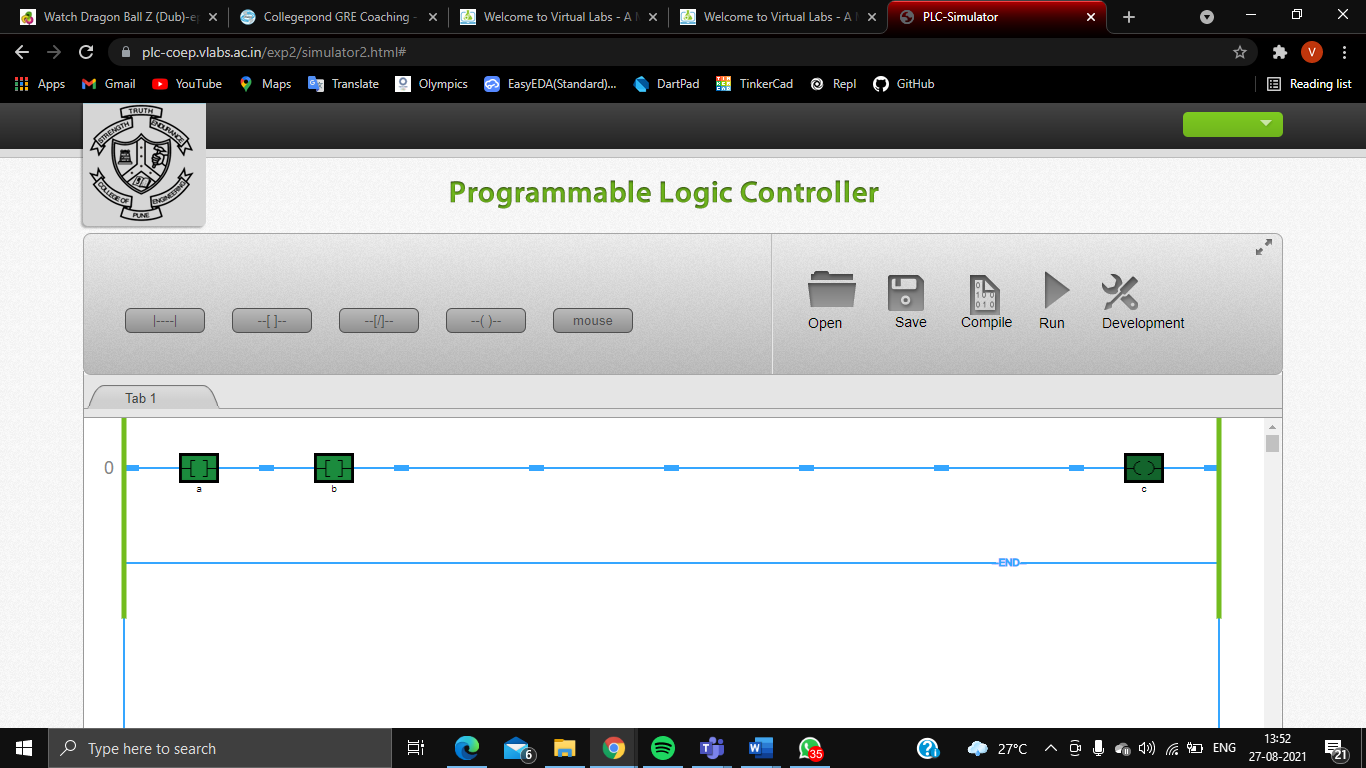


(1,0)



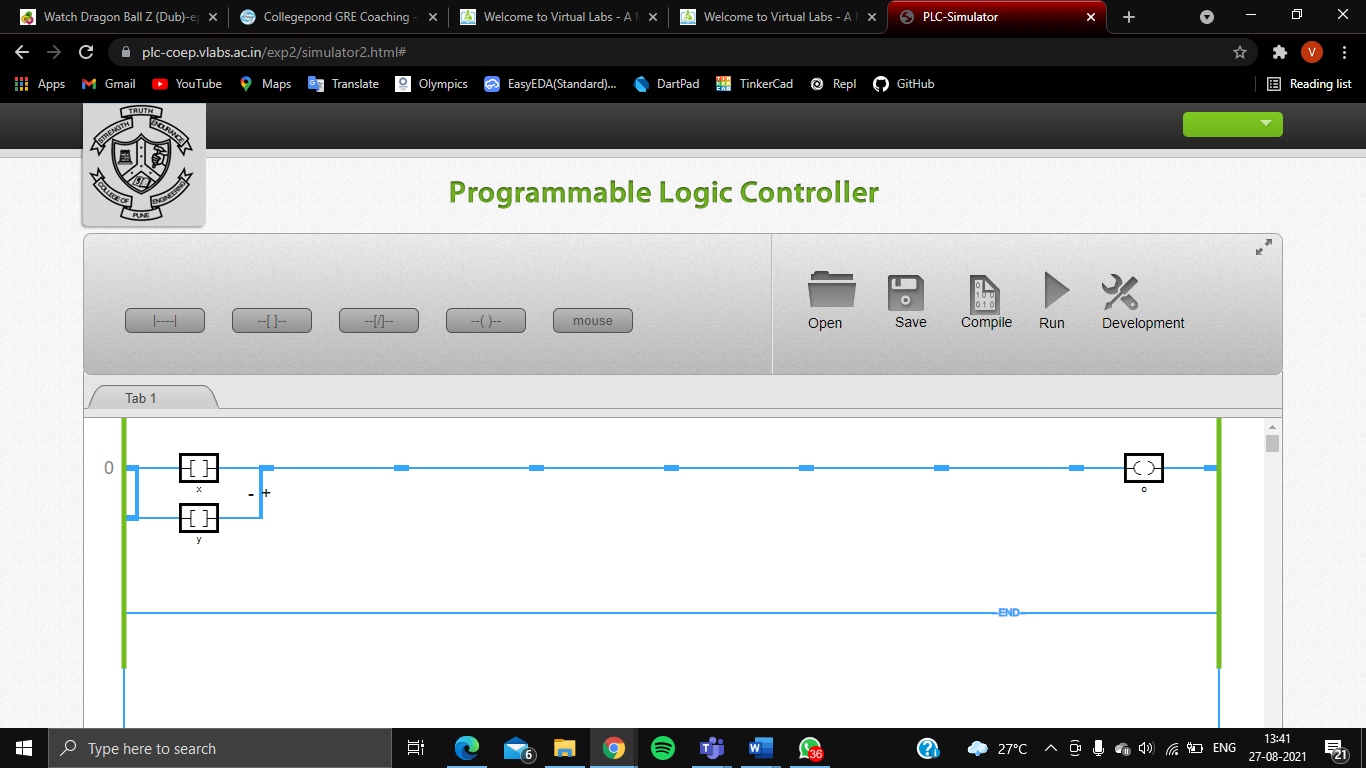
(0,1)

(1,1)

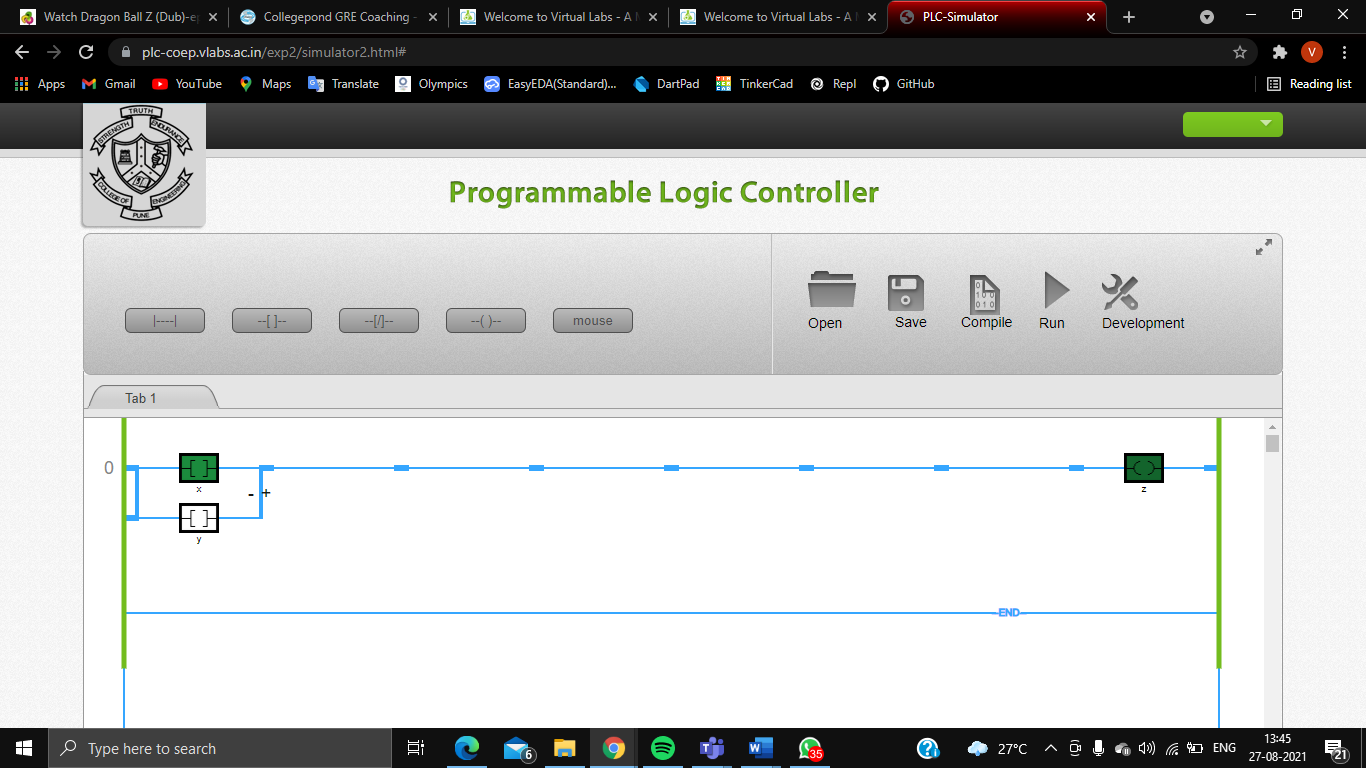


OR

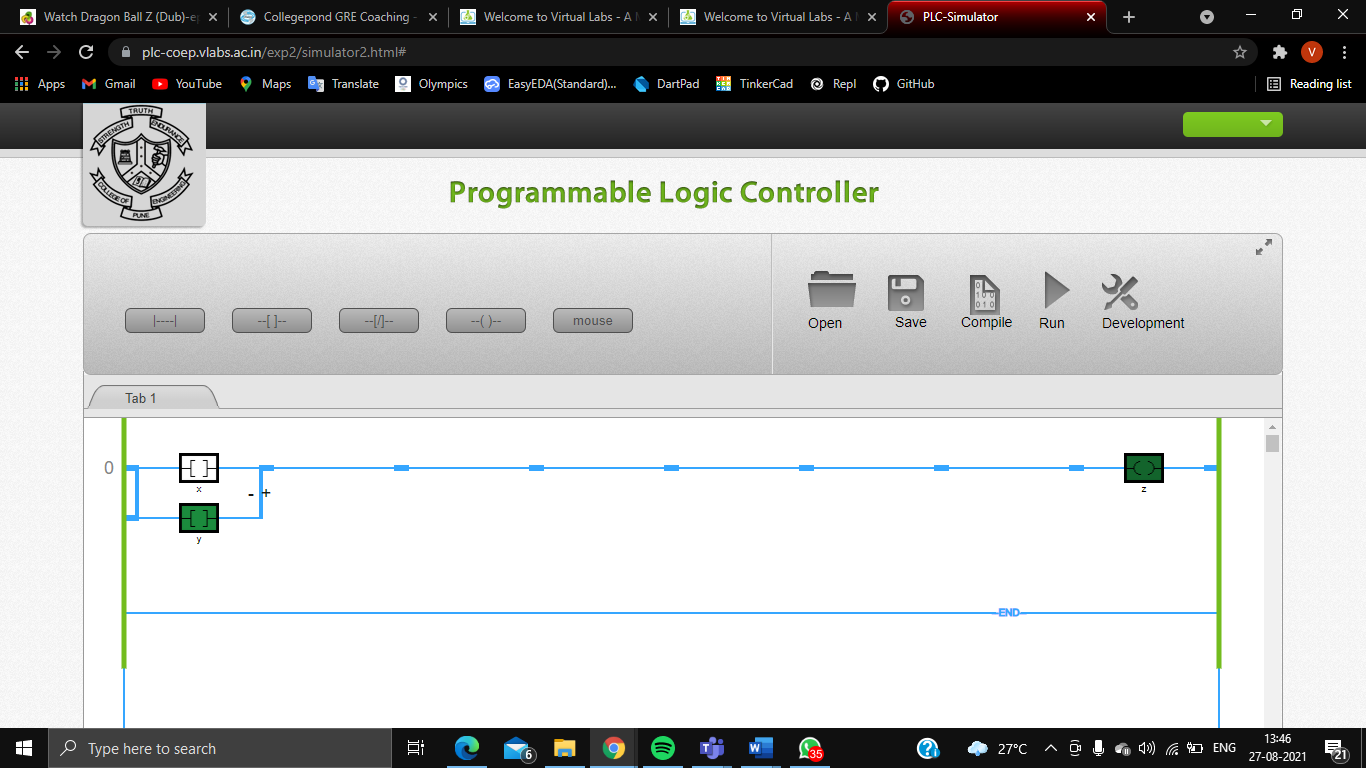
(0,0)



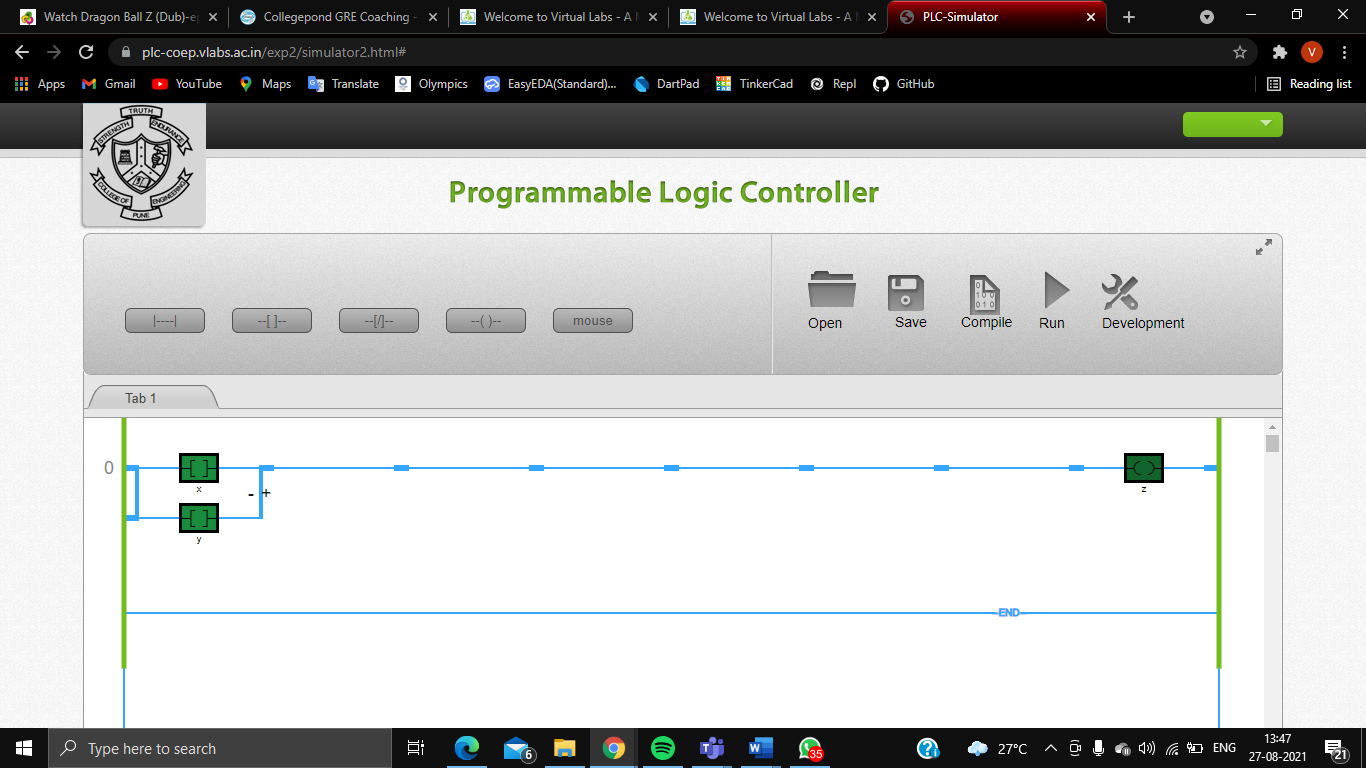
(1,0)



(0,1)

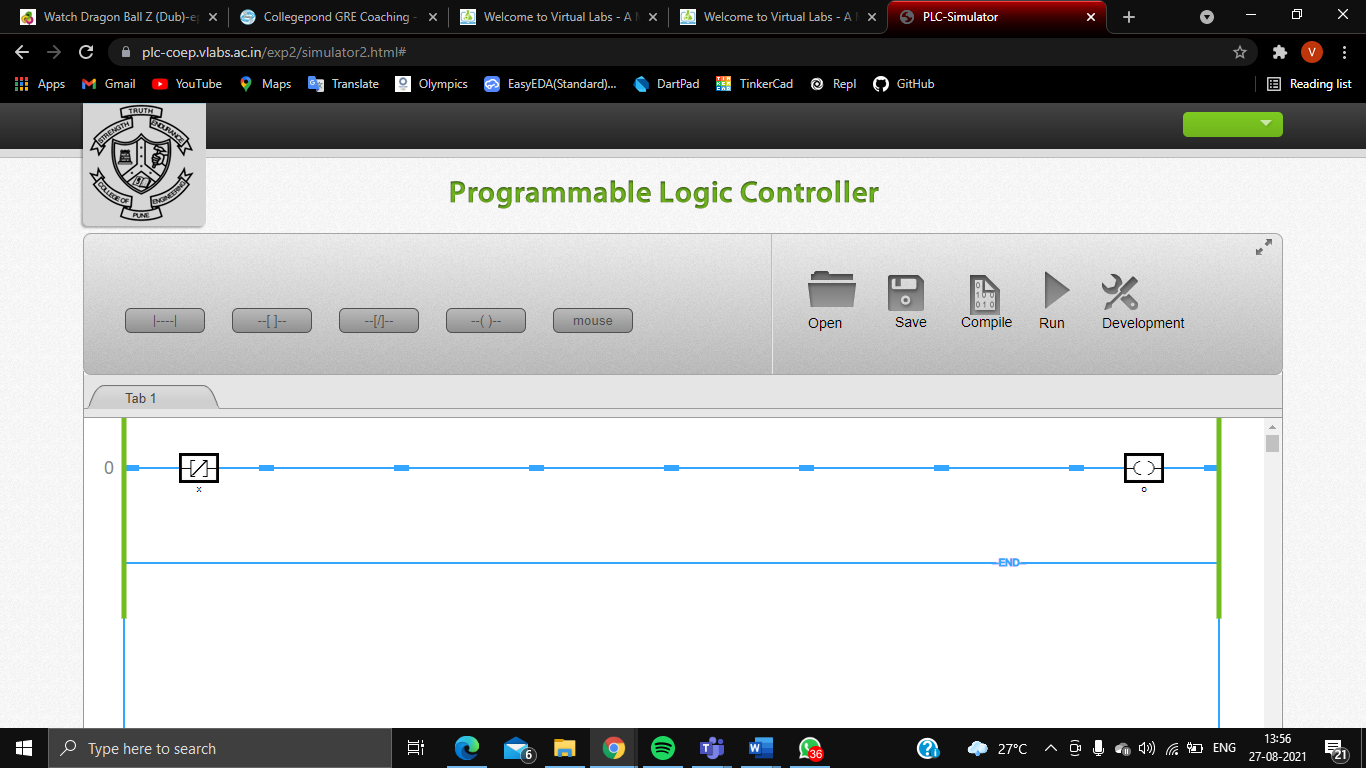


(1,1)

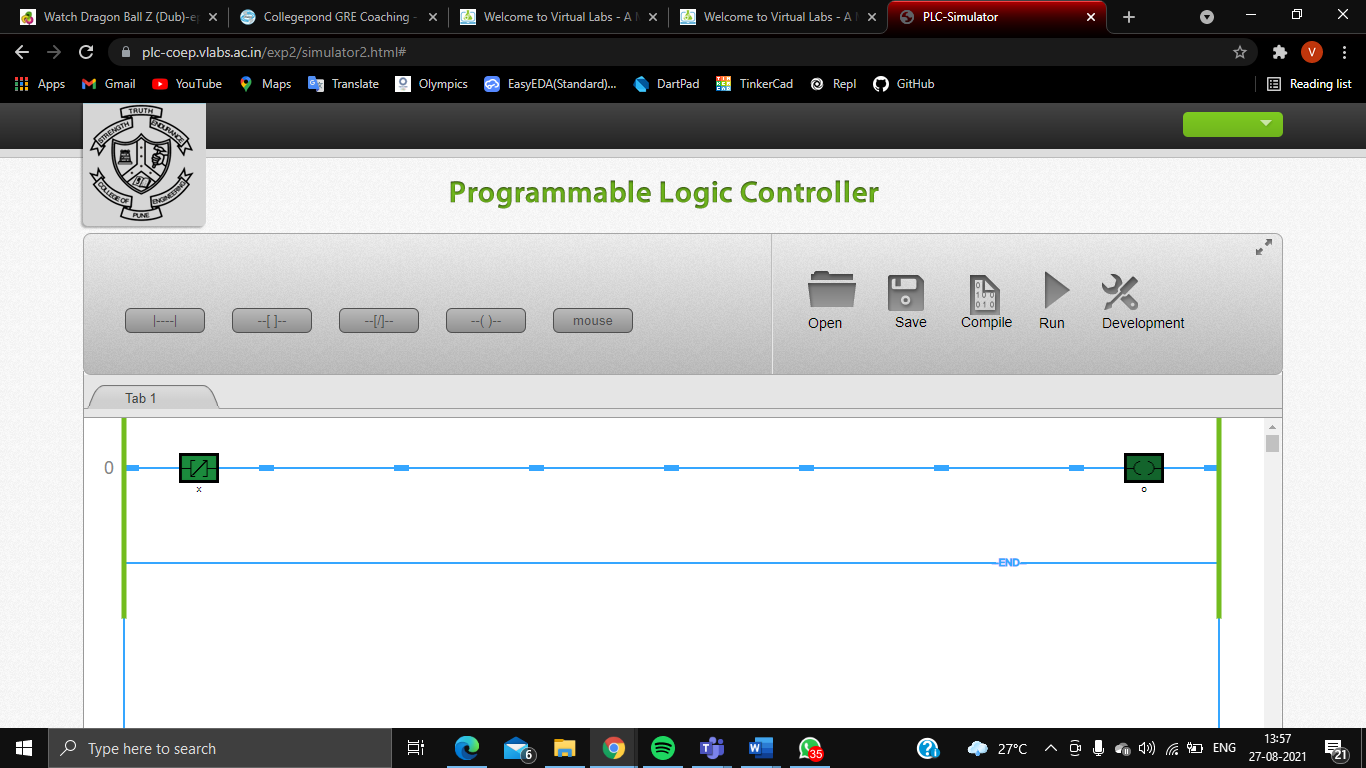


NOT

1

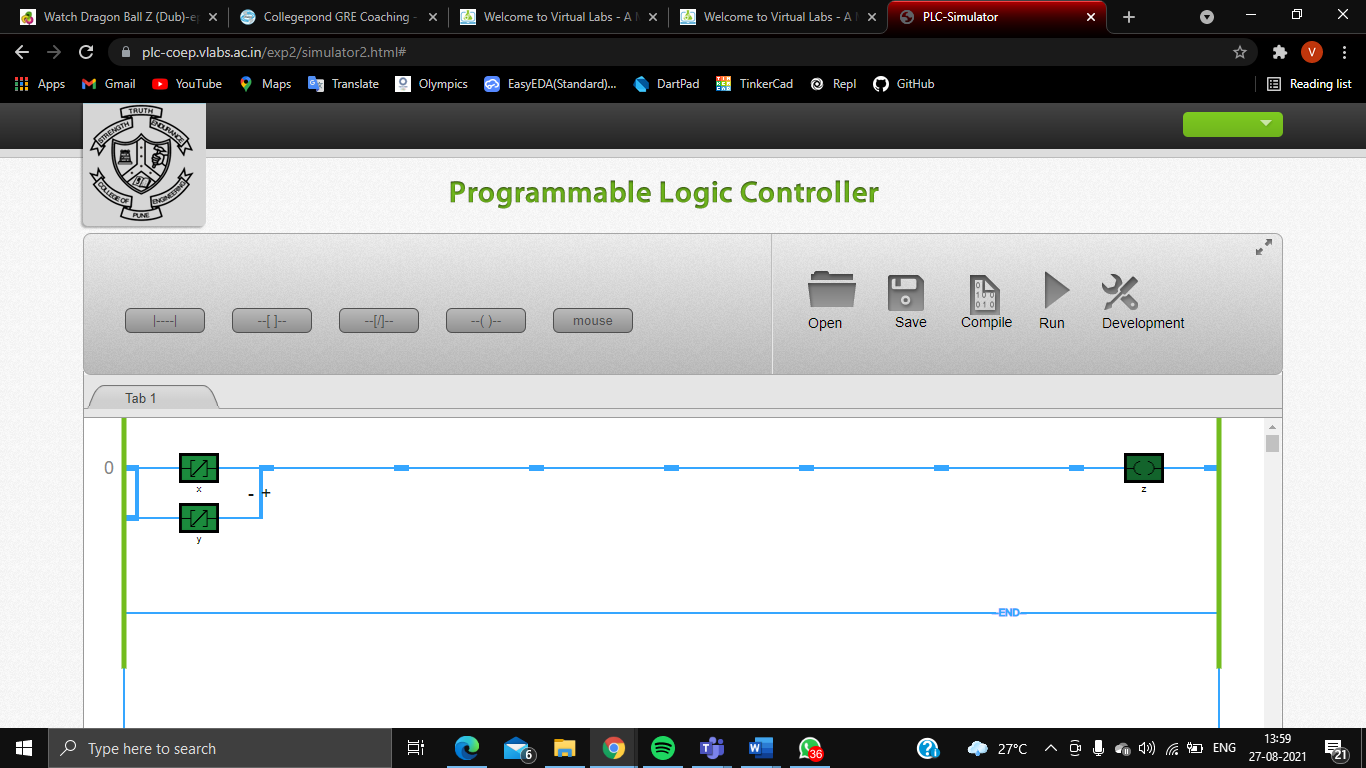


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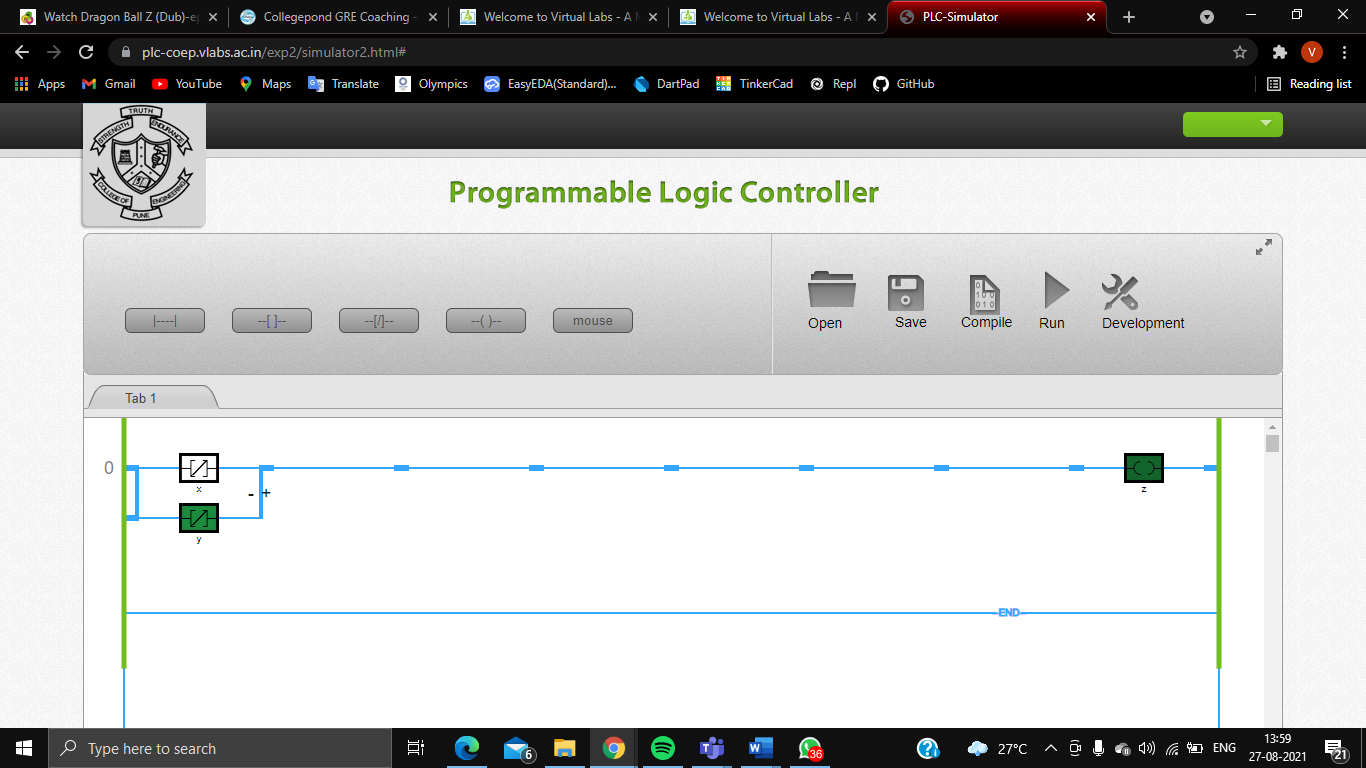


NAND

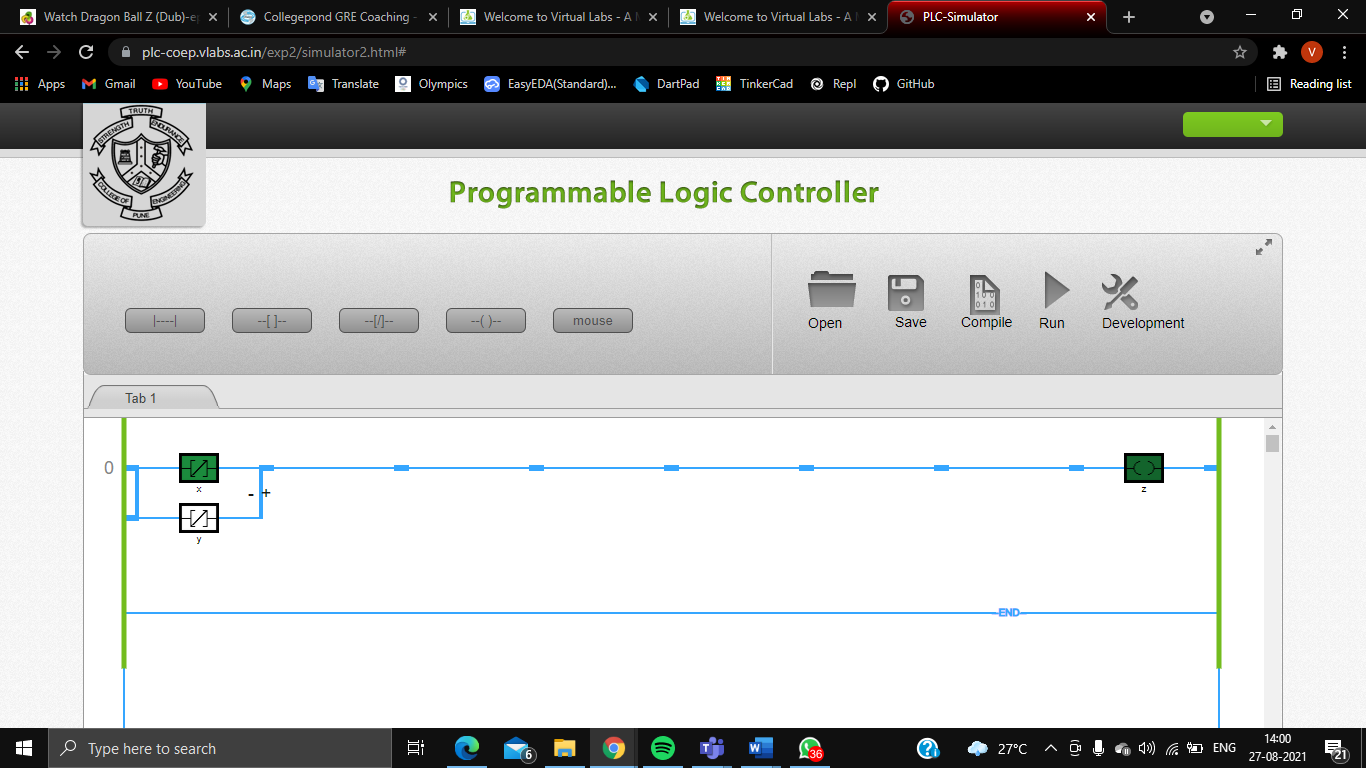
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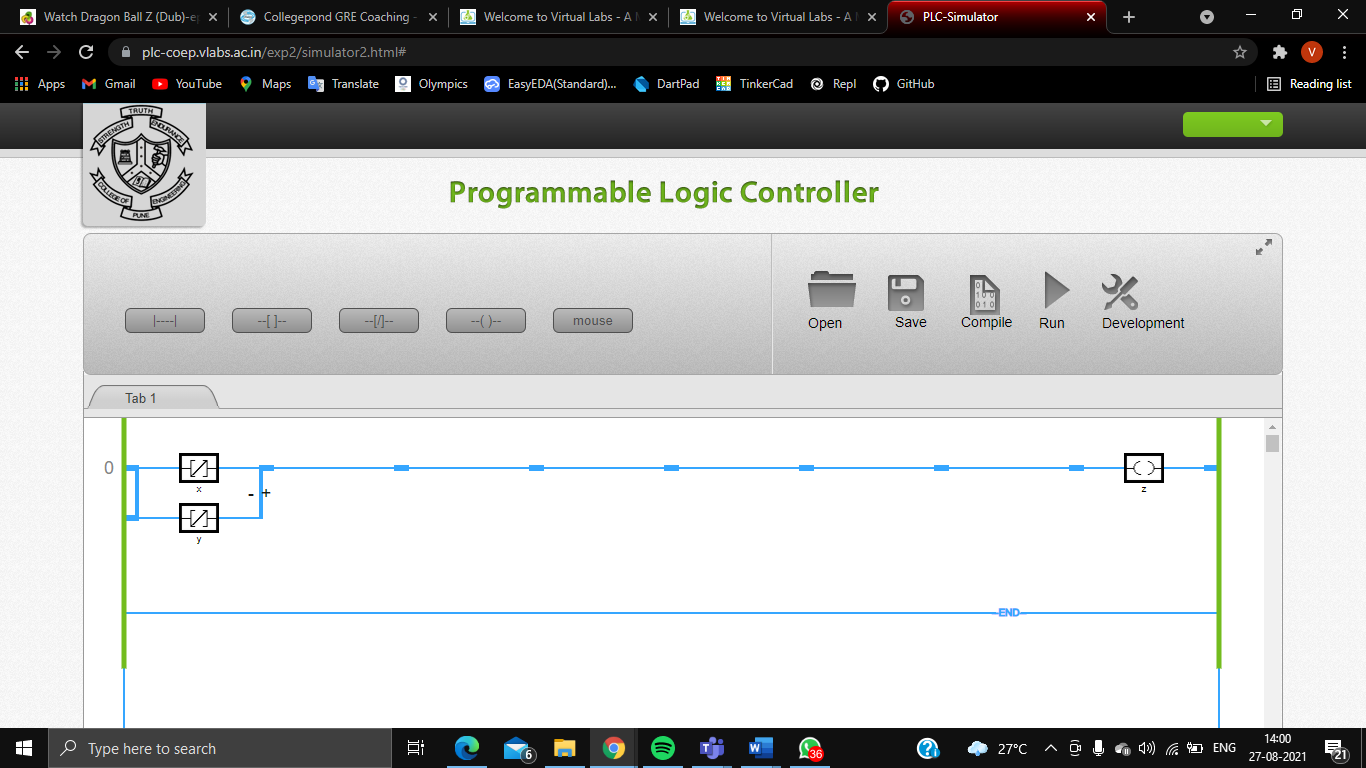
(0,1)



(1,0)

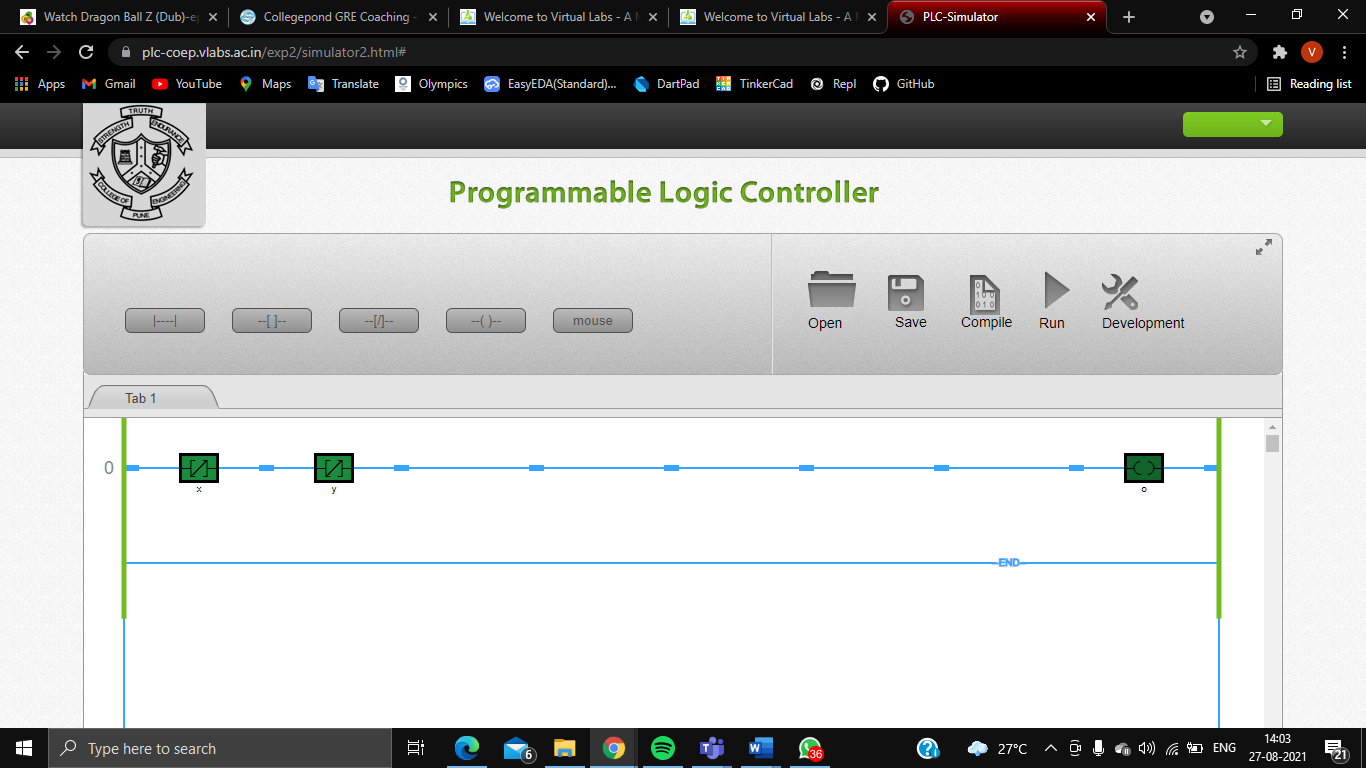


(1,1)

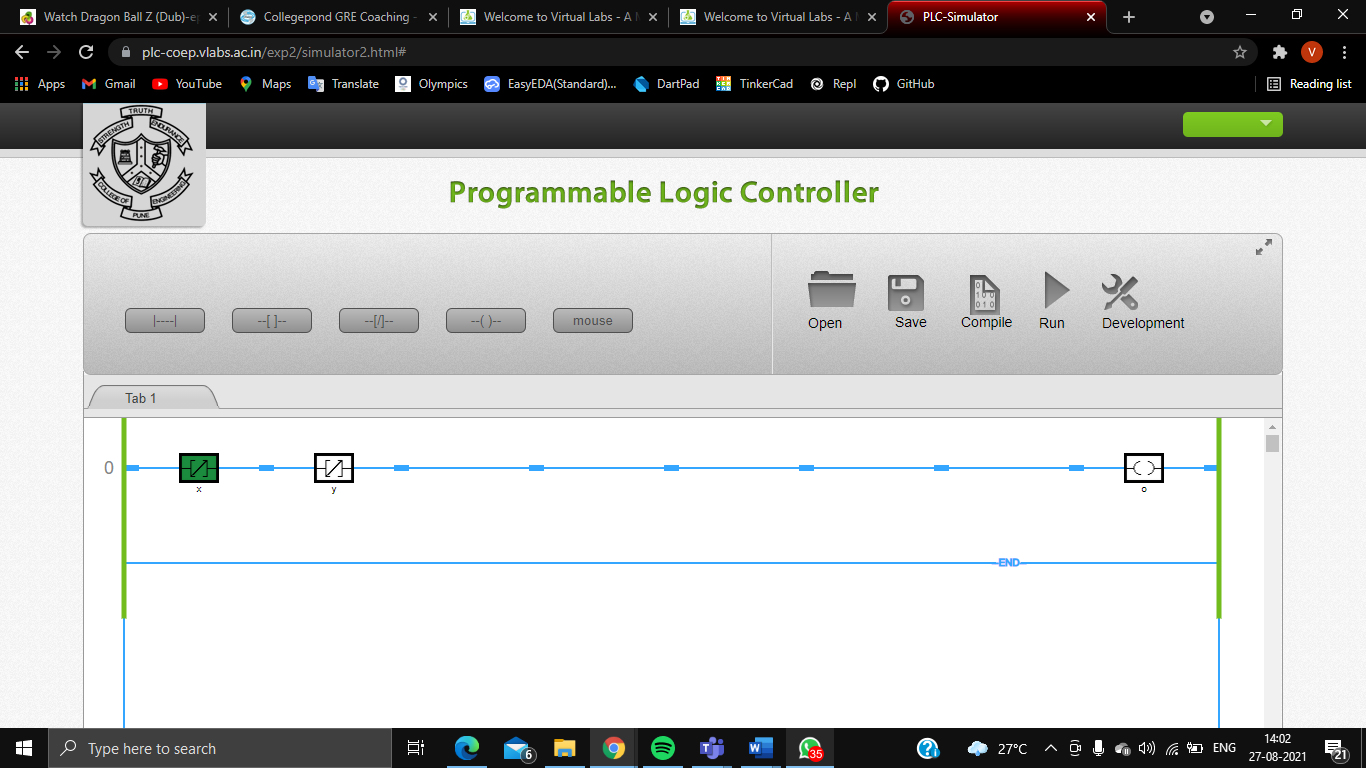


NOR

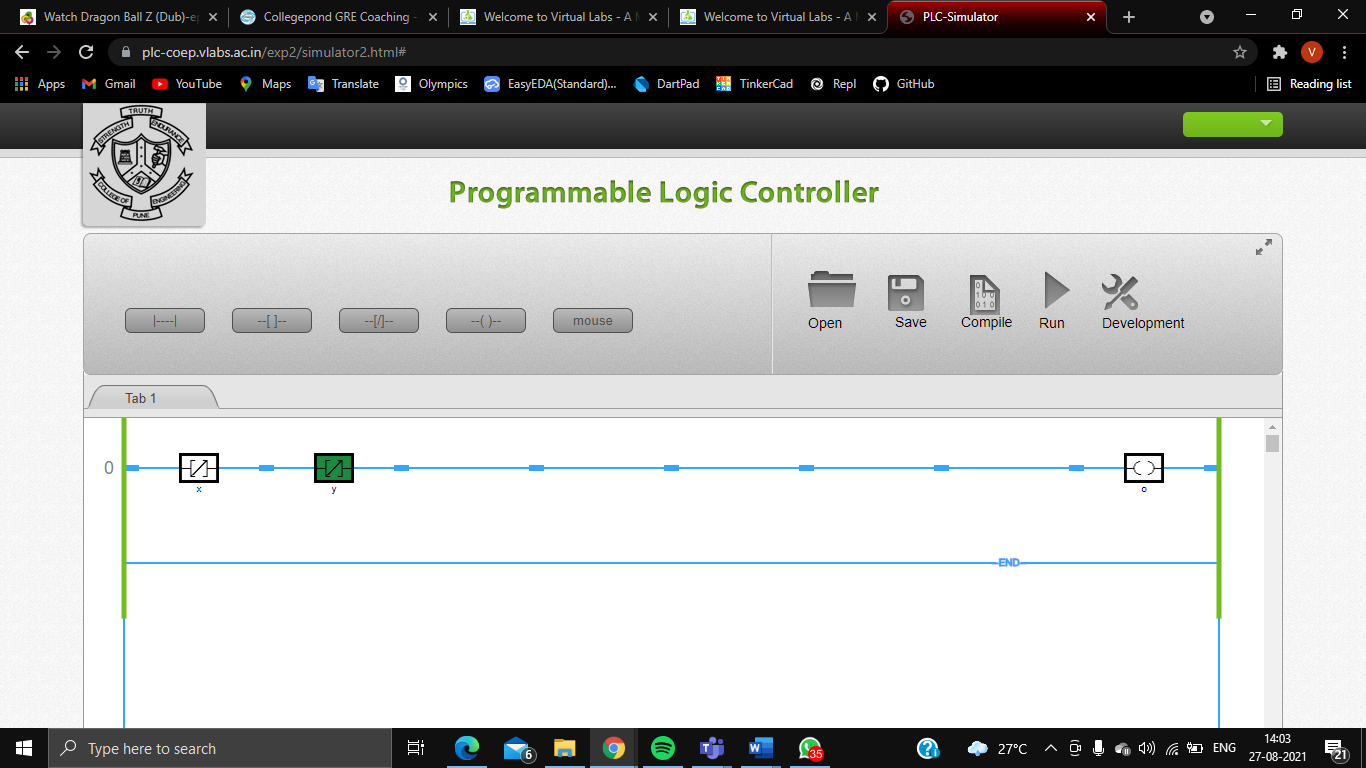
(0,0)



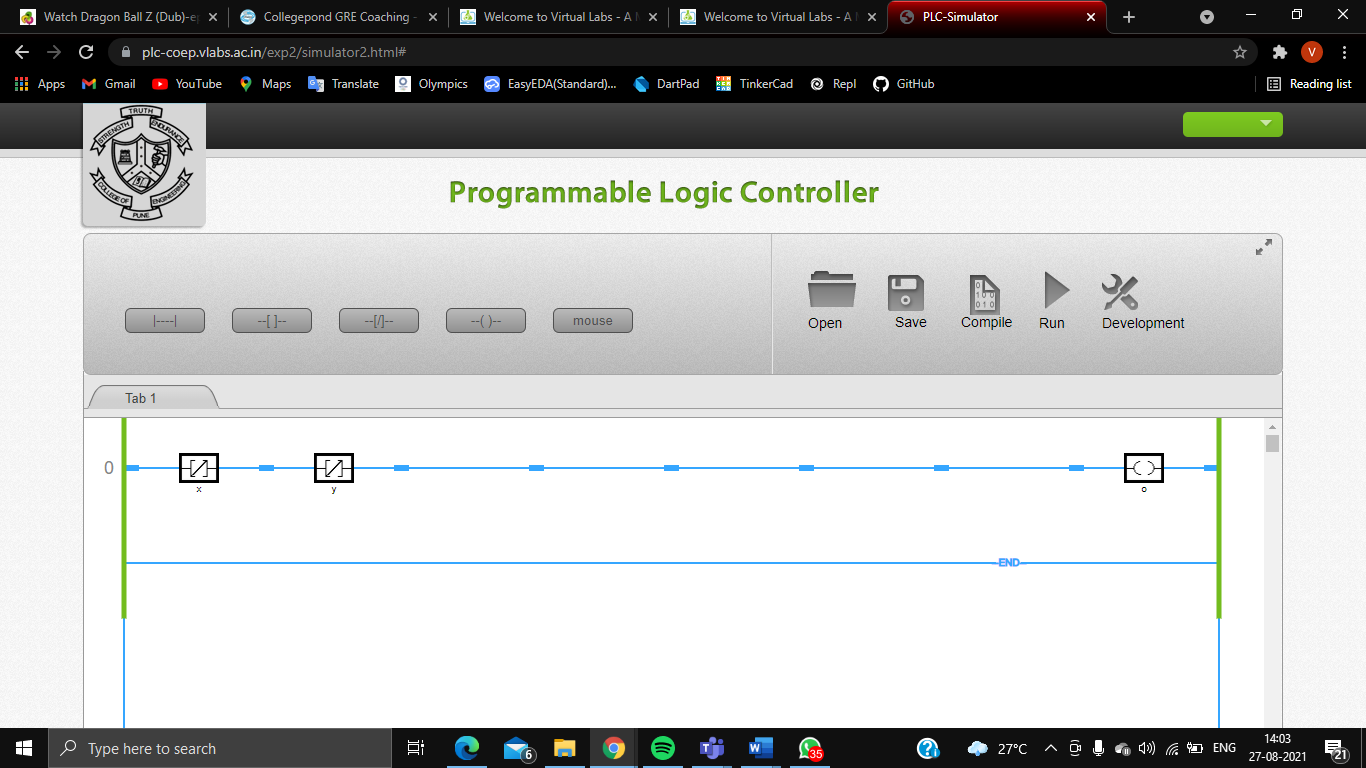
(0,1)



(1,0)

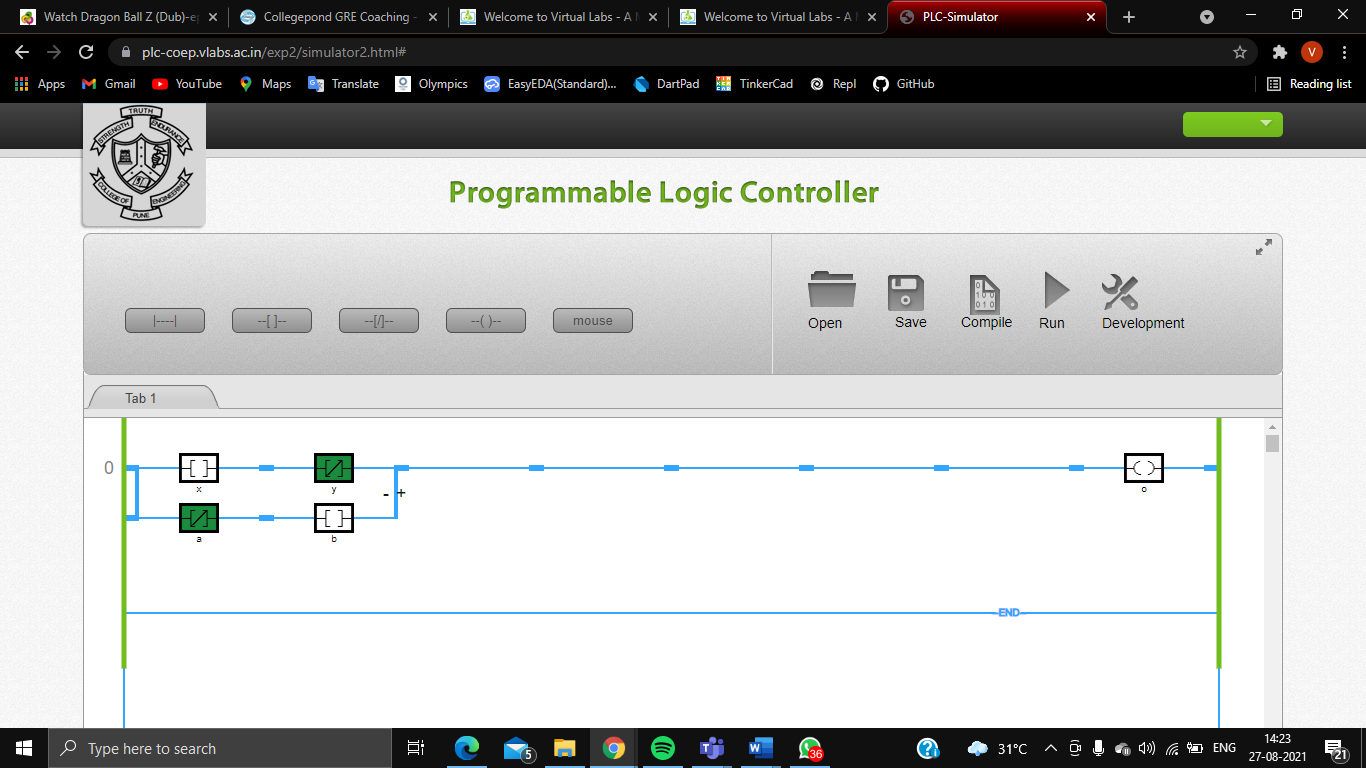


(1,1)

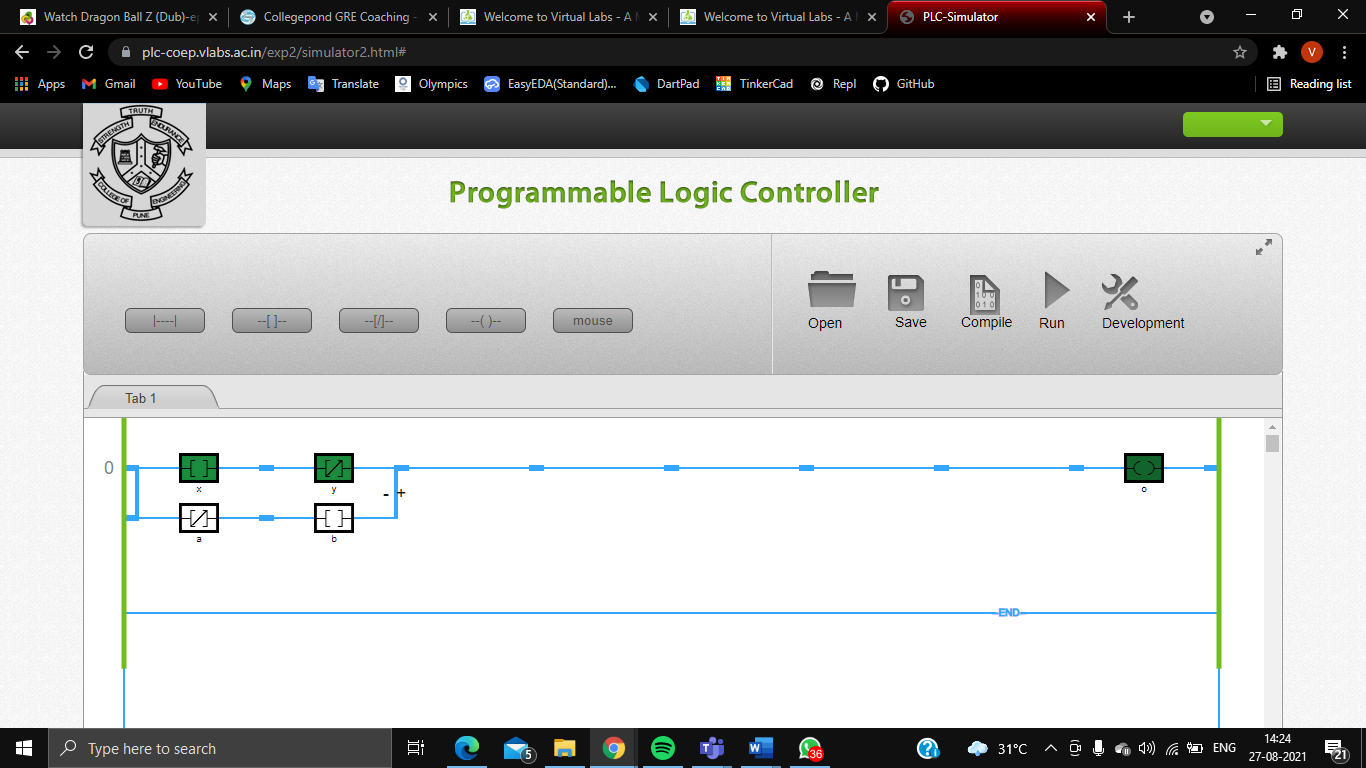


XOR

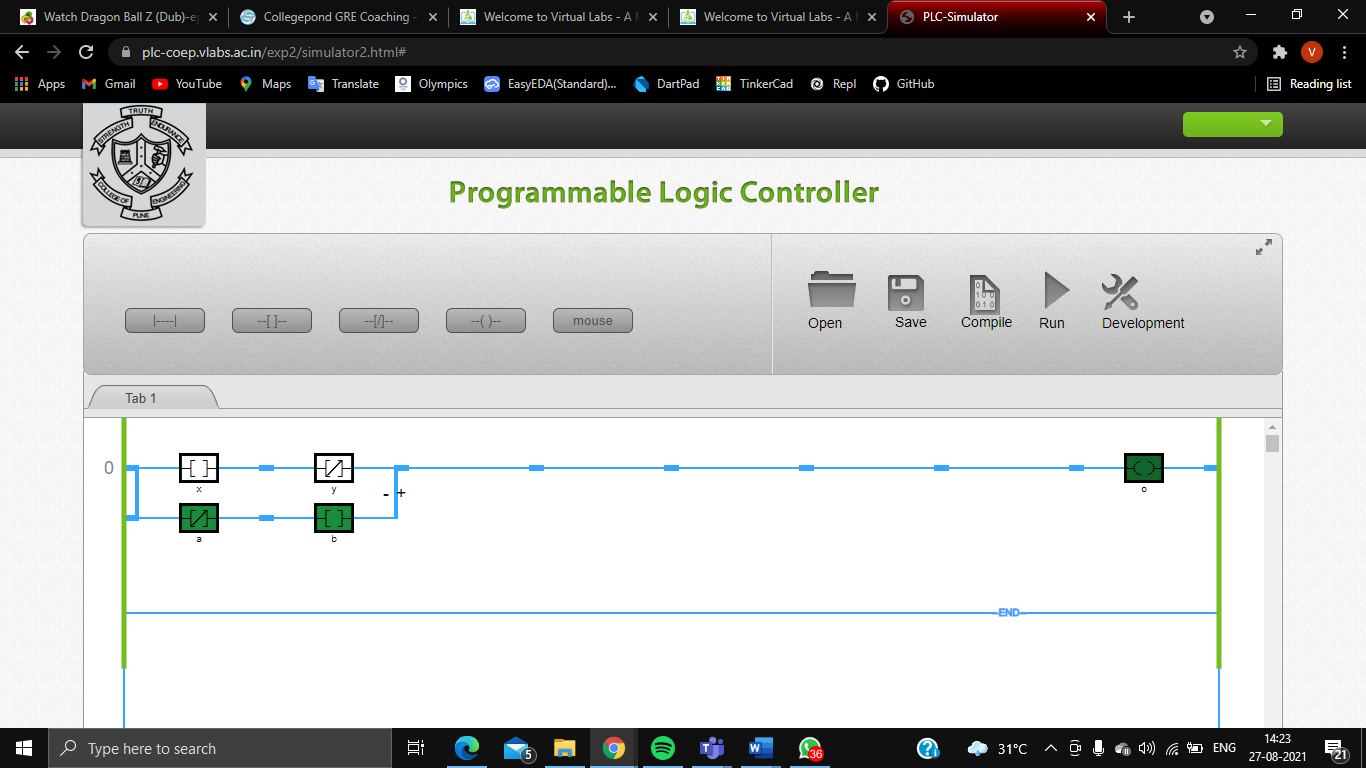
(0,0)



(1,0)



(0,1)



(1,1)

