

Audit Course Session 9

AUDIT COURSE ELECTRONIC CIRCUITS 2: SIMULATION BASED STUDY

Kindly update your name and roll no, once this document is shared with you

Time slot to complete your work is **40 MINUTES**

Date: 16/2/2021

Kindly upload your schematic & waveform images here, every 10 minutes, indicating your progress and intention to completion of WORK within time slot allotted

Time slot allotted to you all for the completion of Session 9 is 40 MINUTES

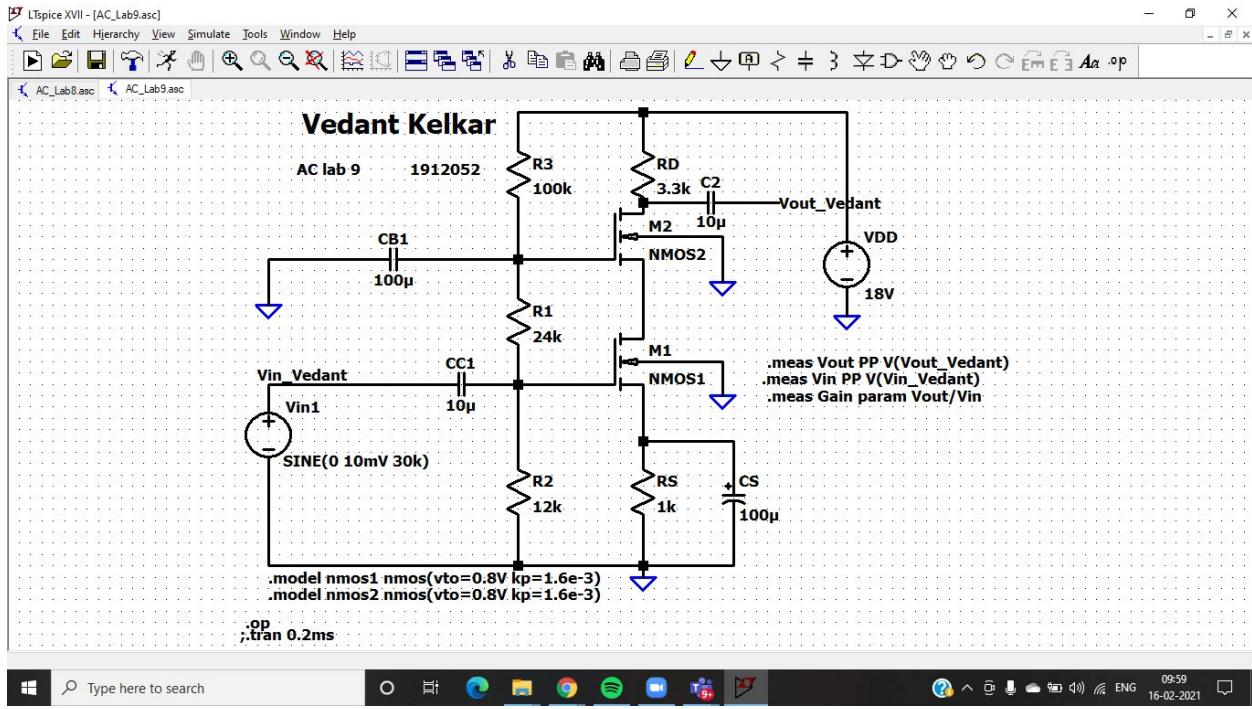
Kindly upload your work (only circuit schematic & waveform in LTSpice) in the shared google doc between this time slot only.

Follow these instruction strictly:

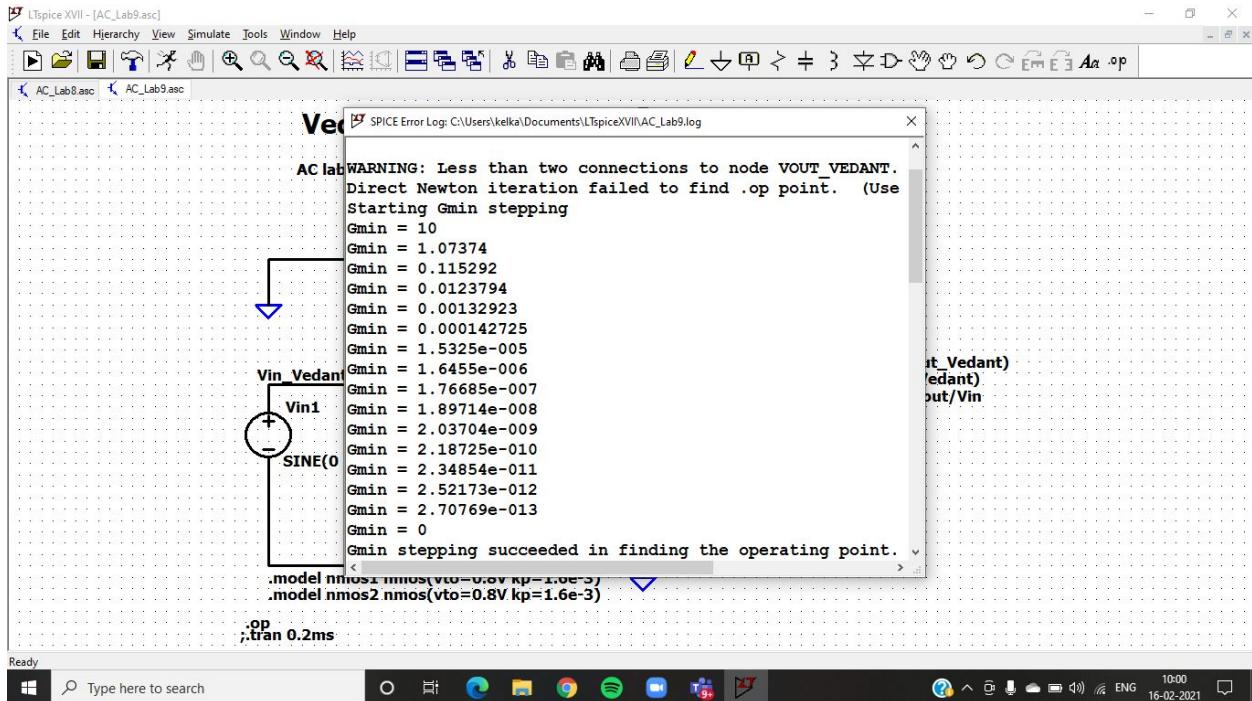
- 1, Start sharp ON TIME, by posting your name and roll no and **screenshot of your LT spice work screen (time and date MUST BE VISIBLE)**
2. Upload your work every 10 minutes, i.e LT spice work screen
3. This means you will upload LT spice work screen 4 times during this time slot.
4. Point 3 indicates your readiness and presences for completion of Session 9

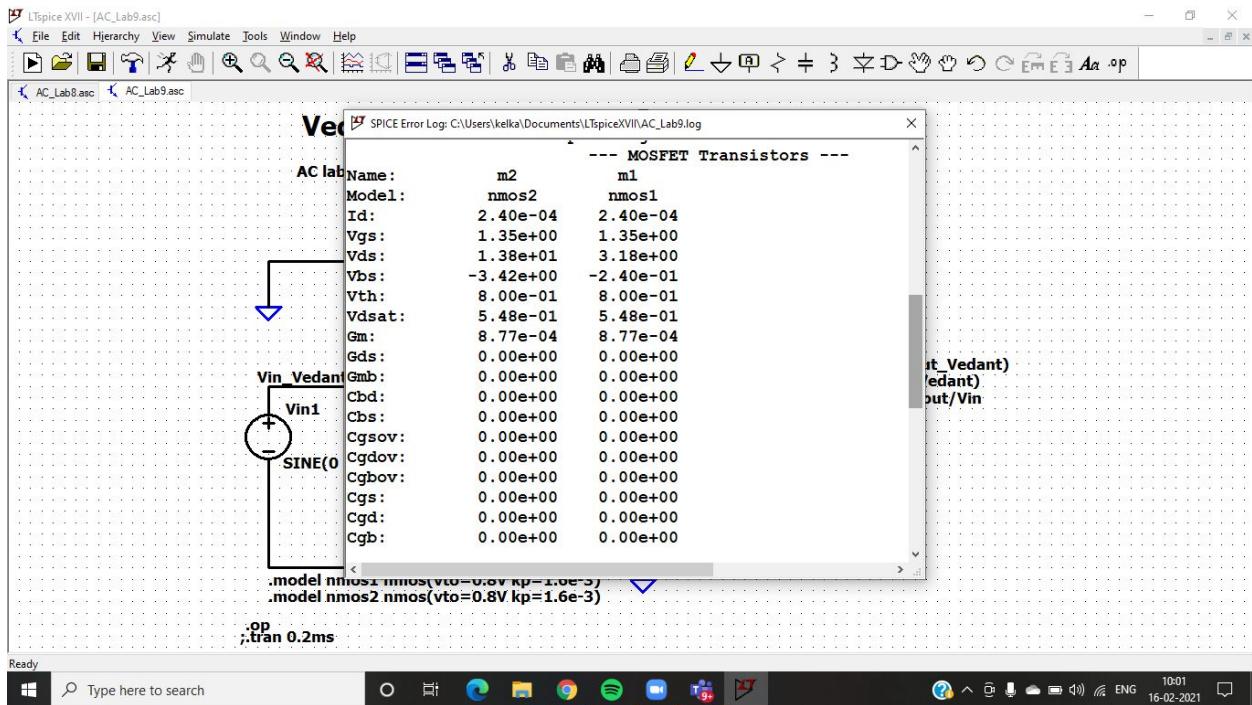
You are entitled for 10 points per session only if you follow above instruction to the details

STUDENTS WORK AREA STARTS HERE

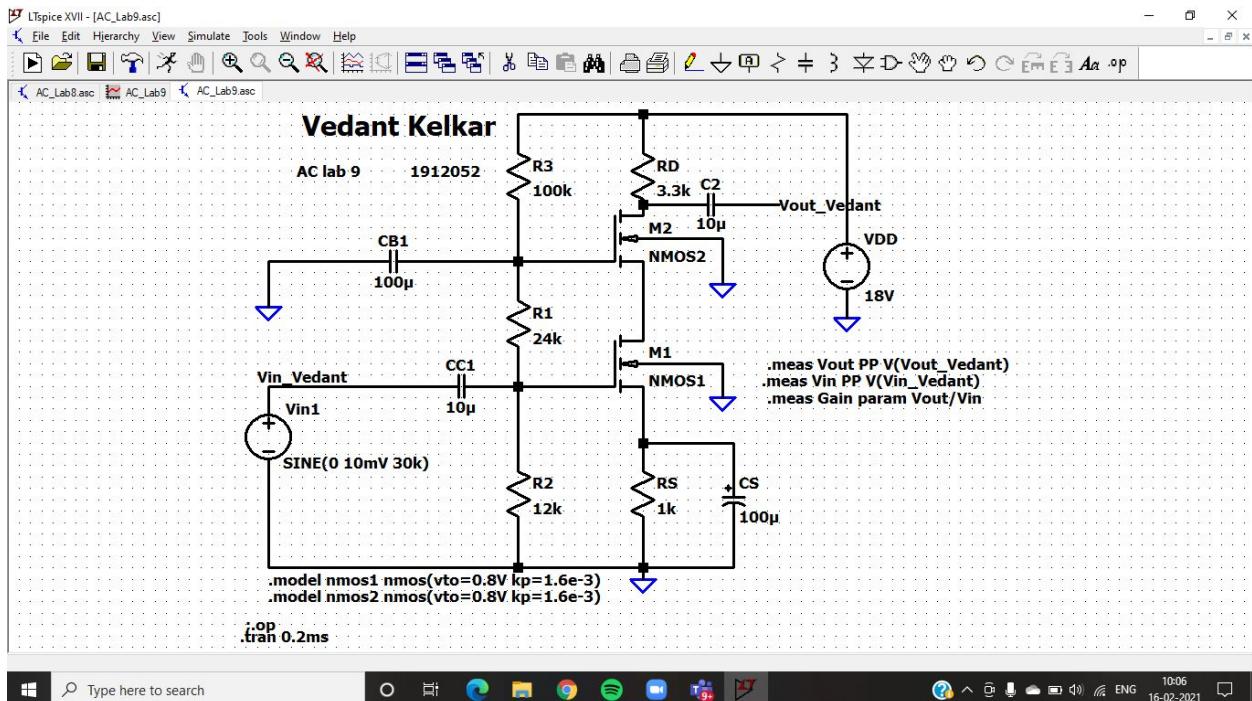


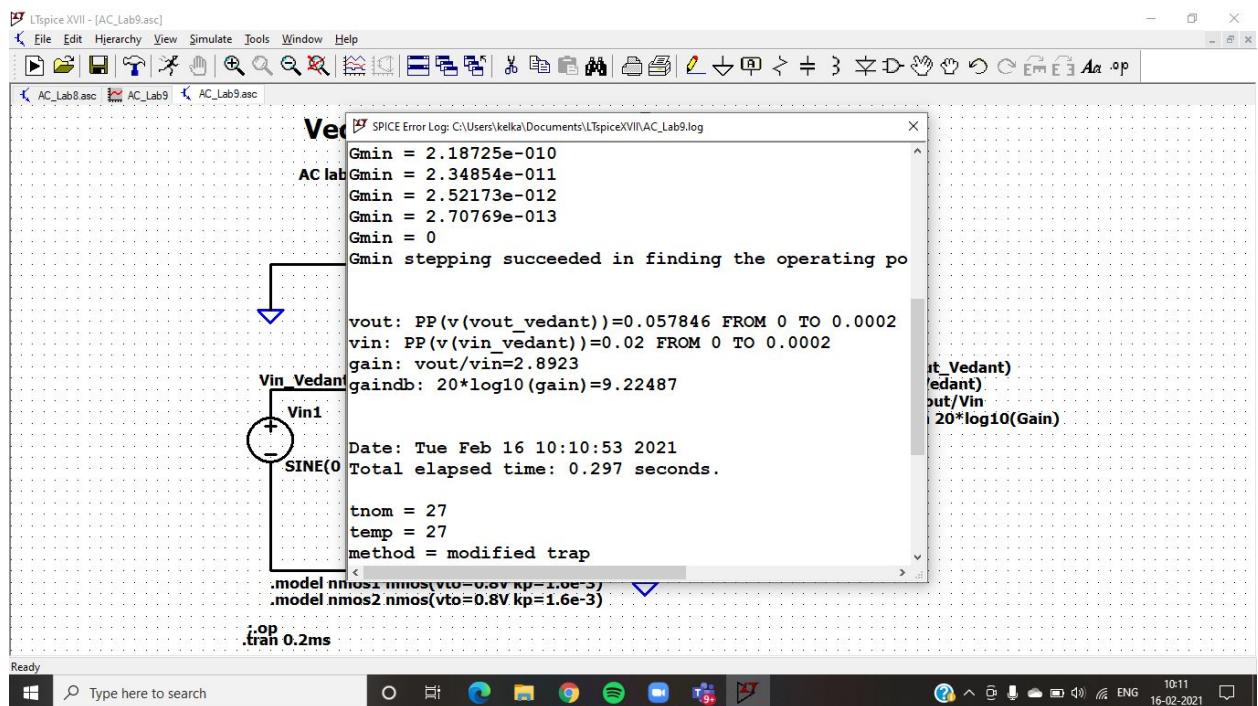
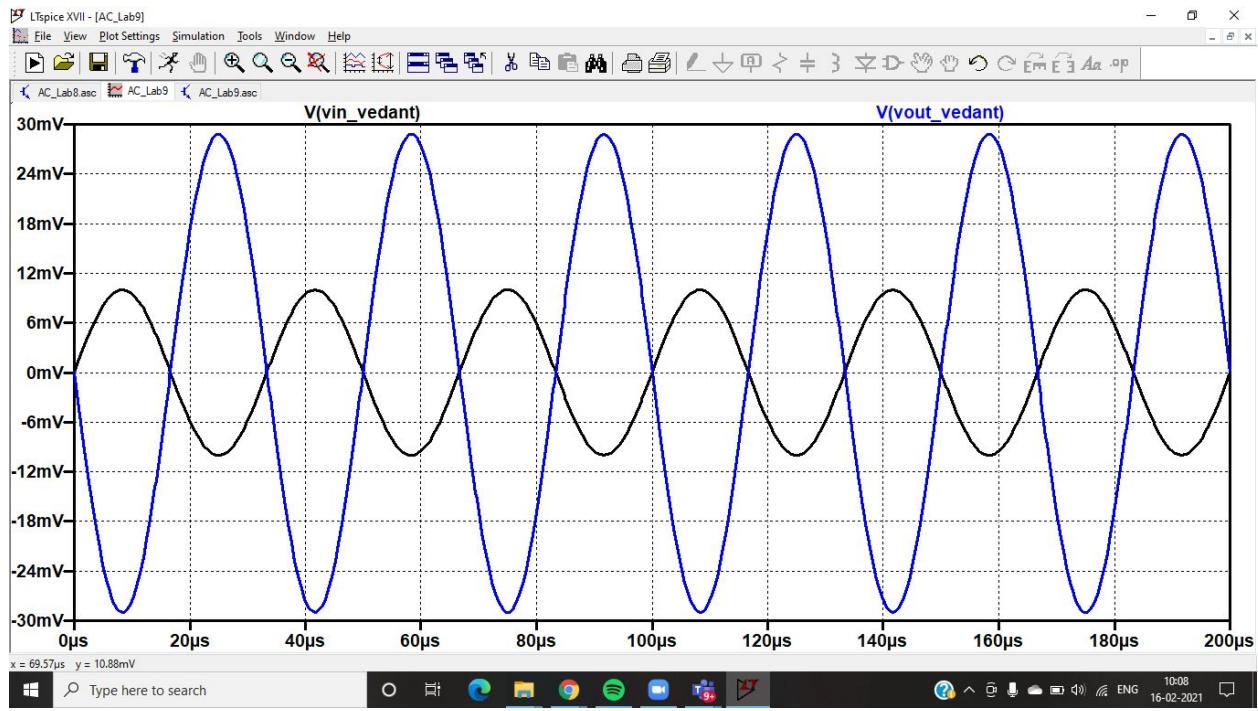
DC



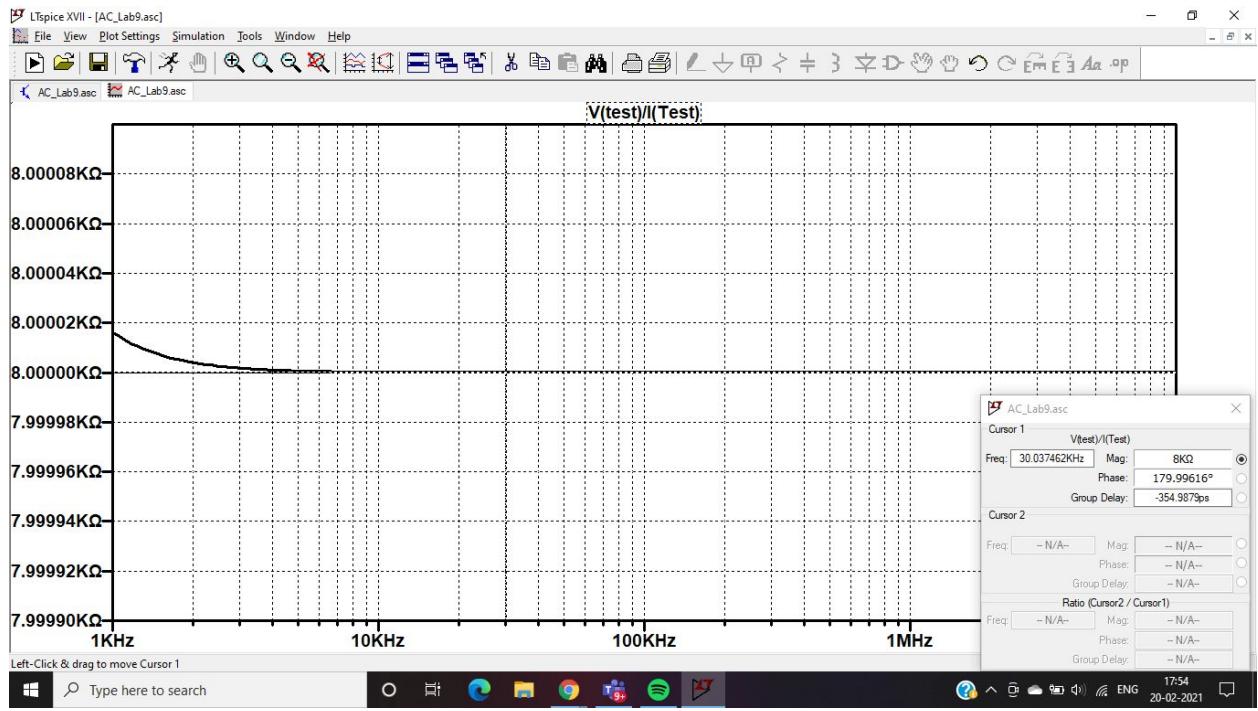


AC

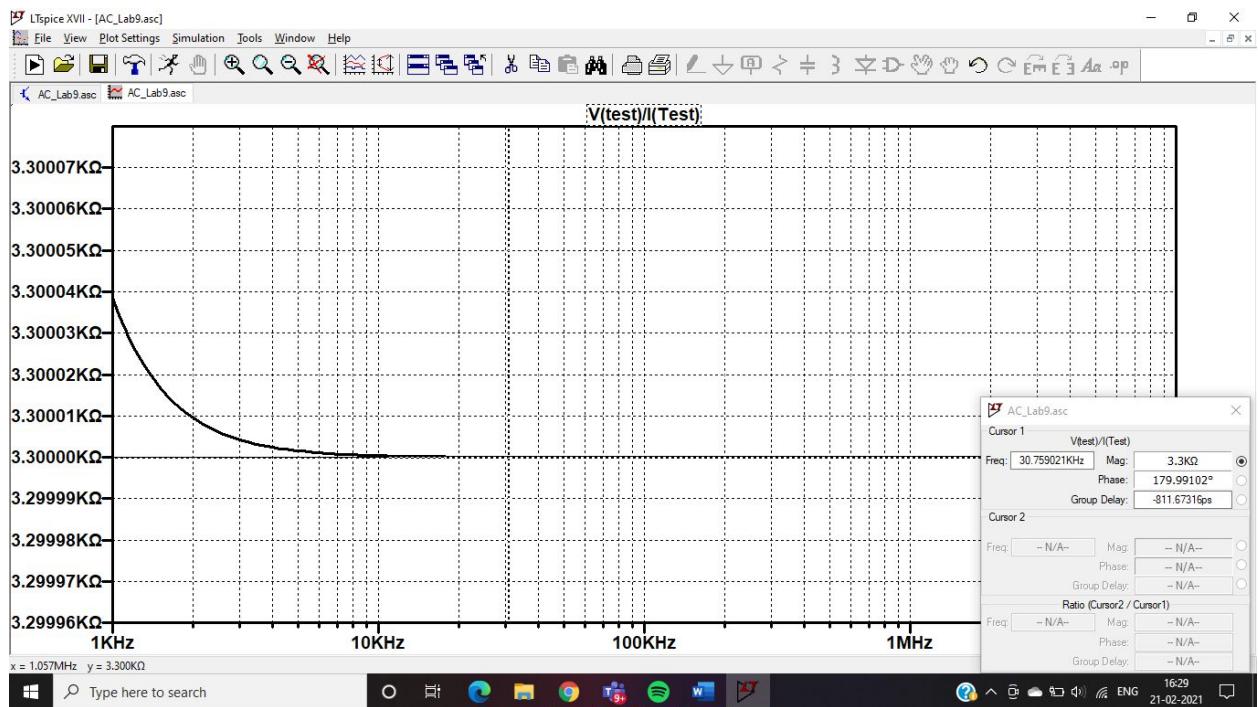




Zin



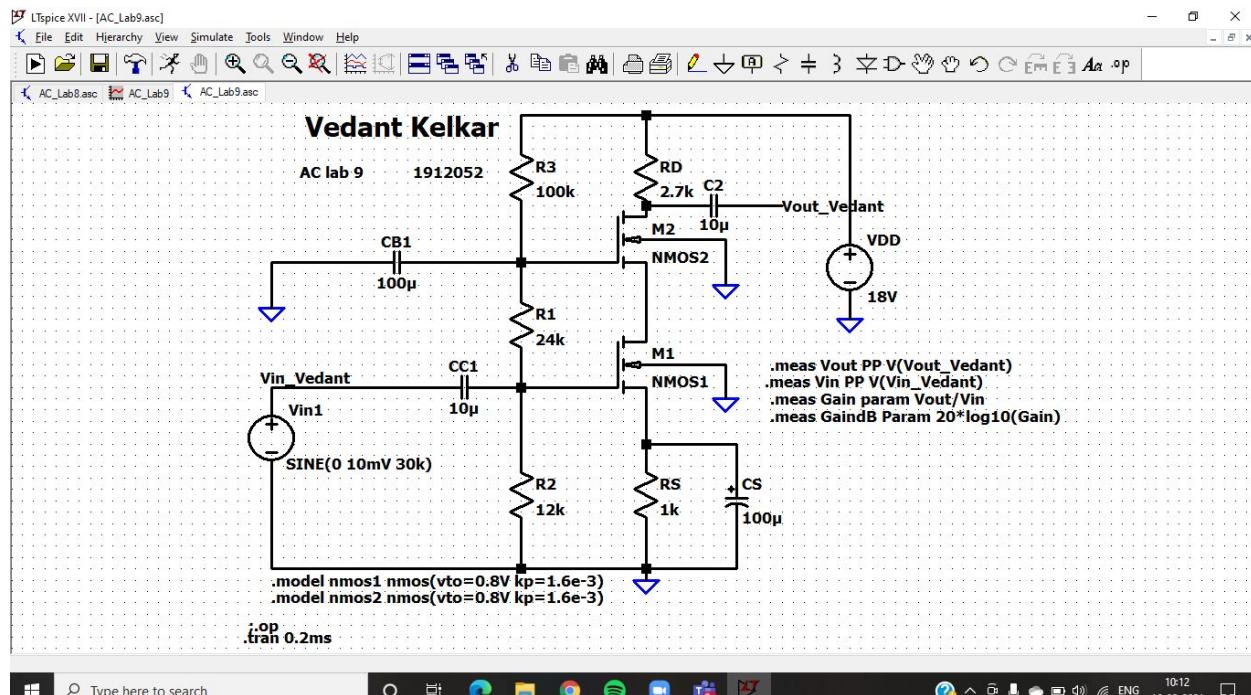
Zout



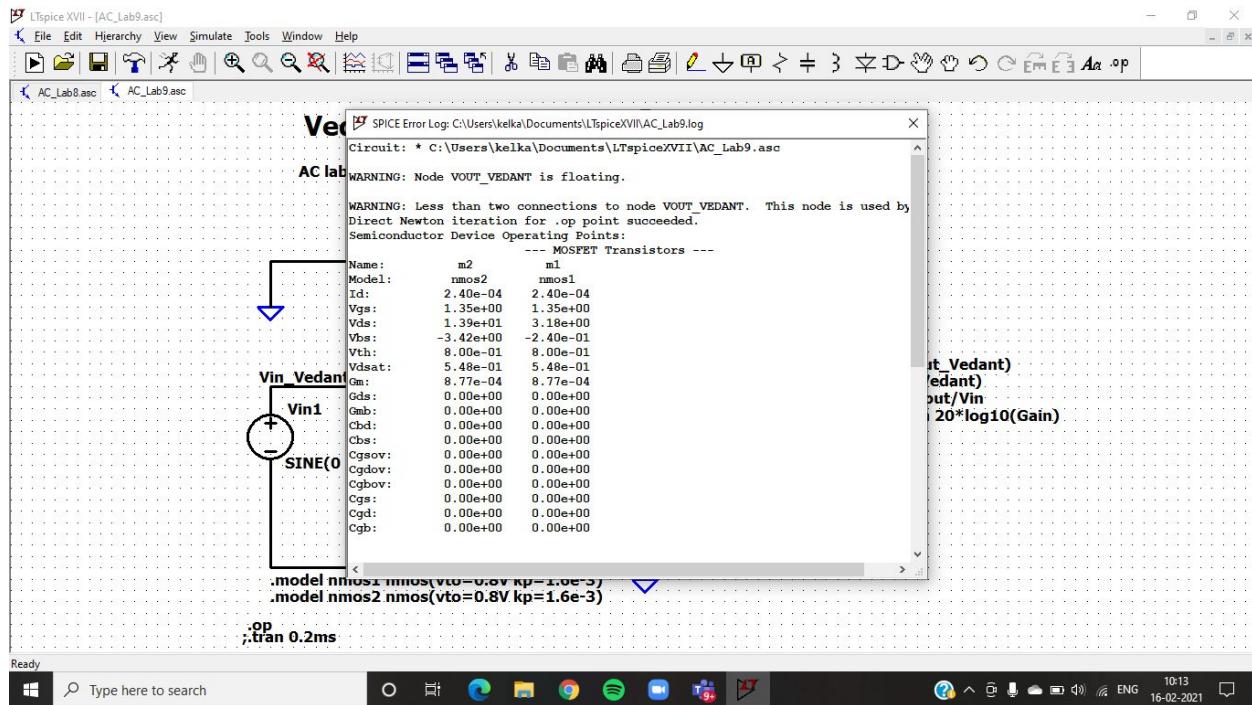
Parameters	Calculated	Simulated
Vgs1	1.3478 V	1.35 V

Vgs2	1.3478 V	1.35 V
Vds1	3.176 V	3.18 V
Vds1	13.784 V	13.8 V
Id1=Ids2	0.24 mA	0.24 mA
gm1=gm2	0.876 mA/V	0.877 mA/V
Zin	8K ohm	8K ohm
Zout	3.3k ohm	3.3k ohm
Avmid	-2.89	-2.8923
Avmid (dB)	9.2179 dB	9.22487 dB

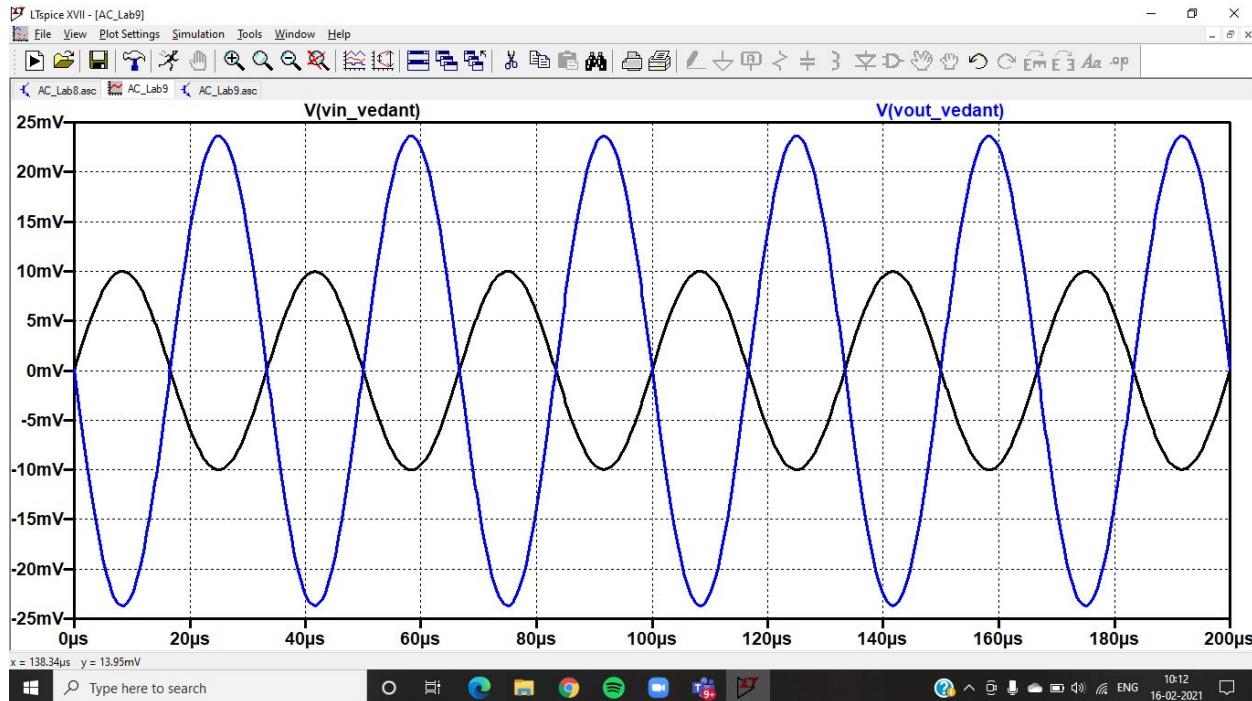
EX1 RD=2.7k

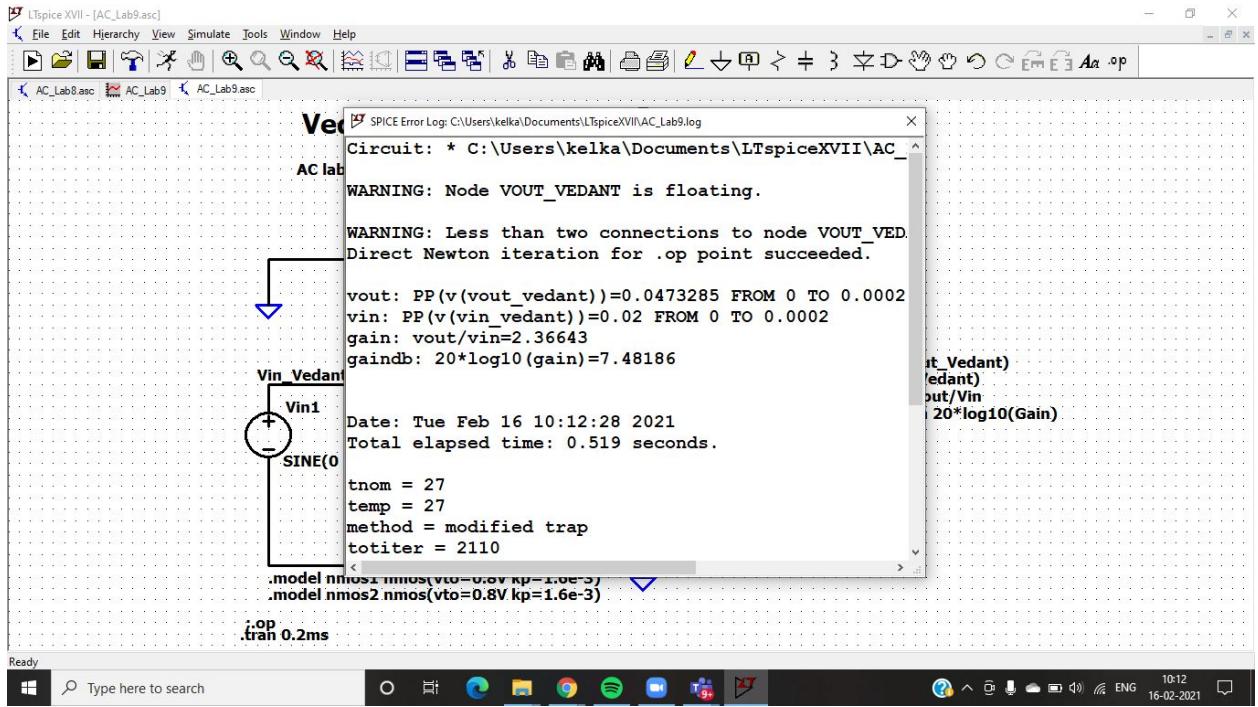


DC

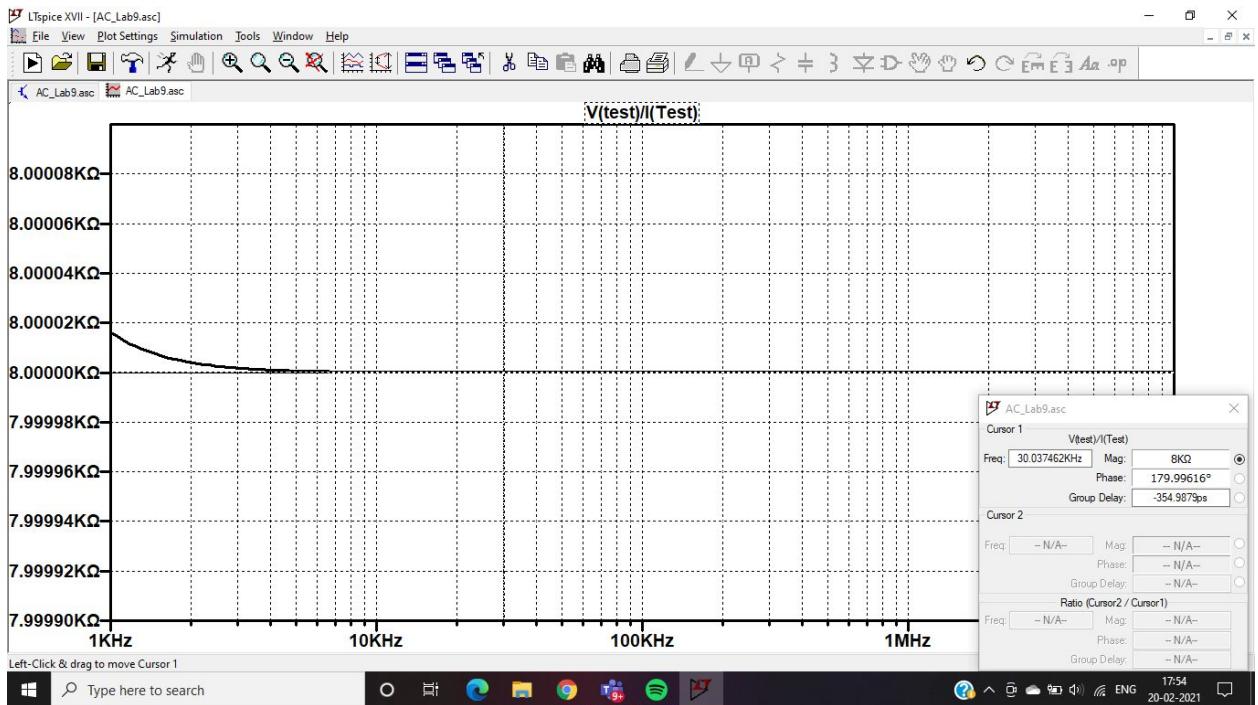


AC

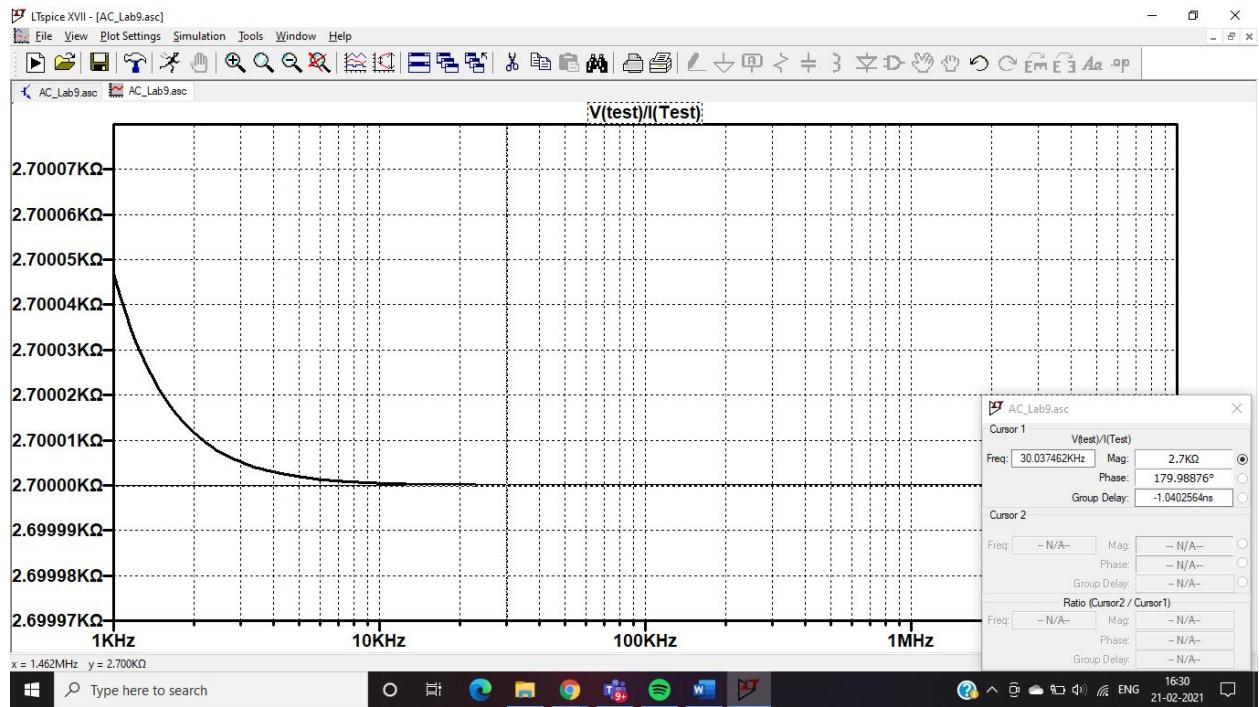




ZIN

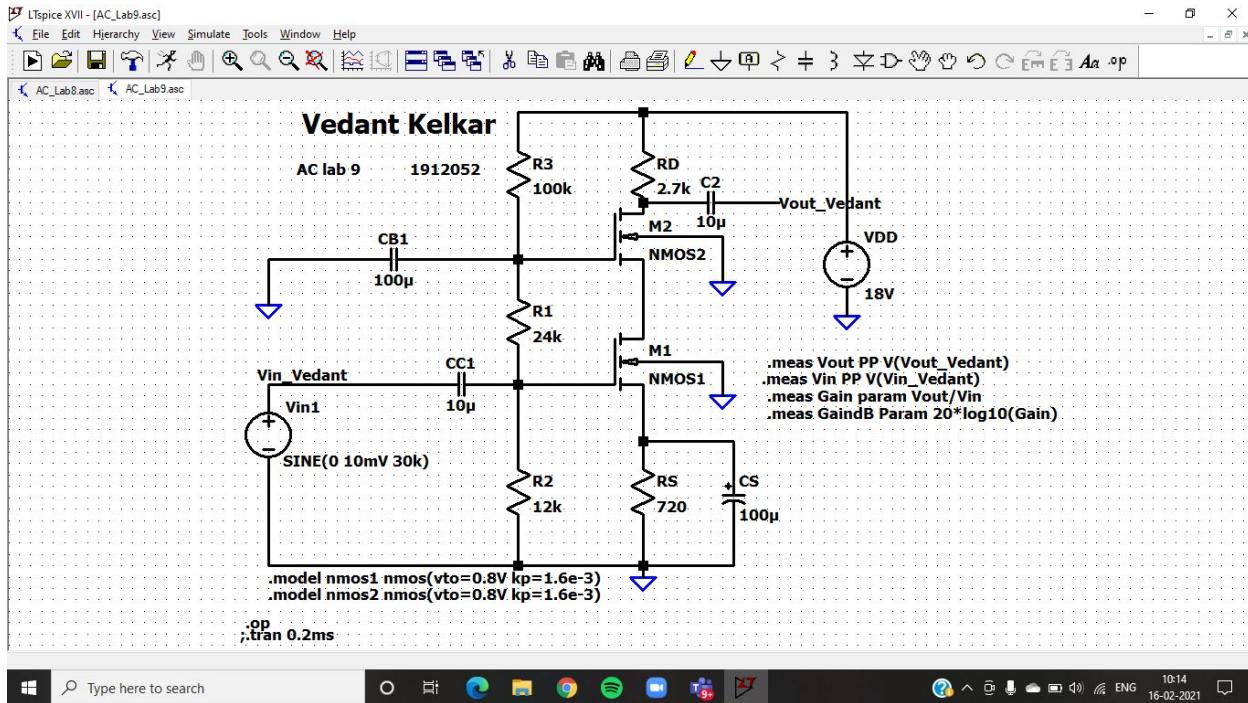


ZOUT

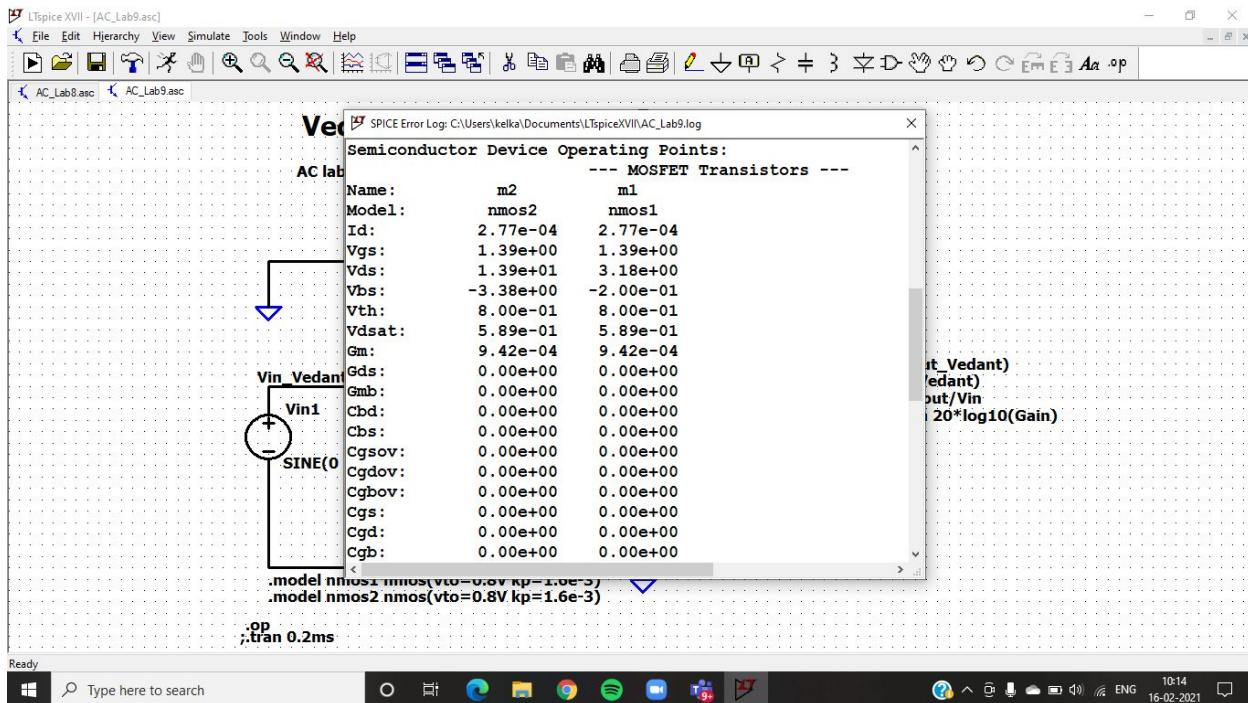


Zin	8K ohm
Zout	2.7k ohm
Avmid	-2.36643
Avmid (dB)	7.48186 dB

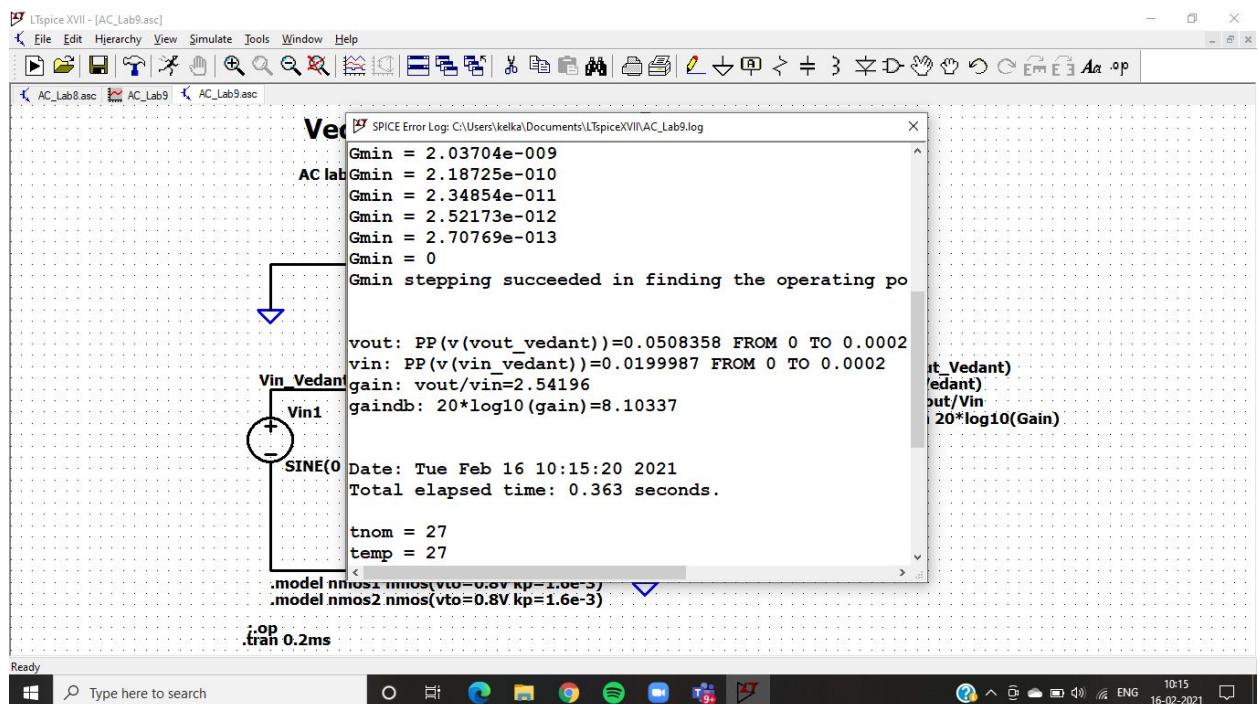
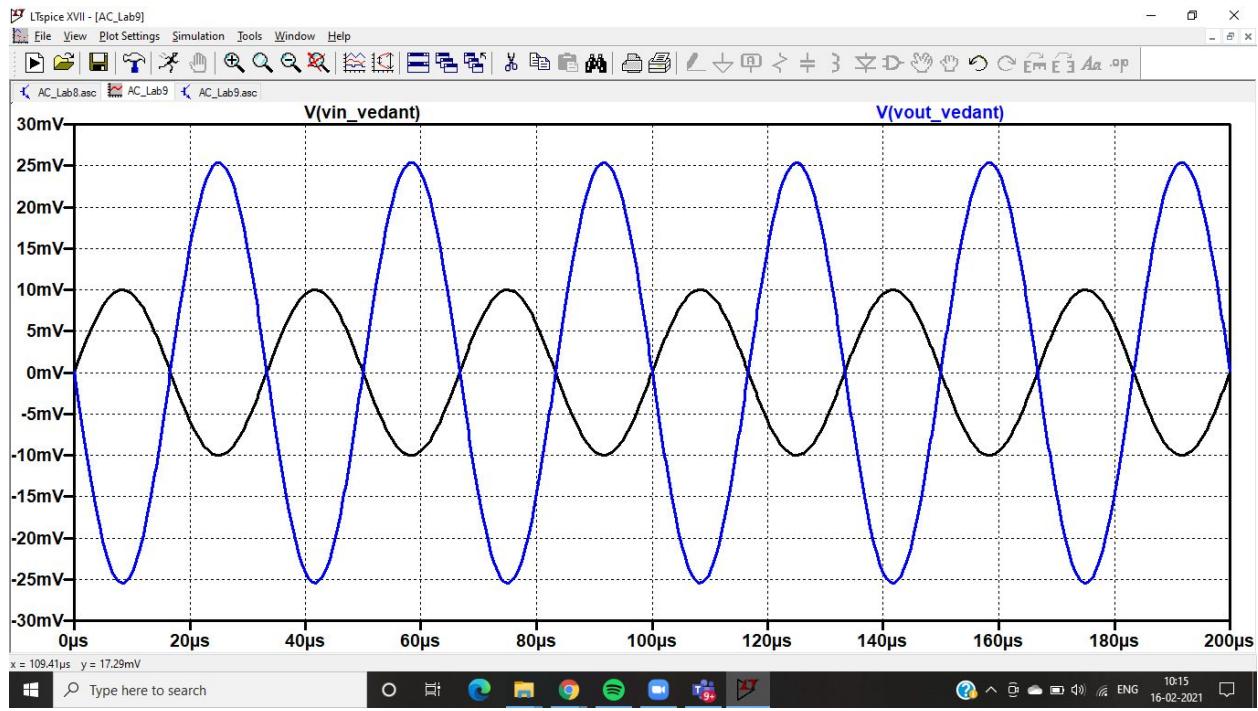
EX2 RS=720



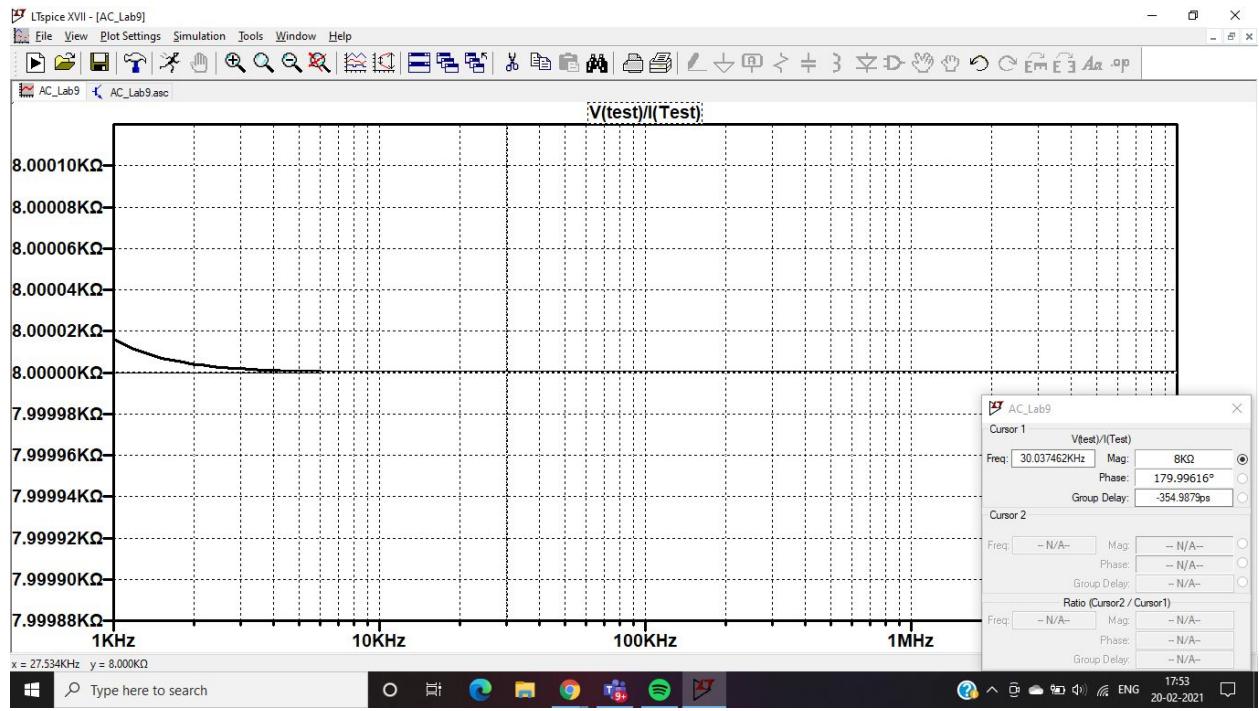
DC



AC

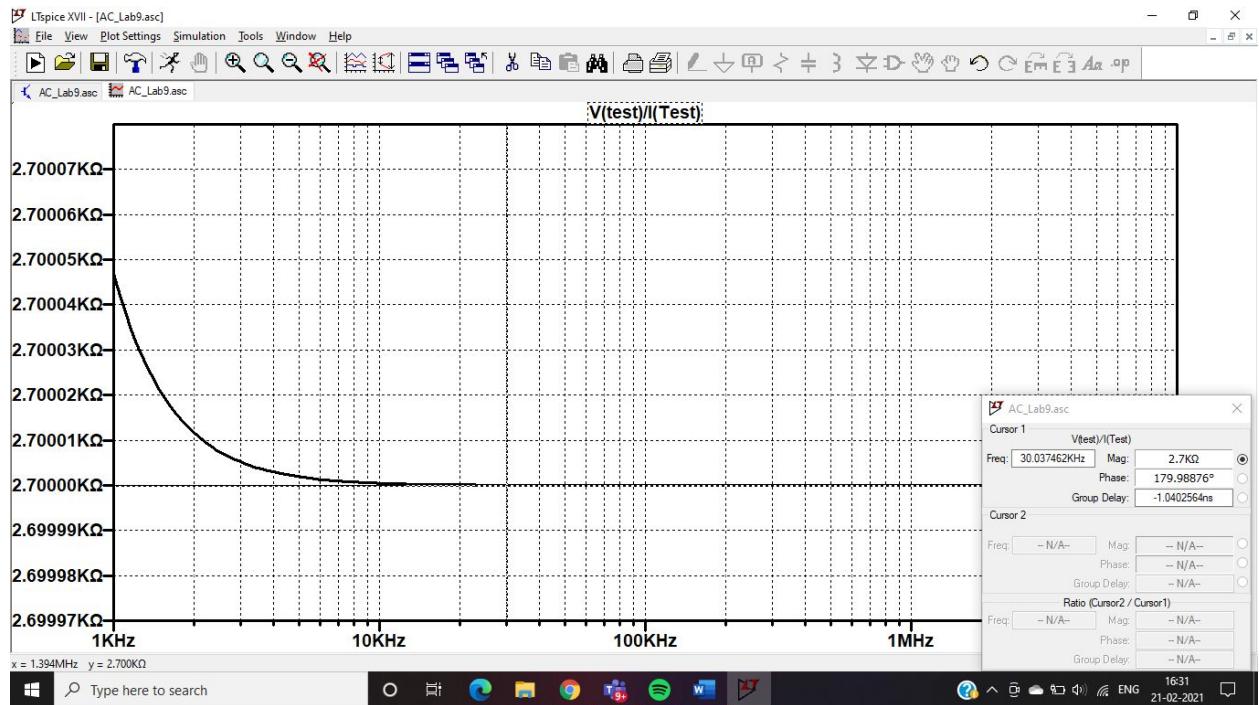


ZIN



8KΩ

ZOUT



Zout

2.7k ohm

Avmid	-2.54196
Avmid (dB)	8.10337 dB

Suggestions: Write inferences clearly, and add observation table

AC LAB 9 is partially approved with suggestions: Inderjit Singh Dhanjal

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