

Audit Course Session 20

AUDIT COURSE ELECTRONIC CIRCUITS 2: SIMULATION BASED STUDY

Kindly update your name and roll no, once this document is shared with you

Time slot to complete your work is **40 MINUTES**

Date: 12/4/2021

Kindly upload your schematic & waveform images here, every 10 minutes, indicating your progress and intention to completion of WORK within time slot allotted

Time slot allotted to you all for the completion of Session 20 is 40 MINUTES

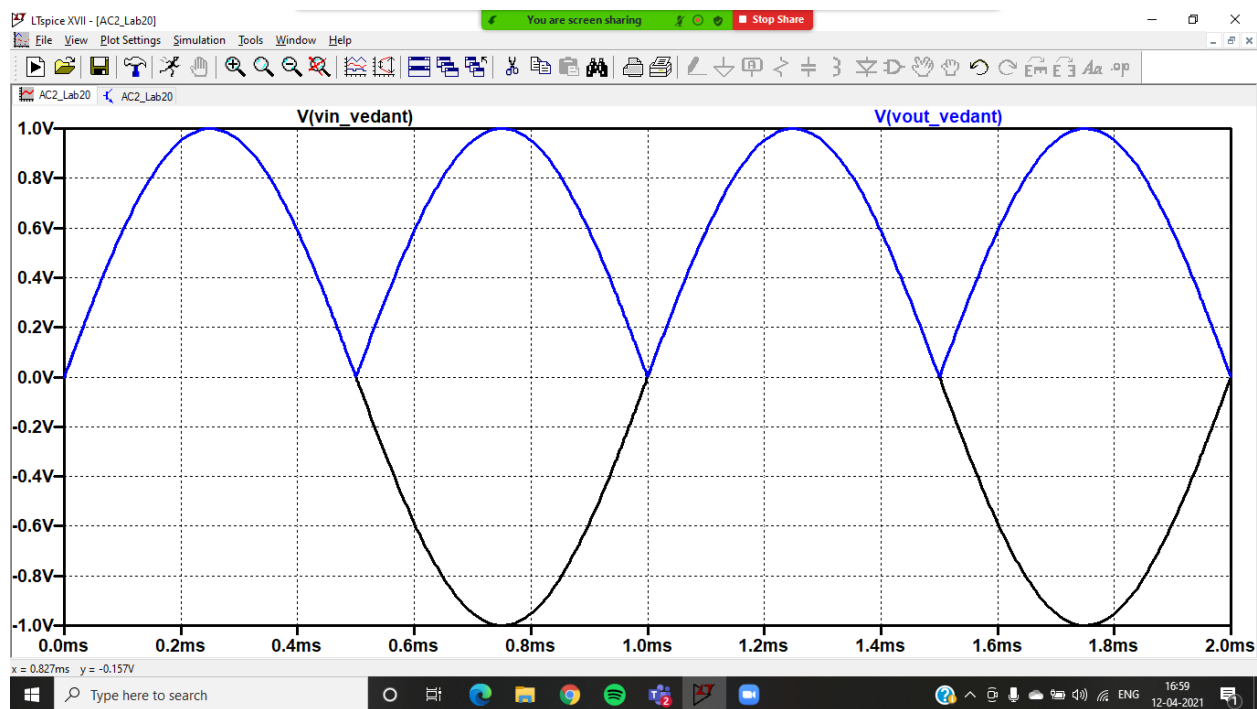
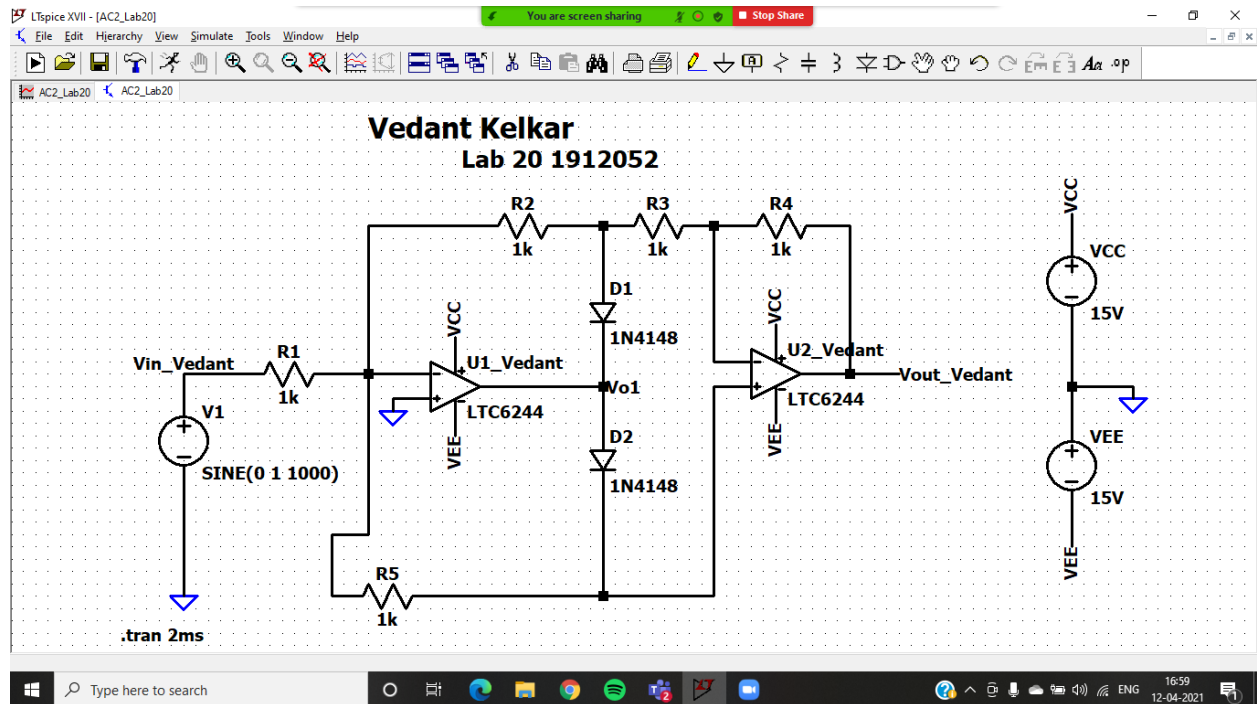
Kindly upload your work (only circuit schematic & waveform in LTspice) in the shared google doc between this time slot only.

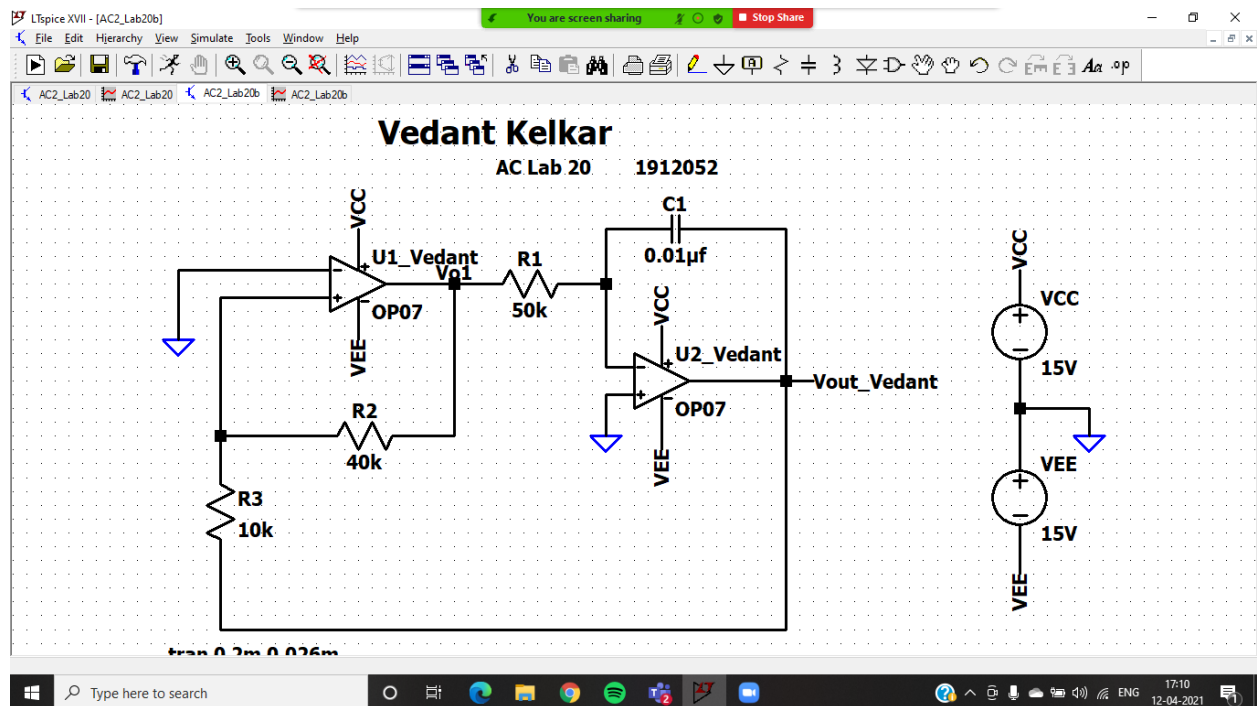
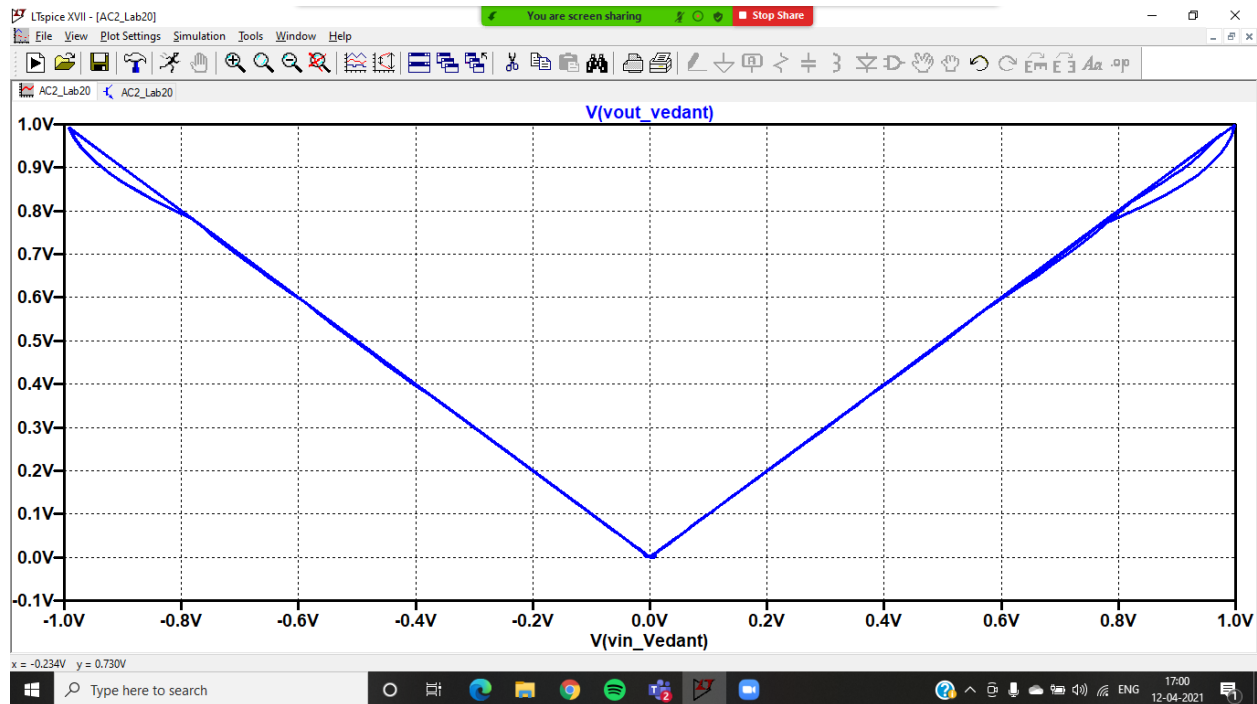
Follow these instruction strictly:

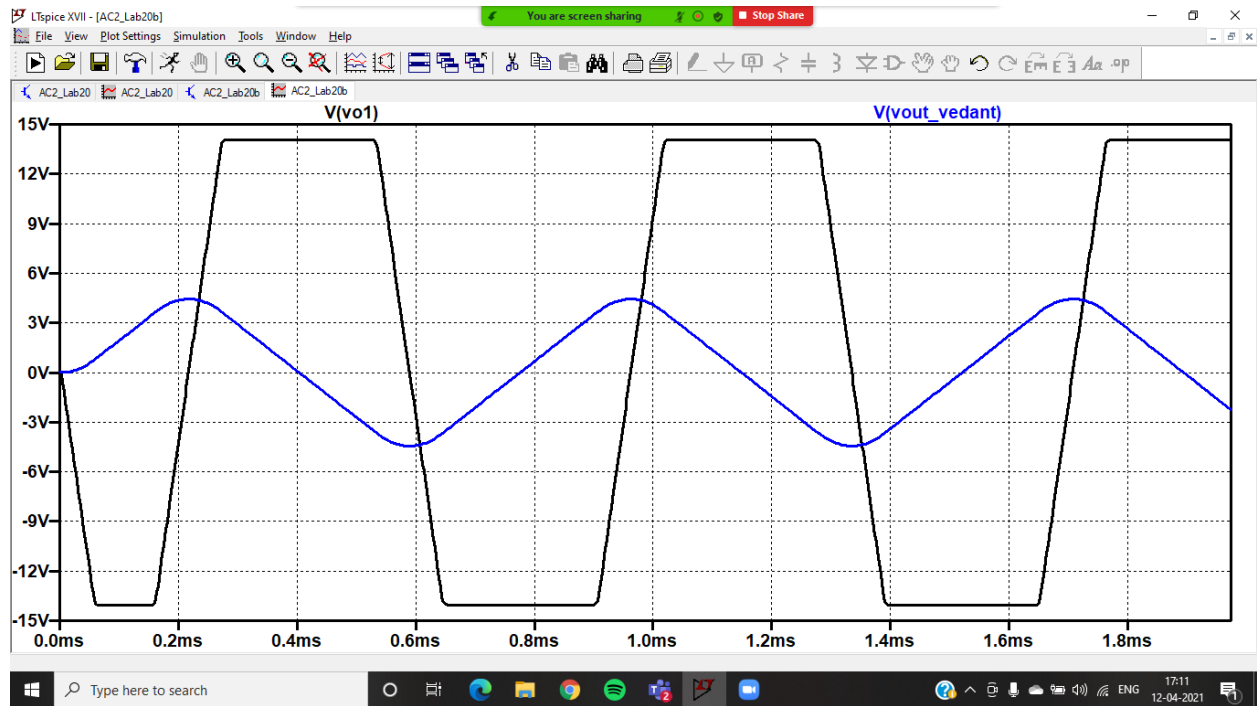
- 1, Start sharp ON TIME, by posting your name and roll no and **screenshot of your LT spice work screen (time and date MUST BE VISIBLE)**
2. Upload your work every 10 minutes, i.e LT spice work screen
3. This means you will upload LT spice work screen 4 times during this time slot.
4. Point 3 indicates your readiness and presences for completion of Session 20

You are entitled for 10 points per session only if you follow above instruction to the details

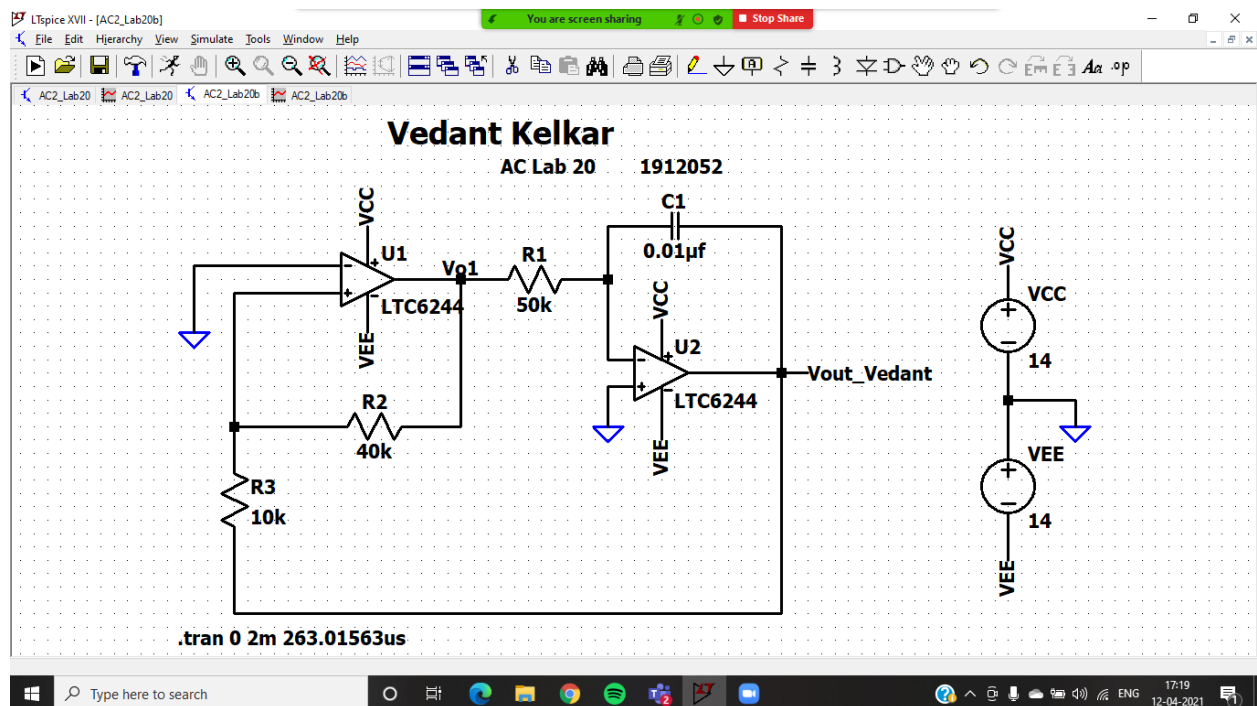
STUDENTS WORK AREA STARTS HERE

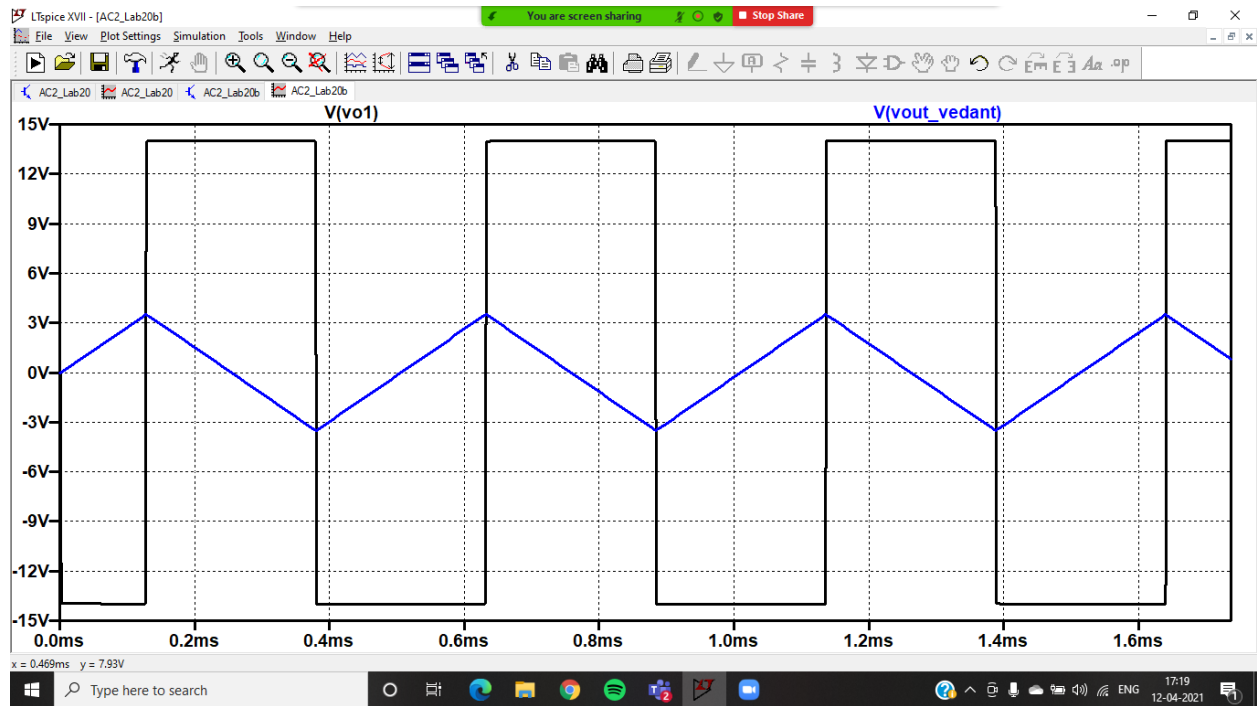






Vo1 Sim	Vo1 Calc	Vout Sim	Vout Calc
14.049V	14V	8.87V	7V





Vo1 Sim	Vo1 Calc	Vout Sim	Vout Calc
13.982577V	14V	6.9757344V	7V

AC LAB 20 is approved: Inderjit Singh Dhanjal