

AUDIT COURSE ELECTRONIC CIRCUITS 1: SIMULATION BASED STUDY

LAB 9

Kindly update your name and roll no, once this document is shared with you

Time slot to complete your work is **40 MINUTES**

Date: 31/8/2020

Kindly upload your schematic & waveform images here, every 10 minutes, indicating your progress and intention to completion of WORK within time slot allotted

Time slot allotted to you all for the completion of WEEK 5 DAY 1 is 40 MINUTES

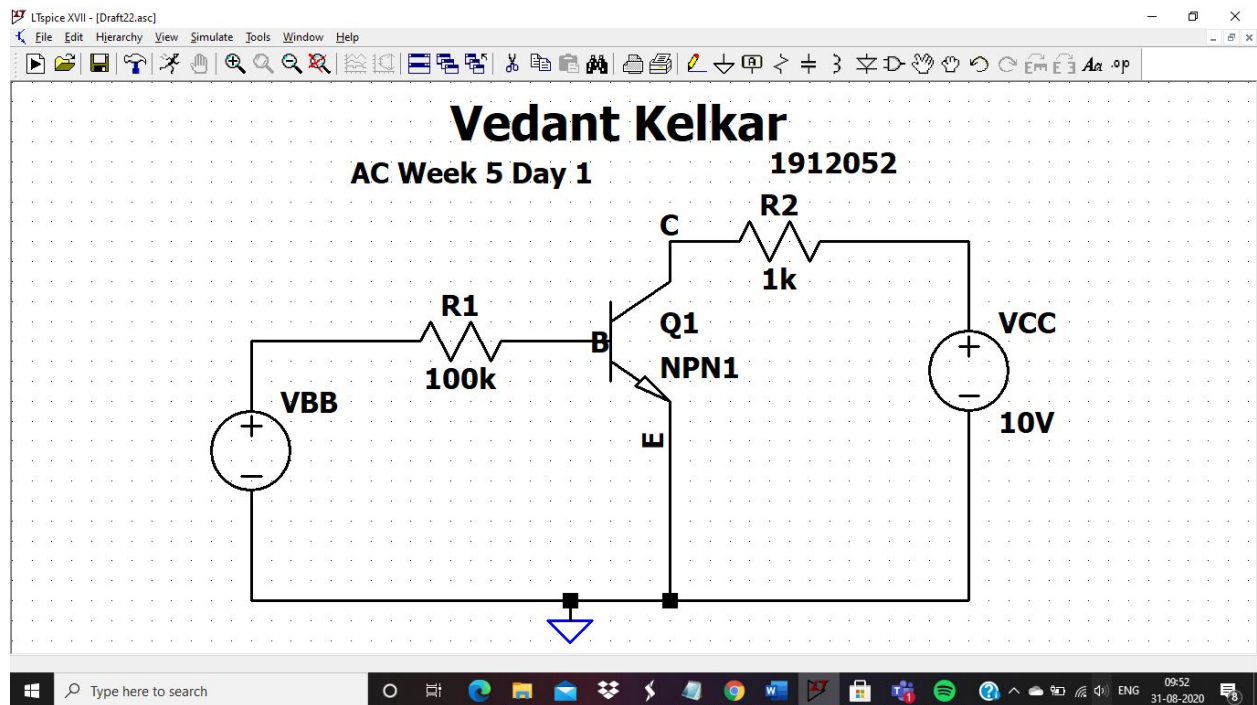
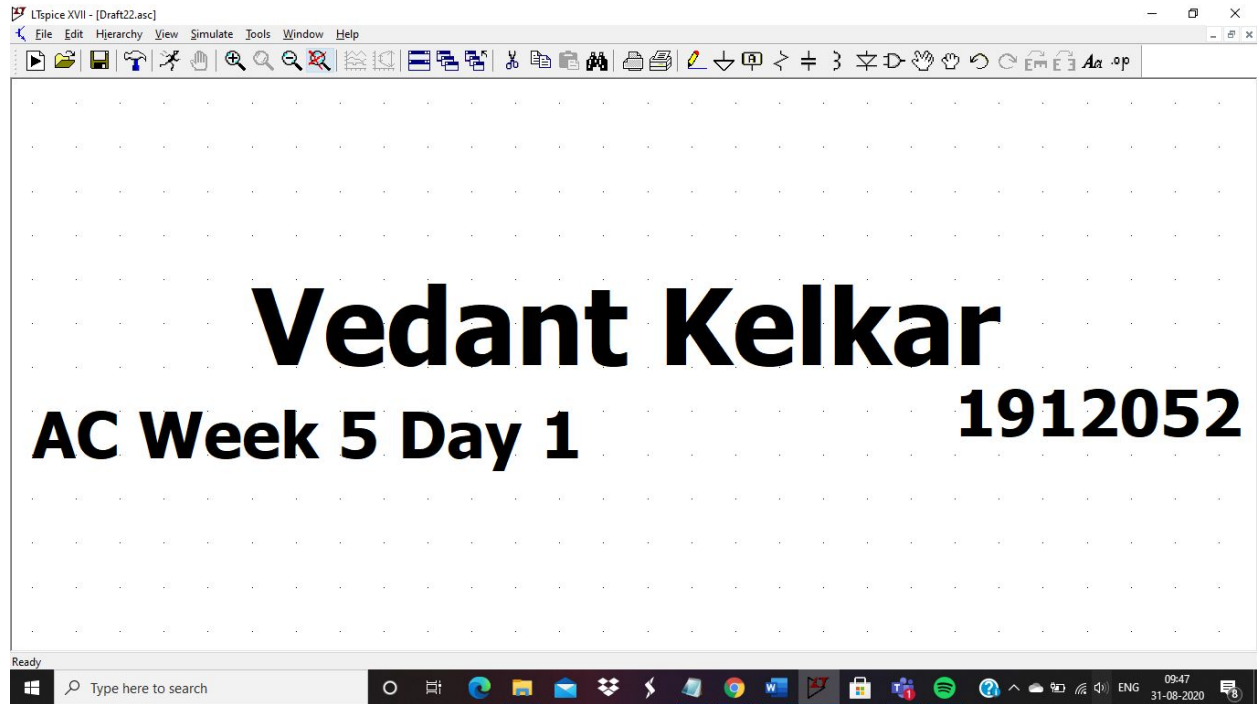
Kindly upload your work (only circuit schematic & waveform in LTSpice) in the shared google doc between this time slot only.

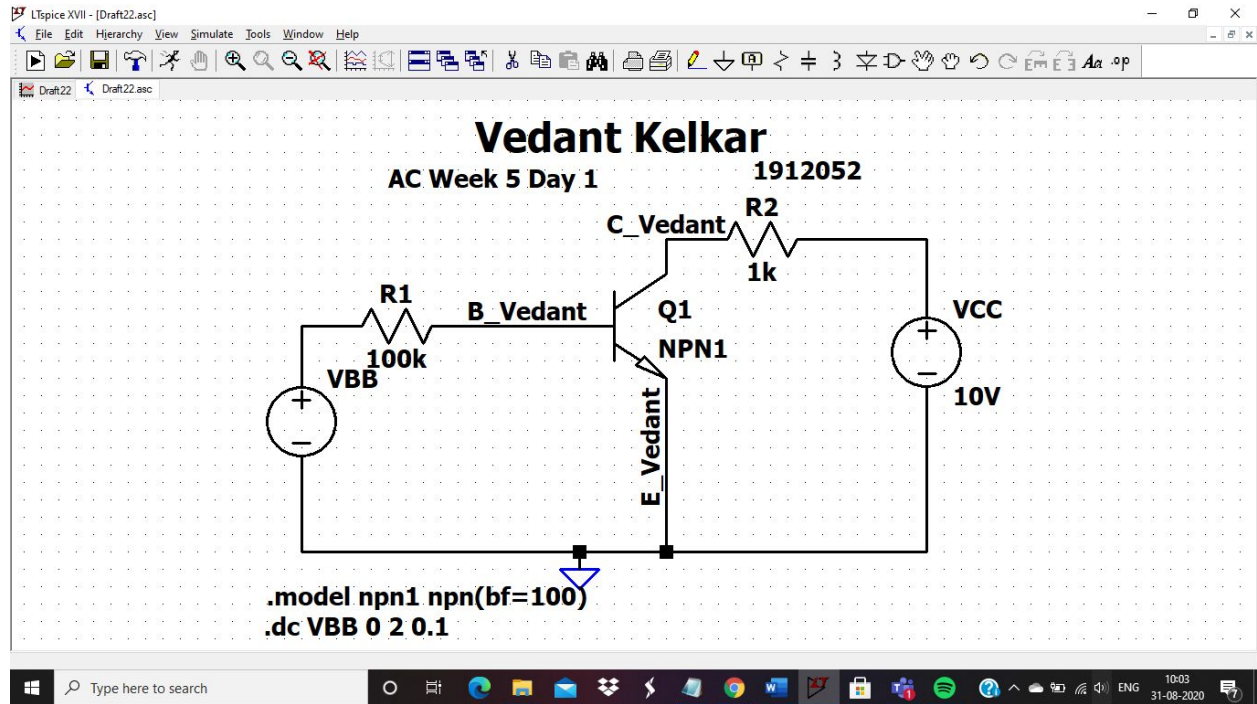
Follow these instruction strictly:

- 1, Start sharp ON TIME, by posting your name and roll no and **screenshot of your LT spice work screen (time and date MUST BE VISIBLE)**
2. Upload your work every 10 minutes, i.e LT spice work screen
3. This means you will upload LT spice work screen 4 times during this time slot.
4. Point 3 indicates your readiness and presences for completion of WEEK 5 DAY 1

You are entitled for 1 CREDIT per Lab only if you follow above instruction to the details

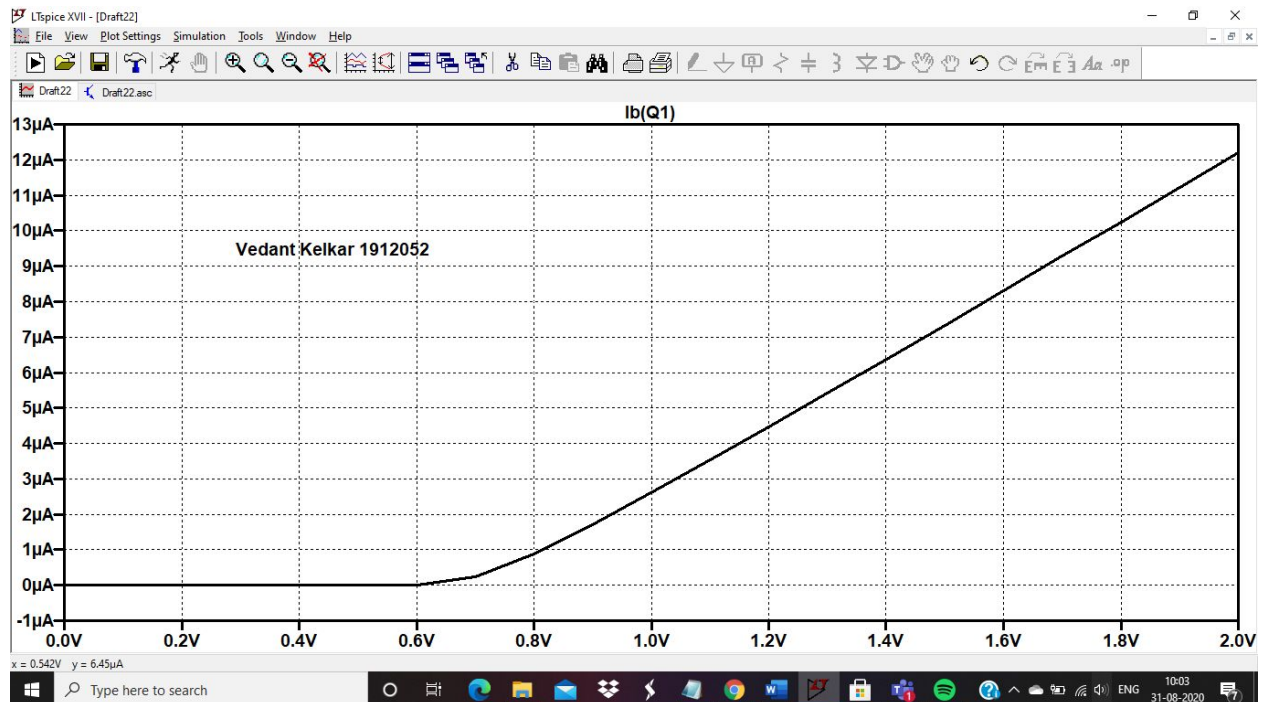
STUDENTS WORK AREA STARTS HERE



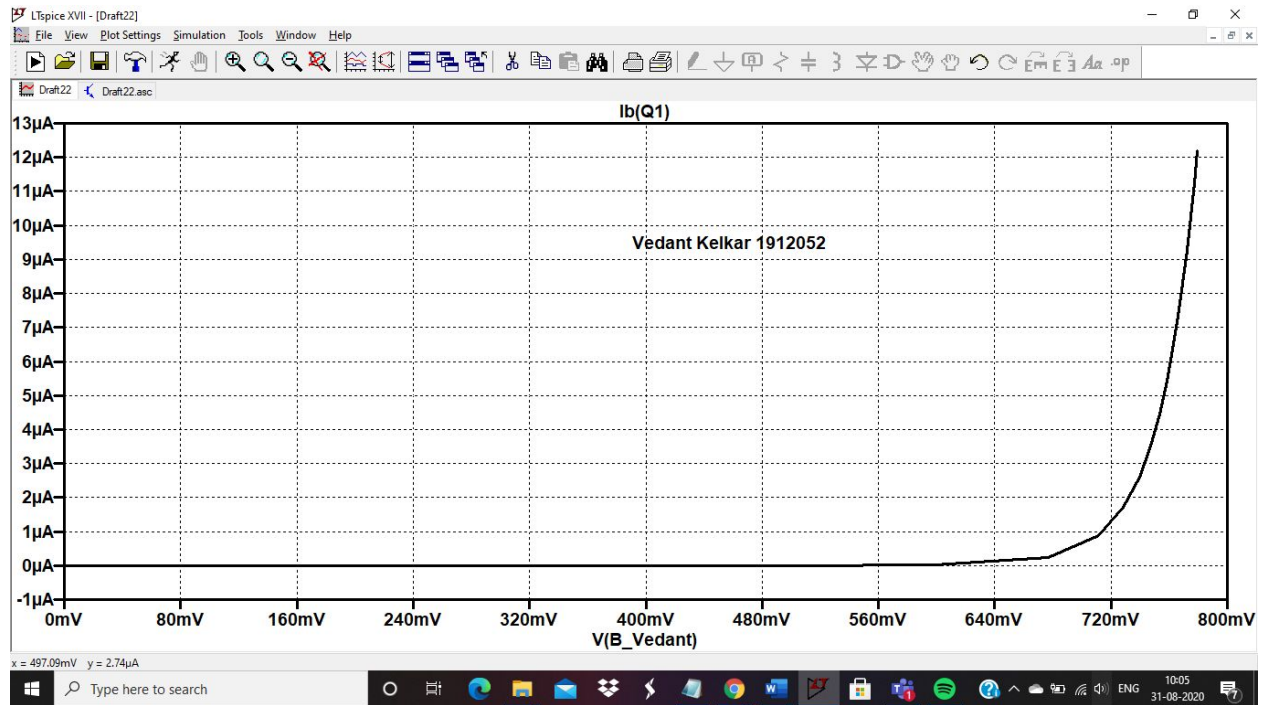


INPUT CHARACTERISTICS::

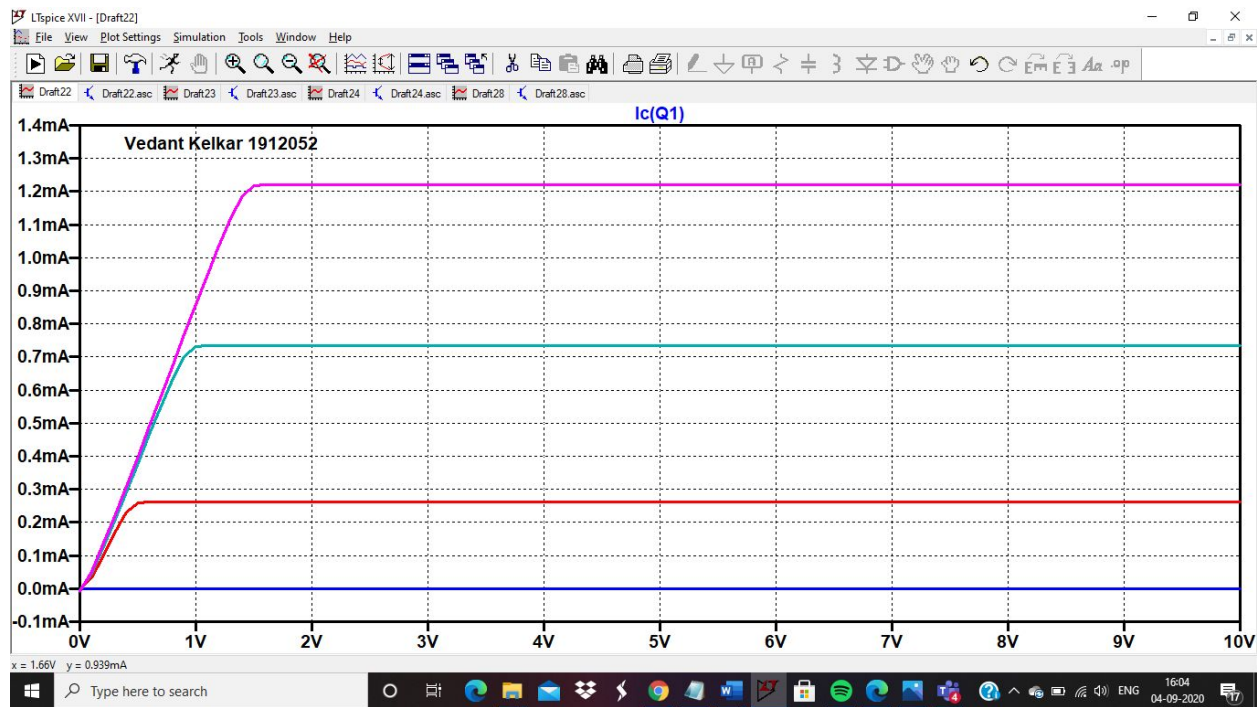
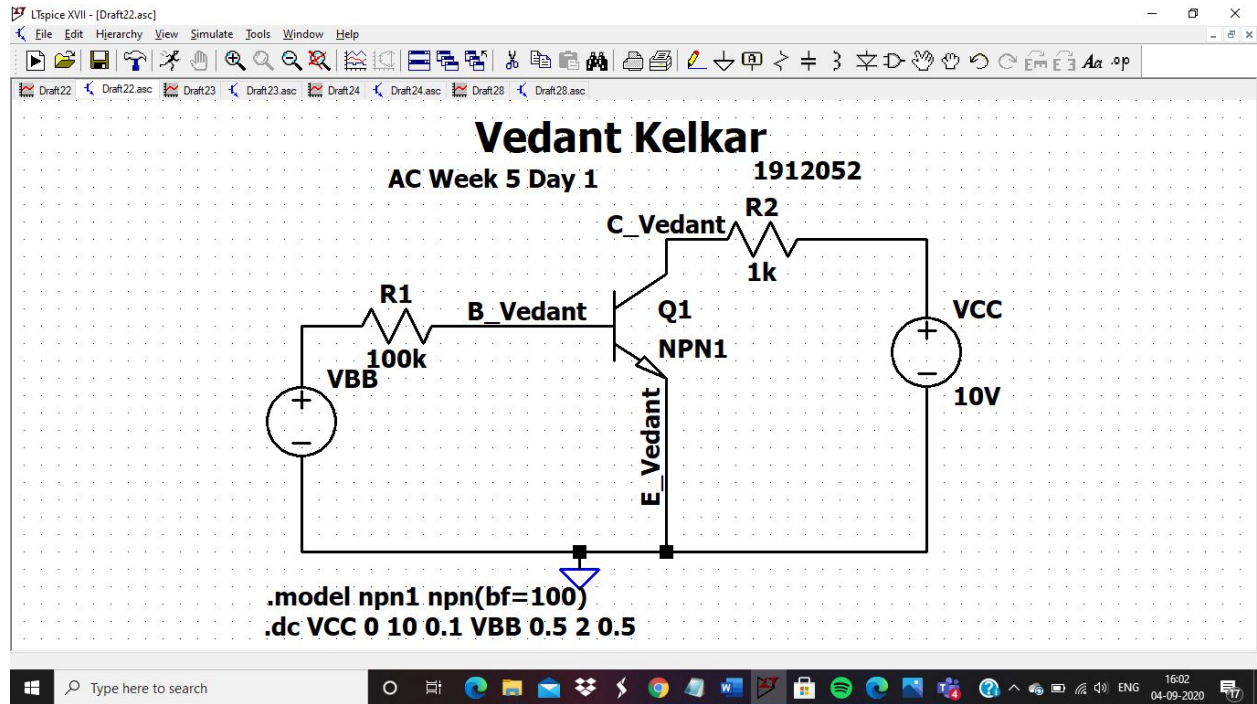
IB vs Vbb

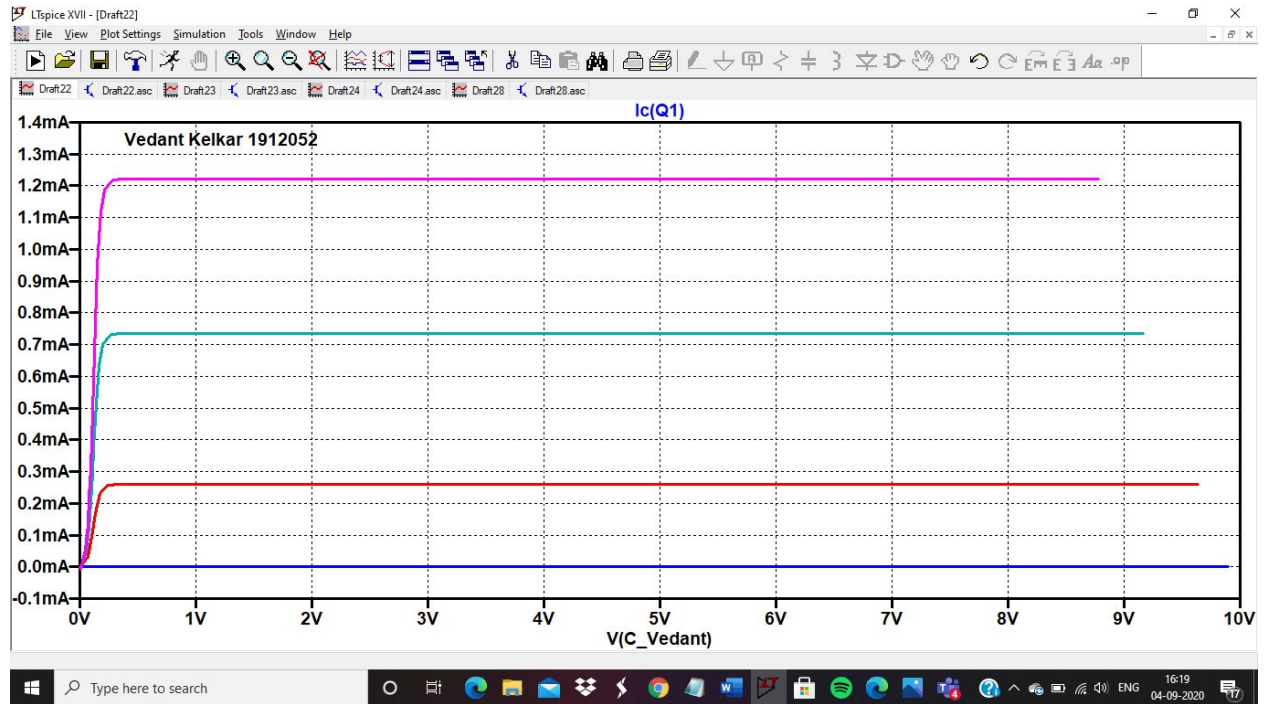


IB vs Vb



VBE	IB
0	0
200mV	-9.5963748pA
400mV	-1.702713pA
600mV	16.775463nA
700mV	669.39401nA
750mV	3.9821223μA
760mV	5.8757558μA
770mV	8.6556765μA





0	0
100mV	234.59uA
200mV	702.03uA
300mV	733.28uA
500mV	734,13uA
1V	0.736mA
3V	0.736mA
5V	0.736mA
8V	0.736mA

AC LAB 9 is approved: Inderjit Singh Dhanjal