

AUDIT COURSE ELECTRONIC CIRCUITS 1: SIMULATION BASED STUDY

LAB 13

Kindly update your name and roll no, once this document is shared with you

Time slot to complete your work is **40 MINUTES**

Date: 14/9/2020

Kindly upload your schematic & waveform images here, every 10 minutes, indicating your progress and intention to completion of WORK within time slot allotted

Time slot allotted to you all for the completion of WEEK 7 DAY 1 is 40 MINUTES

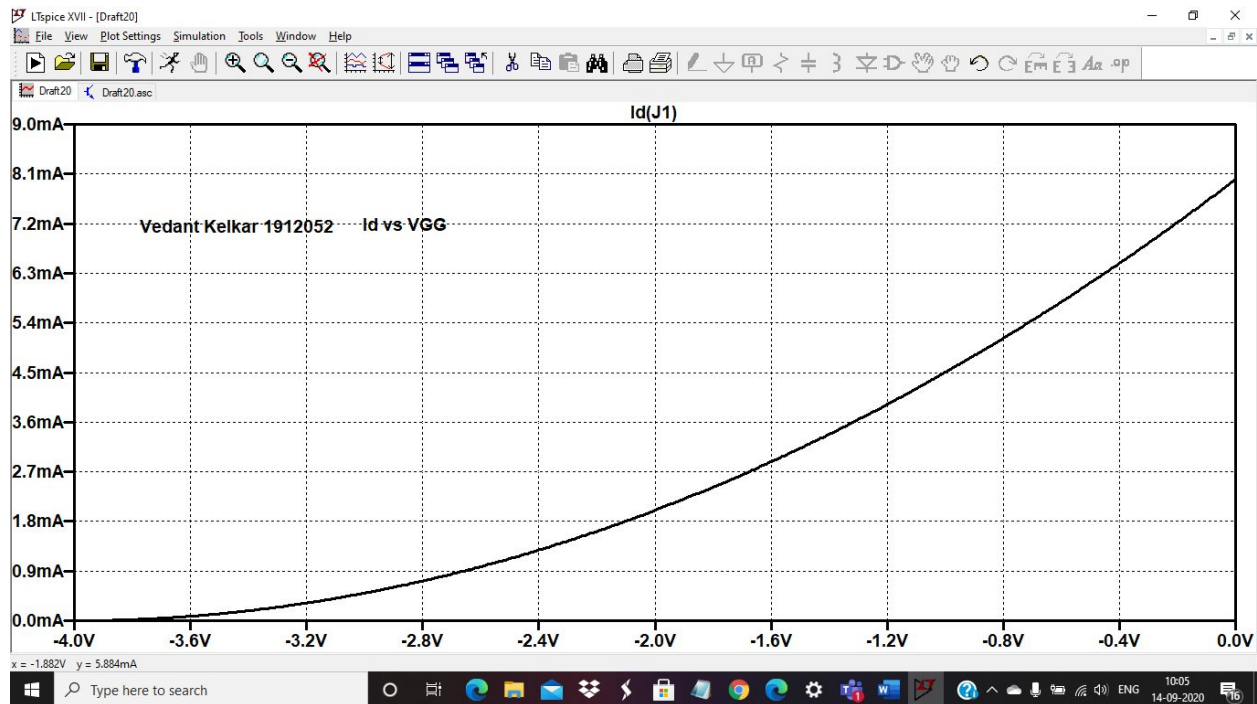
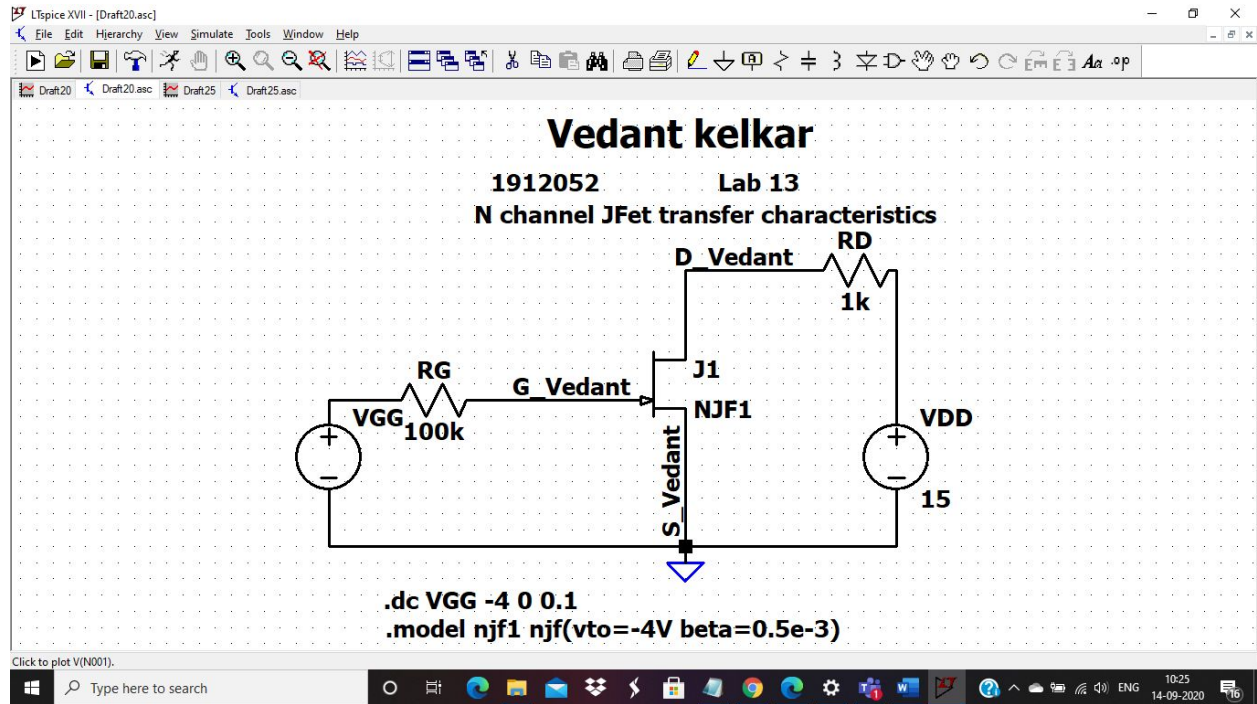
Kindly upload your work (only circuit schematic & waveform in LTSpice) in the shared google doc between this time slot only.

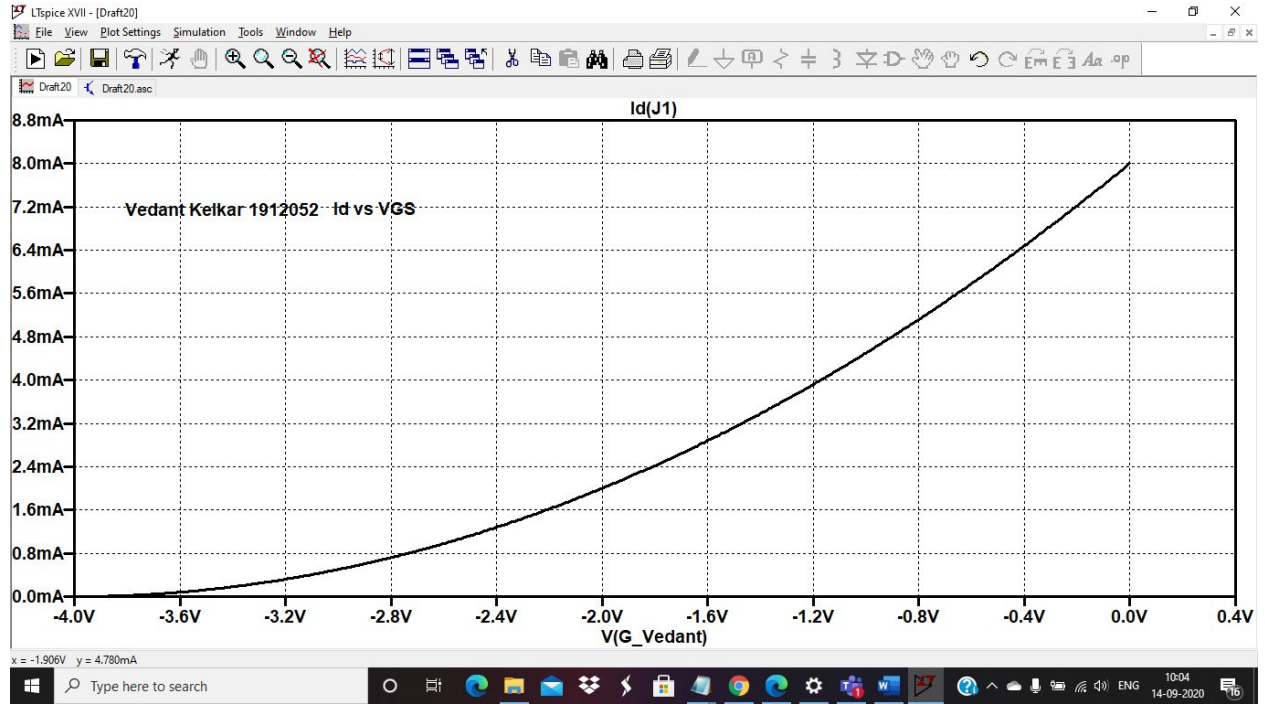
Follow these instruction strictly:

- 1, Start sharp ON TIME, by posting your name and roll no and **screenshot of your LT spice work screen (time and date MUST BE VISIBLE)**
2. Upload your work every 10 minutes, i.e LT spice work screen
3. This means you will upload LT spice work screen 4 times during this time slot.
4. Point 3 indicates your readiness and presences for completion of WEEK 7 DAY 1

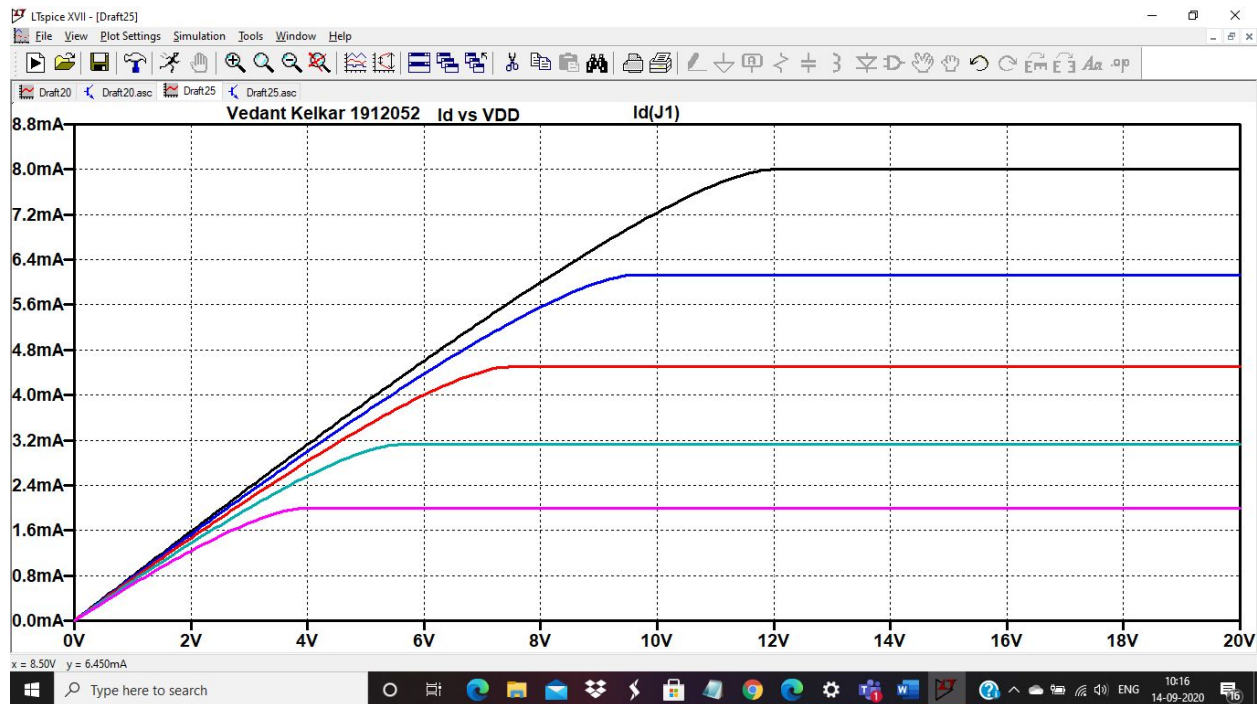
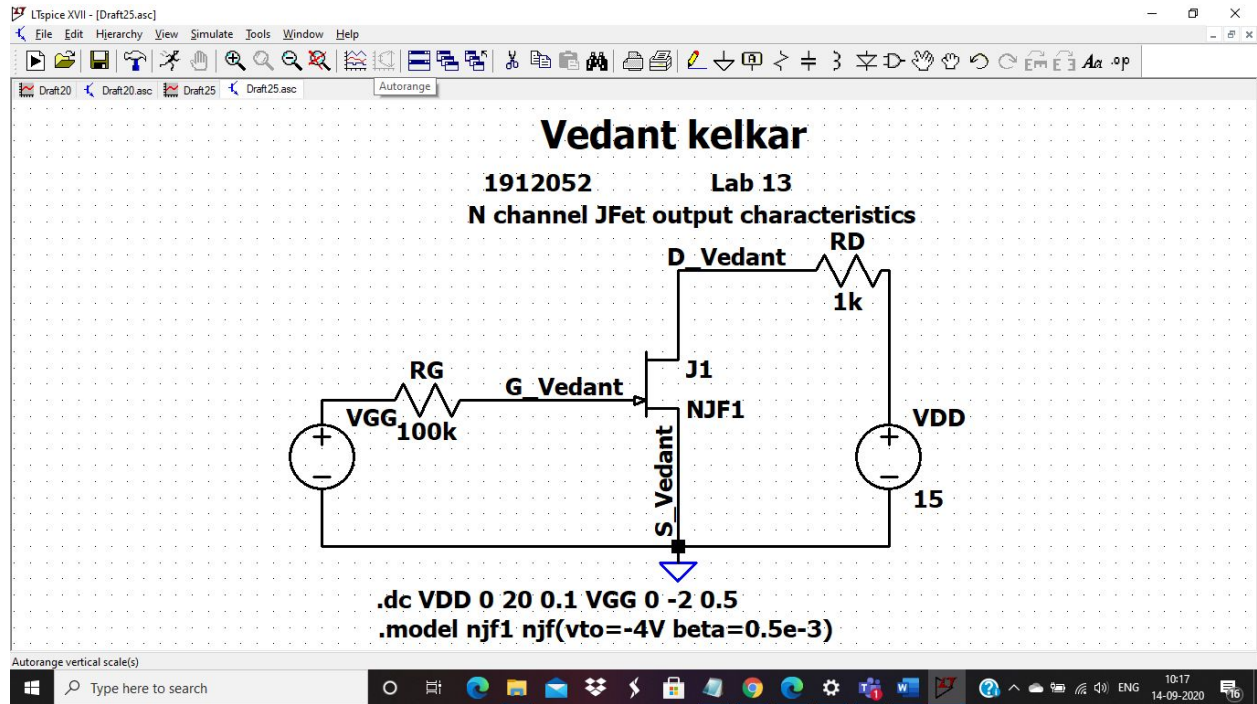
You are entitled for 1 CREDIT per Lab only if you follow above instruction to the details

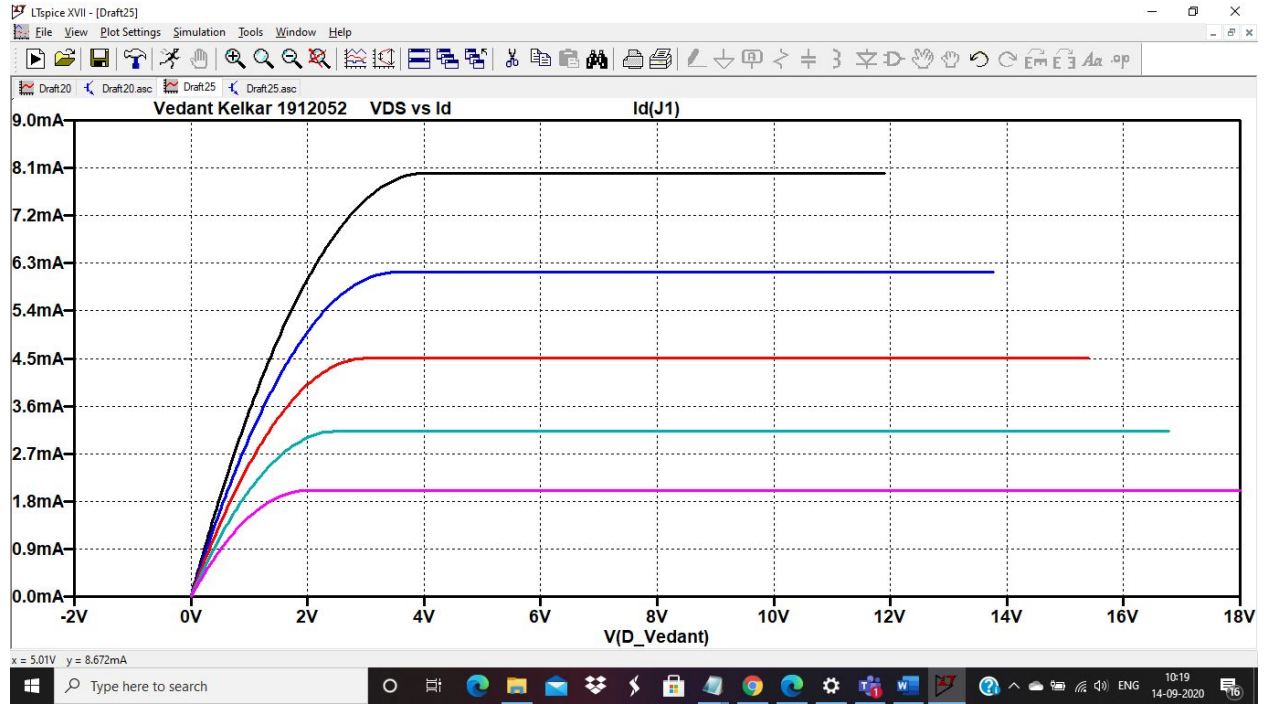
STUDENTS WORK AREA STARTS HERE





VGS	Id
0	8.0000032mA
-0.5V	6.1247249mA
-1V	4.4938674mA
-1.5V	3.1236244mA
-2V	1.9949696mA
-3.5V	122.53538μA
-4V	19.012647pA





Id	VDS
0	0V
1.607mA	0.6V
2.885mA	1.2V
4.052mA	2V
4.509mA	3V
4.509mA	6V
4.509mA	10V
4.509mA	15V

AC LAB 13 is approved: Inderjit Singh Dhanjal