

Audit Course Session 16

AUDIT COURSE ELECTRONIC CIRCUITS 2: SIMULATION BASED STUDY

Kindly update your name and roll no, once this document is shared with you

Time slot to complete your work is **40 MINUTES**

Date: 23/3/2021

Kindly upload your schematic & waveform images here, every 10 minutes, indicating your progress and intention to completion of WORK within time slot allotted

Time slot allotted to you all for the completion of Session 16 is 40 MINUTES

Kindly upload your work (only circuit schematic & waveform in LTspice) in the shared google doc between this time slot only.

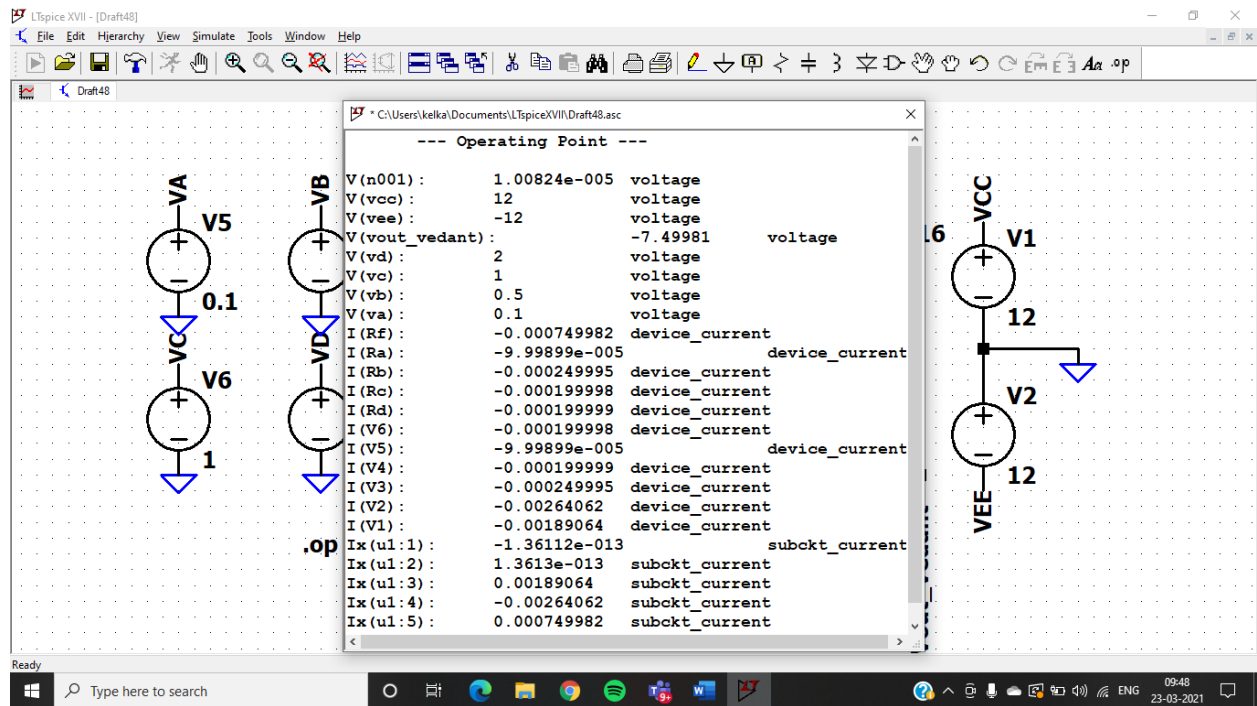
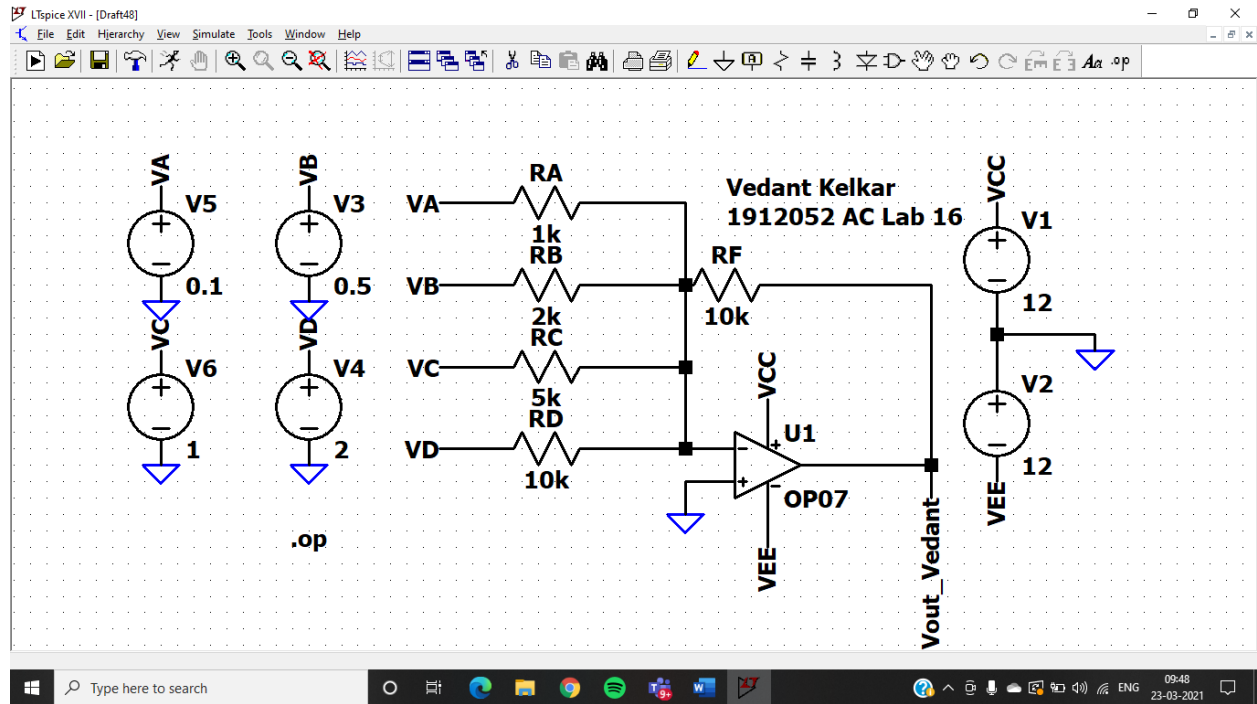
Follow these instruction strictly:

- 1, Start sharp ON TIME, by posting your name and roll no and **screenshot of your LT spice work screen (time and date MUST BE VISIBLE)**
2. Upload your work every 10 minutes, i.e LT spice work screen
3. This means you will upload LT spice work screen 4 times during this time slot.
4. Point 3 indicates your readiness and presences for completion of Session 16

You are entitled for 10 points per session only if you follow above instruction to the details

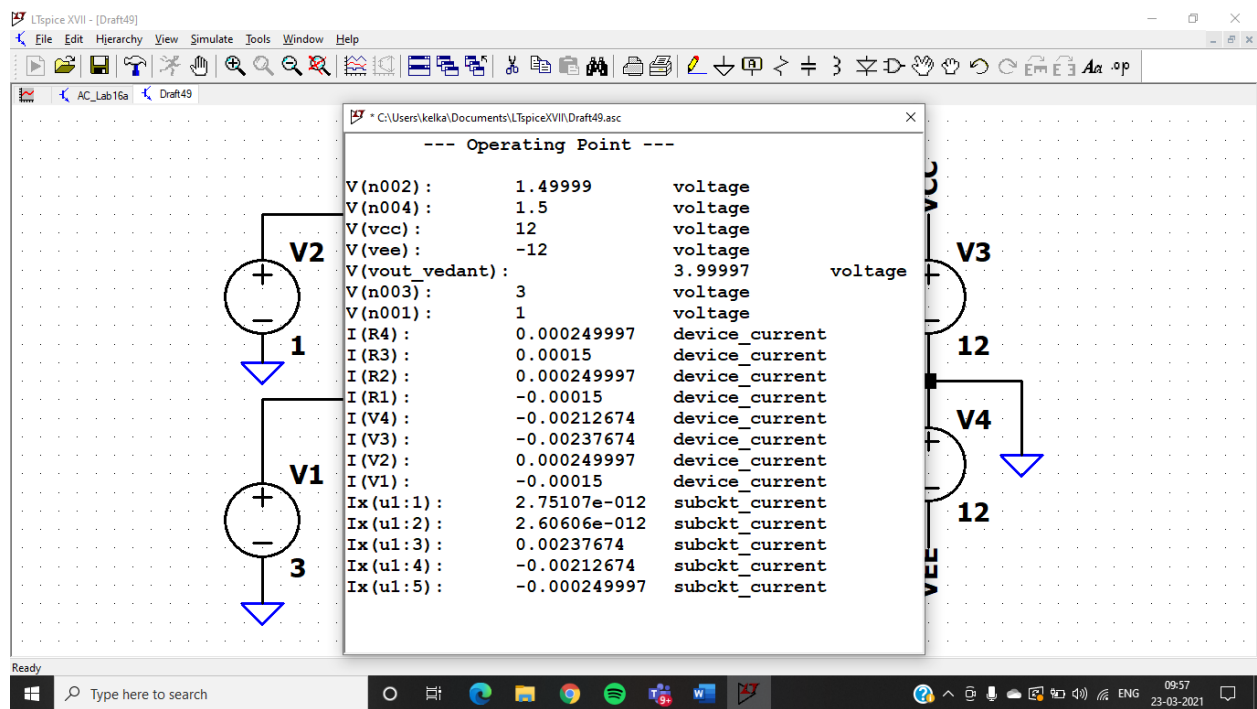
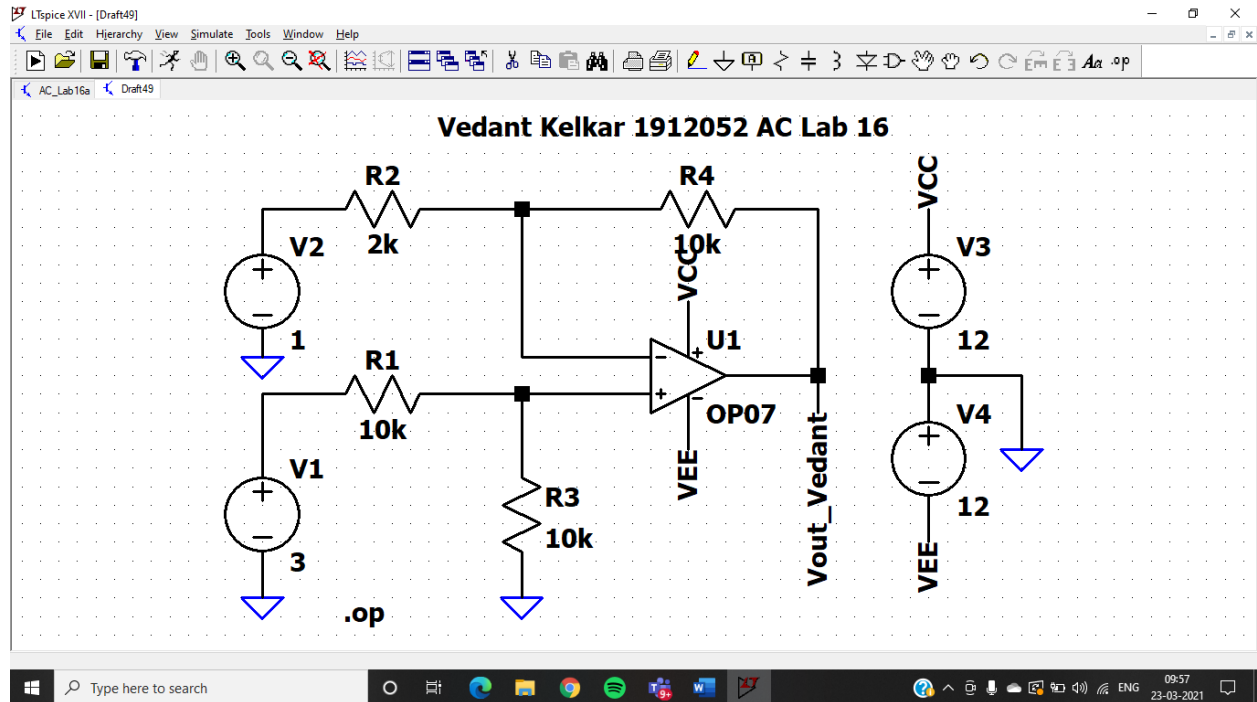
STUDENTS WORK AREA STARTS HERE

Inverting Adder



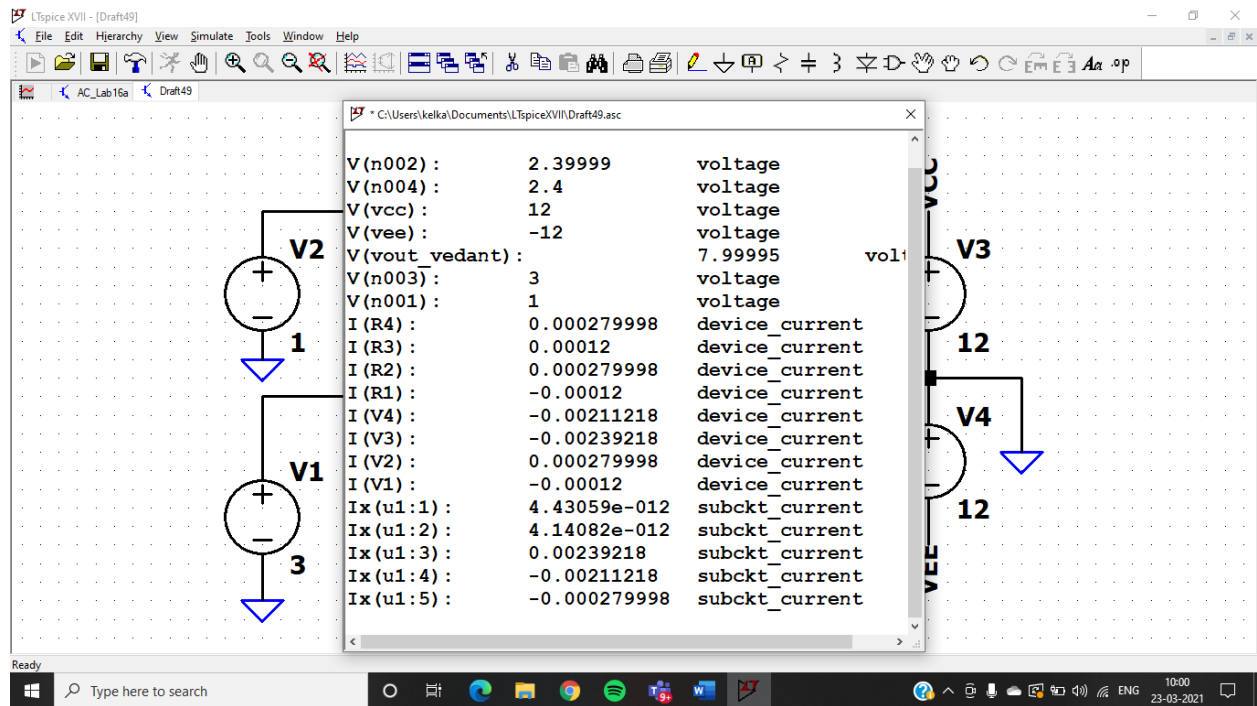
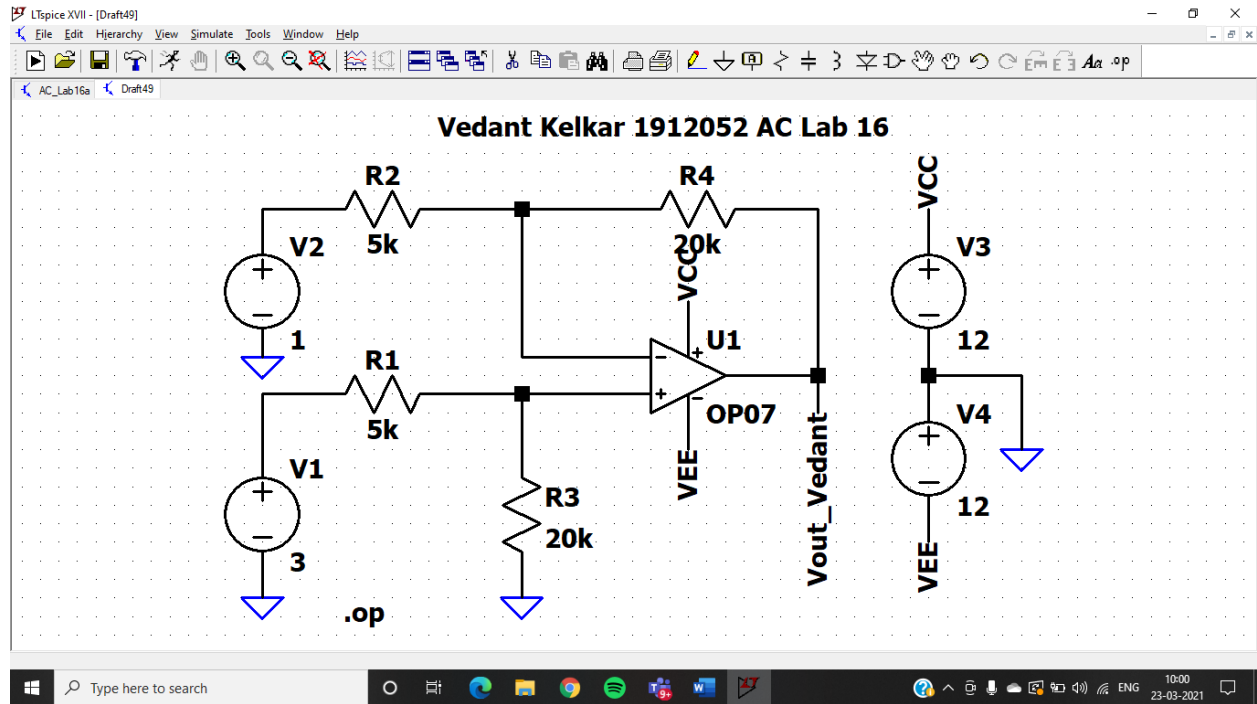
Vout=-7.49981V

Subtractor



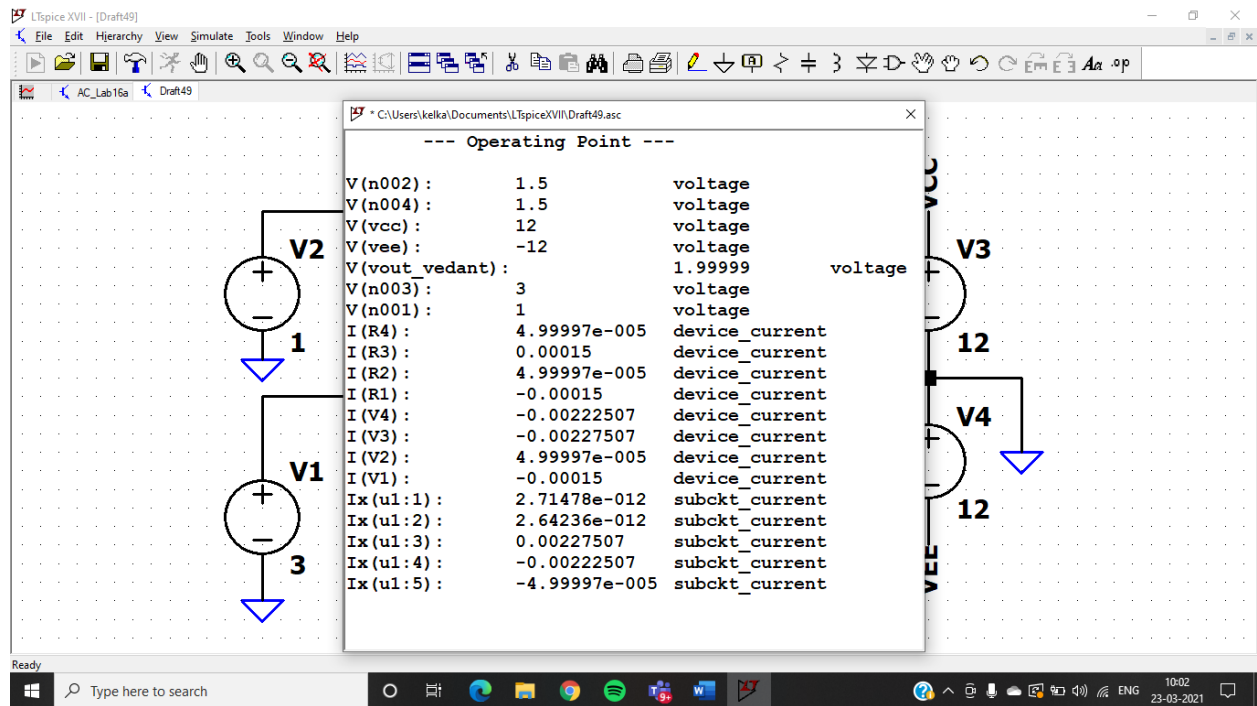
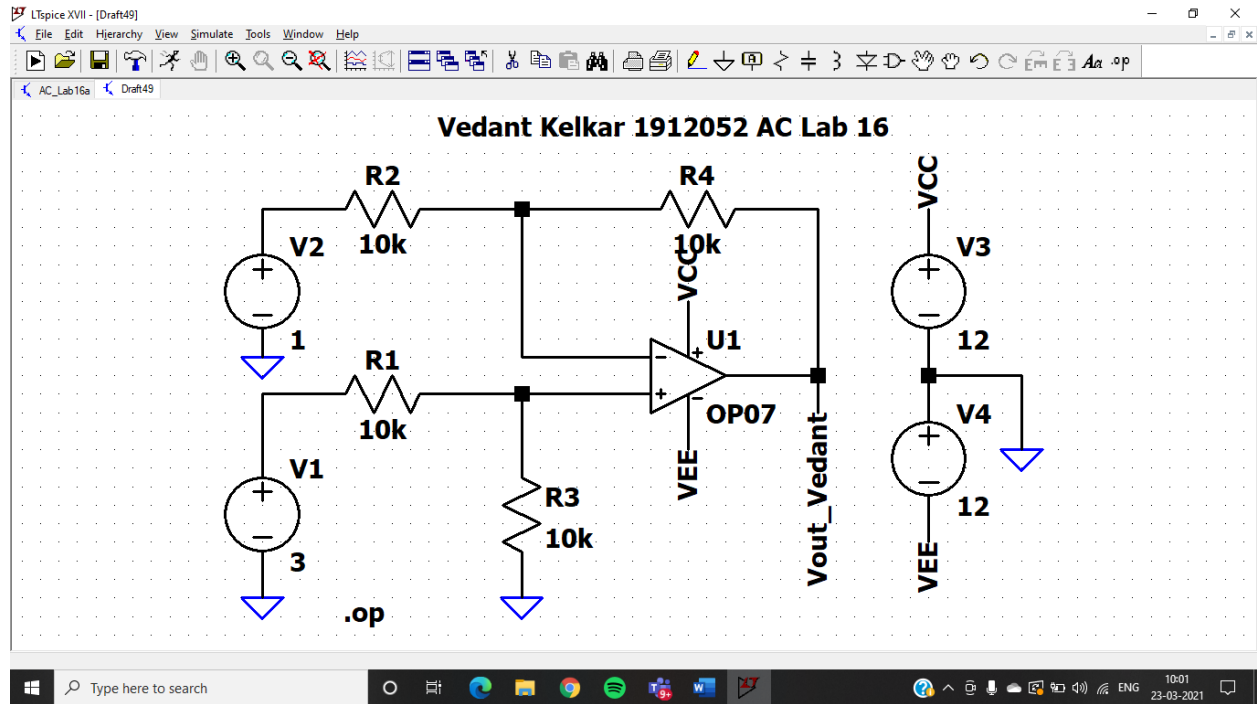
Vout= 3.99997V

R1=R2=5k R3=R4=20k



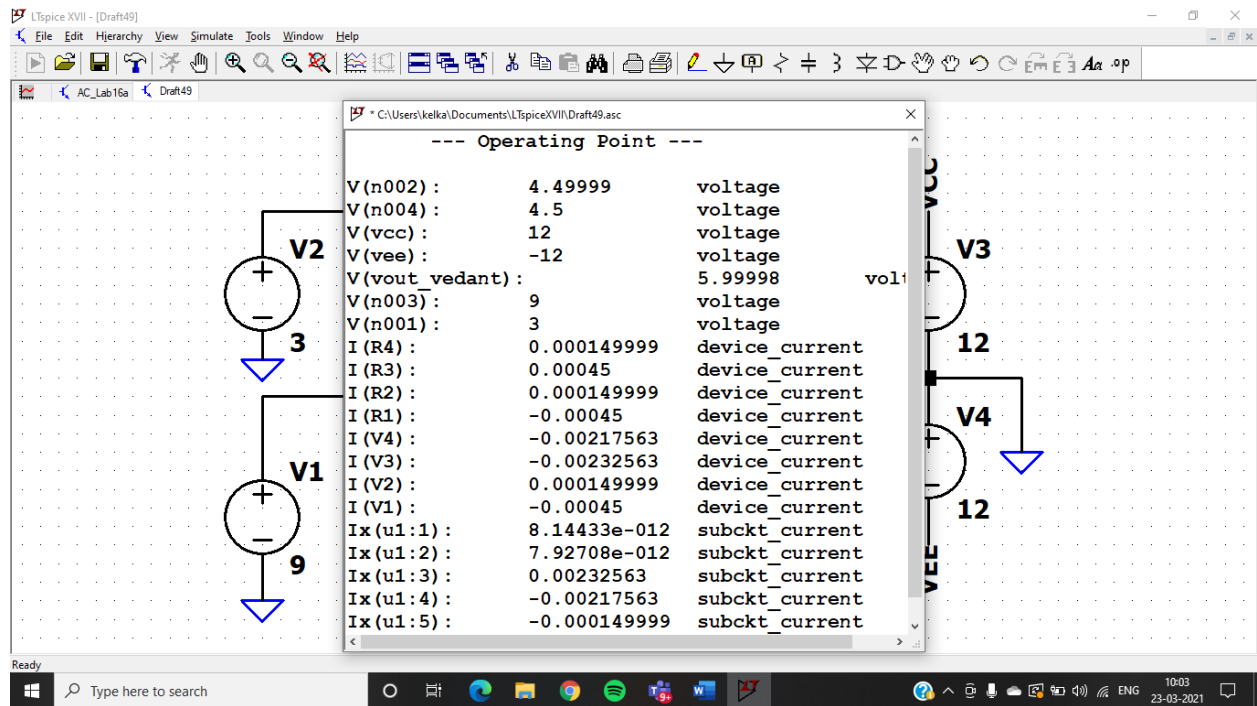
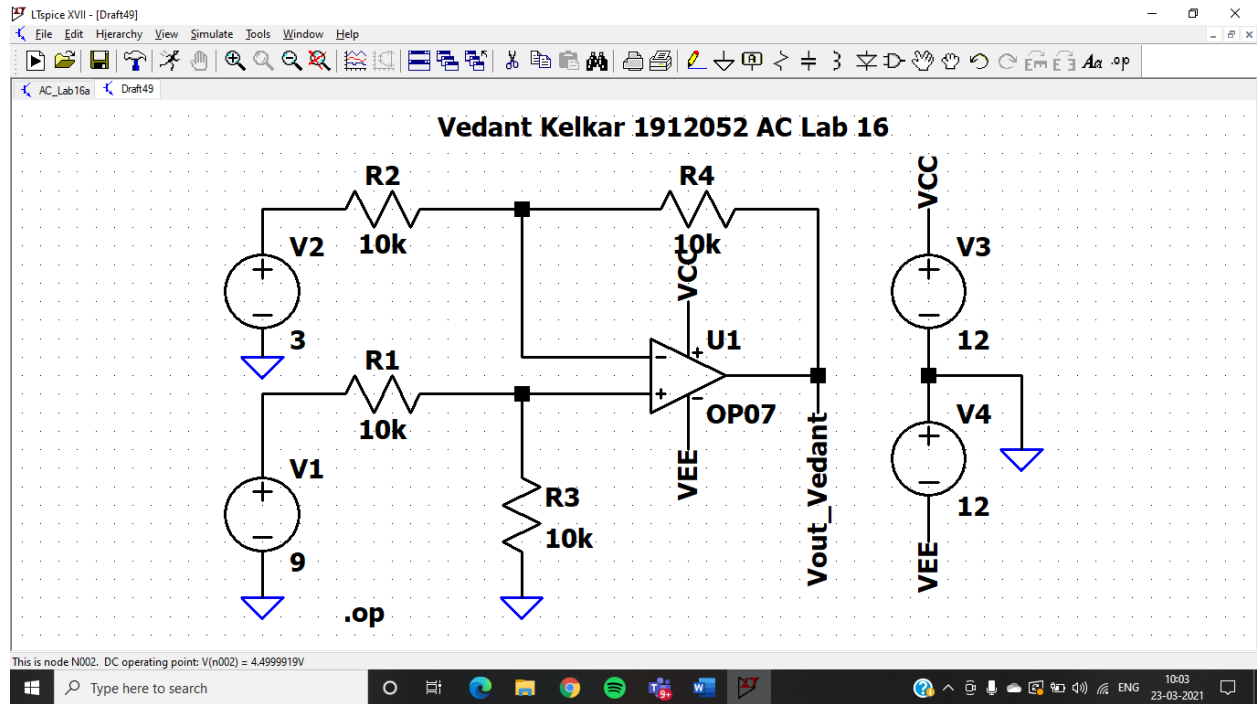
Vout=7.99995V

R1=R2=R3=R4=10k



Vout=1.99999V

V1=9V V2=3V



Vout=5.99998V

AC LAB 16 is approved: Inderjit Singh Dhanjal

