

AUDIT COURSE ELECTRONIC CIRCUITS 1: SIMULATION BASED STUDY

LAB 14

Kindly update your name and roll no, once this document is shared with you

Time slot to complete your work is **40 MINUTES**

Date: 15/9/2020

Kindly upload your schematic & waveform images here, every 10 minutes, indicating your progress and intention to completion of WORK within time slot allotted

Time slot allotted to you all for the completion of WEEK 7 DAY 2 is 40 MINUTES

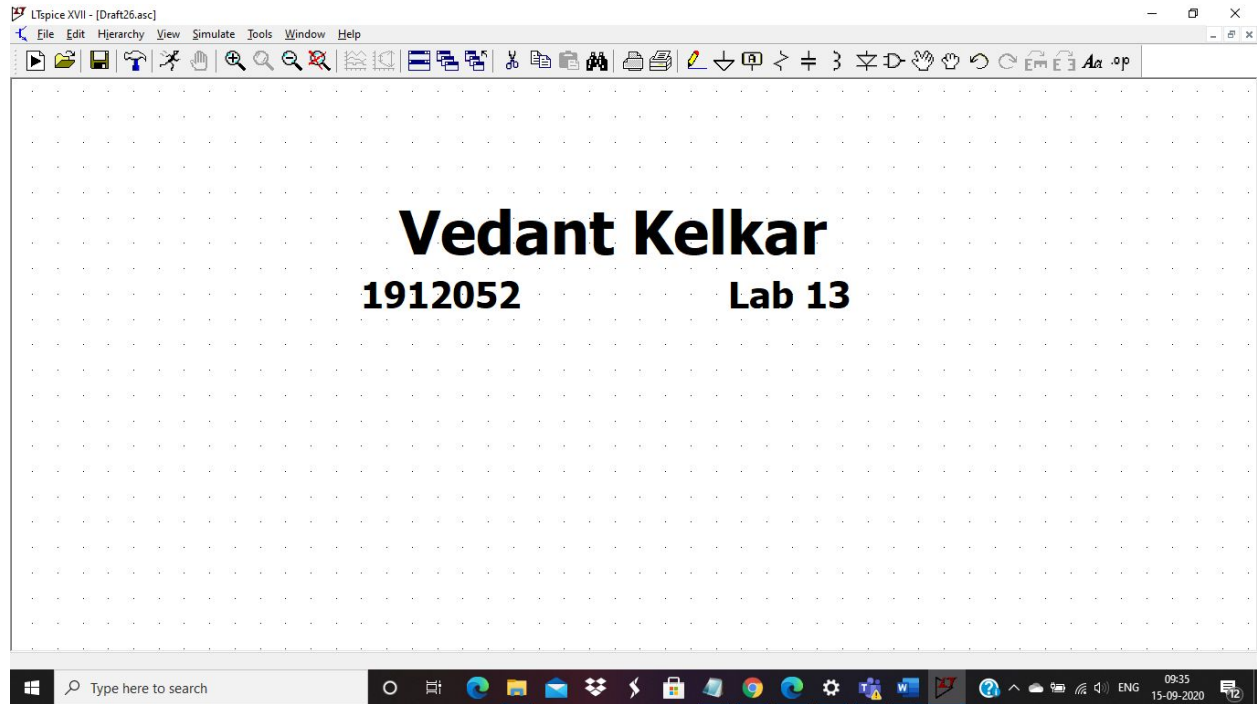
Kindly upload your work (only circuit schematic & waveform in LTSpice) in the shared google doc between this time slot only.

Follow these instruction strictly:

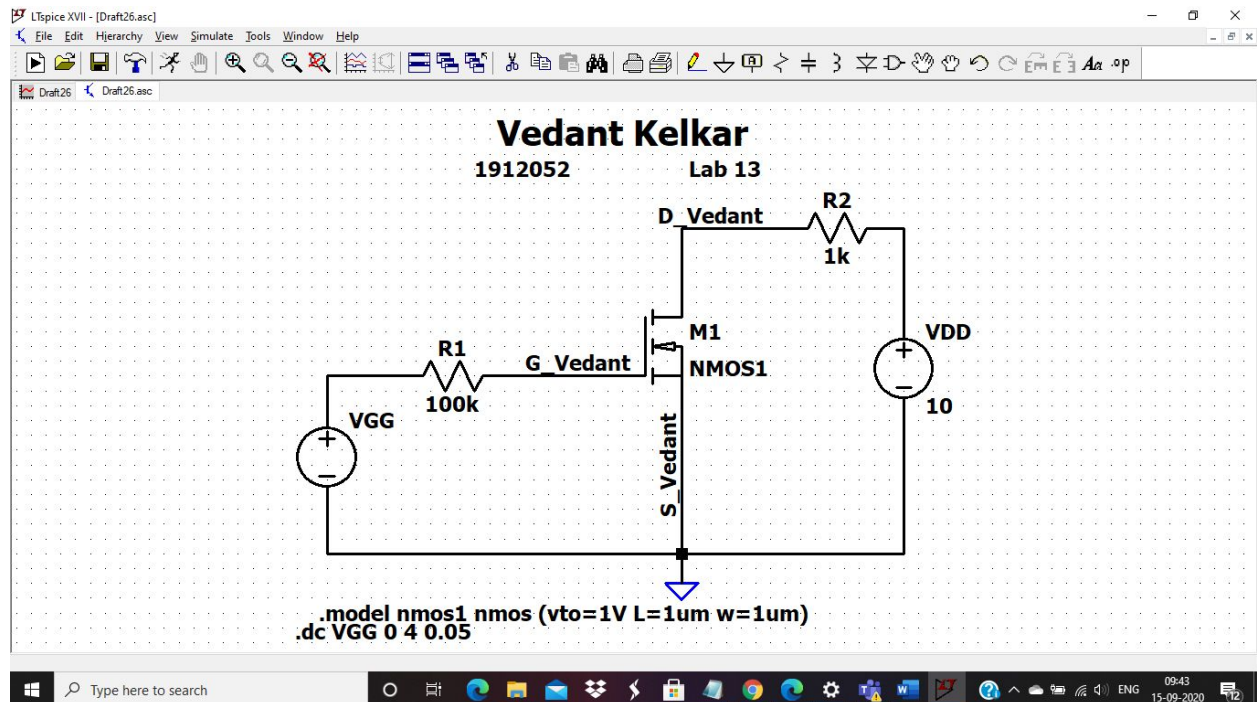
- 1, Start sharp ON TIME, by posting your name and roll no and **screenshot of your LT spice work screen (time and date MUST BE VISIBLE)**
2. Upload your work every 10 minutes, i.e LT spice work screen
3. This means you will upload LT spice work screen 4 times during this time slot.
4. Point 3 indicates your readiness and presences for completion of WEEK 7 DAY 2

You are entitled for 1 CREDIT per Lab only if you follow above instruction to the details

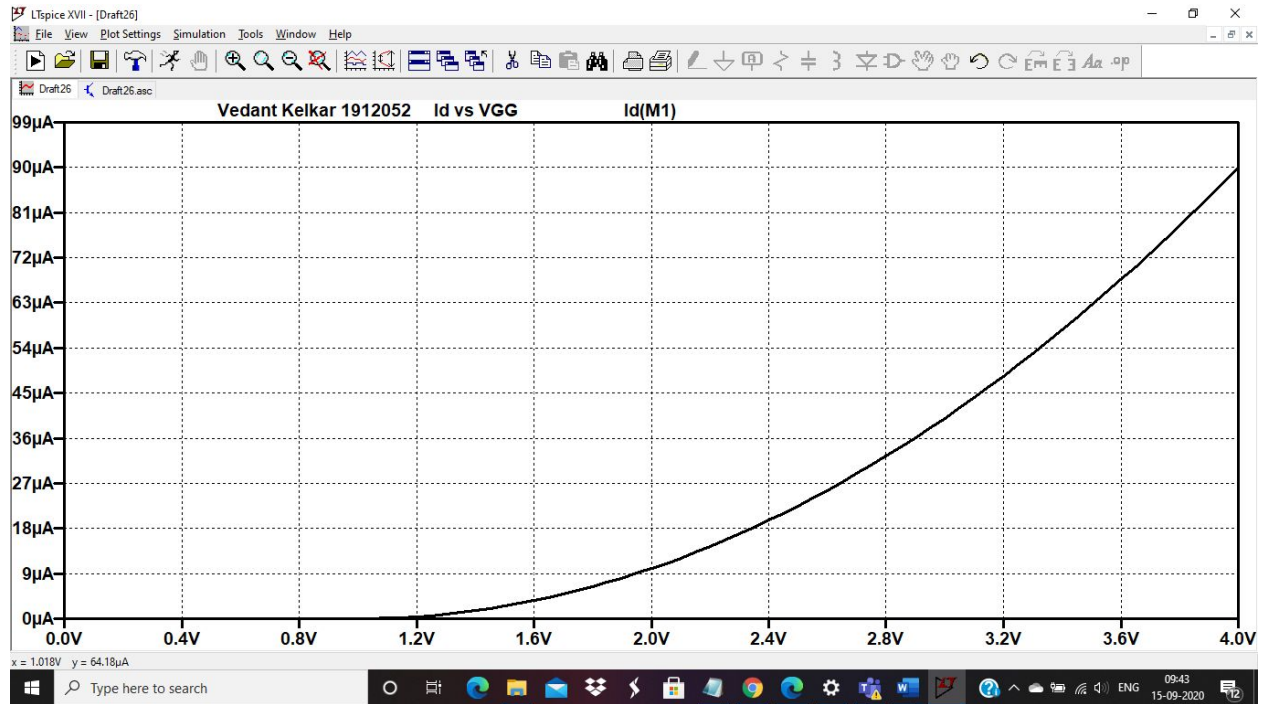
STUDENTS WORK AREA STARTS HERE



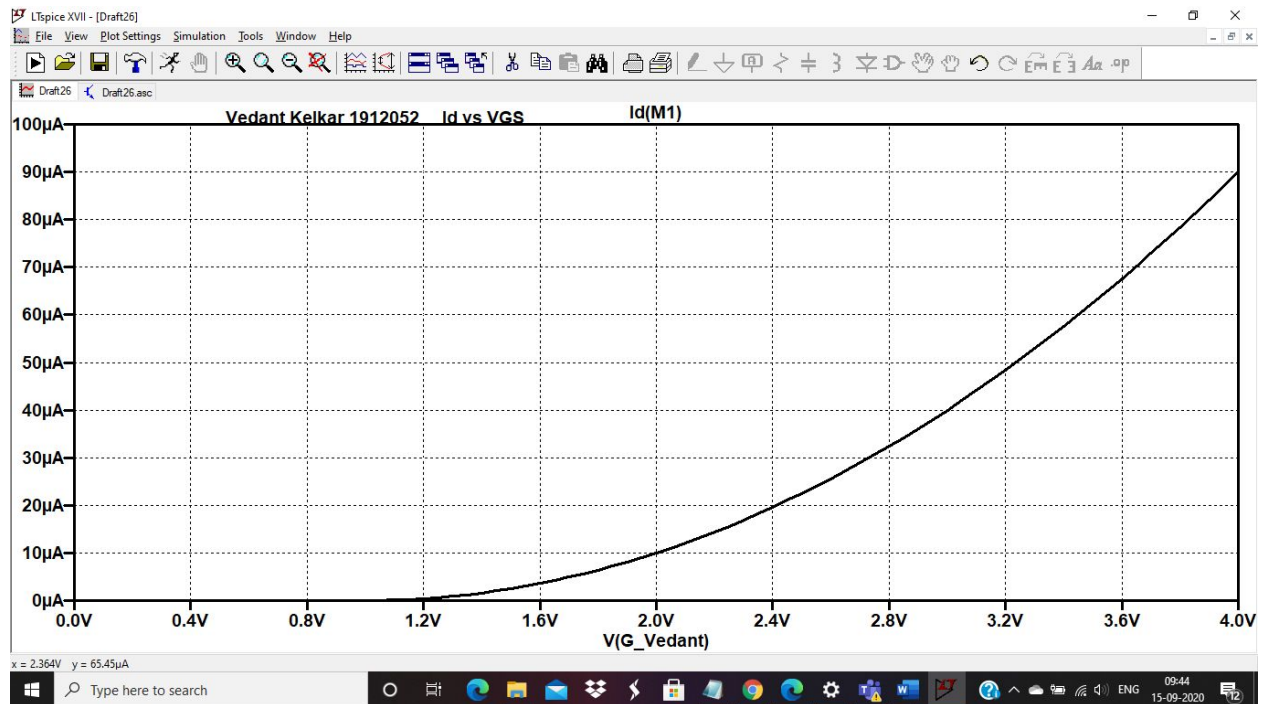
N Channel MOSFET Transfer characteristics



Id vs VGG

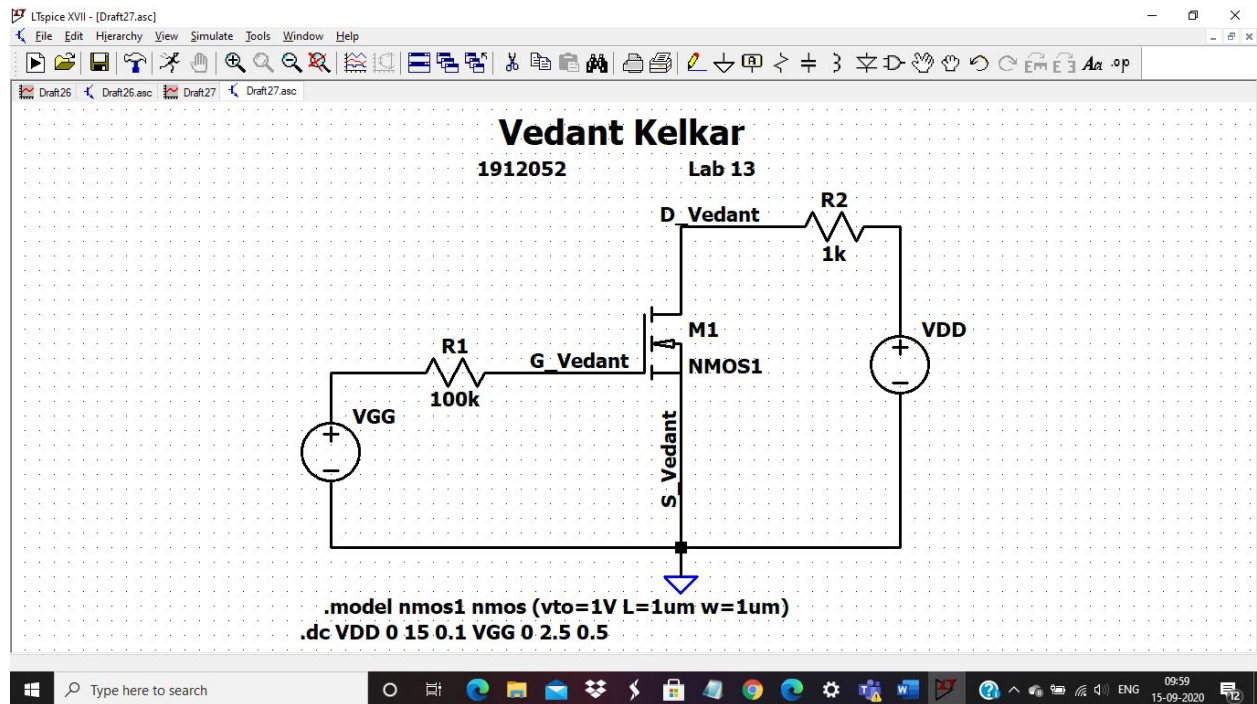


Id vs VGS

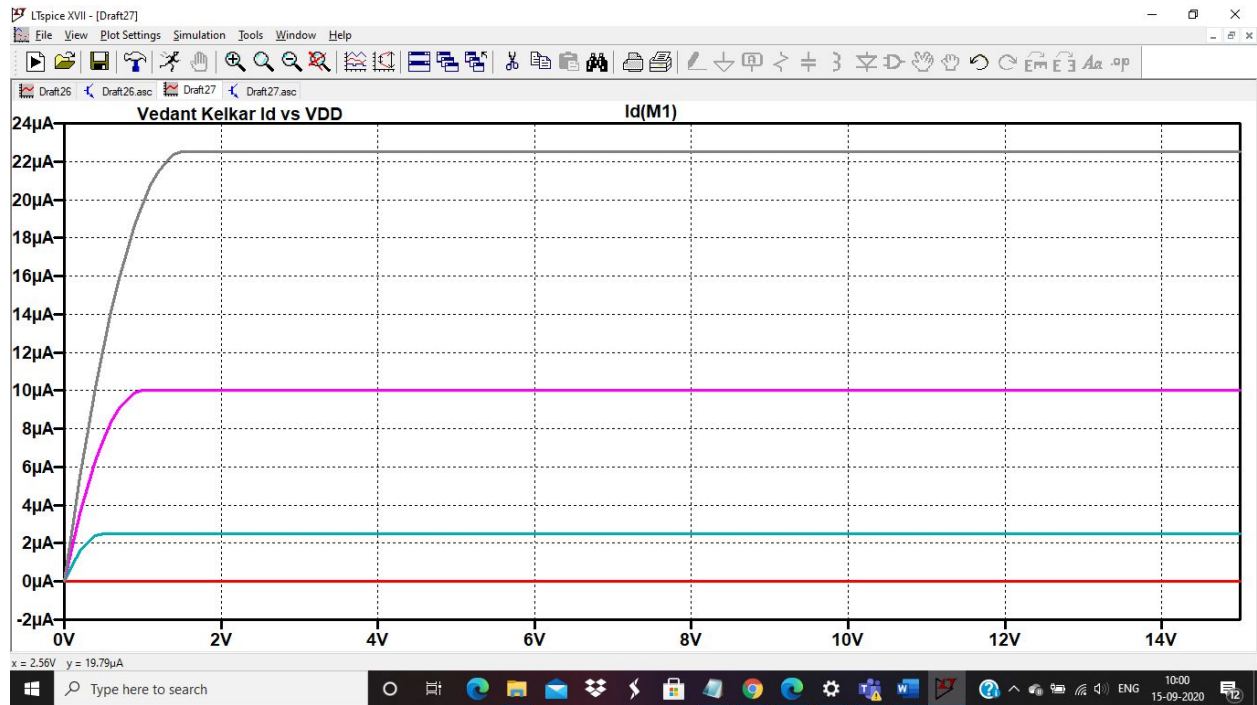


VGS	Id
0V	20.010001pA
0.5V	20.010001pA
1V	804.94008pA
1.5V	2.5082617μA
2V	9.9387952μA
3.5V	62.539657μA
4V	90.000023μA

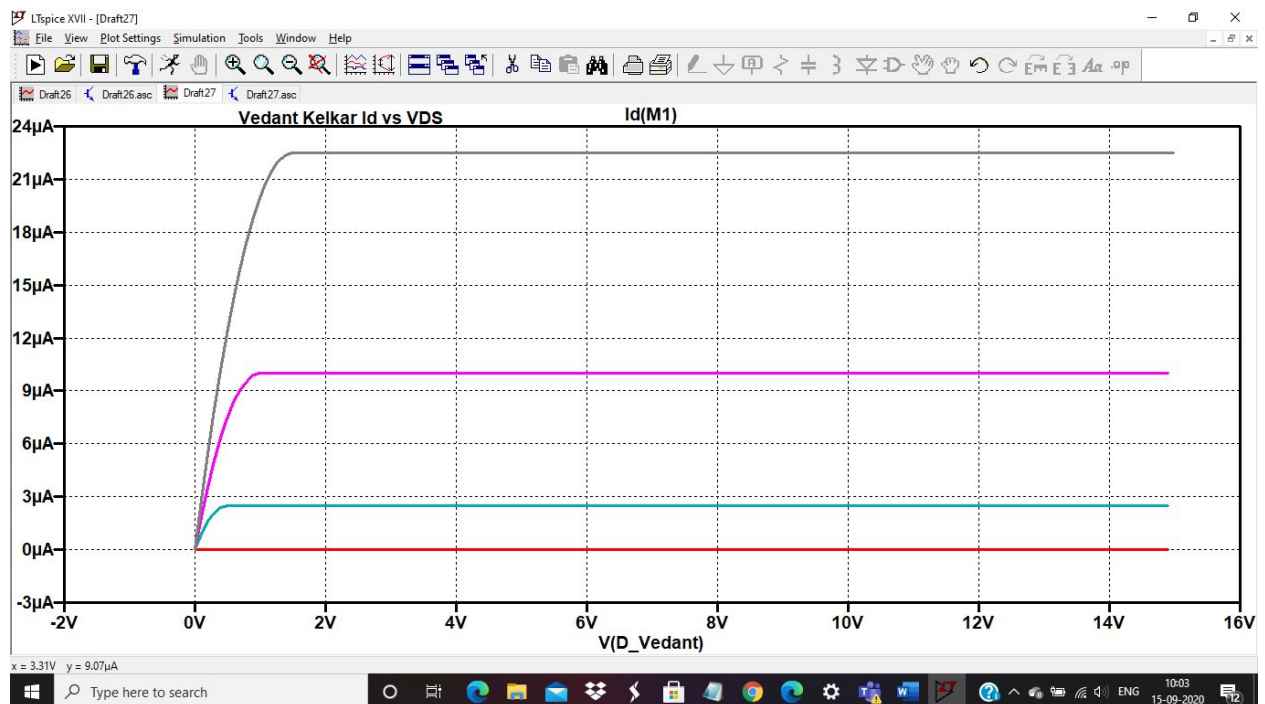
N Channel MOSFET output characteristics



ID vs VDD



ID vs VDS



VDS	ID
0V	0
0.2V	3.53 uA
0.4V	6.38 uA
0.8V	9.59 uA
2V	10.01uA
5V	10.01 uA
10V	10.01 uA

AC LAB 14 is approved: Inderjit Singh Dhanjal