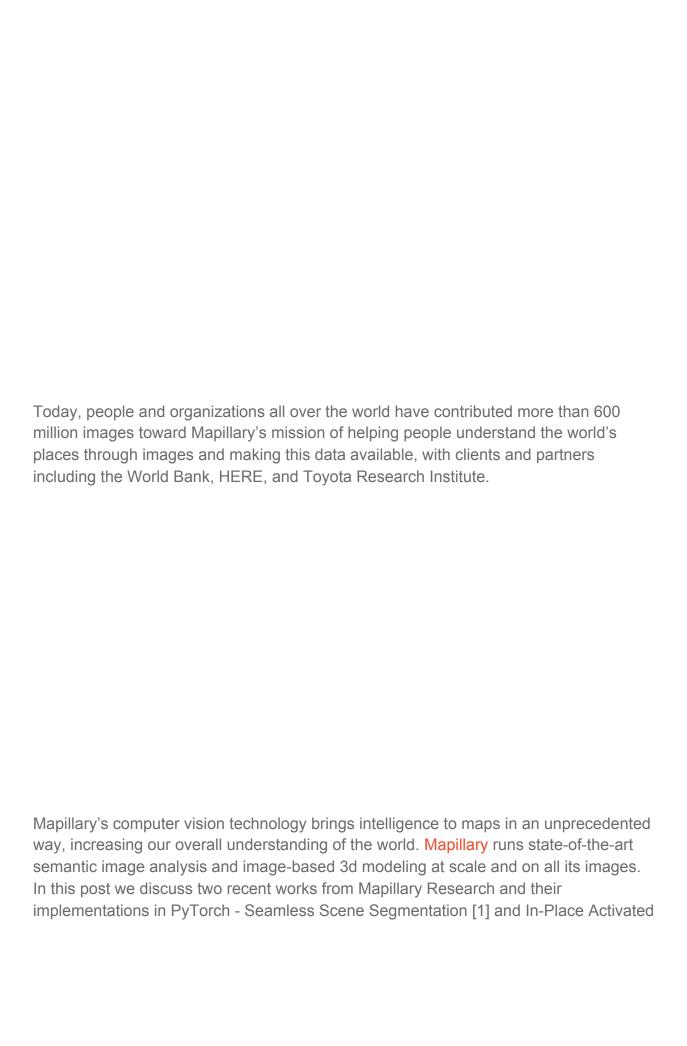
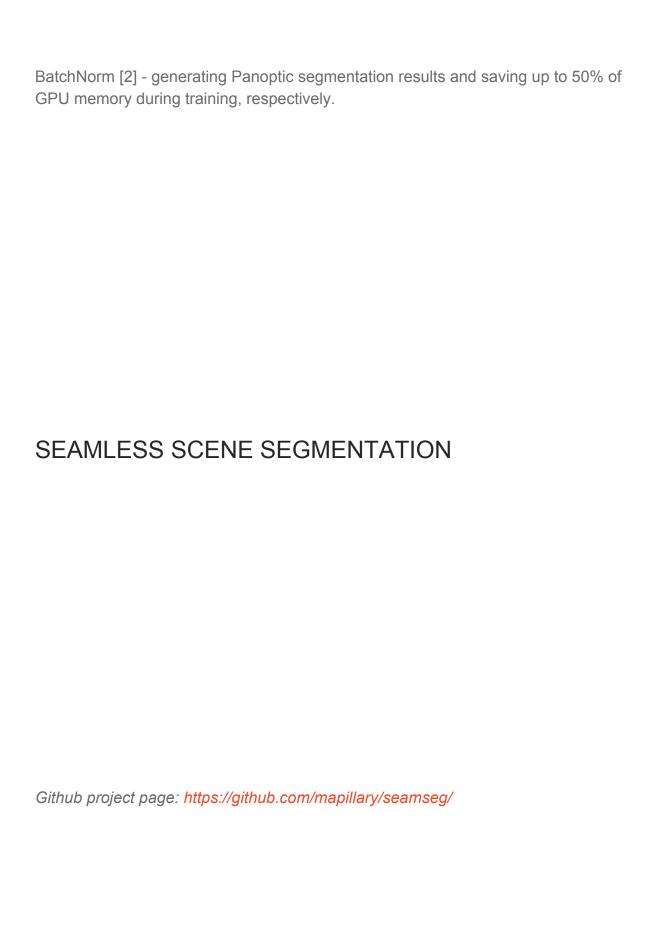
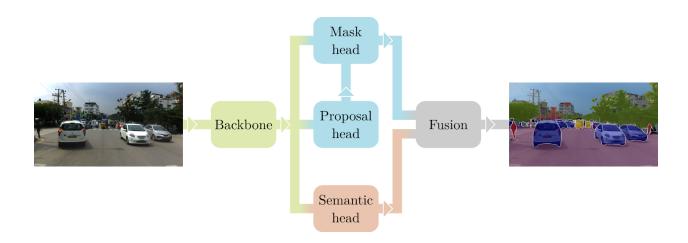
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Mapillary Research: Seamless Scene Segmentation and In-Place Activated BatchNorm

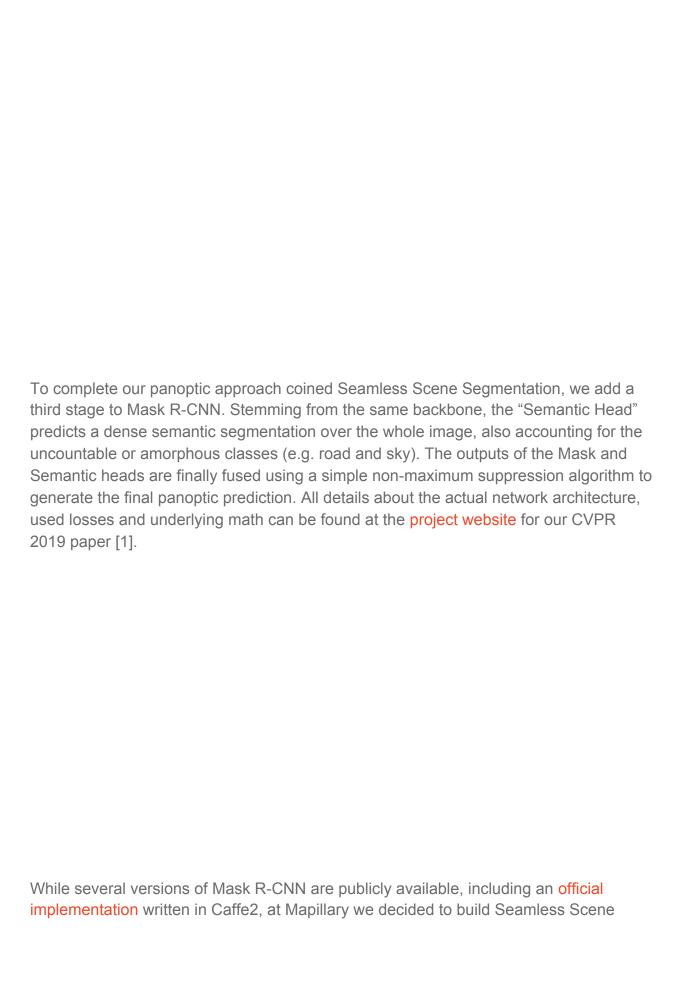






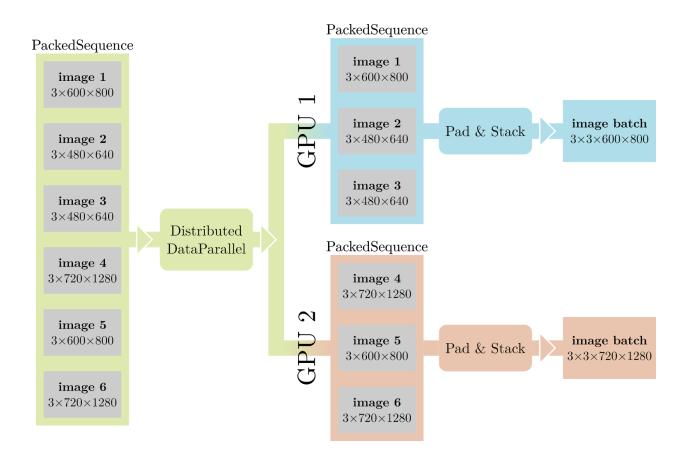


The objective of Seamless Scene Segmentation is to predict a "panoptic" segmentation [3] from an image, that is a complete labeling where each pixel is assigned with a class id and, where possible, an instance id. Like many modern CNNs dealing with instance detection and segmentation, we adopt the Mask R-CNN framework [4], using ResNet50 + FPN [5] as a backbone. This architecture works in two stages: first, the "Proposal Head" selects a set of candidate bounding boxes on the image (i.e. the proposals) that could contain an object; then, the "Mask Head" focuses on each proposal, predicting its class and segmentation mask. The output of this process is a "sparse" instance segmentation, covering only the parts of the image that contain countable objects (e.g. cars and pedestrians).

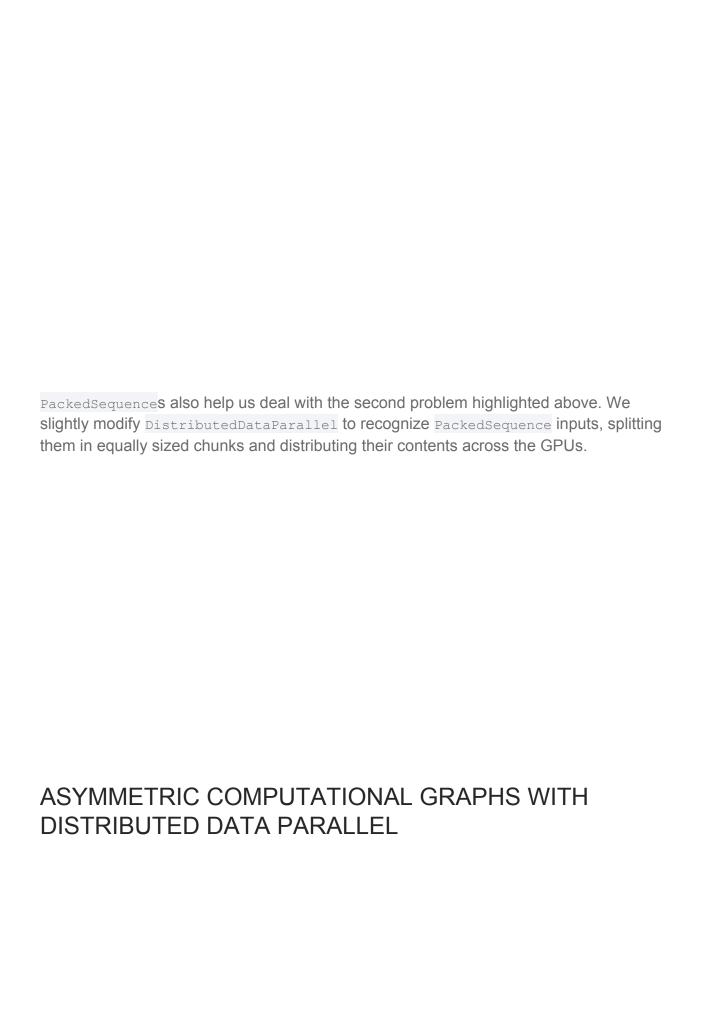




each image, and the images themselves can have different sizes. While this is not a problem per-se — one could just process images one at a time — we would still like to exploit batch-level parallelism as much as possible. Furthermore, when performing distributed training with multiple GPUs, <code>DistributedDataParallel</code> expects its inputs to be batched, uniformly-sized tensors.



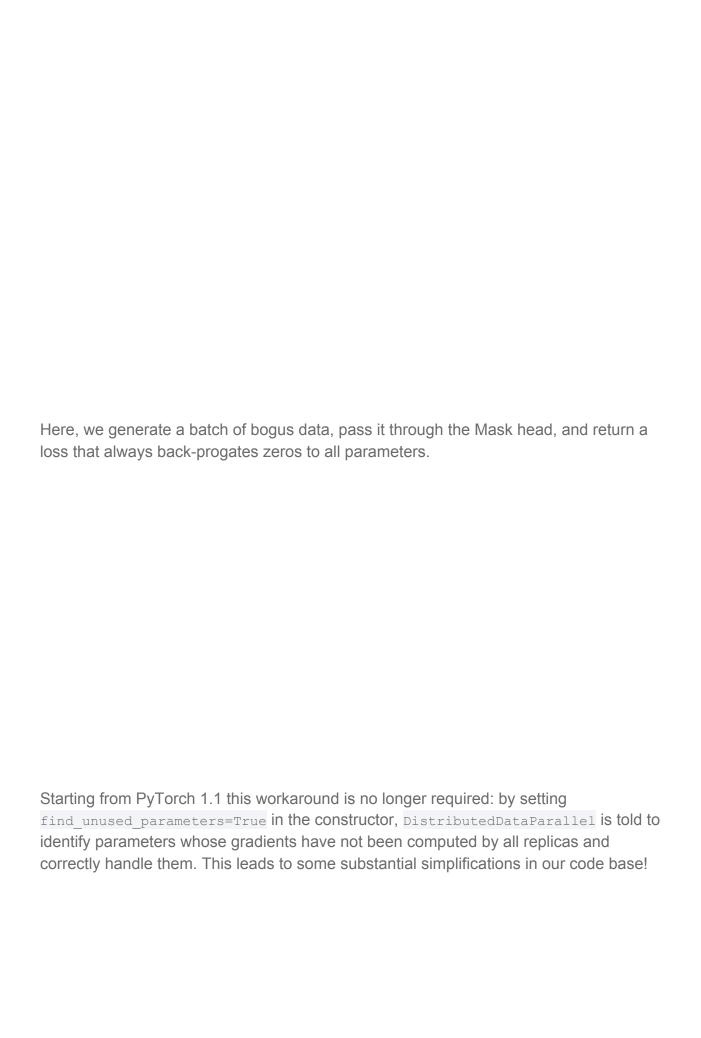
Our solution to these issues is to wrap each batch of variable-sized tensors in a PackedSequence. PackedSequence is little more than a glorified list class for tensors, tagging its contents as "related", ensuring that they all share the same type, and providing useful methods like moving all the tensors to a particular device, etc. When performing light-weight operations that wouldn't be much faster with batch-level parallelism, we simply iterate over the contents of the PackedSequence in a for loop. When performance is crucial, e.g. in the body of the network, we simply concatenate the contents of the PackedSequence, adding zero padding as required (like in RNNs with variable-length inputs), and keeping track of the original dimensions of each tensor.



Another, perhaps more subtle, peculiarity of our network is that it can generate asymmetric computational graphs across GPUs. In fact, some of the modules that compose the network are "optional", in the sense that they are not always computed for all images. As an example, when the Proposal head doesn't output any proposal, the Mask head is not traversed at all. If we are training on multiple GPUs with <code>DistributedDataParallel</code>, this results in one of the replicas not computing gradients for the Mask head parameters.

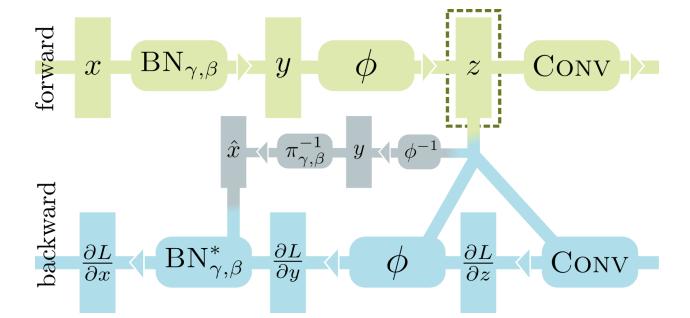
Prior to PyTorch 1.1, this resulted in a crash, so we had to develop a workaround. Our simple but effective solution was to compute a "fake forward pass" when no actual forward is required, i.e. something like this:

```
def fake_forward():
    fake_input = get_correctly_shaped_fake_input()
    fake_output = mask_head(fake_input)
    fake_loss = fake_output.sum() * 0
    return fake_loss
```

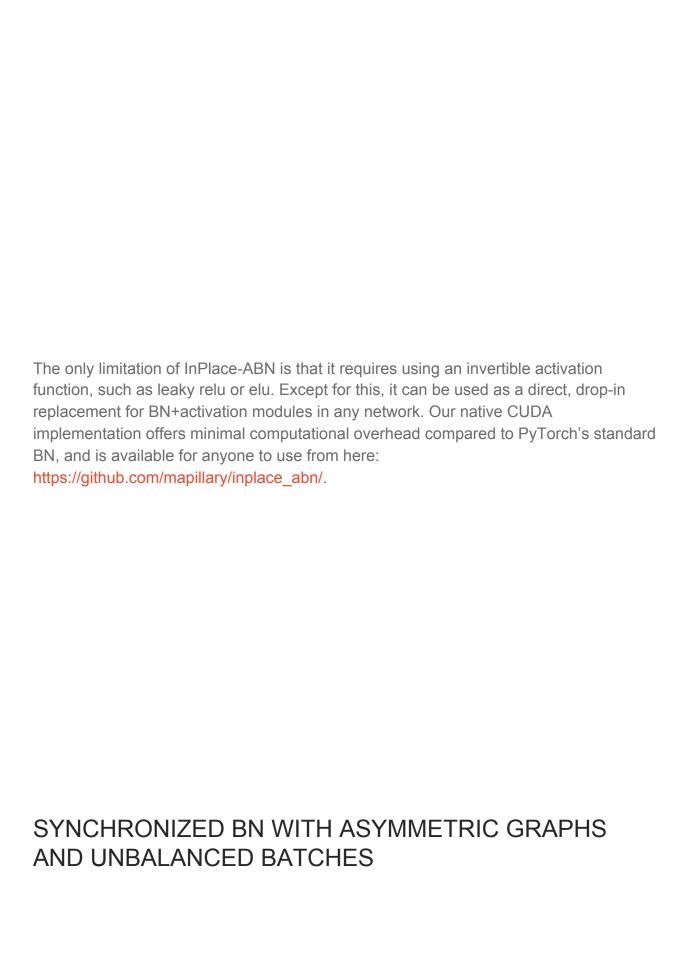




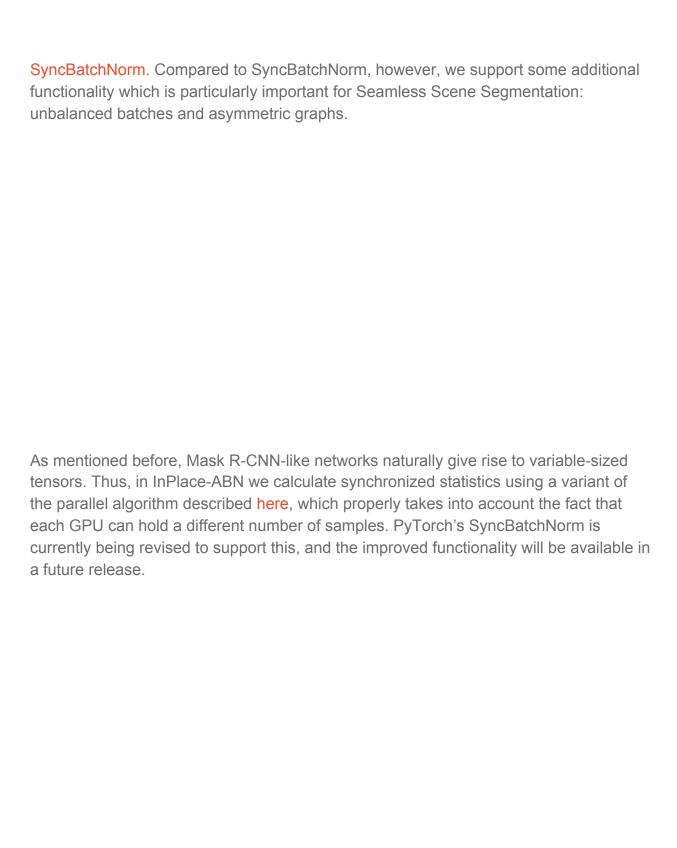
Most researchers would probably agree that there are always constraints in terms of available GPU resources, regardless if their research lab has access to only a few or multiple thousands of GPUs. In a time where at Mapillary we still worked at rather few and mostly 12GB Titan X - style prosumer GPUs, we were searching for a solution that virtually enhances the usable memory during training, so we would be able to obtain and push state-of-the-art results on dense labeling tasks like semantic segmentation. In-place activated BatchNorm is enabling us to use up to 50% more memory (at little computational overhead) and is therefore deeply integrated in all our current projects (including Seamless Scene Segmentation described above).



When processing a BN-Activation-Convolution sequence in the forward pass, most deep learning frameworks (including PyTorch) need to store two big buffers, i.e. the input x of BN and the input z of Conv. This is necessary because the standard implementations of the backward passes of BN and Conv depend on their inputs to calculate the gradients. Using InPlace-ABN to replace the BN-Activation sequence, we can safely discard x, thus saving up to 50% GPU memory at training time. To achieve this, we rewrite the backward pass of BN in terms of its output y, which is in turn reconstructed from z by inverting the activation function.

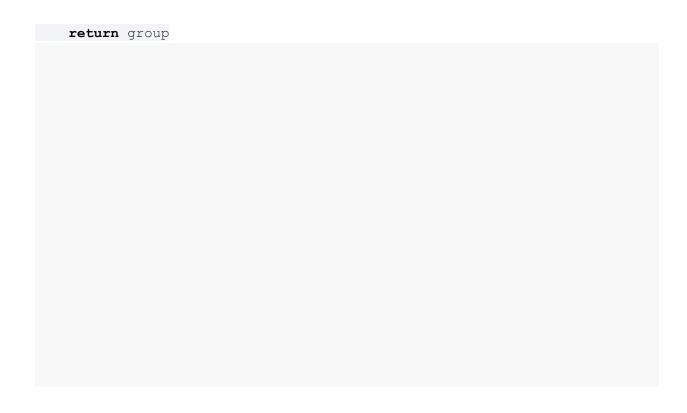




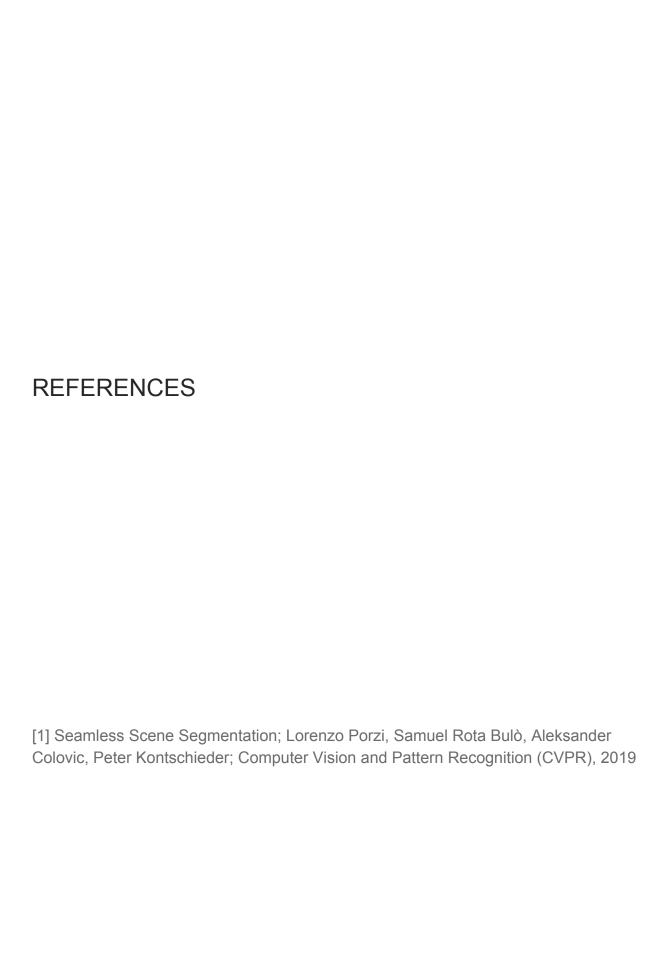


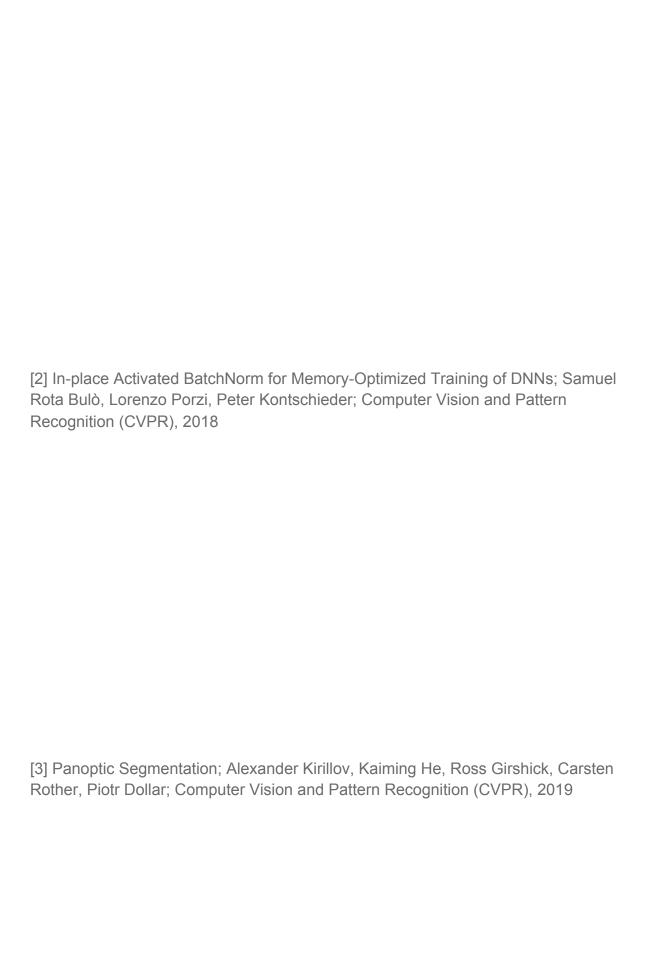
Asymmetric graphs (in the sense mentioned above) are another complicating factor one has to deal with when creating a synchronized BatchNorm implementation. Luckily, PyTorch's distributed group functionality allows us to restrict distributed communication to a subset of workers, easily excluding those that are currently inactive. The only missing piece is that, in order to create a distributed group, each process needs to know the ids of all processes that will participate in the group, and even processes that are not part of the group need to call the new_group() function. In InPlace-ABN we handle it with a function like this:

```
import torch
import torch.distributed as distributed
def active group(active):
   """Initialize a distributed group where each process can independently
decide whether to participate or not"""
world size = distributed.get world size()
rank = distributed.get rank()
# Gather active status from all workers
 active = torch.tensor(rank if active else -1, dtype=torch.long,
device=torch.cuda.current device())
active workers = torch.empty(world size, dtype=torch.long,
device=torch.cuda.current device())
distributed.all_gather(list(active_workers.unbind(0)), active)
# Create group
active workers = [int(i) for i in active workers.tolist() if i != -1]
group = distributed.new group(active workers)
```



First each process, including inactive ones, communicates its status to all others through an all_gather call, then it creates the distributed group with the shared information. In the actual implementation we also include a caching mechanism for groups, since <code>new_group()</code> is usually too expensive to call at each batch.





[4] Mask R-CNN; Kaiming He, Georgia Gkioxari, Piotr Dollar, Ross Girshick; International Conference on Computer Vision (ICCV), 2017
international conference on computer vision (100v), 2017
[5] Feature Pyramid Networks for Object Detection; Tsung-Yi Lin, Piotr Dollar, Ross Girshick, Kaiming He, Bharath Hariharan, Serge Belongie; Computer Vision and Pattern Recognition (CVPR), 2017
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