EE210: Analog Electronics - Quiz 3

NAME (in capital)

Roll No

Time: 15 minutes

1): Consider the circuit in Fig. 1. $V_{DC}=5\,V$. A three terminal non-linear element has been used, whose terminals are defined in the inset. The element has the following characteristics.

 $I_D = I_S = \alpha V_{GS}^2$ for $V_{GS} \ge 0$ and $V_{DS} \ge 0$. $I_D = I_S = 0$ otherwise.

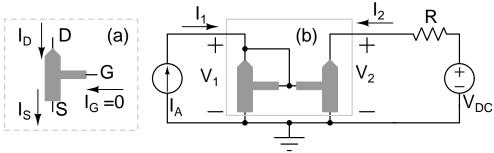


Fig. 1. Problem 1

a) : Assume $I_A = 1 \, mA$, $\alpha = 1 \, mA/V^2$ and $R1 = 1 k\Omega$. Find the small-signal two-port y-parameters of the network within the box (in Fig. 1(b)) and sketch the small-signal two-port network.

$$A = \alpha V_1^2 \Rightarrow V_1 = IV$$

$$A = \alpha V_1^2 \Rightarrow D = IMA$$

$$V_2 = V_{DC} - I_2 R$$

$$= 4V$$

$$A = AV$$

$$AV_1 = AMS$$

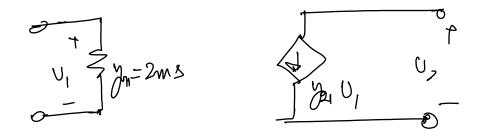
$$AV_2 = AV$$

$$AV_2 = AV$$

$$AV_3 = AV$$

$$AV_4 = AMS$$

..contd..



b) : If $I_A=1\,mA+0.1mA\sin(\omega t)$, find the small signal voltage across V_1 and V_2 . [4]

