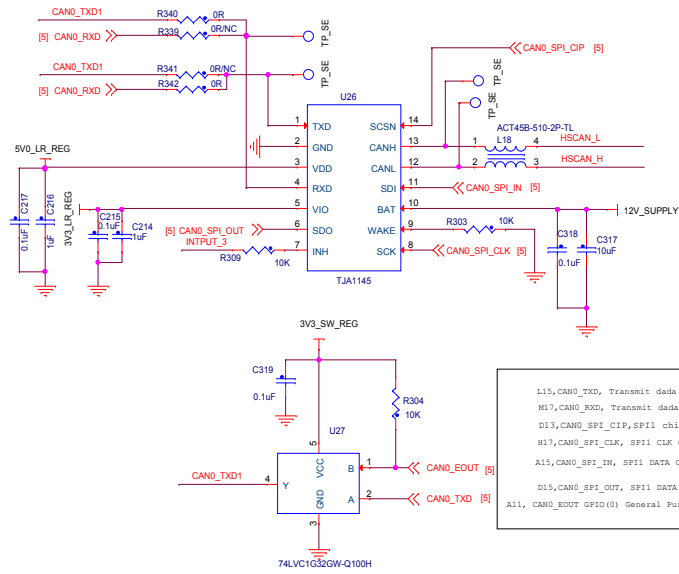


CAN PHY



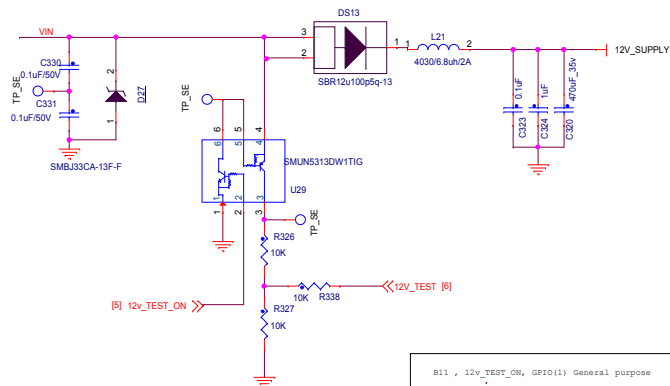
```

L15,CAN0_TXD, Transmit data
M17,CAN0_RXD, Transmit data
D13,CAN0_SPI_CIP, SPI1 chip select
H17,CAN0_SPI_CLK, SPI1 CLK OUT
A15,CAN0_SPI_IN, SPI1 DATA OUT

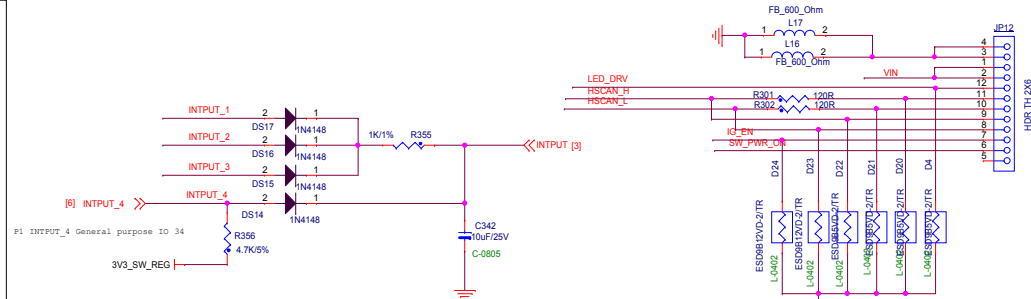
D15,CAN0_SPI_OUT, SPI1 DATA IN
A11, CAN0_EOUT GPIO(0) General Purpose IO 0

```

12V\_TEST



```
B11 , 12v_TEST_ON, GPIO(1) General purpose
T3 , 12V_TEST, AN(11) ADC1 Analog Input 11
```

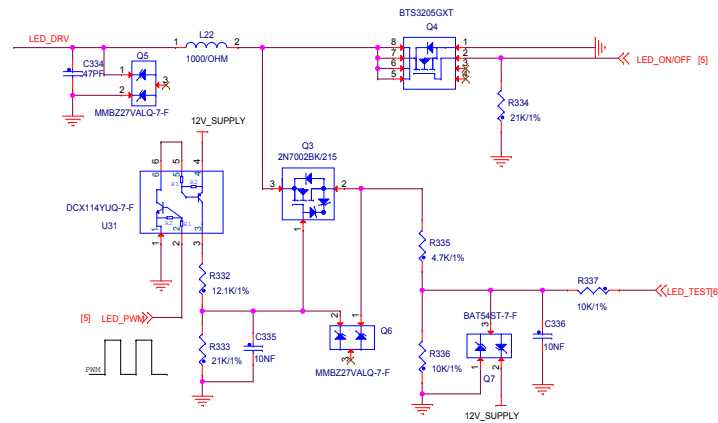


LED\_DRV\_TEST

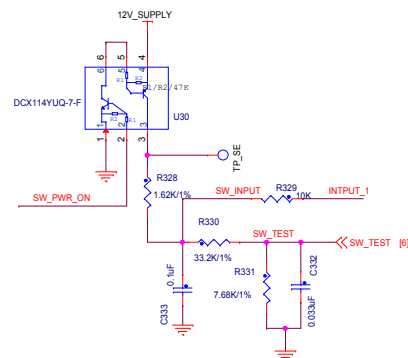
```
B13 , LED_PWM, GPIO(56) General Purpose IO 56

T2 , LED_TEST, AN(13) Analog Input 13

M15 , LED_ON/OFF GPIO(14) General Purpose IO 14
```

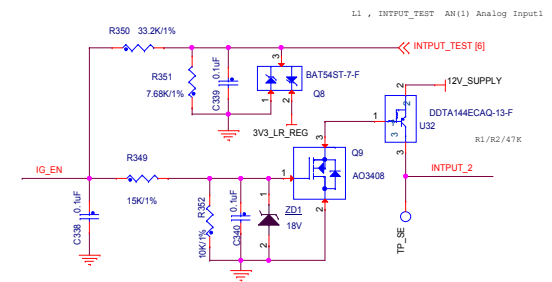


## SW PWR ON TEST



```
R3 , SW_TEST, AN(12)    Analog Input 12
```

## IG EN TEST

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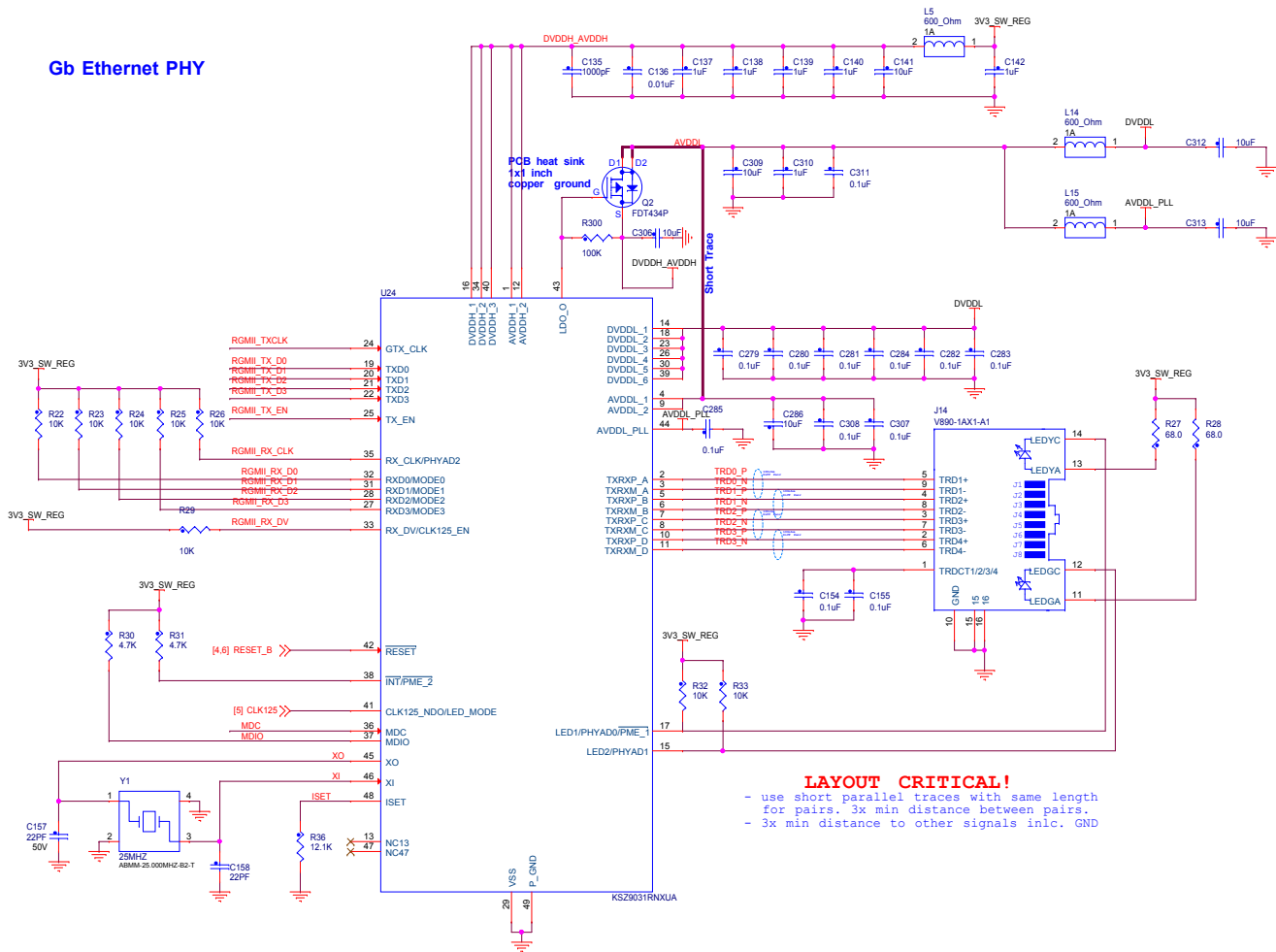
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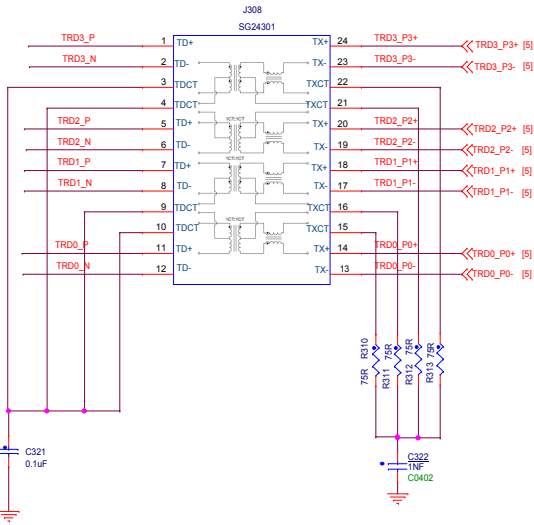
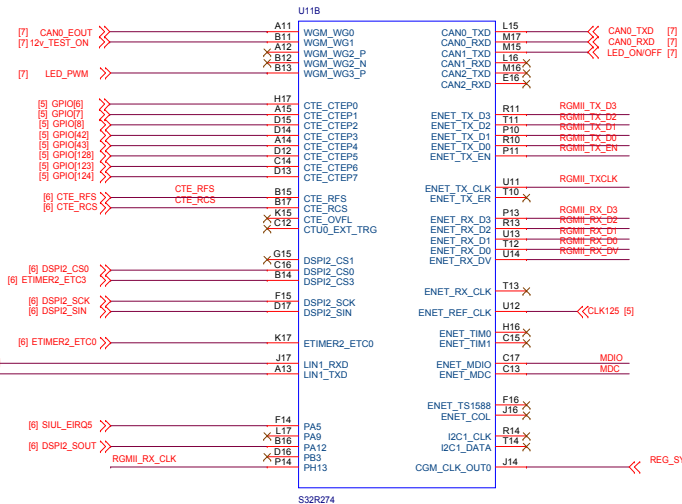
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## Gb Ethernet PHY

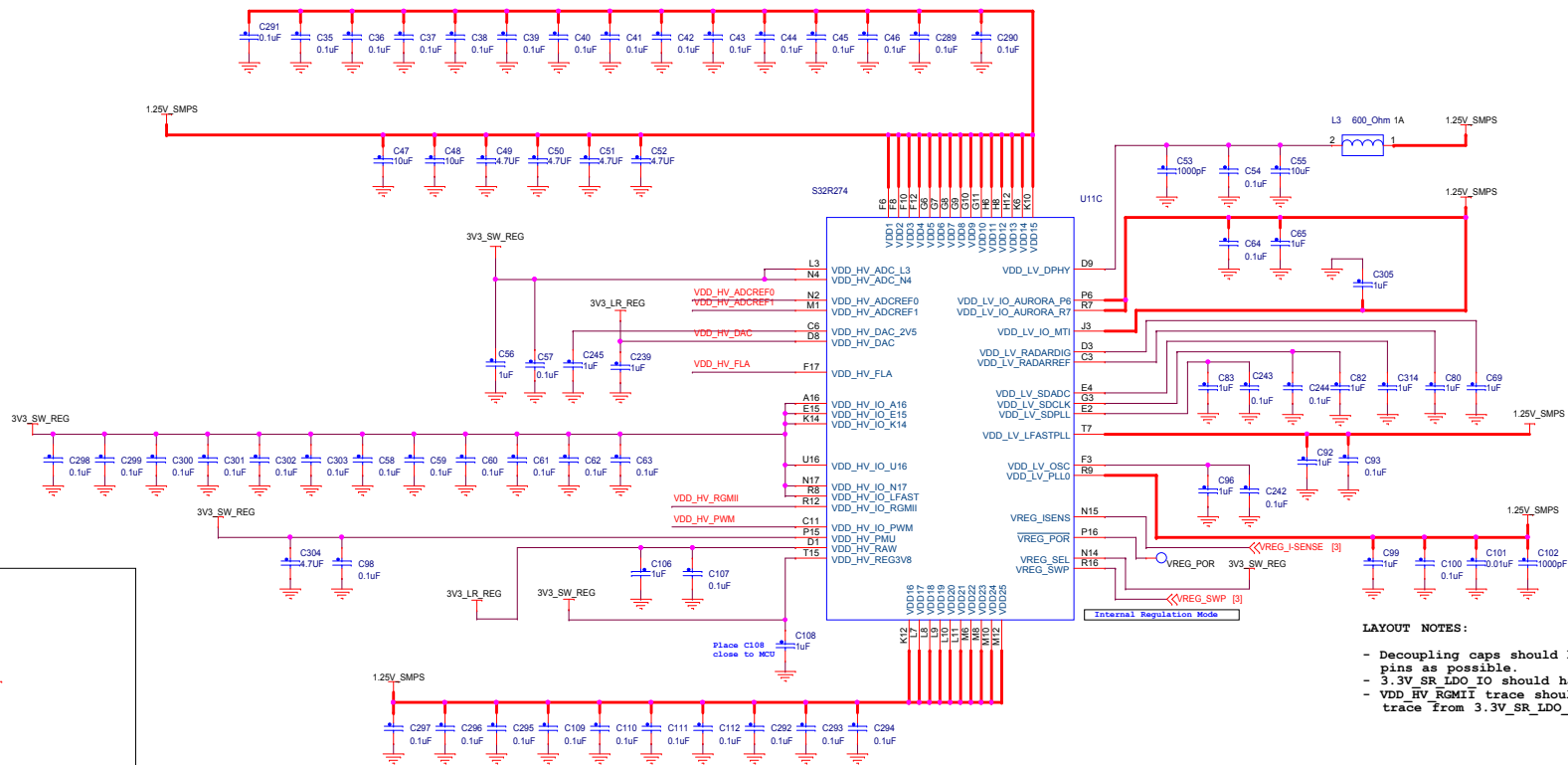


## RRU Connections

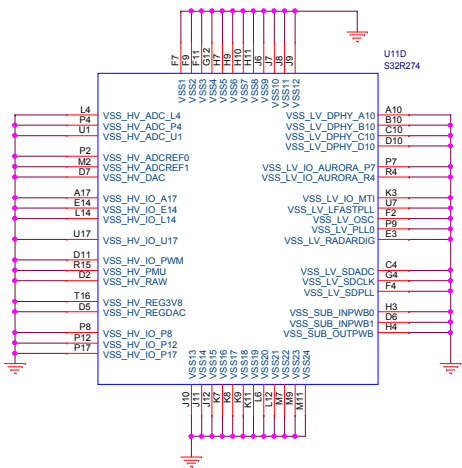


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Drawing Title: 77GHz_RADAR	
PageTitle: Gb Ethernet	
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SCB: VAL701A01E PDF: VAL701A01E	al
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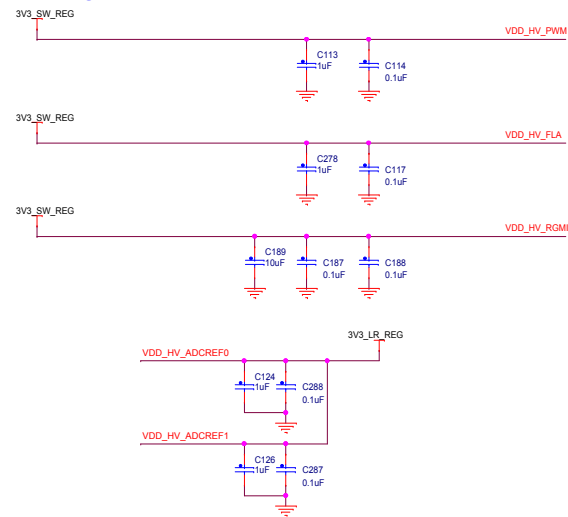
## MCU Power Connections and Decoupling



## MCU Grounds



## Supply Decoupling



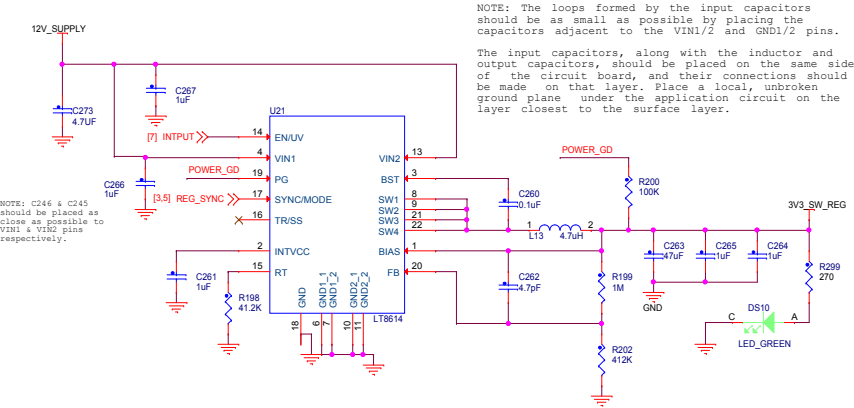
## Reset Circuit



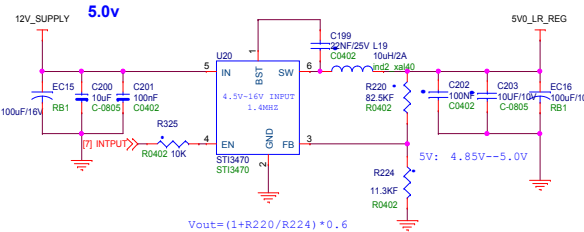
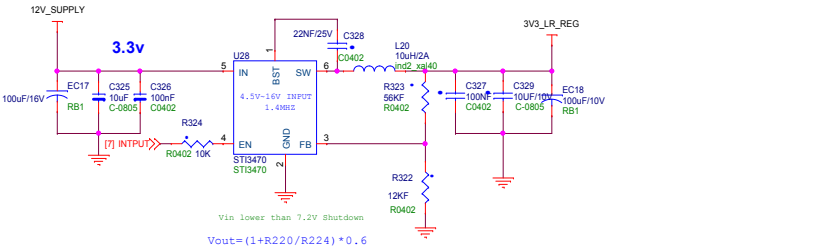
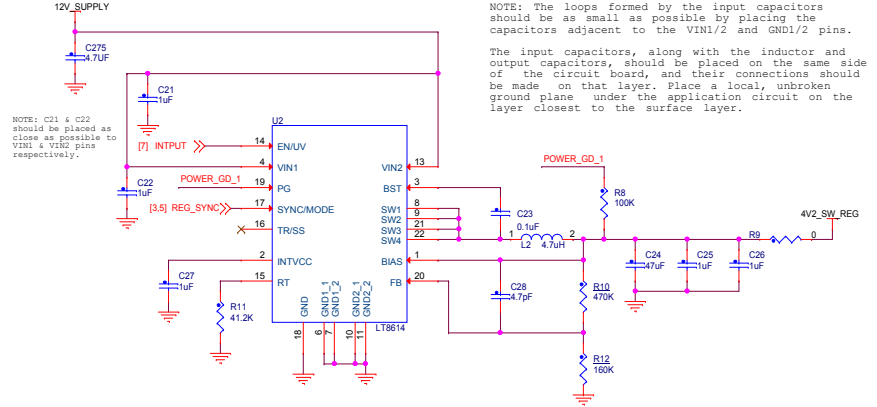
LAYOUT NOTES:

- Decoupling caps should be placed as close to the DUT pins as possible.
- 3.3V SR LDO IO should have a wide trace length
- VDD HV RMII trace should have a wide (at least 200 mil) trace from 3.3V SR LDO IO.

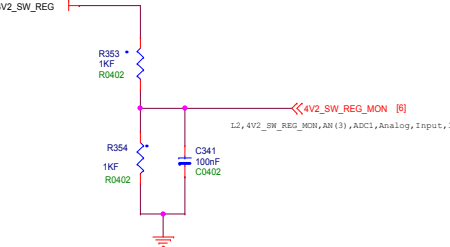
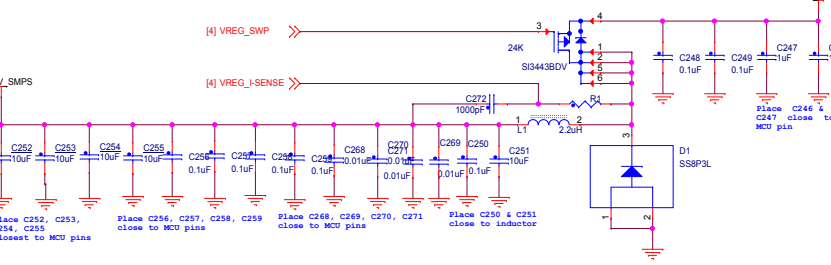
S32R274 SWITCHING REG - 4A, 3.3V



Eagle Supply Switching Reg - 4A, 4.2V



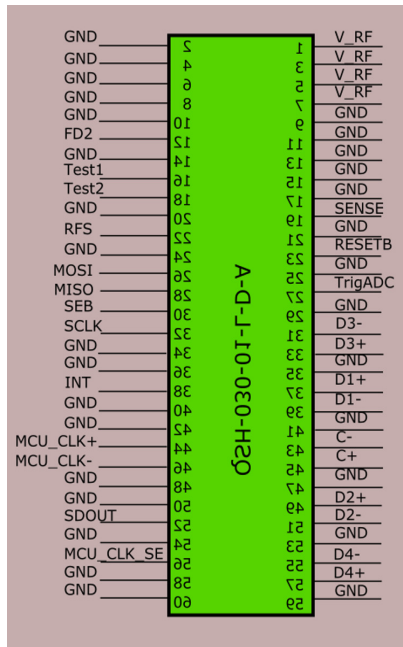
Internal SMPS Circuit



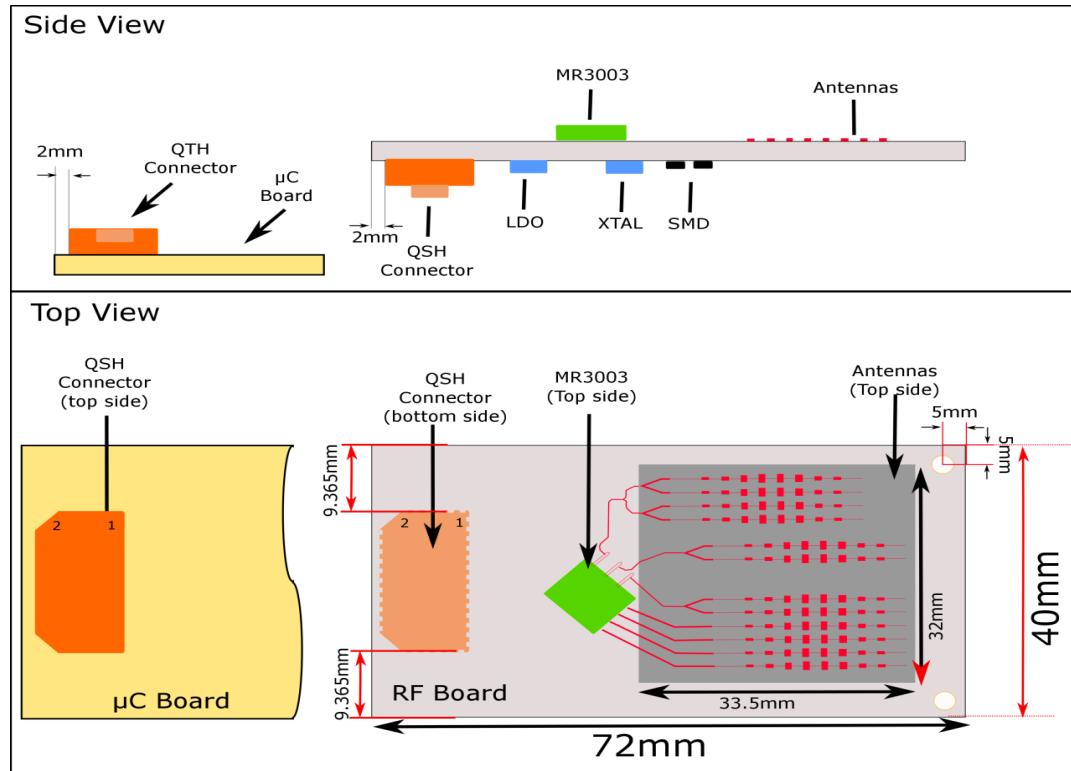
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## Eagle Connector Pinout



## Eagle Board Connector Diagram



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# S32R274-RADAR

		<b>Automotive Product Group</b> 6501 William Cannon Drive West Austin, TX 78358-8598	
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Drawn by: Andrew Robertson		PageTitle: TITLE, TOC & REV	
Approved: Andrew Robertson		Size C	Document Number \$CH:VA1701A01E PDF:VA1701A01E A1
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