

CYT2B75CADES 程序下载说明

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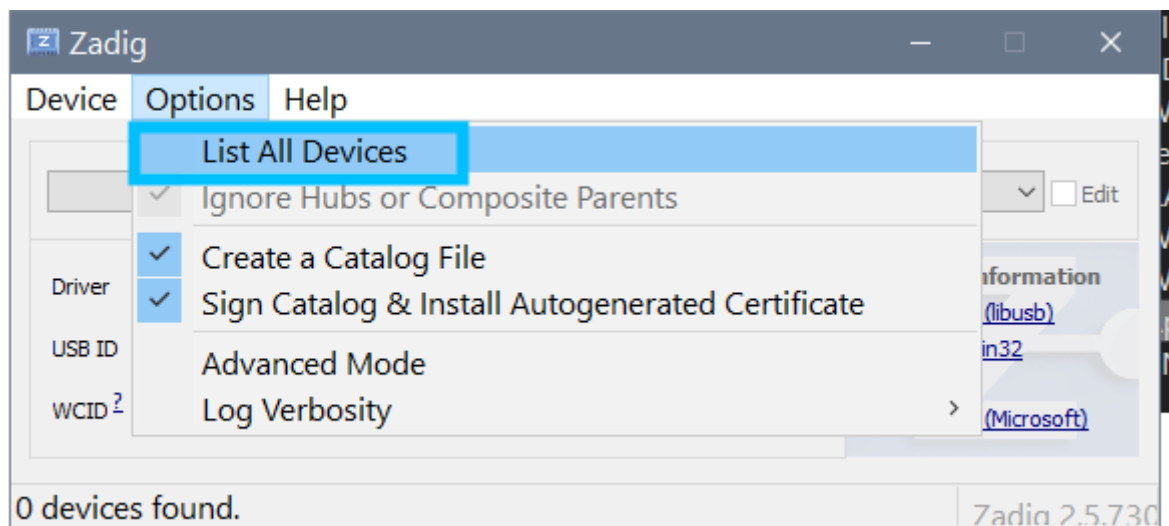
- 1、更换jlink USB驱动
- 2、配置脚本文件
- 3、程序下载

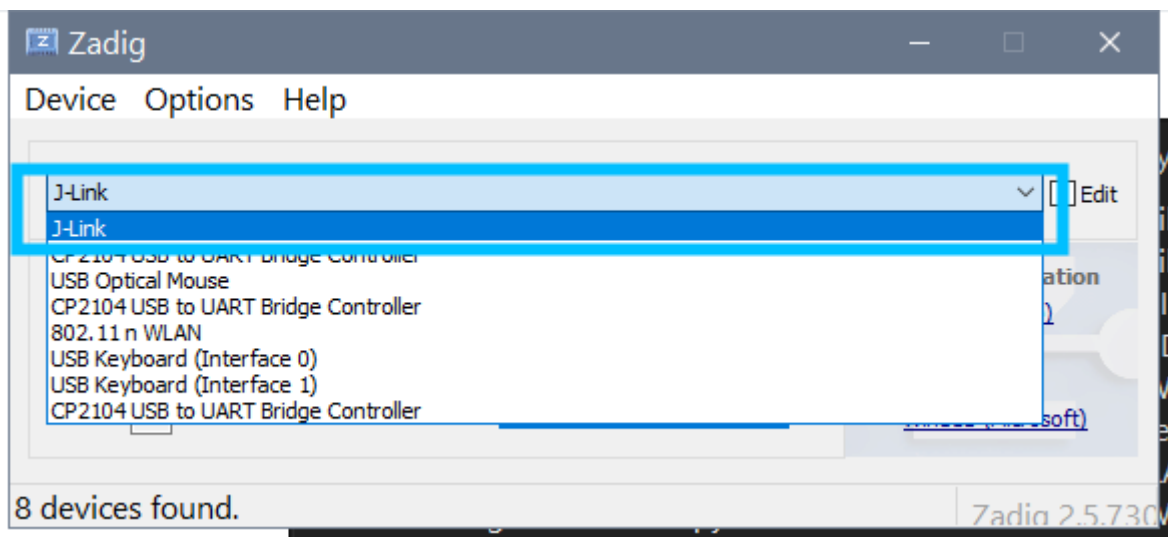
1、更换jlink USB驱动

运行zadig-2.5.exe

| Name | Date modified | Type | Size |
|----------------------------------|-----------------|----------------|----------|
| AutoFlashUtil_1.0.0.277 | 2021/7/1 11:13 | File folder | |
| test | 2021/7/29 14:35 | File folder | |
| AutoFlashUtil_1.0.0.277.zip | 2021/6/25 16:00 | ZIP 压缩文件 | 6,235 KB |
| Cypress_Auto_Flash_Utility_Us... | 2021/6/25 16:11 | PDF File | 2,557 KB |
| Program.bat | 2021/7/28 17:18 | Windows Bat... | 1 KB |
| Readme.txt | 2021/6/25 16:29 | Text Document | 4 KB |
| test.rar | 2021/7/29 14:34 | RAR 压缩文件 | 45 KB |
| TVII_Program_Test - Copy.bat | 2021/7/29 14:34 | Windows Bat... | 1 KB |
| TVII_Program_Test.bat | 2021/7/29 14:34 | Windows Bat... | 1 KB |
| zadig-2.5.exe | 2020/4/13 15:50 | Application | 5,037 KB |
| 微信图片_20210729143647.p... | 2021/7/29 14:36 | PNG File | 117 KB |

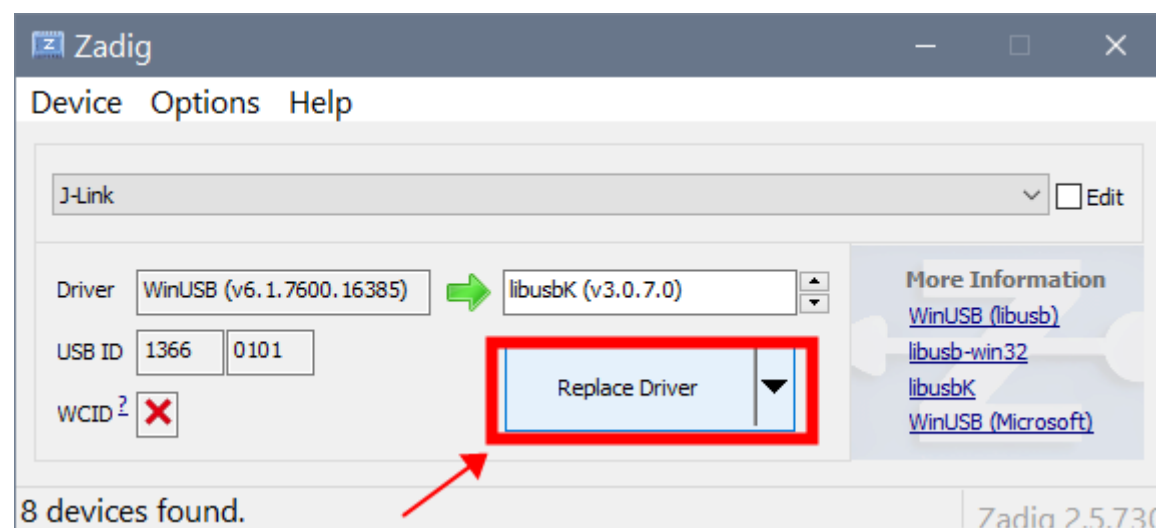
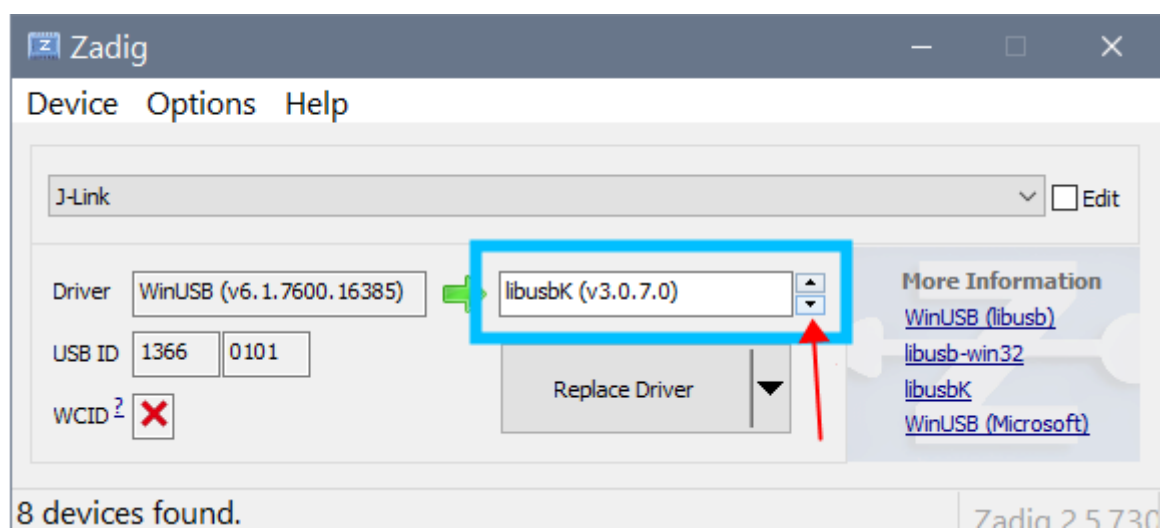
把jlink接到电脑端，选择菜单上Options->List All Devices，设备下拉列表选择jlink

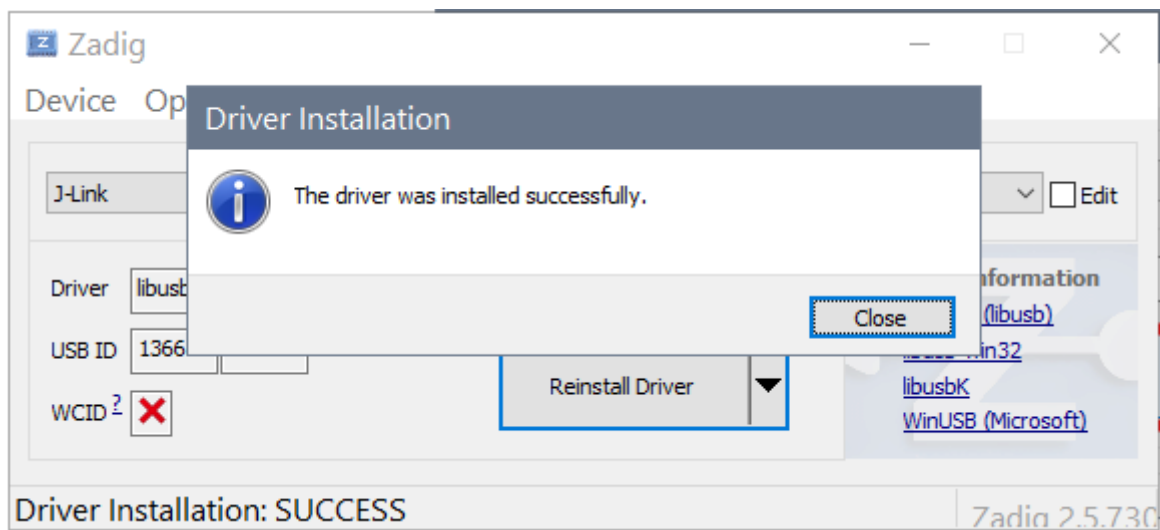
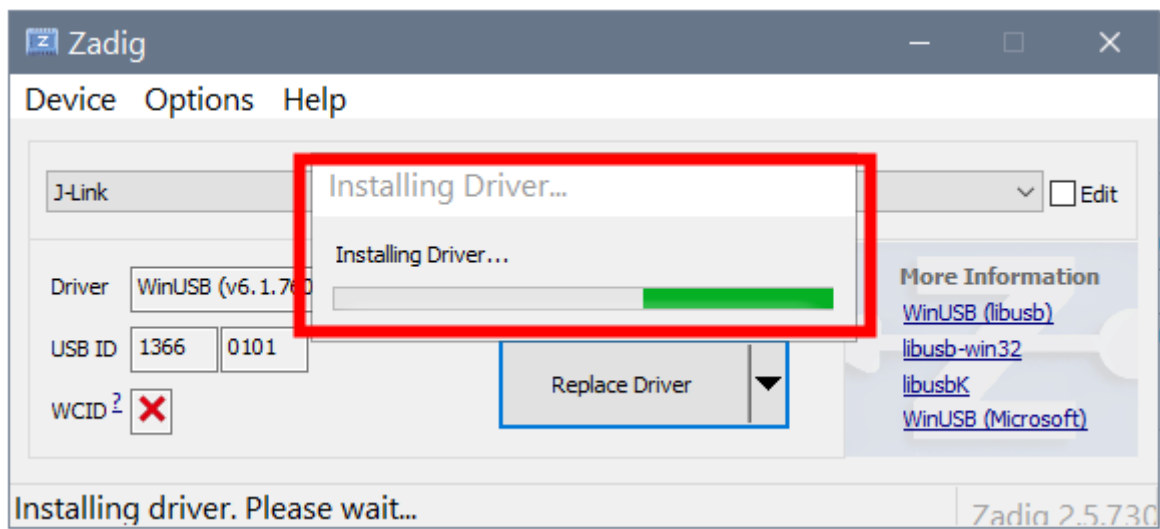




更新USB驱动为**libusbK(v3.0.7.0)**，选择后单击**Replace Driver**按钮，耐心等待安装完成

备注：中途杀毒软件提示权限问题，请选择允许执行





2、配置脚本文件

修改TVII_Program_Test.bat脚本中程序路径

备注：程序路径根据实际存放位置来填写，路径不含中文

```
File Edit Format View Help
SET CYP_PATH=C:\Program Files (x86)\Cypress\Cypress Auto Flash Utility 1.0
::CM4 HEX programming
call "%CYP_PATH%\bin\openocd.exe" -s "%CYP_PATH%\scripts" -f interface/jlink.cfg -c "transport select swd" -f target/traveo2_1m_0A.cfg -c "program D:/test/cm4.hex 0x0 verify exit"
```

3、程序下载

直接双击运行脚本，下载完成后回车关闭窗口

```
D:\YW687\A6W\工具\AutoFlashUtility\AutoFlashUtility>call "C:\Program Files
(x86)\Cypress\Cypress Auto Flash Utility 1.0\bin\openocd.exe" -s "C:\Program
Files (x86)\Cypress\Cypress Auto Flash Utility 1.0\scripts" -f
interface/jlink.cfg -c "transport select swd" -f target/traveo2_1m_0A.cfg -c
"program D:/test/cm4.hex 0x0 verify exit"
open On-Chip Debugger 0.10.0+dev-1.0.0.277 (2019-10-11-11:05)
```

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For bug reports, read

<http://openocd.org/doc/doxygen/bugs.html>

swd

** traveo2_1m_0A.cfg file is deprecated, please use traveo2_1m_a0.cfg

adapter speed: 1000 kHz

** Test Mode acquire not supported by selected adapter

cortex_m reset_config sysresetreq

adapter_nsrst_delay: 100

Info : J-Link ARM-OB STM32 compiled Aug 22 2012 19:52:04

Info : Hardware version: 7.00

Info : VTarget = 3.300 V

Info : clock speed 1000 kHz

Info : SWD DPIDR 0x6ba02477

Info : traveo2.cpu.cm0: hardware has 4 breakpoints, 2 watchpoints

** Use overridden Main Flash size, kb: 1088

** Use overridden work Flash size, kb: 96

** Silicon: 0xE3DA, Family: 0x101, Rev.: 0x23 (B2)

** Detected Device: CYT2B75CAE

** Flash Boot version 3.1.0.515

** Chip Protection: NORMAL

Info : traveo2.cpu.cm4: hardware has 6 breakpoints, 4 watchpoints

Info : traveo2.cpu.cm4: external reset detected

Info : Listening on port 3333 for gdb connections

Info : Listening on port 3334 for gdb connections

Info : SWD DPIDR 0x6ba02477

target halted due to debug-request, current mode: Thread

xPSR: 0x61000000 pc: 0x000000f8 msp: 0x0801f800

** traveo2.cpu.cm4: Ran after reset and before halt...

target halted due to debug-request, current mode: Thread

xPSR: 0x01000000 pc: 0x000001bc msp: 0x0801f800

** Programming Started **

auto erase enabled

Info : Padding image section 0 at 0x1008360c with 500 bytes (bank write end alignment)

warn : Adding extra erase range, 0x10083800 .. 0x10087fff

[100%] [#####] [Erasing]

[100%] [#####] [Programming]

wrote 14336 bytes from file D:/test/cm4.hex in 0.721071s (19.416 KiB/s)

** Programming Finished **

** Verify Started **

verified 13836 bytes in 0.066821s (202.208 KiB/s)

** verified OK **

shutdown command invoked

D:\YW687\A6W\工具\AutoFlashUtility\AutoFlashUtility>PAUSE

Press any key to continue . . .

```
C:\WINDOWS\system32\cmd.exe
Open On-Chip Debugger 0.10.0+dev-1.0.0.277 (2019-10-11-11:05)
Licensed under GNU GPL v2
For bug reports, read
    http://openocd.org/doc/doxygen/bugs.html
swd
*****
** traveo2_lm_0A.cfg file is deprecated, please use traveo2_lm_a0.cfg
*****
adapter speed: 1000 kHz
** Test Mode acquire not supported by selected adapter
cortex_m reset_config sysresetreq
adapter_nsrst_delay: 100
Info : J-Link ARM-OB STM32 compiled Aug 22 2012 19:52:04
Info : Hardware version: 7.00
Info : VTarget = 3.300 V
Info : clock speed 1000 kHz
Info : SWD DPIDR 0x6ba02477
Info : traveo2.cpu.cm0: hardware has 4 breakpoints, 2 watchpoints
*****
** Use overridden Main Flash size, kb: 1088
** Use overridden Work Flash size, kb: 96
** Silicon: 0xE3DA, Family: 0x101, Rev.: 0x23 (B2)
** Detected Device: CYT2B75CAE
** Flash Boot version 3.1.0.515
** Chip Protection: NORMAL
*****
Info : traveo2.cpu.cm4: hardware has 6 breakpoints, 4 watchpoints
Info : traveo2.cpu.cm4: external reset detected
Info : Listening on port 3333 for gdb connections
Info : Listening on port 3334 for gdb connections
Info : SWD DPIDR 0x6ba02477
target halted due to debug-request, current mode: Thread
xPSR: 0x61000000 pc: 0x000000f8 msp: 0x0801f800
** traveo2.cpu.cm4: Ran after reset and before halt...
target halted due to debug-request, current mode: Thread
xPSR: 0x01000000 pc: 0x000001bc msp: 0x0801f800
** Programming Started **
auto erase enabled
Info : Padding image section 0 at 0x1008360c with 500 bytes (bank write end alignment)
Warn : Adding extra erase range, 0x10083800 .. 0x10087fff
[100%] [#####] [ Erasing ]
[100%] [#####] [ Programming ]
wrote 14336 bytes from file D:/test/cm4_hex.in 0.721071s (19.416 KiB/s)
** Programming Finished **
** Verify Started **
verified 13836 bytes in 0.066821s (202.208 KiB/s)
** Verified OK **
shutdown command invoked

D:\YW687\A6W\工具\AutoFlashUtility\AutoFlashUtility>PAUSE
```