

D-FD-M8315-Ø-24	FLOW DIAGRAM
D-FD-M8315-Ø-25	FLOW DIAGRAM
D-FD-M8315-Ø-26	FLOW DIAGRAM
D-FD-M8315-Ø-27	FLOW DIAGRAM
E-FD-PDP8/E-Ø-Ø6	PROCESSOR FLOW CHART
D-TD-PDP8/E-Ø-Ø5	TIMING (PDP8/E)
E-CS-M83ØØ-Ø-1	MAJOR REGISTERS
E-CS-M831Ø-Ø-1	MAJOR REGISTERS CONTROL
E-CS-M832Ø-Ø-1	BUS LOADS
E-CS-M833Ø-Ø-1	TIMING GENERATOR
A-SP-KM8-A-1	FIELD INSTALLATION AND ACCEPTANCE PROCEDURE
D-CS-M8317-Ø-1	OPTION BOARD #2
D-UA-M8317-Ø-Ø	UNIT ASSY
B-PL-M8317-Ø-Ø	PARTS LIST
D-TD-KM8-A-4	AUTO RESTART/BOOT SEQUENCE
D-TD-KM8-A-5	BOOTSTRAP TIMING
D-FD-KM8-A-6	FLOW CHART OPTION #2
A-SP-KM8-A-7	ROM PROG. INST.
A-SP-DKC8-A-1	FIELD INST. AND ACCEPTANCE PROCEDURE
D-CS-M8316-Ø-1	OPTION BOARD #1
D-UA-M8316-Ø-Ø	UNIT ASSY
B-PL-M8316-Ø-Ø	PARTS LIST
E-UA-KC8-A-Ø	BEZEL ASSY
D-AD-7010644-Ø-Ø	KEYBOARD ASSY
D-CS-5411241-Ø-1	INDICATOR DISPLAY
D-CS-5411316-Ø-1	REGISTERS AND CONTROL
A-SP-KT8-A	FIELD INSTALLATION AND ACCEPTANCE PROCEDURE
D-UA-M8416-Ø-Ø	KT8-A UNIT ASSY
B-PL-M8416-Ø-Ø	PARTS LIST
D-CS-M8416-Ø-1	MEMORY MANAGEMENT OPTION
D-CS-M9Ø2Ø-Ø-1	KT8-A TERMINATOR CARD
A-SP-MS8-C-Ø	FIELD INSTALLATION AND ACCEPTANCE PROCEDURE
D-CS-M8417-Ø-1	PDP-8 MOS MEMORY
D-UA-M8417-Ø-Ø	UNIT ASSY
B-PL-M8417-Ø-Ø	PARTS LIST
A-SP-MR8-F-2	FIELD INSTALLATION AND ACCEPTANCE PROCEDURE
D-CS-M8349-Ø-1	1K PROM
A-SP-MS8-A-1	FIELD INSTALLATION AND ACCEPTANCE PROCEDURE
D-CS-M8311-Ø-1	4K X 12 MOS MEMORY

TITLE	8A SEMICONDUCTOR MEMORY FAMILY	SHEET 2 OF 2	SIZE B	CODE TC	NUMBER 8A-1-1	REV. A
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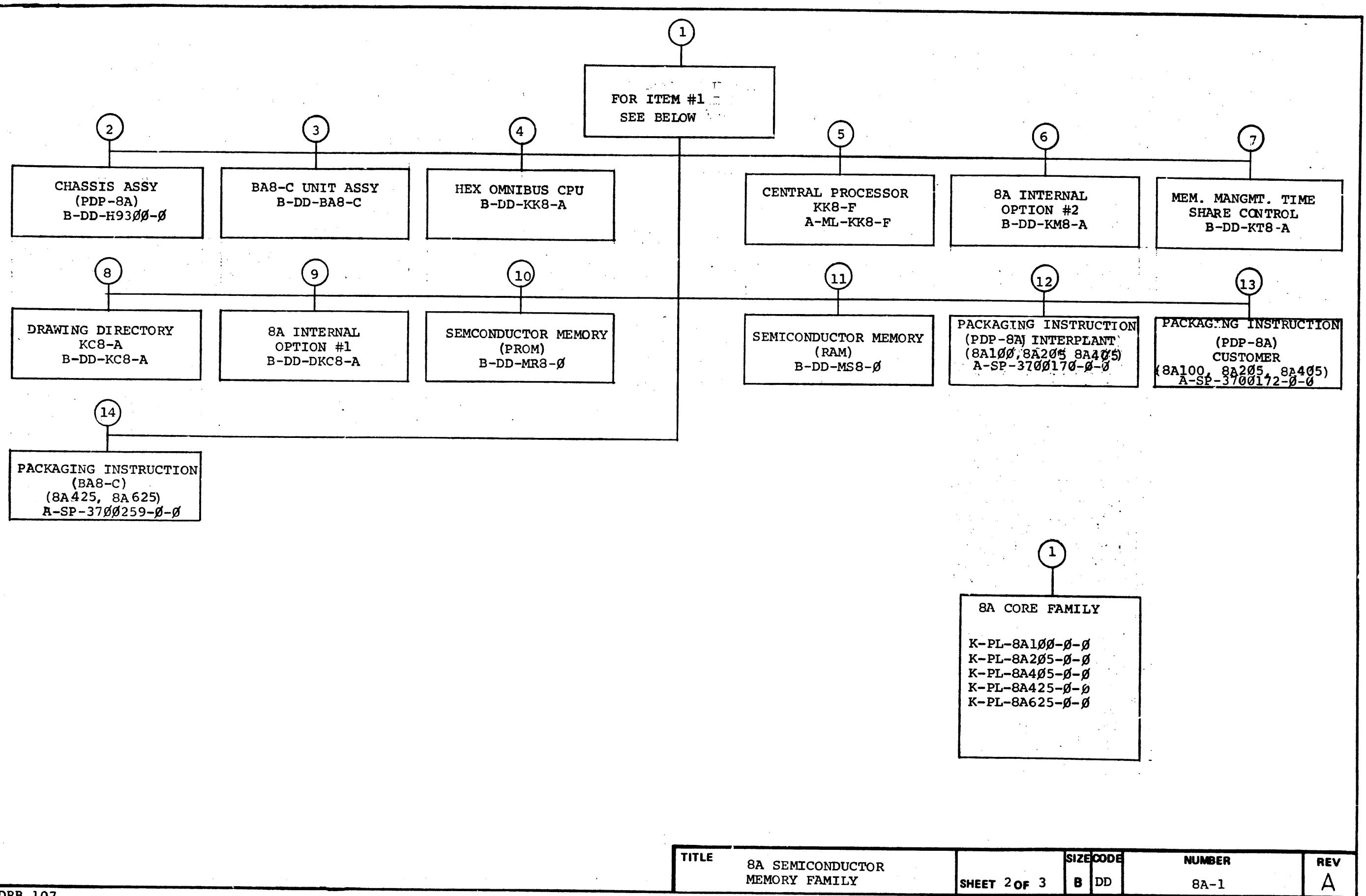
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FOR FIELD MAINTENANCE PRINT SET SEE
B-TC-8A-1-1

DRB 106A

MK



FIND NO.	DRAWING NO.	DESCRIPTION	TYPE	FIND NO.	DRAWING NO.	DESCRIPTION	TYPE
1	MP00415	FIELD MAINTENANCE PRINT SET (MP)	-	9	B-DD-DKC8-A	8A INTERNAL OPTION #1	-
	B-TC-8A-1-1	FIELD MAINTENANCE PRINT SET (TC)	-				
K-PL-8A100-0-0	8A SEMICONDUCTOR MEMORY FAMILY (8A100)	E/M					
K-PL-8A205-0-0	8A CORE FAMILY (8A205)	E/M					
K-PL-8A405-0-0	8A CORE FAMILY (8A405)	E/M					
K-PL-8A425-0-0	8A CORE FAMILY (8A425)	E/M					
K-PL-8A625-0-0	8A CORE FAMILY (8A625)	E/M					
2	B-DD-H9300	CHASSIS ASSY (PDP-8A)	-				
3	B-DD-BA8-C	BA8-C UNIT ASSY	-				
4	B-DD-KK8-A	HEX OMNIBUS CPU	-				
5	A-ML-KK8-F	CENTRAL PROCESSOR KK8-F	-				
6	B-DD-KM8-A	8A INTERNAL OPTION #2	-				
7	B-DD-KT8-A	MEM MANAGEMENT AND TIME SHARE CONTROL	-				
8	B-DD-KC8-A	DRAWING DIRECTORY KC8-A	-				

TYPE: E ELECTRICAL
M MECHANICAL
E/M ELECTRO/MECHANICAL

digital

TITLE: 8A SEMICONDUCTOR MEMORY FAMILY
SHEET 3 OF 3 SIZE: B DD NUMBER: 8A-1 REV: A

PARTS LIST

SHEET A1 OF A2

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION											
				AA	AB	AC	AD	AE	AF	AK	AL	AM	AN	AP	AR
1	1	D-UA-H9300-0-0	H9300-AA	CHASSIS ASSY 8/A W FANS 115V 60H	1	0	1	0	1	0	1	0	0	0	0
2	2	D-UA-H9300-0-0	H9300-AB	CHASSIS ASSY 8/A W FANS 230V 50H	0	1	0	1	0	1	0	1	0	0	0
3	3	D-UA-H9300-0-0	H9300-AC	CHASSIS ASSY 8/A W FANS 230V 50H	0	0	0	0	0	0	0	0	1	0	1
4	4	D-UA-H9300-0-0	H9300-AD	CHASSIS ASSY 8/A W FANS 230V 50H	0	0	0	0	0	0	0	0	1	0	1
5	5	E-UA-BAB-C-0	00BAB-CA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
6	6	E-UA-BAB-C-0	00BAB-CB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
7	7	E-UA-BAB-C-0	00BAB-CH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
8	8	E-UA-BAB-C-0	00BAB-CJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
9	9	A-PL-KK8-A-0	00KK8-A	8A-CPU	1	1	1	1	1	1	1	1	1	1	1
10	10	D-UA-KK8-F-0	00KK8-F	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
11	11	A-PL-MS8-C-0	00MS8-CA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
12	12	A-PL-MS8-C-0	00MS8-CB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
13	13	A-PL-MS8-A-0	00MS8-AA	PDP8A RAM 1K	0	0	1	1	0	0	0	0	0	1	1
14	14	A-PL-MS8-A-0	00MS8-AB	PDP8A RAM 2K	0	0	0	0	1	1	0	0	0	0	0
15	15	A-PL-MS8-A-0	00MS8-AD	PDP8A RAM 4K	0	0	0	0	0	0	1	1	0	0	0
16	16	C-UA-MR8-F-0	00MR8-FB	1KX12 CONTENT ALTERABLE ROM & 25	0	0	0	0	0	0	0	0	0	0	0
17	17	A-PL-KM8-A-0	00KM8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
18	18	A-PL-DKC8-A-0	0DKC8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
19	19	E-UA-KC8-A-0	00KC8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
20	20	D-UA-KT8-A-0	00KT8-A	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
21	21	A-PL-8A-1-2		SHIPPING LIST	1	1	1	1	1	1	1	1	1	1	1
22	22	A-SP-3700170-0-0	3700170-00	INSTR PKG COMPUTER 8A400,600,800	1	1	1	1	1	1	1	1	1	1	1
23	23	A-SP-3700172-0-0	3700172-00	INSTR PKG COMPUTER PDP8A CUSHION	1	1	1	1	1	1	1	1	1	1	1
24	24	A-SP-3700259-0-0	3700259-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
25	25	A-SP-3700259-0-0	3700259-01	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
26	26	A-SP-3700259-0-0	3700259-02	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
27	27	D-UA-H9300-0-0	H9300-BA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
28	28	D-UA-H9300-0-0	H9300-BB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
29	29	D-UA-H9300-0-0	H9300-BH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
30	30	D-UA-H9300-0-0	H9300-BJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-

REVISION HISTORY			BASIC PART NO: BA100	DRN:	M DUGGAN	DATE: 12-MAY-77	DBP	D	I	G	I	T	A	L
ENG	ECO NUMBER	REV	SECTION A OF B											
DF	8A-1-MK002B	B	SECTION. VARIATION INDEX	CHK'D: L NARHI <i>9f</i>		DATE: 8-NOV-77	TITLE	PARTS LIST						
			[CA] AA,AB,AC,AD,AE,AF, AK,AL,AM,AN,AP,AR				8A SEMICONDUCTOR MEMORY FAMILY 8A100							
			[CB] AS,AT,AU,AV,FA,FB, FC,FD	DES.ENG.: L NARHI <i>L.P.</i>		DATE: 8-NOV-77	DOCUMENT NUMBER							
			[CC]	RESP.ENG.: L NARHI <i>L.P.</i>		DATE: 8-NOV-77	SIZE/CODE							
			[CD]	MFG.ENG.: J V KANE		DATE: 8-NOV-77	NUMBER							
			[CE]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER: #B-DD-8A-1	FILE NAME: MK0384.PLS	EDIT #						
			[CF]											

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AUTOMATED BY PRTLST.3P(44)

PARTS LIST

SHEET A2 OF A2

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION										
				AA	AB	AC	AD	AE	AF	AK	AL	AM	AN	AP
31	31	A-PL-KM8-A-0	00KM8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
32	32	A-PL-KM8-A-0	00KM8-AC	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
33	33	A-PL-KM8-A-0	00KM8-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-

D	I	G	I	T	A	L	TITLE	SECTION A OF B	SIZE	CODE	DOCUMENT NUMBER	REV
							8A SEMICONDUCTOR MEMORY FAMILY 8A100		K	PL	8A100-0-0	B

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION						
				AS	AT	AU	AV	FA	FB	FC
1	1	D-UA-H9300-0-0	H9300-AA	CHASSIS ASSY 8/A W FANS 115V 60H	0	0	0	0	1	0
2	2	D-UA-H9300-0-0	H9300-AB	CHASSIS ASSY 8/A W FANS 230V 50H	0	0	0	0	0	1
3	3	D-UA-H9300-0-0	H9300-AC	CHASSIS ASSY 8/A W FANS 230V 50H	1	0	1	0	0	0
4	4	D-UA-H9300-0-0	H9300-AD	CHASSIS ASSY 8/A W FANS 230V 50H	0	1	0	1	0	0
5	5	E-UA-BA8-C-0	00BA8-CA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	1
6	6	E-UA-BA8-C-0	00BA8-CB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-
7	7	E-UA-BA8-C-0	00BA8-CH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-
8	8	E-UA-BA8-C-0	00BA8-CJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-
9	9	A-PL-KK8-A-0	00KK8-A	8A-CPU	1	1	1	1	1	1
10	10	D-UA-KK8-F-0	00KK8-F	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-
11	11	A-PL-MS8-C-0	00MS8-CA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-
12	12	A-PL-MS8-C-0	00MS8-CB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-
13	13	A-PL-MS8-A-0	00MS8-AA	PDP8A RAM 1K	-	-	-	-	-	-
14	14	A-PL-MS8-A-0	00MS8-AB	PDP8A RAM 2K	0	0	0	0	0	0
15	15	A-PL-MS8-A-0	00MS8-AD	PDP8A RAM 4K	1	1	0	0	0	0
16	16	C-UA-MR8-F-0	00MR8-FB	1KX12 CONTENT ALTERABLE ROM & 25	0	0	1	1	0	0
17	17	A-PL-KM8-A-0	00KM8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	1	1
18	18	A-PL-DKC8-A-0	0DKC8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-
19	19	E-UA-KC8-A-0	00KC8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-
20	20	D-UA-KT8-A-0	00KT8-A	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-
21	21	A-PL-8A-1-2		SHIPPING LIST	1	1	1	1	1	1
22	22	A-SP-3700170-0-0	3700170-00	INSTR PKG COMPUTER 8A400,600,800	1	1	1	1	1	1
23	23	A-SP-3700172-0-0	3700172-00	INSTR PKG COMPUTER PDP8A CUSHION	1	1	1	1	1	1
24	24	A-SP-3700259-0-0	3700259-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-
25	25	A-SP-3700259-0-0	3700259-01	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-
26	26	A-SP-3700259-0-0	3700259-02	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-
27	27	D-UA-H9300-0-0	H9300-BA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-
28	28	D-UA-H9300-0-0	H9300-BB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-
29	29	D-UA-H9300-0-0	H9300-BH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-
30	30	D-UA-H9300-0-0	H9300-BJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-

REVISION HISTORY		BASIC PART NO:	8A100	DRN:	M DUGGAN	DATE:	12-MAY-77	DBP	D	I	G	I	T	A	L	
ENG!	ECO NUMBER	REV	SECTION B OF B									TITLE	PARTS LIST			
DF	8A-1-MK002B	B	SECTION, VARIATION INDEX	CHK'D:	L NARHI	DATE:	8-NOV-77						8A SEMICONDUCTOR MEMORY FAMILY			
			[A] AA,AB,AC,AD,AE,AF, AK,AL,AM,AN,AP,AR										BA100			
			[B] AS,AT,AU,AV,FA,FB, FC,FD	DES.ENG.:	L NARHI	DATE:	8-NOV-77									
			[C]										DOCUMENT NUMBER			
				RESP.ENG.:	L NARHI	DATE:	8-NOV-77									
			[D]										SIZE	CODE	NUMBER	REV
			[E]										K	PL	8A100-0-0	B
			[F]		ASSEMBLY NUMBER:			TOP DOCUMENT NUMBER:					FILE NAME:		EDIT #	
								#B-DD-8A-1					MK0384.PLS			7

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AUTOMATED BY FRTLST, 3P(44)

PARTS LIST

SHEET B2 OF B2

LINE ITEM DOCUMENT NUMBER

PART NUMBER

DESCRIPTION

QUANTITY PER VARIATION

QUANTITY PER VARIATION

31 31 A-PL-KM8-A-0
32 32 A-PL-KM8-A-0
33 33 A-PL-KM8-A-0

00KMB-AB
00KMB-AC
00KMB-AD

*** THIS ITEM IS NOT USED ***
*** THIS ITEM IS NOT USED ***
*** THIS ITEM IS NOT USED ***

DIGITAL! TITLE: 8A SEMICONDUCTOR MEMORY FAMILY
SECTION B OF B
8A100 ! SIZE! CODE! DOCUMENT NUMBER! REV!
K PL! 8A100-0-0! B!

PARTS LIST

SHEET A1 OF A2

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION												
				AA	AB	AC	AD	BM	BN	BP	BR	BS				
1	1	D-UA-H9300-0-0	H9300-AA	CHASSIS ASSY 8/A W FANS 115V 60H	1	0	0	0	1	0	1	0	0	0	0	0
2	2	D-UA-H9300-0-0	H9300-AB	CHASSIS ASSY 8/A W FANS 230V 50H	0	1	0	0	0	1	0	1	0	0	0	0
3	3	D-UA-H9300-0-0	H9300-AC	CHASSIS ASSY 8/A W FANS 230V 50H	0	0	1	0	0	0	0	0	1	0	1	0
4	4	D-UA-H9300-0-0	H9300-AD	CHASSIS ASSY 8/A W FANS 230V 50H	0	0	0	1	0	0	0	0	1	0	1	0
5	5	E-UA-BAB-C-0	00BA8-CA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	
6	6	E-UA-BAB-C-0	00BA8-CB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	
7	7	E-UA-BAB-C-0	00BA8-CH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	
8	8	E-UA-BAB-C-0	00BA8-CJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	
9	9	A-PL-KK8-A-0	00KK8-A	8A-CPU	-	-	-	-	-	-	-	-	-	-	-	
10	10	D-UA-KK8-F-0	00KK8-F	*** THIS ITEM IS NOT USED ***	1	1	1	1	1	1	1	1	1	1	1	1
11	11	A-PL-MS8-C-0	00MS8-CA	16K 12BIT RAM, 4K CHIPS	0	0	0	0	1	1	0	0	1	1	0	0
12	12	A-PL-MS8-C-0	00MS8-CB	32K 12 BIT MOS RAM 4K CHIPS	0	0	0	0	0	0	1	1	0	0	1	1
13	13	A-PL-MS8-A-0	00MS8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	
14	14	A-PL-MS8-A-0	00MS8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	
15	15	A-PL-MS8-A-0	00MS8-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	
16	16	C-UA-MR8-F-0	00MR8-FB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	
17	17	A-PL-KM8-A-0	00KM8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	
18	18	A-PL-DKC8-A-0	00KC8-AA	OPTION BOARD #1: SLU,XTAL CLOCK,	0	0	0	0	0	0	0	0	0	0	0	0
19	19	E-UA-KC8-A-0	00KC8-AA	PROGRAMMER'S CONSOLE	0	0	0	0	0	0	0	0	0	0	0	0
20	20	D-UA-KT8-A-0	00KT8-A	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	
21	21	A-PL-8A-1-2		SHIPPING LIST	-	-	-	-	-	-	-	-	-	-	-	
22	22	A-SP-3700170-0-0	3700170-00	INSTR PKG COMPUTER BA400,600,800	1	1	1	1	1	1	1	1	1	1	1	1
23	23	A-SP-3700172-0-0	3700172-00	INSTR PKG COMPUTER PIP8A CUSHION	1	1	1	1	1	1	1	1	1	1	1	1
24	24	A-SP-3700259-0-0	3700259-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	
25	25	A-SP-3700259-0-0	3700259-01	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	
26	26	A-SP-3700259-0-0	3700259-02	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	
27	27	D-UA-H9300-0-0	H9300-BA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	
28	28	D-UA-H9300-0-0	H9300-BB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	
29	29	D-UA-H9300-0-0	H9300-BH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	
30	30	D-UA-H9300-0-0	H9300-BJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	

REVISION HISTORY	BASIC PART NO:	8A205	DRN:	M DUGGAN	DATE:	12-MAY-77	DBP	D	I	G	I	T	A	L
ENG!	ECO NUMBER	REV	SECTION A OF C											
DF	8A-1-MK002B	B	SECTION. VARIATION INDEX	CHK'D:	L NARHI	DATE: 8-NOV-77	TITLE	PARTS LIST						
			[CA] AA,AB,AC,AD,BM,BN, BP,BR,BS,BT,BU,BV											
			[CB] CM,CN,CP,CR,CS,CT, CU,CV,DM,DN,DP,DR	DES.ENG.:	L NARHI	DATE: 8-NOV-77								
			[CC] DS,DT,DU,DV	RESP.ENG.:	L NARHI	DATE: 8-NOV-77								
			[CD]											
			[CE]	MFG.ENG.:	J V KANE	DATE: 8-NOV-77	K	PL	8A205-0-0					
			[CF]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER: #B-DD-8A-1				FILE NAME: MK0385.FLS		EDIT # 5		

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MK

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PARTS LIST

SHEET A2 OF A2

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION										
				AA	AB	AC	AD	BM	BN	BP	BR	BS	BT	BU
31	31	A-PL-KM8-A-0	00KM8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
32	32	A-PL-KM8-A-0	00KM8-AC	8A INTERNAL OPTION 2	1	1	1	1	1	1	1	1	1	1
33	33	A-PL-KM8-A-0	00KM8-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-

TITLE				SIZE	CODE	DOCUMENT NUMBER	REV					
D	I	G	I	T	A	L						
				8A SEMICONDUCTOR MEMORY FAMILY				SECTION A OF C				
				8A205								
K	PL	8A205-0-0						B				

PARTS LIST

SHEET B1 OF B2

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION												
				CM	CN	CP	CR	CS	CT	CU	CV	DM	DN	DP	DR	
1	1	D-UA-H9300-0-0	H9300-AA	CHASSIS ASSY 8/A W FANS 115V 60H	1	0	1	0	0	0	0	0	1	0	1	0
2	2	D-UA-H9300-0-0	H9300-AB	CHASSIS ASSY 8/A W FANS 230V 50H	0	1	0	1	0	0	0	0	0	1	0	1
3	3	D-UA-H9300-0-0	H9300-AC	CHASSIS ASSY 8/A W FANS 230V 50H	0	0	0	0	1	0	1	0	0	0	0	0
4	4	D-UA-H9300-0-0	H9300-AD	CHASSIS ASSY 8/A W FANS 230V 50H	0	0	0	0	0	1	0	1	0	0	0	0
5	5	E-UA-BAB-C-0	00BA8-CA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
6	6	E-UA-BAB-C-0	00BA8-CB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
7	7	E-UA-BAB-C-0	00BA8-CH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
8	8	E-UA-BAB-C-0	00BA8-CJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
9	9	A-PL-KK8-A-0	00KK8-A	8A-CPU	1	1	1	1	1	1	1	1	1	1	1	1
10	10	D-UA-KK8-F-0	00KK8-F	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
11	11	A-PL-MS8-C-0	00MS8-CA	16K 12BIT RAM, 4K CHIPS	1	1	0	0	1	1	0	0	1	1	0	0
12	12	A-PL-MS8-C-0	00MS8-CB	32K 12 BIT MOS RAM 4K CHIPS	0	0	1	1	0	0	1	1	0	0	1	1
13	13	A-PL-MS8-A-0	00MS8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
14	14	A-PL-MS8-A-0	00MS8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
15	15	A-PL-MS8-A-0	00MS8-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
16	16	C-UA-MR8-F-0	00MR8-FB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
17	17	A-PL-KM8-A-0	00KM8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
18	18	A-PL-DKC8-A-0	0DKC8-AA	OPTION BOARD #1: SLU,XTAL CLOCK,	1	1	1	1	1	1	1	1	1	1	1	1
19	19	E-UA-KC8-A-0	00KC8-AA	PROGRAMMER'S CONSOLE	0	0	0	0	0	0	0	0	0	1	1	1
20	20	D-UA-KT8-A-0	00KT8-A	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
21	21	A-PL-8A-1-2		SHIPPING LIST	1	1	1	1	1	1	1	1	1	1	1	1
22	22	A-SP-3700170-0-0	3700170-00	INSTR PKG COMPUTER 8A400,600,800	1	1	1	1	1	1	1	1	1	1	1	1
23	23	A-SP-3700172-0-0	3700172-00	INSTR PKG COMPUTER PDP8A CUSHION	1	1	1	1	1	1	1	1	1	1	1	1
24	24	A-SP-3700259-0-0	3700259-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
25	25	A-SP-3700259-0-0	3700259-01	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
26	26	A-SP-3700259-0-0	3700259-02	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
27	27	D-UA-H9300-0-0	H9300-BA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
28	28	D-UA-H9300-0-0	H9300-BB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
29	29	D-UA-H9300-0-0	H9300-BH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-
30	30	D-UA-H9300-0-0	H9300-BJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-	-

REVISION HISTORY	BASIC PART NO:	8A205	DRN:	M DUGGAN	DATE:	12-MAY-77	DBP	D	I	G	I	T	A	L
ENG	ECO NUMBER	REV	SECTION B OF C				TITLE	PARTS LIST						
DF	18A-1-MK002B	B	SECTION, VARIATION INDEX	CHK'D:	L NARHI	DATE: 8-NOV-77								
			[A] AA,AB,AC,AD,BM,BN, BP,BR,BS,BT,BU,BV											
			[B] CM,CN,CP,CR,CS,CT, CU,CV,DM,DN,DP,DR	DES.ENG.:	L NARHI	DATE: 8-NOV-77								
			[C] DS,DT,DU,DV	RESP.ENG.:	L NARHI	DATE: 8-NOV-77								
			[D]											
			[E]	MFG.ENG.:	J V KANE	DATE: 8-NOV-77	K	PL	8A205-0-0					
			[F]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER: #B-DD-8A-1				FILE NAME: MK0385.PLS				

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AUTOMATED BY PRTLST.3F(44)

PARTS LIST

SHEET B2 OF B2

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION											
				CM	CN	CP	CR	CS	CT	CU	CV	DM	DN	DP	DR
31	31	A-PL-KM8-A-0	00KM8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
32	32	A-PL-KM8-A-0	00KM8-AC	8A INTERNAL OPTION 2	1	1	1	1	1	1	1	1	1	1	1
33	33	A-PL-KM8-A-0	00KM8-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-

D	I	G	I	T	A	L	TITLE	SECTION B OF C	SIZE	CODE	DOCUMENT NUMBER	REV
D	I	G	I	T	A	L	8A SEMICONDUCTOR MEMORY FAMILY 8A205		K	PL	8A205-0-0	B

PARTS LIST

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION			
				DS	DT	DU	DV
1	1	D-UA-H9300-0-0	H9300-AA	CHASSIS ASSY 8/A W FANS 115V 60H	0	0	0
2	2	D-UA-H9300-0-0	H9300-AB	CHASSIS ASSY 8/A W FANS 230V 50H	0	0	0
3	3	D-UA-H9300-0-0	H9300-AC	CHASSIS ASSY 8/A W FANS 230V 50H	1	0	1
4	4	D-UA-H9300-0-0	H9300-AD	CHASSIS ASSY 8/A W FANS 230V 50H	0	1	0
5	5	E-UA-BAB-C-0	00BAB-CA	*** THIS ITEM IS NOT USED ***	-	-	-
6	6	E-UA-BAB-C-0	00BAB-CB	*** THIS ITEM IS NOT USED ***	-	-	-
7	7	E-UA-BAB-C-0	00BAB-CH	*** THIS ITEM IS NOT USED ***	-	-	-
8	8	E-UA-BA8-C-0	00BA8-CJ	*** THIS ITEM IS NOT USED ***	-	-	-
9	9	A-PL-KK8-A-0	00KK8-A	8A-CPU	-	-	-
10	10	D-UA-KK8-F-0	00KK8-F	*** THIS ITEM IS NOT USED ***	1	1	1
11	11	A-PL-MS8-C-0	00MS8-CA	16K 12BIT RAM, 4K CHIPS	-	-	-
12	12	A-PL-MS8-C-0	00MS8-CB	32K 12 BIT MOS RAM 4K CHIPS	1	1	0
13	13	A-PL-MS8-A-0	00MS8-AA	*** THIS ITEM IS NOT USED ***	0	0	1
14	14	A-PL-MS8-A-0	00MS8-AB	*** THIS ITEM IS NOT USED ***	-	-	-
15	15	A-PL-MS8-A-0	00MS8-AD	*** THIS ITEM IS NOT USED ***	-	-	-
16	16	C-UA-MR8-F-0	00MR8-FB	*** THIS ITEM IS NOT USED ***	-	-	-
17	17	A-PL-KM8-A-0	00KM8-AA	*** THIS ITEM IS NOT USED ***	-	-	-
18	18	A-PL-IDKC8-A-0	0IDKC8-AA	OPTION BOARD #1: SLU,XTAL CLOCK,	1	1	1
19	19	E-UA-KC8-A-0	00KC8-AA	PROGRAMMER'S CONSOLE	1	1	1
20	20	D-UA-KT8-A-0	00KT8-A	*** THIS ITEM IS NOT USED ***	-	-	-
21	21	A-PL-BA-1-2		SHIPPING LIST	-	-	-
22	22	A-SP-3700170-0-0	3700170-00	INSTR PKG COMPUTER 8A400,600,800	1	1	1
23	23	A-SP-3700172-0-0	3700172-00	INSTR PKG COMPUTER PDP8A CUSHION	1	1	1
24	24	A-SP-3700259-0-0	3700259-00	*** THIS ITEM IS NOT USED ***	-	-	-
25	25	A-SP-3700259-0-0	3700259-01	*** THIS ITEM IS NOT USED ***	-	-	-
26	26	A-SP-3700259-0-0	3700259-02	*** THIS ITEM IS NOT USED ***	-	-	-
27	27	D-UA-H9300-0-0	H9300-BA	*** THIS ITEM IS NOT USED ***	-	-	-
28	28	D-UA-H9300-0-0	H9300-BB	*** THIS ITEM IS NOT USED ***	-	-	-
29	29	D-UA-H9300-0-0	H9300-BH	*** THIS ITEM IS NOT USED ***	-	-	-
30	30	D-UA-H9300-0-0	H9300-BJ	*** THIS ITEM IS NOT USED ***	-	-	-

REVISION HISTORY			BASIC PART NO: 8A205	DRN:	M DUGGAN	DATE: 12-MAY-77	DBP	D	I	G	I	T	A	L
ENG	ECO NUMBER	REV	SECTION C OF C	CHK'D:	L NARHI	DATE: 8-NOV-77	TITLE	PARTS LIST						
DF	8A-1-MK002B	B	SECTION. VARIATION INDEX	CHK'D: [A] AA,AB,AC,AD,BM,BN, BP,BR,BS,BT,BU,BV [B] CM,CN,CP,CR,CS,CT, CU,CV,DM,DN,DP,DR [C] DS,DT,DU,DV	L NARHI	DATE: 8-NOV-77	8A SEMICONDUCTOR MEMORY FAMILY 8A205							
				DES.ENG.: [D]	L NARHI	DATE: 8-NOV-77		DOCUMENT NUMBER						
				RESP.ENG.: [E]	L NARHI	DATE: 8-NOV-77		SIZE	CODE	NUMBER			REV	
				MFG.ENG.: [F]	J V KANE	DATE: 8-NOV-77	K	PL	8A205-0-0				B	
				ASSEMBLY NUMBER: [G]		TOP DOCUMENT NUMBER: #B-DD-8A-1				FILE NAME: MK0385.PLS		EDIT #		5

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PARTS LIST

SHEET C2 OF C2

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION			
				DS	DT	DU	DV
31	31	A-PL-KM8-A-0	00KM8-AB	*** THIS ITEM IS NOT USED ***	-	-	-
32	32	A-PL-KM8-A-0	00KM8-AC	8A INTERNAL OPTION 2	1	1	1
33	33	A-PL-KM8-A-0	00KM8-AD	*** THIS ITEM IS NOT USED ***	-	-	-

TITLE						SECTION C OF C		SIZE/CODE DOCUMENT NUMBER REV		
D	I	G	I	T	A	L				
!	!	!	!	!	!	!	8A SEMICONDUCTOR MEMORY FAMILY			
!	!	!	!	!	!	!	BA205			
!	!	!	!	!	!	!	K PL	8A205-0-0	B	
!	!	!	!	!	!	!				

PARTS LIST

SHEET A1 OF A2

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION										
				AA	AB	AC	AD	BM	BN	BP	BR	BS	BT	BU
1	1	D-UA-H9300-0-0	H9300-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
2	2	D-UA-H9300-0-0	H9300-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
3	3	D-UA-H9300-0-0	H9300-AC	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
4	4	D-UA-H9300-0-0	H9300-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
5	5	E-UA-BAB-C-0	00BAB-CA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
6	6	E-UA-BAB-C-0	00BAB-CB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
7	7	E-UA-BAB-C-0	00BAB-CH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
8	8	E-UA-BAB-C-0	00BAB-CJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
9	9	A-PL-KK8-A-0	00KK8-A	8A-CPU	1	1	1	1	1	1	1	1	1	1
10	10	D-UA-KK8-F-0	00KK8-F	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
11	11	A-PL-MS8-C-0	00MS8-CA	16K 12BIT RAM, 4K CHIPS	0	0	0	0	1	1	0	0	1	0
12	12	A-PL-MS8-C-0	00MS8-CB	32K 12 BIT MOS RAM 4K CHIPS	0	0	0	0	0	0	1	1	0	1
13	13	A-PL-MS8-A-0	00MS8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
14	14	A-PL-MS8-A-0	00MS8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
15	15	A-PL-MS8-A-0	00MS8-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
16	16	C-UA-MR8-F-0	00MR8-FB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
17	17	A-PL-KM8-A-0	00KM8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
18	18	A-PL-DKC8-A-0	0DKC8-AA	OPTION BOARD #1: SLU,XTAL CLOCK,	0	0	0	0	0	0	0	0	0	0
19	19	E-UA-KC8-A-0	00KC8-AA	PROGRAMMER'S CONSOLE	0	0	0	0	0	0	0	0	0	0
20	20	D-UA-KT8-A-0	00KT8-A	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
21	21	A-PL-8A-1-2		SHIPPING LIST	1	1	1	1	1	1	1	1	1	1
22	22	A-SP-3700170-0-0	3700170-00	INSTR PKG COMPUTER 8A400,600,800	1	1	1	1	1	1	1	1	1	1
23	23	A-SP-3700172-0-0	3700172-00	INSTR PKG COMPUTER PDP8A CUSHION	1	1	1	1	1	1	1	1	1	1
24	24	A-SP-3700259-0-0	3700259-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
25	25	A-SP-3700259-0-0	3700259-01	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
26	26	A-SP-3700259-0-0	3700259-02	INSTR PKG COMPUTER 8A820	0	0	0	0	0	0	0	0	0	0
27	27	D-UA-H9300-0-0	H9300-BA	CHASSIS ASSY 8/A 8 AMP 115V 60HZ	1	0	0	0	1	0	1	0	0	0
28	28	D-UA-H9300-0-0	H9300-BB	CHASSIS ASSY 8/A 4 AMP 230V 50HZ	0	1	0	0	0	1	0	1	0	0
29	29	D-UA-H9300-0-0	H9300-BH	H9300-AB EXCEPT G8018 230V 60HZ	0	0	1	0	0	0	0	1	0	1
30	30	D-UA-H9300-0-0	H9300-BJ	H9300-AA EXCEPT G8018 115V 50HZ	0	0	0	1	0	0	0	0	1	0

ENG!	ECO NUMBER	REV	SECTION A OF C	DRN:	M DUGGAN	DATE: 12-MAY-77	DBP	D	I	G	I	T	A	L
DF	8A-1-MK002B	B	SECTION. VARIATION INDEX	CHK'D:	L NARHI	DATE: 8-NOV-77	TITLE	PARTS LIST						
			[A] AA,AB,AC,AD,BM,BN, BP,BR,BS,BT,BU,BV				8A SEMICONDUCTOR MEMORY FAMILY							
			[B] CM,CN,CP,CR,CS,CT, CU,CV,DM,DN,DP,DR	DES.ENG.:	L NARHI	DATE: 8-NOV-77	BA405							
			[C] DS,DT,DU,DV,LM,LN, LP,LR,LS,LT,LU,LV	RESP.ENG.:	L NARHI	DATE: 8-NOV-77	DOCUMENT NUMBER							
			[D]				SIZE	CODE	NUMBER					REV
			[E]	MFG.ENG.:	J V KANE	DATE: 8-NOV-77	K	PL	BA405-0-0					B
			[F]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER: #B-DD-8A-1	FILE NAME: MK0386.PLS	EDIT #						4

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PARTS LIST

SHEET A2 OF A2

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION										
				AA	AB	AC	AD	BM	BN	BP	BR	BS	BT	BU
31	31	A-PL-KM8-A-0	00KM8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
32	32	A-PL-KM8-A-0	00KM8-AC	8A INTERNAL OPTION 2	1	1	1	1	1	1	1	1	1	1
33	33	A-PL-KM8-A-0	00KM8-AD	KM8-AC W NO BOOTSTRAP ROMS	0	0	0	0	0	0	0	0	0	0

D	I	G	I	T	A	L	TITLE	8A SEMICONDUCTOR MEMORY FAMILY	SECTION A OF C	SIZE	CODE	DOCUMENT NUMBER	REV
!	!	!	!	!	!	!		8A405		K	PL	8A405-0-0	B

PARTS LIST

SHEET B1 OF B2

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION											
				CM	CN	CP	CR	CS	CT	CU	CV	DM	DN	DP	DR
1	1	D-UA-H9300-0-0	H9300-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
2	2	D-UA-H9300-0-0	H9300-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
3	3	D-UA-H9300-0-0	H9300-AC	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
4	4	D-UA-H9300-0-0	H9300-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
5	5	E-UA-BA8-C-0	00BA8-CA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
6	6	E-UA-BA8-C-0	00BA8-CB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
7	7	E-UA-BA8-C-0	00BA8-CH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
8	8	E-UA-BA8-C-0	00BA8-CJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
9	9	A-PL-KK8-A-0	00KK8-A	BA-CPU	1	1	1	1	1	1	1	1	1	1	1
10	10	D-UA-KK8-F-0	00KK8-F	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
11	11	A-PL-MS8-C-0	00MS8-CA	16K 12BIT RAM, 4K CHIPS	1	1	0	0	1	1	0	0	1	1	0
12	12	A-PL-MS8-C-0	00MS8-CB	32K 12 BIT MOS RAM 4K CHIPS	0	0	1	1	0	0	1	1	0	0	1
13	13	A-PL-MS8-A-0	00MS8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
14	14	A-PL-MS8-A-0	00MS8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
15	15	A-PL-MS8-A-0	00MS8-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
16	16	C-UA-MR8-F-0	00MR8-FB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
17	17	A-PL-KM8-A-0	00KM8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
18	18	A-PL-DKC8-A-0	0DKC8-AA	OPTION BOARD #1: SLU,XTAL CLOCK,	1	1	1	1	1	1	1	1	1	1	1
19	19	E-UA-KC8-A-0	00KC8-AA	PROGRAMMER'S CONSOLE	0	0	0	0	0	0	0	0	1	1	1
20	20	D-UA-KT8-A-0	00KT8-A	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
21	21	A-PL-8A-1-2		SHIPPING LIST	1	1	1	1	1	1	1	1	1	1	1
22	22	A-SP-3700170-0-0	3700170-00	INSTR PKG COMPUTER BA400,600,800	1	1	1	1	1	1	1	1	1	1	1
23	23	A-SP-3700172-0-0	3700172-00	INSTR PKG COMPUTER PDP8A CUSHION	1	1	1	1	1	1	1	1	1	1	1
24	24	A-SP-3700259-0-0	3700259-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
25	25	A-SP-3700259-0-0	3700259-01	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
26	26	A-SP-3700259-0-0	3700259-02	INSTR PKG COMPUTER BA820	0	0	0	0	0	0	0	0	0	0	0
27	27	D-UA-H9300-0-0	H9300-BA	CHASSIS ASSY 8/A 3 AMP 115V 60HZ	1	0	1	0	0	0	0	0	0	0	0
28	28	D-UA-H9300-0-0	H9300-BB	CHASSIS ASSY 8/A 4 AMP 230V 50HZ	0	1	0	1	0	0	0	0	1	0	0
29	29	D-UA-H9300-0-0	H9300-BH	H9300-AB EXCEPT G8018 230V 60HZ	0	0	0	0	1	0	0	0	0	1	0
30	30	D-UA-H9300-0-0	H9300-BJ	H9300-AA EXCEPT G8018 115V 50HZ	0	0	0	0	0	1	0	0	0	0	0

REVISION HISTORY		BASIC PART NO:	8A405	DRN:	M DUGGAN	DATE: 12-MAY-77	DBP	D	I	G	I	T	A	L	
ENG! ECO NUMBER		!REV	SECTION B OF C												
DF !8A-1-MK002B		B	SECTION, VARIATION INDEX	CHK'D:	L NARHI	DATE: 8-NOV-77	TITLE	PARTS LIST							
			[CA] AA,AB,AC,AD,BM,BN, BP,BR,BS,BT,BU,BV					8A SEMICONDUCTOR MEMORY FAMILY 8A405							
			[CB] CM,CN,CP,CR,CS,CT, CU,CV,DM,DN,DP,DR	DES.ENG.:	L NARHI	DATE: 8-NOV-77		DOCUMENT NUMBER							
			[CC] DS,DT,DU,DV,LM,LN, LP,LR,LS,LT,LU,LV	RESP.ENG.:	L NARHI	DATE: 8-NOV-77		SIZE!CODE! NUMBER							
			[CD]					! REV							
			[CE]	MFG.ENG.:	J V KANE	DATE: 8-NOV-77	K	PL	8A405-0-0						B
			[CF]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER: #B-DD-8A-1	FILE NAME: MK0386.PLS								EDIT #4

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PARTS LIST

SHEET B2 OF B2

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION									
				CM	CN	CP	CR	CS	CT	CU	CV	DM	DN
31	31	A-PL-KM8-A-0	00KM8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-
32	32	A-PL-KM8-A-0	00KM8-AC	8A INTERNAL OPTION 2	1	1	1	1	1	1	1	1	1
33	33	A-PL-KM8-A-0	00KM8-AD	KM8-AC W NO BOOTSTRAP ROMS	0	0	0	0	0	0	0	0	0

D	I	G	I	T	:	A	L	TITLE
---	---	---	---	---	---	---	---	-------

8A SEMICONDUCTOR MEMORY FAMILY	SECTION B OF C
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8A405

SIZE	CODE	DOCUMENT NUMBER	REV
K	PL	8A405-0-0	B

PARTS LIST

SHEET C1 OF C2

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION											
				DS	DT	DU	DV	LM	LN	LP	LR	LS	LT	LU	LV
1	1	D-UA-H9300-0-0	H9300-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
2	2	D-UA-H9300-0-0	H9300-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
3	3	D-UA-H9300-0-0	H9300-AC	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
4	4	D-UA-H9300-0-0	H9300-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
5	5	E-UA-BAB-C-0	00BAB-CA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
6	6	E-UA-BAB-C-0	00BAB-CB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
7	7	E-UA-BAB-C-0	00BAB-CH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
8	8	E-UA-BAB-C-0	00BAB-CJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
9	9	A-PL-KK8-A-0	00KK8-A	BA-CPU	-	-	-	-	-	-	-	-	-	-	-
10	10	D-UA-KK8-F-0	00KK8-F	*** THIS ITEM IS NOT USED ***	1	1	1	1	1	1	1	1	1	1	1
11	11	A-PL-MS8-C-0	00MS8-CA	16K 12BIT RAM, 4K CHIPS	-	-	-	-	-	-	-	-	-	-	-
12	12	A-PL-MS8-C-0	00MS8-CB	32K 12 BIT MOS RAM 4K CHIPS	1	1	0	0	1	1	0	0	1	1	0
13	13	A-PL-MS8-A-0	00MS8-AA	*** THIS ITEM IS NOT USED ***	0	0	1	1	0	0	1	1	0	0	1
14	14	A-PL-MS8-A-0	00MS8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
15	15	A-PL-MS8-A-0	00MS8-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
16	16	C-UA-MR8-F-0	00MR8-FB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
17	17	A-PL-KM8-A-0	00KM8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
18	18	A-PL-IDC8-A-0	00KC8-AA	OPTION BOARD #1: SLU,XTAL CLOCK,	1	1	1	1	1	1	1	1	1	1	1
19	19	E-UA-KC8-A-0	00KC8-AA	PROGRAMMER'S CONSOLE	1	1	1	1	0	0	0	0	0	0	0
20	20	D-UA-KT8-A-0	00KT8-A	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
21	21	A-PL-8A-1-2		SHIPPING LIST	-	-	-	-	-	-	-	-	-	-	-
22	22	A-SP-3700170-0-0	3700170-00	INSTR PKG COMPUTER 8A400,600,800	1	1	1	1	1	1	1	1	1	1	1
23	23	A-SP-3700172-0-0	3700172-00	INSTR PKG COMPUTER FIFPA CUSHION	1	1	1	1	1	1	1	1	1	1	1
24	24	A-SP-3700259-0-0	3700259-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
25	25	A-SP-3700259-0-0	3700259-01	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
26	26	A-SP-3700259-0-0	3700259-02	INSTR PKG COMPUTER 8A820	0	0	0	0	0	0	0	0	0	0	0
27	27	D-UA-H9300-0-0	H9300-BA	CHASSIS ASSY 8/A 8 AMP 115V 60HZ	0	0	0	0	1	0	1	0	0	0	0
28	28	D-UA-H9300-0-0	H9300-BB	CHASSIS ASSY 8/A 4 AMP 230V 50HZ	0	0	0	0	0	1	0	1	0	0	0
29	29	D-UA-H9300-0-0	H9300-BH	H9300-AB EXCEPT G8018 230V 60HZ	1	0	1	0	0	0	0	0	1	0	1
30	30	D-UA-H9300-0-0	H9300-BJ	H9300-AA EXCEPT G8018 115V 50HZ	0	1	0	1	0	0	0	0	1	0	1

REVISION HISTORY	BASIC PART NO:	8A405	DRN:	M DUGGAN	DATE: 12-MAY-77	DBP	D	I	G	I	T	A	L
ENG!	ECO NUMBER	REV	SECTION C OF C			TITLE	PARTS LIST						
DF	18A-1-MK002B	B	SECTION, VARIATION INDEX	CHK'D:	L NARHI	DATE: 8-NOV-77							
			[A] AA,AB,AC,AD,BM,BN, BF,BR,BS,BT,BU,BV				8A SEMICONDUCTOR MEMORY FAMILY						
			[B] CM,CN,CP,CR,CS,CT, CU,CV,DM,DN,DP,IR	DES.ENG.:	L NARHI	DATE: 8-NOV-77	8A405						
			[C] DS,DT,DU,DV,LM,LN, LP,LR,LS,LT,LU,LV	RESP.ENG.:	L NARHI	DATE: 8-NOV-77		DOCUMENT NUMBER					
			[D]					SIZE	CODE	NUMBER			
			[E]	MFG.ENG.:	J V KANE	DATE: 8-NOV-77	K	PL	8A405-0-0				
			[F]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER: #B-00-8A-1		FILE NAME: MK0386.PLS	EDIT #: 4				

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PARTS LIST

SHEET C2 OF C2

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION											
				DS	DT	DU	DV	LM	LN	LP	LR	LS	LT	LU	LV
31	31	A-PL-KM8-A-0	00KM8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	
32	32	A-PL-KM8-A-0	00KM8-AC	8A INTERNAL OPTION 2	1	1	1	1	0	0	0	0	0	0	
33	33	A-PL-KM8-A-0	00KM8-AD	KM8-AC W NO BOOTSTRAP ROMS	0	0	0	0	1	1	1	1	1	1	

TITLE										SIZE		CODE		DOCUMENT NUMBER		REV	
D	I	G	I	T	A	L											

8A SEMICONDUCTOR MEMORY FAMILY
SECTION C OF C
8A405

K PL 8A405-0-0 B

PARTS LISTS

SHEET A1 OF A2

REVISION HISTORY		BASIC PART NO:	8A425	DRN:	M DUGGAN	DATE:	12-MAY-77	DBP	D I G I T A L		
ENGI	ECO NUMBER	REV	SECTION A OF B								
DF	8A-1-MK002B	B	SECTION, VARIATION INDEX	CHK'D:	L NARHI	DATE:	8-NOV-77	TITLE	PARTS LIST		
			[CA] AA,AB,AC,AD,BH,BJ, BK,BL,BM,BN,BP,BR					8A SEMICONDUCTOR MEMORY FAMILY			
			[CB] BS,BT,BU,BV,CM,CN, CP,CR,CS,CT,CU,CV	DES.ENG.:	L NARHI <i>Mayt Price</i>	DATE:	8 NOV-77	8A425			
		C		RESP.ENG.:	L NARHI <i>Mayt Price</i>	DATE:	8-NOV-77	DOCUMENT NUMBER			
		D						SIZE	CODE	NUMBER	REV
		E		MFG.ENG.:	J V KANE	DATE:	8-NOV-77	K	PL	8A425-0-0	B
		F		ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:		EDIT #	
						#B-DD-8A-1		MK0387.PLS		4	

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M1

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PARTS LIST

SHEET A2 OF A2

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION										
				AA	AB	AC	AD	BH	BJ	BK	BL	BM	BN	BP
31	31	A-PL-KM8-A-0	00KM8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
32	32	A-PL-KM8-A-0	00KM8-AC	8A INTERNAL OPTION 2	1	1	1	1	1	1	1	1	1	1
33	33	A-PL-KM8-A-0	00KM8-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-

TITLE				SECTION A - OF B				SIZE				CODE		DOCUMENT NUMBER		REV	
D	I	G	I	T	A	L	I	8A SEMICONDUCTOR MEMORY FAMILY	8A425	K	PL	8A425-0-0	B				

PARTS LIST

SHEET B1 OF B2

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION											
				BS	BT	BU	BV	CM	CN	CP	CR	CS	CT	CU	CV
1	1	D-UA-H9300-0-0	H9300-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
2	2	D-UA-H9300-0-0	H9300-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
3	3	D-UA-H9300-0-0	H9300-AC	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
4	4	D-UA-H9300-0-0	H9300-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
5	5	E-UA-BA8-C-0	00BA8-CA	20-SLOT OMNIBUS,10.5 X 21",2 G80	0	0	0	0	1	0	1	0	0	0	0
6	6	E-UA-BA8-C-0	00BA8-CB	20-SLOT OMNIBUS,10.5 X 21",2 G80	0	0	0	0	0	1	0	1	0	0	0
7	7	E-UA-BA8-C-0	00BA8-CH	SAME AS BA8-CB EXCEPT 240V 60HZ	1	0	1	0	0	0	0	1	0	1	0
8	8	E-UA-BA8-C-0	00BA8-CJ	SAME AS BA8-CA EXCEPT 120V 50HZ	0	1	0	1	0	0	0	0	1	0	1
9	9	A-PL-KK8-A-0	00KK8-A	8A-CPU	1	1	1	1	1	1	1	1	1	1	1
10	10	D-UA-KK8-F-0	00KK8-F	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
11	11	A-PL-MS8-C-0	00MS8-CA	16K 12BIT RAM, 4K CHIPS	0	0	0	0	0	0	0	0	0	0	0
12	12	A-PL-MS8-C-0	00MS8-CB	32K 12 BIT MOS RAM 4K CHIPS	1	1	2	2	1	1	2	2	1	1	2
13	13	A-PL-MS8-A-0	00MS8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
14	14	A-PL-MS8-A-0	00MS8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
15	15	A-PL-MS8-A-0	00MS8-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
16	16	C-UA-MR8-F-0	00MR8-FB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
17	17	A-PL-KM8-A-0	00KM8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
18	18	A-PL-DKC8-A-0	00KC8-AA	OPTION BOARD #1: SLU,XTAL CLOCK,	0	0	0	0	1	1	1	1	1	1	1
19	19	E-UA-KC8-A-0	00KC8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
20	20	D-UA-KT8-A-0	00KT8-A	MEM MAN OPTION FOR KT8-A SYS	0	0	1	1	0	0	1	1	0	0	1
21	21	A-PL-8A-1-2		SHIPPING LIST	1	1	1	1	1	1	1	1	1	1	1
22	22	A-SP-3700170-0-0	3700170-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
23	23	A-SP-3700172-0-0	3700172-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
24	24	A-SP-3700259-0-0	3700259-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
25	25	A-SP-3700259-0-0	3700259-01	INSTR PKG BA8-C CHASSIS ASSEMBLY	1	1	1	1	1	1	1	1	1	1	1
26	26	A-SP-3700259-0-0	3700259-02	INSTR PKG COMPUTER 8A820	1	1	1	1	1	1	1	1	1	1	1
27	27	D-UA-H9300-0-0	H9300-BA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
28	28	D-UA-H9300-0-0	H9300-BB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
29	29	D-UA-H9300-0-0	H9300-BH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
30	30	D-UA-H9300-0-0	H9300-BJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-

REVISION HISTORY	BASIC PART NO:	8A425	DRN:	M DUGGAN	DATE:	12-MAY-77	DBP	D	I	G	I	T	A	L
ENG	ECO NUMBER	REV	SECTION B OF B				TITLE	PARTS LIST						
DF	8A-1-MK002B	B	SECTION. VARIATION INDEX	CHK'D:	L NARHI	DATE:	8-NOV-77	8A SEMICONDUCTOR MEMORY FAMILY						
			[CA] AA,AB,AC,AD,BH,BJ, BK,BL,BM,BN,BP,BR				8A425							
			[CB] BS,BT,BU,BV,CM,CN, CP,CR,CS,CT,CU,CV	DES.ENG.:	L NARHI	DATE:	8 NOV-77	DOCUMENT NUMBER						
			[CC]		RESP.ENG.:	L NARHI	DATE:	8-NOV-77	SIZE	CODE	NUMBER		REV	
			[CD]											
			[CE]		MFG.ENG.:	J V KANE	DATE:	8-NOV-77	K	PL	8A425-0-0		B	
			[CF]		ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:				FILE NAME:	EDIT #		
							#B-DD-8A-1				MK0387.PLS	4		

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LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION											
				AA	AB	AC	AD	BH	BJ	BK	BL	BM	BN	BP	BR
1	1	D-UA-H9300-0-0	H9300-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	
2	2	D-UA-H9300-0-0	H9300-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	
3	3	D-UA-H9300-0-0	H9300-AC	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	
4	4	D-UA-H9300-0-0	H9300-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	
5	5	E-UA-BA8-C-0	00BA8-CA	20-SLOT OMNIBUS, 10.5 X 21", 2 G80	1	0	0	0	1	0	0	0	1	0	1
6	6	E-UA-BA8-C-0	00BA8-CB	20-SLOT OMNIBUS, 10.5 X 21", 2 G80	0	1	0	0	0	1	0	0	0	1	0
7	7	E-UA-BA8-C-0	00BA8-CH	SAME AS BA8-CB EXCEPT 240V 60HZ	0	0	1	0	0	0	1	0	0	0	0
8	8	E-UA-BA8-C-0	00BA8-CJ	SAME AS BA8-CA EXCEPT 120V 50HZ	0	0	0	1	0	0	0	1	0	0	0
9	9	A-PL-KK8-A-0	00KK8-A	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
10	10	D-UA-KK8-F-0	00KK8-F	CENTRAL PROCESSOR [8EJ]	1	1	1	1	1	1	1	1	1	1	1
11	11	A-PL-MS8-C-0	00MS8-CA	16K 12BIT RAM, 4K CHIPS	0	0	0	0	1	1	1	1	0	0	0
12	12	A-PL-MS8-C-0	00MS8-CB	32K 12 BIT MOS RAM 4K CHIPS	0	0	0	0	0	0	0	0	1	1	2
13	13	A-PL-MS8-A-0	00MS8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
14	14	A-PL-MS8-A-0	00MS8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
15	15	A-PL-MS8-A-0	00MS8-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
16	16	C-UA-MR8-F-0	00MR8-FB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
17	17	A-PL-KM8-A-0	00KM8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
18	18	A-PL-DKC8-A-0	00KC8-AA	OPTION BOARD #1: SLU,XTAL CLOCK,	0	0	0	0	0	0	0	0	0	0	0
19	19	E-UA-KC8-A-0	00KC8-AA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
20	20	D-UA-KT8-A-0	00KT8-A	MEM MAN OPTION FOR KT8-A SYS	0	0	0	0	0	0	0	0	0	1	1
21	21	A-PL-8A-1-2		SHIPPING LIST	1	1	1	1	1	1	1	1	1	1	1
22	22	A-SP-3700170-0-0	3700170-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
23	23	A-SP-3700172-0-0	3700172-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
24	24	A-SP-3700259-0-0	3700259-00	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
25	25	A-SP-3700259-0-0	3700259-01	INSTR PKG BA8-C CHASSIS ASSEMBLY	1	1	1	1	1	1	1	1	1	1	1
26	26	A-SP-3700259-0-0	3700259-02	INSTR PKG COMPUTER 8A820	1	1	1	1	1	1	1	1	1	1	1
27	27	D-UA-H9300-0-0	H9300-BA	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
28	28	D-UA-H9300-0-0	H9300-BB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
29	29	D-UA-H9300-0-0	H9300-BH	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-
30	30	D-UA-H9300-0-0	H9300-BJ	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-	-

REVISION HISTORY	BASIC PART NO:	8A625	DRN:	M DUGGAN	DATE:	12-MAY-77	DBP	D	I	G	I	T	A	L
ENG	ECO NUMBER	REV	SECTION A OF B											
DF	8A-1-MK002B	B	SECTION. VARIATION INDEX	CHK'D:	L NARHI	JL	DATE:	8-NOV-77						
			[AJ AA,AB,AC,AD,BH,BJ, BK,BL,BM,BN,BP,BR [BJ BS,BT,BU,BV,CM,CN, CP,CR,CS,CT,CU,CV	DES.ENG.:	L NARHI	Jay 2 Rev	DATE:	8-NOV-77						
			[CJ	RESP.ENG.:	L NARHI	Jay 2 Rev	DATE:	8-NOV-77						
			[D]				SIZE	CODE	NUMBER					REV
			[EJ	MFG.ENG.:	J V KANE		DATE:	8-NOV-77	K	PL	8A625-0-0			B
			[FJ	ASSEMBLY NUMBER:			TOP DOCUMENT NUMBER:							EDIT #
							#B-DD-8A-1							6
														MK

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AUTOMATED BY PRTLST.3P(44)

PARTS LIST

SHEET A2 OF A2

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION										
				AA	AB	AC	AD	BH	BJ	BK	BL	BM	BN	BP
31	31	A-PL-KM8-A-0	00KM8-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
32	32	A-PL-KM8-A-0	00KM8-AC	8A INTERNAL OPTION 2	1	1	1	1	1	1	1	1	1	1
33	33	A-PL-KM8-A-0	00KM8-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-

TITLE				ISIZE		CODE		DOCUMENT NUMBER		REV	
D	I	G	I	T	A	L					
!	!	!	!	!	!	!	8A SEMICONDUCTOR MEMORY FAMILY	SECTION A OF B	K	PL	8A625-0-0
!	!	!	!	!	!	!	8A625				B

AUTOMATED BY FRTLST.3P(44)

卷之三

SHEET B1 OF B2

REVISION HISTORY		BASIC PART NO:	8A625	DRN:	M DUGGAN	DATE:	12-MAY-77	DBP	D	I	G	I	T	A	L
ENG	ECO NUMBER	REV	SECTION B OF B					TITLE	PARTS LIST						
DF	EA-1-MK002B	B	SECTION. VARIATION INDEX	CHK'D:	L NARHI	DATE:	8-NOV-77		8A SEMICONDUCTOR MEMORY FAMILY						
			[AJ AA,AB,AC,AD,BH,BJ, BK,BL,BM,BN,BP,BR]						8A625						
			[BJ BS,BT,BU,BV,CM,CN, CP,CR,CS,CT,CU,CV]	DES.ENG.:	L NARHI	DATE:	8 NOV-77		DOCUMENT NUMBER						
			[CJ]	RESP.ENG.:	L NARHI	DATE:	8-NOV-77								
			[D]						SIZE	CODE	NUMBER			REV	
			[EJ]	MFG.ENG.:	J V KANE	DATE:	8-NOV-77	K	PL	8A625-0-0					B
			[FJ]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:					FILE NAME:		EDIT		
						#B-DD-8A-1					MK0388.PLS				6

AUTOMATED BY PRTLST.3P(44)

PARTS LIST

SHEET B2 OF B2

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION									
				BS	BT	BU	BV	CM	CN	CP	CR	CS	CT
31	31	A-PL-KM8-A-0	00KMB-AB	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-
32	32	A-PL-KM8-A-0	00KMB-AC	8A INTERNAL OPTION 2	1	1	1	1	1	1	1	1	1
33	33	A-PL-KM8-A-0	00KMB-AD	*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-

D	I	G	I	T	A	L	TITLE		SECTION B OF B		SIZE	CODE	DOCUMENT NUMBER	REV
!	!	!	!	!	!	!	BA SEMICONDUCTOR MEMORY FAMILY	!	!	!	K	PL	8A625-0-0	B
!	!	!	!	!	!	!	BA625	!	!	!	!	!	!	!

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS
PARTS LIST

MADE BY	M. DUGGAN	CHECKED	Darryn Starke	SECTION
DATE	12 MAY 77	DATE	11-JUL-77	1
ENG	Darryn Starke	PROD	Ji Xian 25C177	ISSUED SECT
DATE	11-JUL-77	DATE		1

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS
PARTS LIST

MADE BY	M.DUGGAN	CHECKED	L. NARHI	SECTION
DATE	12-MAY-77	DATE	11-JUL-77	1
ENG	LARRY.NARHI	PROD	J.KANE	ISSUED SECT
DATE	11-JUL-77	DATE	25-OCT-77	

TITLE SOFTWARE LIST 8A SEMICONDUCTOR
MEMORY FAMILY
(8A100,8A205,8A405,8A425,8A625)

ASSY NO.
B-DD-8A-1

SIZE COD
A PL

NUMBER
8A-1-3

REV.	ECO NO.
A	8A-1 M6991

**DEC FORM DEC 16-(325)-1031-N870
DRA 110**

DIGITAL EQUIPMENT CORPORATION								
MAYNARD, MASSACHUSETTS								
ENGINEERING SPECIFICATION								
DATE 26-apr-77								
TITLE 8A100, 205, 405, 425, 625 FIELD INSTALLATION AND ACCEPTANCE PROCEDURE								
REVISIONS								
REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE		

ENG 205-11 Jul-77 APPD Gary Harle 11-4 SIZE CODE NUMBER REV
DEC 15-1977 DRAFTED BY SP 8A-1-4
DRA 107

ENGINEERING SPECIFICATION								
CONTINUATION SHEET								
TITLE 8A100, 205, 405, 425, 625 FIELD INSTALLATION ACCEPTANCE PROCEDURE								
I. GENERAL								
Installation of the basic 8A100, 205, 405, 425 and 625 computer requires no special tools or equipment. Normal hand tools are all that are required.								
II. UNPACKING								
Unpack and inspect the equipment using the procedure provided in the Operator's Handbook.								
III. INSPECTION								
After removing the equipment packing material, inspect the equipment.								
1. Internally inspect the 8A enclosure and console for damage, loose nuts, bolts, screws, etc.								
2. Inventory all hardware against shipping list.								
3. Inventory all software against software list, if ordered.								
4. Inventory all prints against shipping list if ordered.								
IV. INSTALLATION PROCEDURE								
Install the equipment using the following procedure:								
1. Turn off the power switch of the Limited Function Console.								
WARNING								
DO NOT TOUCH THE COMPUTER AFTER PLUGGING IT IN UNTIL IT IS CHECKED FOR THE PROPER GROUNDING.								
2. Insure that all power is received from the same source.								
3. Plug in the power cord.								

ENGINEERING SPECIFICATION								
CONTINUATION SHEET								
TITLE 8A100, 205, 405, 425, 625 FIELD INSTALLATION AND ACCEPTANCE PROCEDURE								
I. GENERAL								
II. UNPACKING								
III. INSPECTION								
IV. INSTALLATION PROCEDURE								
V. ACCEPTANCE PROCEDURE								

ENGINEERING SPECIFICATION								
CONTINUATION SHEET								
TITLE 8A100, 205, 405, 425, 625 FIELD INSTALLATION ACCEPTANCE PROCEDURE								
I. GENERAL								
4. Before touching the computer, check frame to ground to insure that no AC voltage is present.								
5. Unplug power cord.								
6. Turn "on" Power ON/OFF switch and set regulator circuit breaker to the "ON" position. Behind the Limited Function Console on the 8A100, 8A205, and 8A425 or inside the rear panel on the 8A625 and 8A655.)								
7. Repeat Steps 3 and 4.								
8. Power should now be applied to the 8A; fans should be running, and the power light on the Limited Function Console should be "ON". The light labeled 'battery charging' should be illuminated on the 8A205 and 8A425 indicating that DC power is okay.								
If none of the above occur, remove the Limited Function panel and check the Master/Slave switch located below the ON/OFF switch on the Limited Function Board.								
9. The Run light should not be on. If it is, switch Power Off via the ON/OFF switch.								
Remove the M8315 CPU module (on all except 8A625) and set switches as indicated below. Then insert CPU in the first slot in the OMNIBUS and turn power back on. The Run light should remain off.								
S1-1 thru S1-6, S1-8 set to "OFF" position. S1-7 set to "ON" position.								
10. Check modules to insure they are located in their proper position in the OMNIBUS. Refer to the Operator's Handbook.								
11. Check the operation of the Programmer's Console.								
12. Manually load, deposit and examine to insure that memory modules are associated with the correct memory fields. If not, turn the unit off and reconfigure the memory modules to the correct fields.								

DEC FORM NO EN-0102-16-N707(381)
DRA 108

DEC FORM NO EN-0102-16-N707(381)
DRA 108

SHEET 4 OF 6

SHEET 3 OF 6

SHEET 2 OF 6

ENGINEERING SPECIFICATION

DIGITAL

CONTINUATION SHEET

TITLE 8A100,205,405,425,625 FIELD INSTALLATION AND ACCEPTANCE PROCEDURE

V. ACCEPTANCE PROCEDURE

Perform the acceptance tests referred to in table A. If abnormal indications are encountered, refer to the diagnostic listings for error descriptions. Refer to the operators handbook and the diagnostic listings for instructions on loading diagnostics.

Equipment required

1. 8A100,205,405,425,625 with 1-32K of semiconductor memory.
2. Programmer's Console (KC8A and DKC8A)
3. Paper tape input device.
4. Diagnostics and listings.

NOTE: If programmer's panel and paper tape input device are not available as part of the system being installed, they must be provided by the customer in good working order. If semiconductor memory is 1K PROM only, refer to the MR8-F Engineering Spec.

Table A

Acceptance 8A100,205,405,425,625

<u>Program Name</u>	<u>MAINDEC #</u>	<u>Accept Time</u>
PDP8A Central Processor Test	08-DJKKA	20 Minutes
1-32K Random Exerciser	08-DJEXA	20 Minutes
MS8-A or MS8-C/D MOS Memory	Refer to Acceptance Procedure for MS8-C/D and MS8-A.	
KT8-A Memory Management Option	Refer to Acceptance Procedure for KT8-A.	

ENGINEERING SPECIFICATION

DIGITAL

CONTINUATION SHEET

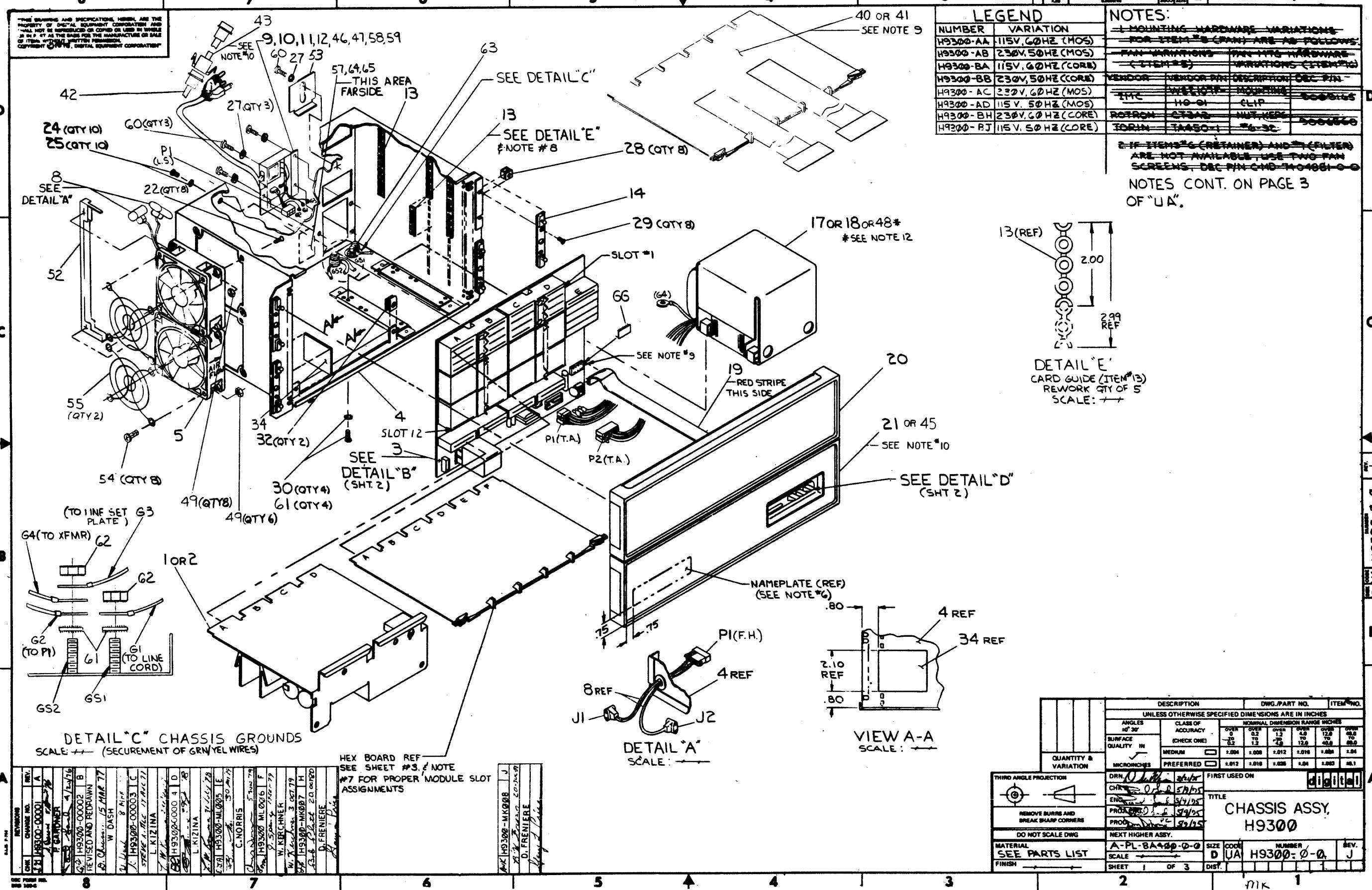
TITLE 8A100,205,405,425,625 FIELD INSTALLATION ACCEPTANCE PROCEDURE

DKC8A Option One

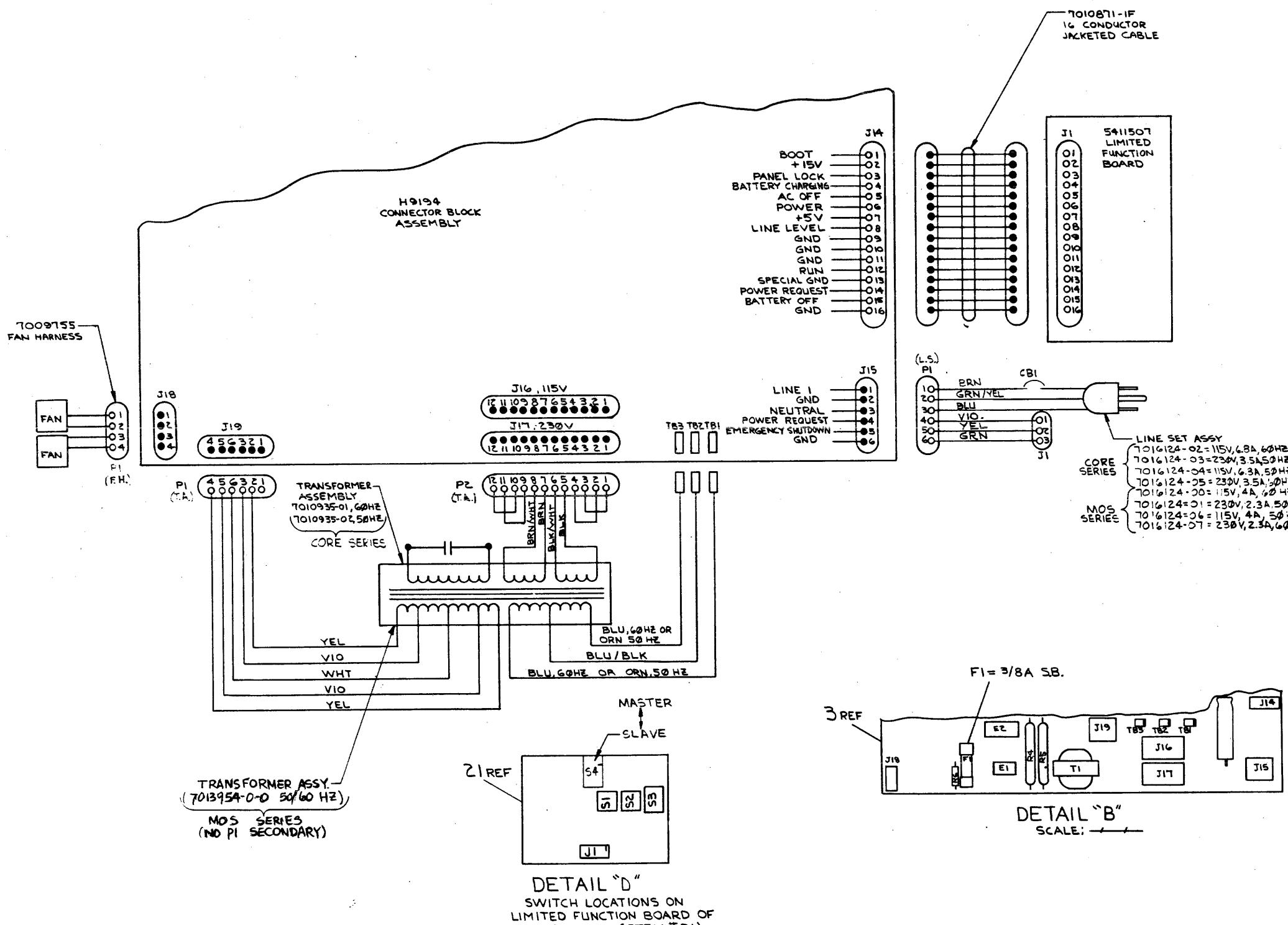
Refer to Acceptance Procedure for DKC8A.

KM8A Option Two

Refer to Acceptance Procedure for KM8A.



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ASSEMBLY INSTRUCTIONS

TITLE CHASSIS ASSY, H9300	SIZE CODE D UA	NUMBER H9300-0-0	REV. J
SCALE — — — — —	SHEET 2 OF 3	DIST.	

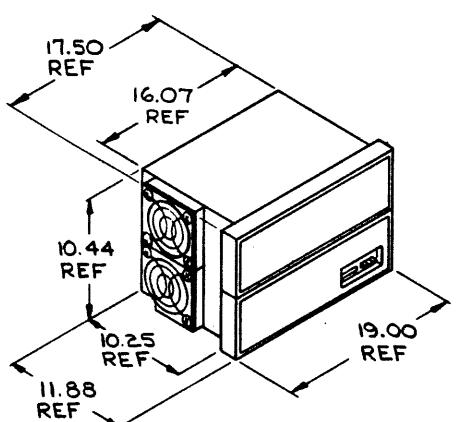
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MODULE ASSIGNMENTS AND POWER REQUIREMENTS (SEE NOTES #7&8)

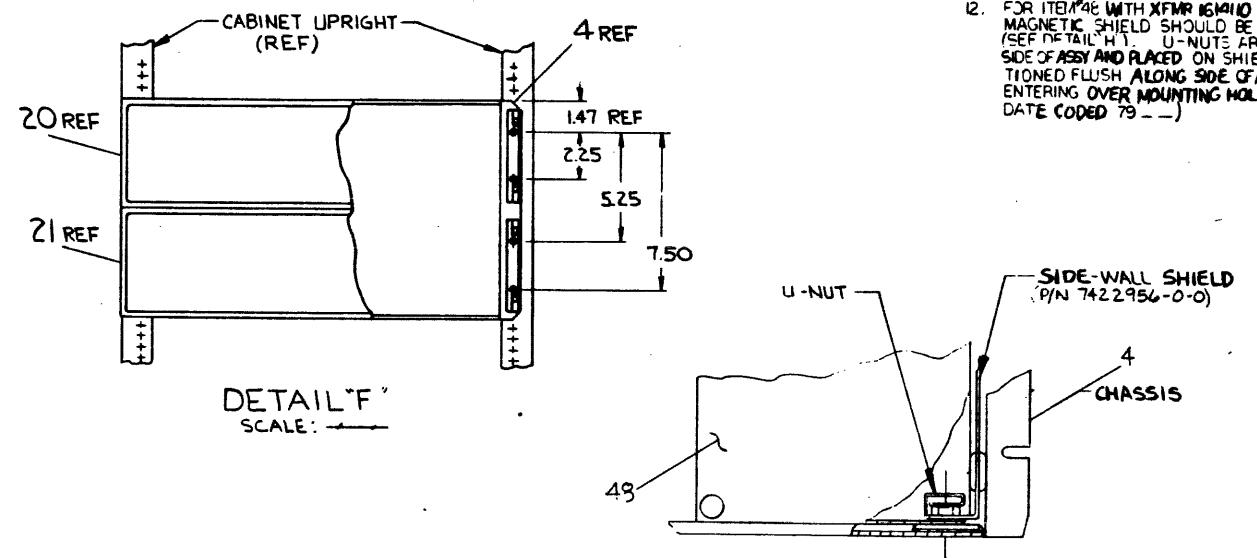
OPTION	DESCRIPTION	BOARD SIZE	NO. SLOTS USED	ASSIGNED SLOT NO.	CURRENT		
					+5V	+15V	-15V
CMB-F	CARD RDR CONT.	QUAD	1	4 - 12	.55A	—	—
CRB-F	CARD RDR CONT.	↓	1	4 - 12	.55A	—	—
DBB-EA	INTERPROC. BUFFER	↓	1	2 - 12	.88A	—	.03A
DKB-EC	RTC, CRYSTAL	↓	1	2 - 12	.34A	—	—
DKB-EP	RTC, PROG.	QUAD	2	2 - 12	1.43A	—	.07A
DKC8-A	OPTION #1	HEX	1	2 - 3	2.8A	.08A	.10A
DPE-EA,-EB	MODEM INTERFACE	QUAD	2	2 - 12	1.88A	.05A	.11A
DR8-EA	DIGITAL I/O	QUAD	1	2 - 12	2.25A	—	—
KAB-E	POSITIVE I/O	QUAD	1	4 - 12	1.48A	—	—
KCB-AA,-AB	PROG. CONSOLE	PNL. MT.	0	N.A.	2.5A	—	—
KD8-E	DATA BREAK	QUAD	1	4 - 12	1.2A	—	—
KG8-EA	REDUNDANCY CHECK	QUAD	1	4 - 12	.94A	—	—
KK8-A	C.P.U.	HEX	1	1	5.8A	—	.04A
KL8-JA	ASYNC. DATA CONT	QUAD	1	2 - 12	1.1A	.05A	.18A
KL8-M	MODEM CONTROL	QUAD	1	2 - 12	.48A	.04A	.04A
KM8-A	OPTION #2	HEX	1	2 - 3	2.8A	—	—
KM8-E	MEM. EXT. & T.S. CONT.	QUAD	1	4 - 12	1.8A	—	—
LE8-XX	LINE PRINTER CONT.	QUAD	1	2 - 12	.35A	—	—
LS8-F	LINE PRINTER CONT.	QUAD	1	2 - 12	.48A	—	—
M88-AA	8K CORE, OPERATING	HEX	2	4 - 8	2.5A	—	—
M88-AA	8K CORE, STANDBY	HEX	2	4 - 8	2.5A	—	—
M88-AB	16K CORE, OPERATING	HEX	2	4 - 8	2.5A	—	—
M88-AB	16K CORE, STANDBY	HEX	2	4 - 8	2.5A	—	—
MR8-AA	1K ROM	QUAD	1	2 - 12	2.8A	—	—
MR8-AB	2K ROM	↓	1	2 - 12	3.0A	—	—
MR8-AC	3K ROM	↓	1	2 - 12	4.0A	—	—
MR8-AD	4K ROM	↓	1	2 - 12	5.8A	—	—
MR8-FB	1K PROM	↓	1	2 - 12	3.8A	—	.35A
MS8-AA	1K RAM	↓	1	4 - 12	1.4A	—	—
MS8-AB	2K RAM	↓	1	4 - 12	2.1A	—	—
MS8-AC	3K RAM	↓	1	4 - 12	2.8A	—	—
MS8-AD	4K RAM	↓	1	4 - 12	3.5A	—	—
PCB-E,PRB-E	RDR/PUNCH CONTROL	↓	1	4 - 12	.84A	—	.05A
RXB-E	RX81 CONTROL	↓	1	4 - 8	1.5A	—	—
RKB-EA	RKB8 CONTROL	↓	3	4 - 12	3.10A	—	—
TAB-AA	TUB8 CONTROL	↓	1	2 - 12	2.88A	—	—
TMB-EA,-FA	TU1B CONTROL	↓	4	4 - 12	4.18A	—	—
VCB-E	DISPLAY CONTROL	↓	2	2 - 12	.31A	—	—
VTB-E	DISPLAY CONTROL	↓	3	4 - 12	3.78A	.09A	.13A
XVB-E	PLOTTER CONTROL	QUAD	1	4 - 12	.42A	.01A	.03A
KK8-E	M8300 MAJOR REG.	QUAD	1	12	1.7	—	—
	M8310 MAJOR REG. CONT	QUAD	1	11	.6	—	—
	M8330 TIMING GEN	QUAD	1	10	1.2	—	—
	M8320 BUS LOAD	QUAD	1	1	1.0	1.0	.53
M88-CA	16K MOS RAM	HEX	1	4-8	3.3A	—	.7A
M88-CP	32K MOS RAM	↓	1	4-8	3.5A	—	.7A
KTB-A	MEIA MANAGEMENT	↓	1	4-8	3.8A	—	—
FLE-A	RLOI CONTROL	HEX	1	4-12	2.5A	.2A	.1A

AVAILABLE CURRENT - H7300-AA, AB **+15V** **+15V** **-15V**
 - H7300-BA BB **20A** **2A** **2A**

REVISIONS		
CHK	CHANGE NO.	REV



MAX. UNIT WEIGHT = 55 LB.



DETAIL "F
SCALE: -

DETAIL "H"
SCALE: NONE

MOUNTING INSTRUCTIONS

1. SEE DETAIL "F" FOR MTG DIM
 2. THE DIM FROM CENTER LINE OF RIGHT CAB UPRIGHT MOUNTING HOLE TO LEFT CAB UPRIGHT MOUNTING HOLE CENTER LINE IS 10.31.
 3. REMOVE THE BLANK BEZEL ASSY.
 4. REMOVE THE LIMITED FUNCTION PANEL AND DISCONNECT THE CABLE FROM THE LIMITED FUNCTION BD.
 5. REMOVE THE LATCH MOULDING (4 PLACES).
 6. REMOVE THE SPEED NUT, AND INSTALL ON CABINET POST. 8 PLACES PER MOUNTING DIMENSIONS.
 7. ~~IT MAY BE NECESSARY TO REMOVE THE FILTER RETAINER AND THE FILTER IN ORDER TO MOUNT THE BOX IN A CABINET.~~
 8. WITH THE BOX IN PLACE, IN THE CABINET, REPLACE THE LATCH MOLDING AND SPACERS SO AS TO SECURE THE BOX TO THE CABINET.
 9. PLUG THE CABLE INTO THE LIMITED FUNCTION BD AND REPLACE LIMITED FUNCTION PANEL.
 10. REPLACE THE BLANK BEZEL ASSY; ~~REINSTALL THE FILTER RETAINER AND THE FILTER.~~
 11. FOR MOUNTING INSTRUCTIONS #4 AND #9, SEE NOTES 9 AND 10.

- ~~3. TO CREATE A 115V TO 120V VARIATION USE THE H93BB-00
REPLACE THE LINE SET (ITEM #12) WITH A 115V 60HZ 0.4A
LINE SET (DEC P/N D400-1010010-001) AND PLATE P/N 12 PIN
CONN OF THE TRANSFORMER ASSEMBLY INTO J10-111977~~

4. ALL H93BB POWER SUPPLY DC OUTPUTS ARE PROVIDED TO
DRIVE LOGIC INTERNAL TO THE BASIC MACHINE ENCLOSURE.
DIGITAL WILL NOT BE RESPONSIBLE FOR THE PERFORMANCE
OF THE H93BB IF ANY DC POWER IS TAKEN OUTSIDE THE
MACHINE.

5. ENVIRONMENTAL CONDITIONS FOR H93BB ARE SPECIFIED IN
DEC STD 102 CLASS "C" ENVIRONMENT.

6. THIS ITEM (NAMEPLATE) IS SHOWN FOR REFERENCE ONLY.
IT WILL BE ADDED ON A HIGHER LEVEL ASSEMBLY.

7. INSTALL MODULES AS FOLLOWS:
PLACEMENT OF HEX MODULES IS FROM SLOT #1(TOP OF
BACKPLANE) DOWN.
PLACEMENT OF QUAD. MODULES IS FROM SLOT #12(BOTTOM OF
BACKPLANE) UP.

8. CARD GUIDES (ITEM #13) ARE PROVIDED FOR SLOTS #1-10.
WHEN A QUAD. MODULE WITH AN H8510 OR H8511 CONNECTOR
BLOCK (NFB-A, NSB-A, ETC) ON THE "E" SET OF FINGERS
IS INSTALLED, IT IS NECESSARY TO CLIP OFF THE FRONT-
LEFT CARD GUIDE IN THOSE SLOTS
SO THAT THE CONNECTOR BLOCK MAY BE PROPERLY
SEATED (REF DETAIL "E" FOR AN EXAMPLE OF CARD
GUIDE REWORK).

9. WHEN USED AS AN EXPANDER BOX THE BC0AC ITEM 40,
OR THE BC0BH CABLE ITEM 41 GOES INTO SLOT 1 OF
THE H9300. ALSO THE 16 CONDUCTOR CABLE ITEM 19 IS
REMOVED IN EXPANDER BOX AND THE REMOTE SLAVE CIRCUIT
ITEM 44 IS INSTALLED IN J14 OF THE H9194 CONNECTOR
BLOCK ASSEMBLY.

10. ITEM 21 IS REPLACED BY ITEM 45 IN EXPANDER BOX AND
ITEM 42 AND 43 ARE INCLUDED IN EXPANDER BOX
VARIATIONS. ITEM 43 IS TO BE USED WITH ITEM 42 WHEN
EXPANDING TO 8E. IN ALL OTHER EXPANSION VARIATIONS
ITEM 42 IS USED ALONE.

~~11. REMOVE GROUND WIRE (GND)EL OR GND) FROM
LINE SET ASSY ITEM 30, J12-14 OR J17 WELD
STUD NOTE: LEAD MAY ALREADY BE FREE
ATTACH THIS GROUND WIRE TO CHASSIS (SEE DETAIL "F")
USING ITEMS 27-4930 AND ST. NOTE: HOLE IN
RIGHT REAR SIDE MAY HAVE TO BE OPENED TO
DO IN FOR 1 SCREW~~

12. FOR ITEM #46 WITH XFRM 161410 REV-B, A SPECIAL SIDE WALL
MAGNETIC SHIELD SHOULD BE INSTALLED DEC 7422956-0-0
(SEE DETAIL H). U-NUTS ARE REMOVED FROM RIGHT
SIDE OF ASSY AND PLACED ON SHIELD: SHIELD IS THEN POSI-
TIONED FLUSH ALONG SIDE OF ASSY WITH U-NUTS
ENTERING OVER MOUNTING HOLES OF ASSY. (ITEM 48,
DATE CODED 79).

NUMBER 1930-0-0
CODE

2

A

TITLE CHASSIS ASSY , H9300	SIZE CODE D UA	NUMBER H9300-0-0	REV. J
SCALE 	SHEET 3 OF 3	DIST.	

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION										
				AA	AB	AC	AD	BA	BB	BC	BD	BE	BF	
1	1	D-CS-G8016-0-1	G8016-00	REGULATOR FOR H763	1	1	1	1	-	-	-	-	-	-
2	2	D-CS-G8018-0-1	G8018-00	H774 REGULATOR	-	-	-	-	1	1	1	1	1	1
3	3	D-AD-H9194-0-0	H9194-00	BUS CONN 8/A 8+4 SLOTS	1	1	1	1	1	1	1	1	1	1
4	4	E-IA-7016715-0-0	7016715-00	CHASSIS WELDMENT	1	1	1	1	1	1	1	1	1	1
5	5		1209403-01	FAN, 115CFM, SLEEVE BRNG	2	2	2	2	2	2	2	2	2	2
6	6	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
7	7	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
8	8	D-IA-7009755-0-0	7009755-00	HARNESS FAN (H763) PDP8A	1	1	1	1	1	1	1	1	1	1
9	9	D-AD-7016124-0-0	7016124-00	C.B. LINE SET ASSY	1	-	-	-	-	-	-	-	-	-
10	10	D-AD-7016124-0-0	7016124-01	LINESET ASSY	-	1	-	-	-	-	-	-	-	-
11	11	D-AD-7016124-0-0	7016124-02	LINESET ASSY	-	-	-	-	1	-	1	-	-	-
12	12	D-AD-7016124-0-0	7016124-03	LINESET ASSY	-	-	-	-	-	1	-	1	-	-
13	13		1211630-00	CARD GUIDE	10	10	10	10	10	10	10	10	10	10
14	14		1209224-00	LATCH, NORYL PLASTIC	4	4	4	4	4	4	4	4	4	4
15	15	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
16	16	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
17	17	D-IA-7010935-0-0	7010935-01	TRANSFORMER ASSY 50HZ	-	-	-	-	1	-	1	-	1	-
18	18	D-IA-7010935-0-0	7010935-02	TRANSFORMER ASSY 50HZ	-	-	-	-	-	1	-	1	-	1
19	19	C-IA-7010871-0-0	7010871-1F	CABLE KEY BOARD 8A	1	1	1	1	1	-	-	-	-	1
20	20	D-AD-7009978-0-0	7009978-00	BEZEL ASSY (H763) PDP8A	1	1	1	1	1	1	1	1	1	1
21	21	D-AD-7010039-0-0	7010039-04	PANEL LIMITED FUNCTION	1	1	1	1	1	1	-	-	-	1
22	22		9006026-02	SCREW, FLAT, PHIL, 6-32X 3/4	8	8	8	8	8	8	8	8	8	8
23	23	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
24	24		9006035-01	SCREW, PAN, PHIL 8-32X 1/4 SS	10	10	10	10	10	10	10	10	10	10
25	25		9008072-00	WASHER, LOCK, EXTERNAL TOOTH #8	10	10	10	10	10	10	10	10	10	10
26	26	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-	-
27	27		9007649-00	WASHER, LOCK, EXTERNAL TOOTH #6	4	4	4	4	4	4	4	4	4	4
28	28		9007786-01	RETAINER, U-NUT, 10-32	8	8	8	8	8	8	8	8	8	8
29	29		9006075-02	SCREW, FLAT, PHIL, 10-32X 3/4	8	8	8	8	8	8	8	8	8	8
30	30		9006037-01	SCREW, PAN, PHIL 8-32X 3/8 SS	4	4	4	4	4	4	4	4	4	4

REVISION HISTORY		BASIC PART NO: H9300		IDRN:	D. SULLIVAN	DATE: 21-FEB-75	DBP	D	I	G	I	T	A	L	
ENG	ECO NUMBER	REV	SECTION A OF B												
DF	IH9300-MK007	H	SECTION. VARIATION INDEX	CHK'D:	P. GARDNER	DATE: 09-MAY-75	L3-JUL-81	TITLE	PARTS LIST						
DF	IH9300-MK008	J	[A] AA, AB, AC, AD, BA, BB, BC, BD, BE, BF, BH, BJ [B] BK, BL, BM, BN	[C]	P. GARDNER	DATE: 09-MAY-75		H9300 UNIT ASSEMBLY							
				[D]	P. GARDNER	DATE: 09-MAY-75		DOCUMENT NUMBER							
				[E]	Gary J Price	DATE: 22-OCT-80		SIZE!CODE! NUMBER						REV	
				[F]	MFG. ENG.: D. DEHOME	DATE: 09-MAY-75	K	PL	H9300-0-0					J	
					ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER:				FILE NAME:		EDIT #			
					D-UA-H9300-0-0	#B-DD-H9300-0				MK0245.PLS		13			

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MK

PARTS LIST

SHEET A2 OF A2

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION									
				AA	AB	AC	AD	BA	BB	BC	BD	BE	BF
31	31	BLANK											
32	32	BLANK	9008196-00	*** THIS ITEM IS NOT USED *** RECP. CLIP ON F/1/4 TURN FASTNR	2	2	2	2	2	2	2	2	2
33	33	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-
34	34		9009087-00	FOAM, TAPE, SINGLE SIDED 1/8 THK	1	1	1	1	1	1	1	1	1
35	35	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-
36	36	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-
37	37	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-
38	38	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-
39	39	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-
40	40	D-UA-BC80C-0-0	BC80C-04	BC80C CABLE	-	-	-	-	-	-	-	1	1
41	41	D-UA-BC08H-0-0	BC08H-1F	CABLE	-	-	-	-	-	2	2	-	-
42	42	C-IA-7008288-0-0	7008288-3F	CABLE ASSY	-	-	-	-	-	1	1	1	1
43	43	C-IA-7013953-0-0	7013953-01	8E POWER CONTROL ADAPTER CABLE	-	-	-	-	-	-	-	-	-
44	44	D-UA-5413011-0-0	5413011-00	H9300 REMOTE SLAVE CIRCUIT	-	-	-	-	-	-	1	1	1
45	45	D-AD-7009978-0-0	7009978-01	BLANK BEZEL ASSY	-	-	-	-	-	1	1	1	1
46	46	D-AD-7016124-0-0	7016124-04	LINESET ASSY	-	-	-	-	-	-	-	-	1
47	47	D-AD-7016124-0-0	7016124-05	LINESET ASSY	-	-	-	-	-	-	-	-	1
48	48	D-IA-7013954-0-0	7013954-00	MOS TRANSFORMER ASSY	1	1	1	1	-	-	-	-	-
49	49		9008185-00	NUT,KEP 6-32X 1/4 AF	14	14	14	14	14	14	14	14	14
50	50	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-
51	51	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-
52	52	D-IA-7421088-0-0	7421088-00	COVER,FAN HARNESS	1	1	1	1	1	1	1	1	1
53	53	C-MD-7421087-0-0	7421087-00	ENCLOSURE PLATE	1	1	1	1	1	1	1	1	1
54	54		9006025-03	SCREW,TRUS,PHIL, 6-32X 5/8	8	8	8	8	8	8	8	8	8
55	55		1210263-00	GUARD,FINGER 4.125 X 4.125 MTG H	2	2	2	2	2	2	2	2	2
56	56	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-	-	-	-	-	-	-
57	57		3613210-00	/REPLACED BY 36-17674-00	1	1	1	1	1	1	1	1	1
58	58	D-AD-7016124-0-0	7016124-06	LINESET ASSY	-	-	-	1	-	-	-	-	-
59	59	D-AD-7016124-0-0	7016124-07	LINESET ASSY	-	-	1	-	-	-	-	-	-
60	60		9006020-01	SCREW,PAN,PHIL 6-32X 1/4 SS	4	4	4	4	4	4	4	4	4
61	61		9007651-00	WASHER, LOCK, EXTERNAL TOOTH #10	6	6	6	6	6	6	6	6	6
62	62		9006565-00	NUT,KEP 10-32X 3/8 AF	2	2	2	2	2	2	2	2	2
63	63		3612680-01	DECAL, GROUND SIGN PER 3S6 *	1	1	1	1	1	1	1	1	1
64	64		3613211-00	DECAL,CLEAR PREPRINTED CSA 1-1/4	1	1	1	1	1	1	1	1	1
65	65	A-DC-7416197-0-0	7416197-02	DECAL-UL LISTED EDP	1	1	1	1	1	1	1	1	1
66	66	C-IA-7013952-0-0	7013952-00	REMOTE INTERLOCK JUMPER ASSY	-	-	-	-	-	1	1	1	-

TITLE	SECTION A OF B	SIZE	CODE	DOCUMENT NUMBER	REV
D I G I T A L	H9300 UNIT ASSEMBLY	K	PL	H9300-0-0	J

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION			
				BK	BL	BM	BN
1	1	D-CS-G8016-0-1	G8016-00	*** THIS ITEM IS NOT USED ***	-	-	-
2	2	D-CS-G8018-0-1	G8018-00	H774 REGULATOR	1	1	1
3	3	D-AD-H9194-0-0	H9194-00	BUS CONN S/A 8+4 SLOTS	1	1	1
4	4	E-IA-7016715-0-0	7016715-00	CHASSIS WELDMENT	1	1	1
5	5		1209403-01	FAN, 115CFM, SLEEVE BRNG	2	2	2
6	6	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-
7	7	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-
8	8	D-IA-7009755-0-0	7009755-00	HARNESS FAN (H763) PDP8A	1	1	1
9	9	D-AD-7016124-0-0	7016124-00	*** THIS ITEM IS NOT USED ***	-	-	-
10	10	D-AD-7016124-0-0	7016124-01	*** THIS ITEM IS NOT USED ***	-	-	-
11	11	D-AD-7016124-0-0	7016124-02	*** THIS ITEM IS NOT USED ***	-	-	-
12	12	D-AD-7016124-0-0	7016124-03	*** THIS ITEM IS NOT USED ***	-	-	-
13	13		1211630-00	CARD GUIDE	10	10	10
14	14		1209224-00	LATCH, NORYL PLASTIC	4	4	4
15	15	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-
16	16	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-
17	17	D-IA-7010935-0-0	7010935-01	TRANSFORMER ASSY 60HZ	1	-	1
18	18	D-IA-7010935-0-0	7010935-02	TRANSFORMER ASSY 50HZ	-	1	-
19	19	C-IA-7010871-0-0	7010871-1F	*** THIS ITEM IS NOT USED ***	-	-	-
20	20	D-AD-7009978-0-0	7009978-00	BEZEL ASSY (H763)PDP8A	1	1	1
21	21	D-AD-7010039-0-0	7010039-04	*** THIS ITEM IS NOT USED ***	-	-	-
22	22		9006026-02	SCREW, FLAT, PHIL, 6-32X 3/4	8	8	8
23	23	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-
24	24		9006035-01	SCREW, PAN, PHIL 8-32X 1/4 SS	10	10	10
25	25		9008072-00	WASHER, LOCK, EXTERNAL TOOTH #8	10	10	10
26	26	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-
27	27		9007649-00	WASHER, LOCK, EXTERNAL TOOTH #6	4	4	4
28	28		9007786-01	RETAINER, U-NUT, 10-32	8	8	8
29	29		9006075-02	SCREW, FLAT, PHIL, 10-32X 3/4	8	8	8
30	30		9006037-01	SCREW, PAN, PHIL 8-32X 3/8 SS	4	4	4

REVISION HISTORY	BASIC PART NO:	H9300	IDRN:	D. SULLIVAN	DATE:	21-FEB-75	DBP	D I G I T A L
ENG! ECO NUMBER	REV	SECTION B OF B		A. Karrick	23-JUL-81	TITLE	PARTS LIST	
DF H9300-MK007	H	SECTION. VARIATION INDEX	CHR'D:	P. GARDNER	DATE:	09-MAY-75		H9300 UNIT ASSEMBLY
DF H9300-MK008	J	[A] AA,AB,AC,AD,BA,BB, BC,BD,BE,BF,BH,BJ [B] BK,BL,BM,BN	DES.ENG.:	P. GARDNER	DATE:	09-MAY-75		
				D. PRENIERE	DATE:	22-OCT-80		DOCUMENT NUMBER
								SIZE CODE NUMBER REV
				MFG.ENG.:	D. DEHOME	DATE: 09-MAY-75	K PL H9300-0-0	J
				ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:	FILE NAME:	EDIT #
				ID-UA-H9300-0-0	#B-DD-H9300-0		MK0245.PLS	13
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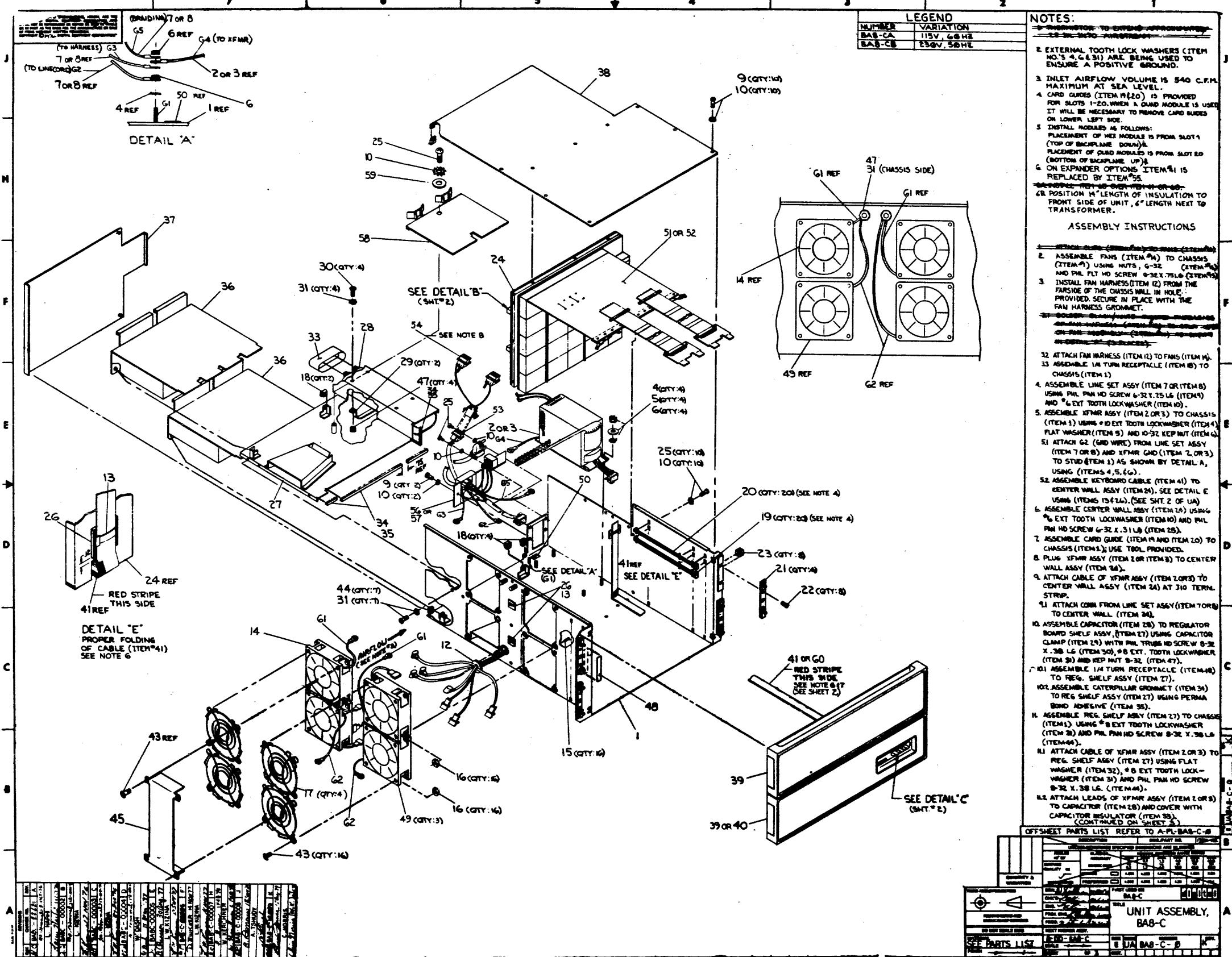
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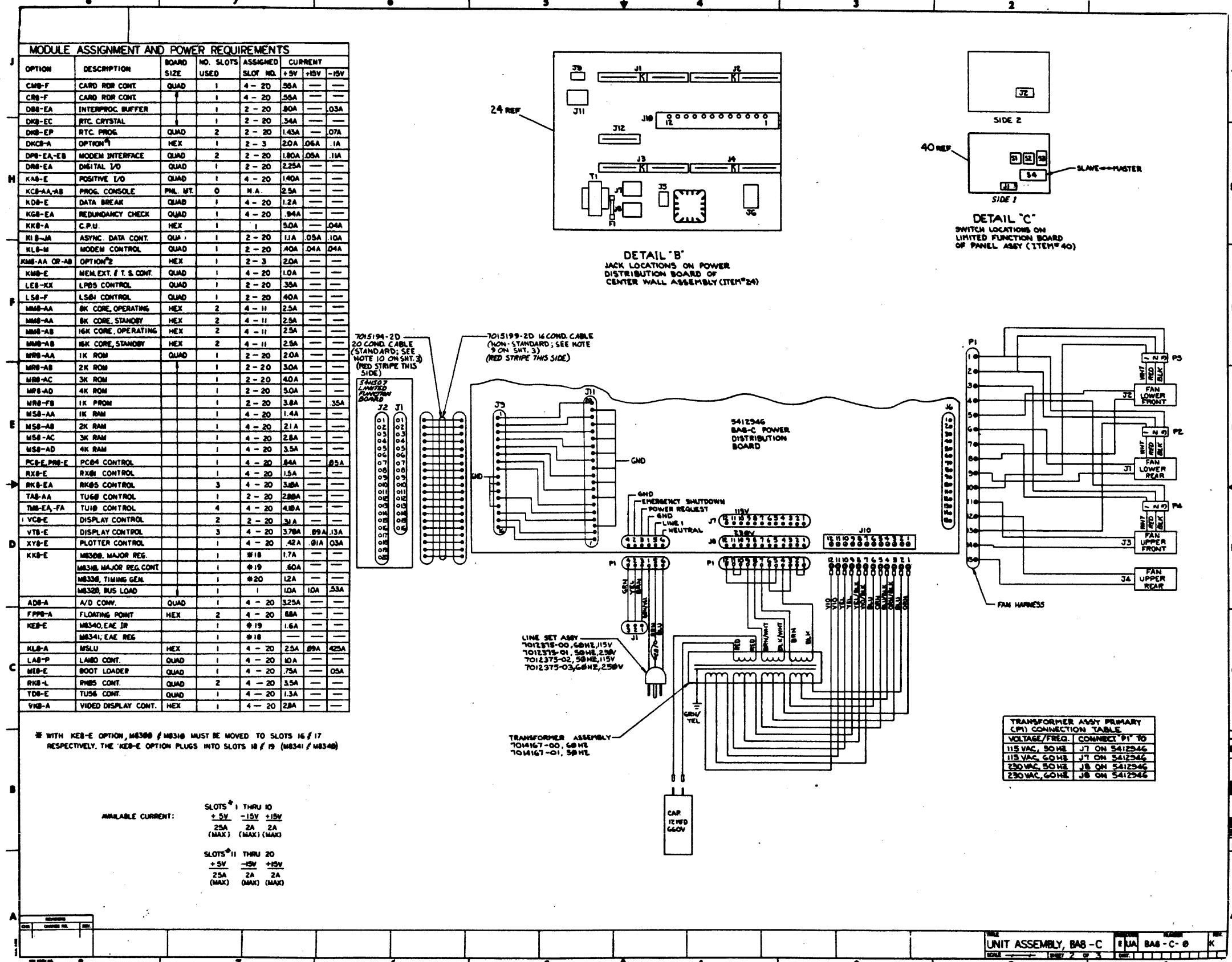
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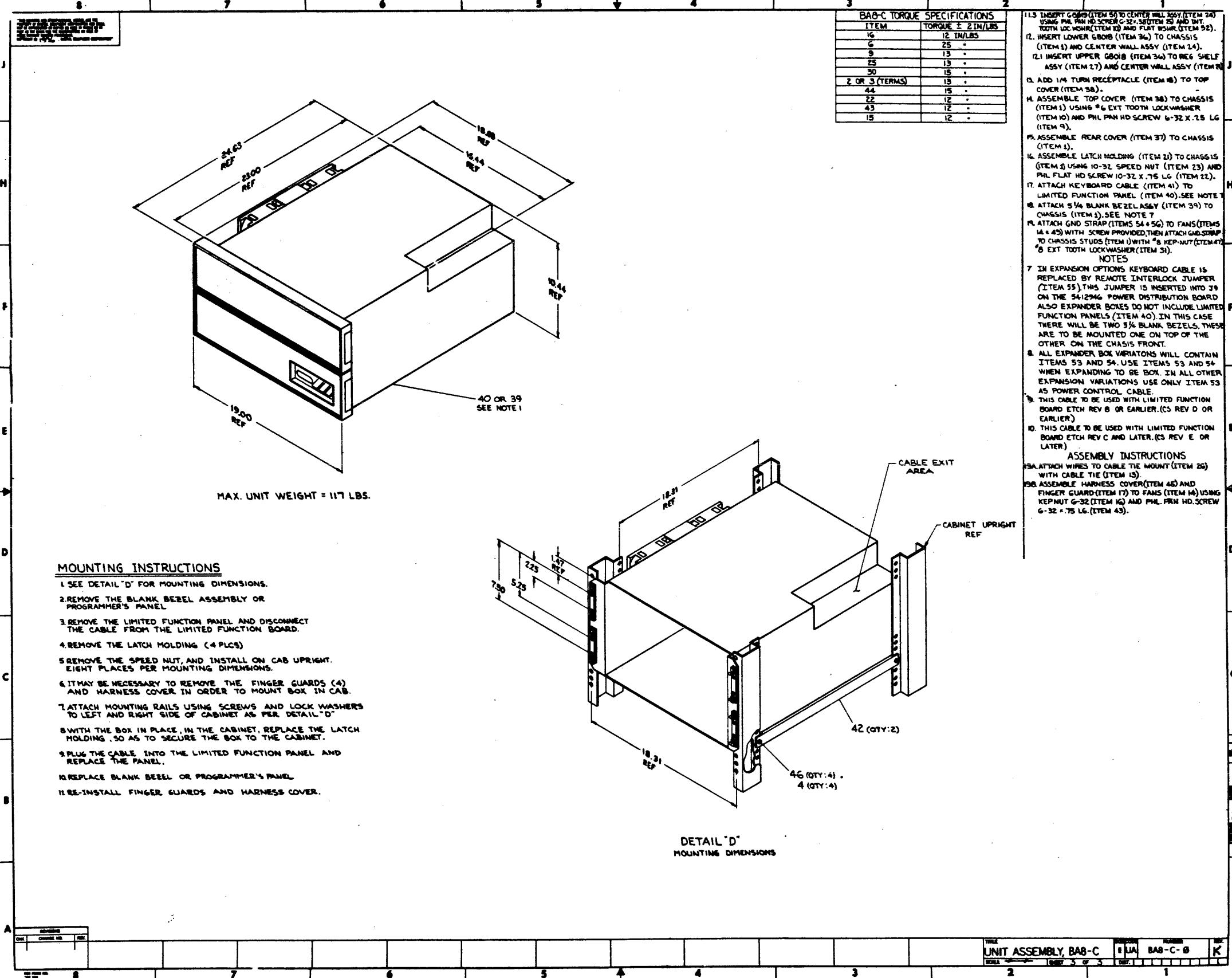
SHEET B2 OF B2

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION			
				BK	BL	BM	BN
31	31	BLANK					
32	32		9008196-00	*** THIS ITEM IS NOT USED ***	-	-	-
33	33	BLANK		RECP. CLIP ON F/1/4 TURN FASTNR	2	2	2
34	34		9009087-00	*** THIS ITEM IS NOT USED ***	-	-	-
35	35	BLANK		FOAM, TAPE, SINGLE SIDED 1/8 THK	1	1	1
36	36	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-
37	37	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-
38	38	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-
39	39	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-
40	40	D-UA-BC80C-0-0	BC80C-04	BC80C CABLE	-	-	1
41	41	D-UA-BC08H-0-0	RC08H-1F	CABLE	2	2	-
42	42	C-IA-7008288-0-0	7008288-3F	CABLE ASSY	1	1	1
43	43	C-IA-7013953-0-0	7013953-01	8E POWER CONTROL ADAPTER CABLE	-	-	1
44	44	D-UA-5413011-0-0	5413011-00	H9300 REMOTE SLAVE CIRCUIT	1	1	1
45	45	D-AD-7009978-0-0	7009978-01	BLANK BEZEL ASSY	1	1	1
46	46	D-AD-7016124-0-0	7016124-04	LINESET ASSY	-	1	-
47	47	D-AD-7016124-0-0	7016124-05	LINESET ASSY	1	-	1
48	48	D-IA-7013954-0-0	7013954-00	*** THIS ITEM IS NOT USED ***	-	-	-
49	49		9008185-00	NUT,KEP 6-32X 1/4 AF	14	14	14
50	50	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-
51	51	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-
52	52	D-IA-7421088-0-0	7421088-00	COVER,FAN HARNESS	1	1	1
53	53	C-MD-7421087-0-0	7421087-00	ENCLOSURE PLATE	1	1	1
54	54		9006025-03	SCREW,TRUS,PHIL, 6-32X 5/8	8	8	8
55	55		1210263-00	GUARD,FINGER 4.125 X 4.125 MTG H	2	2	2
56	56	BLANK		*** THIS ITEM IS NOT USED ***	-	-	-
57	57		3613210-00	/REPLACED BY 36-17674-00	1	1	1
58	58	D-AD-7016124-0-0	7016124-06	*** THIS ITEM IS NOT USED ***	-	-	-
59	59	D-AD-7016124-0-0	7016124-07	*** THIS ITEM IS NOT USED ***	-	-	-
60	60		9006020-01	SCREW,PAN,PHIL 6-32X 1/4 SS	4	4	4
61	61		9007651-00	WASHER, LOCK, EXTERNAL TOOTH #10	6	6	6
62	62		9006565-00	NUT,KEP 10-32X 3/8 AF	2	2	2
63	63		3612680-01	DECAL, GROUND SIGN PER 356 *	1	1	1
64	64		3613211-00	DECAL,CLEAR PREPRINTED CSA 1-1/4	1	1	1
65	65	A-DC-7416197-0-0	7416197-02	DECAL-UL LISTED EDP	1	1	1
66	66	C-IA-7013952-0-0	7013952-00	REMOTE INTERLOCK JUMPER ASSY	1	1	1

! ! ! ! ! ! ! !	TITLE	! ! ! ! ! ! ! !	SIZE ! CODE ! DOCUMENT NUMBER ! REV !
D I G I T A L	H9300 UNIT ASSEMBLY	SECTION B OF B	K PL H9300-0-0 J







DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS
PARTS LIST

MADE BY D. SULLIVAN
DATE 22 MAR 76
ENG *Larry Martin*
DATE 26 JULY 76

CHECKED *J. H. Eng* SECTION 1
PROD *Standard* ISSUED SECT. 1

DATE 26 JULY 76

ITEM NO. DWG NO./PART NO. DESCRIPTION

23	9007786-01	NUT, SPEED #10-32
24	7014245-0-0	WALL ASSY, CENTER
25	9006021-01	SCR, PHL PAN HD #6-32 X .3 LG.
26	9008264	MOUNT, CABLE TIE
27	D-AD-7012541-0-0	SHELF ASSY, REG. BOARD
28	1011729-01	CAPACITOR, 12 MFD, 660 V
29	1213156	CLAMP, CAPACITOR
30	9006037-03	SCR, PHL TRUSS HD, #8-32 X .3 LG.
31	9008072	WASHER, #8 EXT TOOTH LOCK
32	9006030	WASHER, FLAT, .625 X .200 I.D. X .032 THK
33	1213683	TERMINAL, BOOT CAP
34	9007036	GROMMET, CATERPILLAR
35	9009157	ADHESIVE, PERMA BOND #102
36	D-CS-G8018-0-1	REGULATOR BOARD ASSEMBLY (CORE)
37	D-IA-7415703-0-0	COVER, REAR
38	D-AD-7415706-0-0	COVER, TOP
39	D-AD-7012452-0-0	BEZEL ASSY, 5.25 BLANK
40	D-AD-7010039-03	PANEL, LIMITED FUNCTION
41	C-IA-7015194-2D	20 COND. PANEL CABLE
42	C-MD-7415702-0-0	RAIL, CHASSIS MOUNTING
43	9006026-03	SCR, PHL TRUSS HD #6-32 X .75 LG.
44	9006037-01	SCR, PHL PAN HD #8-32 X .38 LG.

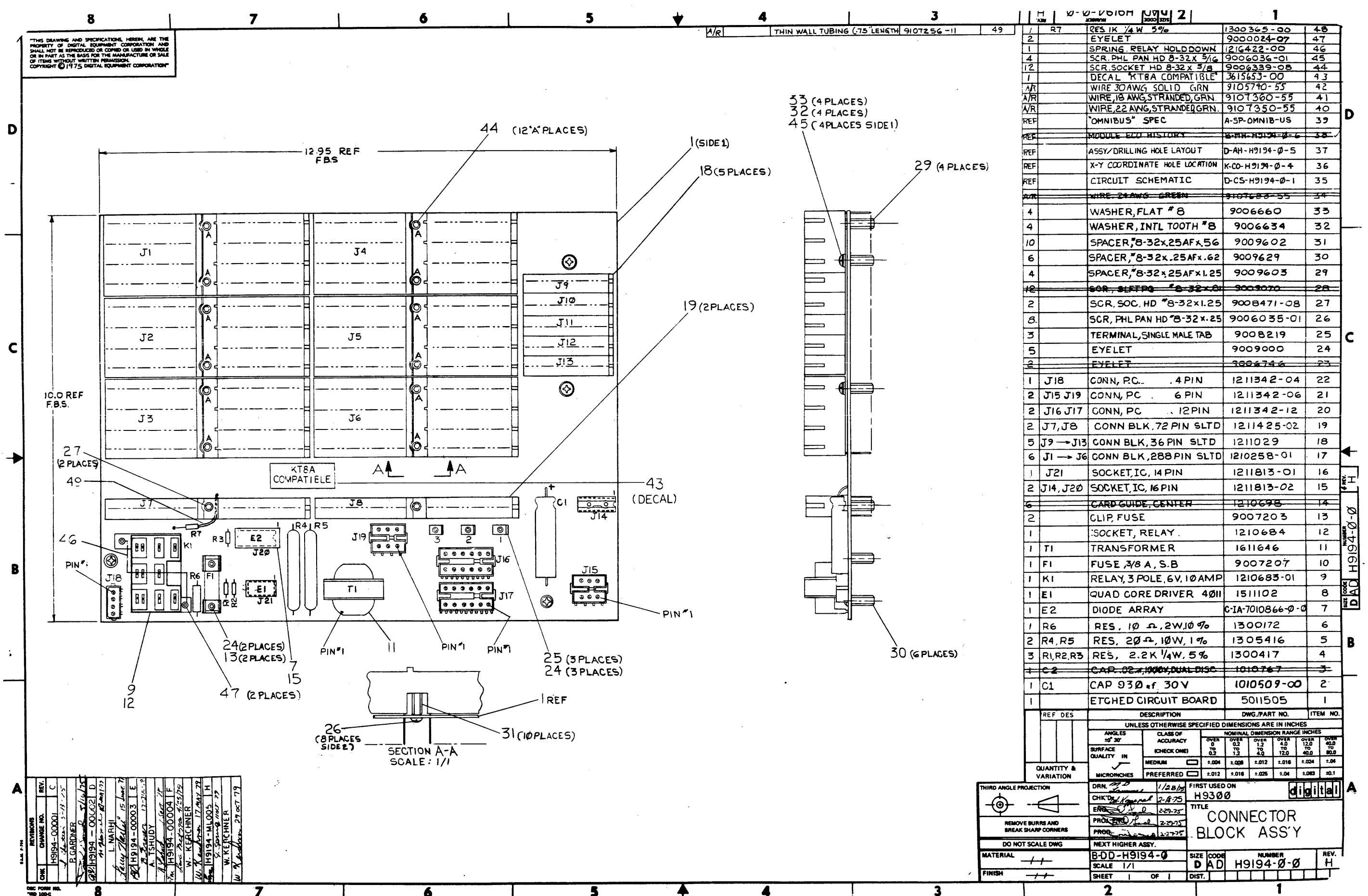
TITLE BA8-C ASSY NO. B-DD-BA8-C SIZE CODE A PL NUMBER BAB-C-0 REV. K ECO NO.

UNIT ASSEMBLY

SHEET 2 OF 3 DIST.

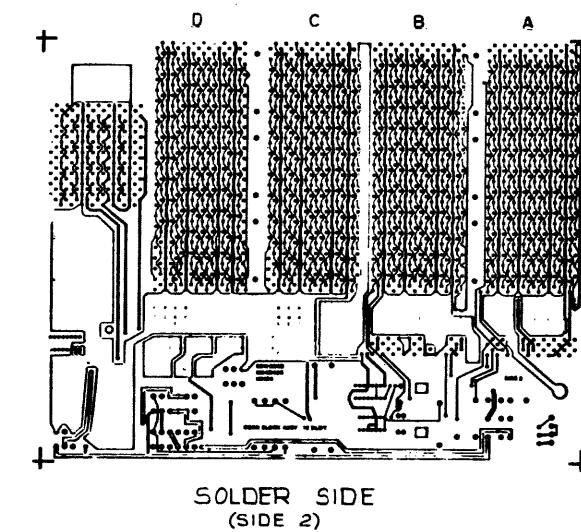
DEC FORM DEC 16-(325)-1031-N870
DRA 110

QUANTITY / VARIATION											
BAB-C-A	(115V-60HZ)	(230V-50HZ)	BAB-C-B	(115V-60HZ)	(230V-50HZ)	BAB-C-C	(115V-60HZ)	(230V-50HZ)	BAB-C-D	(115V-60HZ)	(230V-50HZ)
8	8	8	8	8	8	8	8	8	8	8	8
BAB-C-E	(115V-60HZ)	(230V-50HZ)	BAB-C-F	(115V-60HZ)	(230V-50HZ)	BAB-C-G	(115V-60HZ)	(230V-50HZ)	BAB-C-H	(115V-60HZ)	(230V-50HZ)
10	12	10	12	10	12	12	10	12	10	12	10
BAB-C-I	(115V-60HZ)	(230V-50HZ)	BAB-C-J	(115V-60HZ)	(230V-50HZ)	BAB-C-K	(115V-60HZ)	(230V-50HZ)	BAB-C-L	(115V-60HZ)	(230V-50HZ)
3	3	3	3	3	3	3	3	3	3	3	3
BAB-C-M	(115V-60HZ)	(230V-50HZ)	BAB-C-N	(115V-60HZ)	(230V-50HZ)	BAB-C-O	(115V-60HZ)	(230V-50HZ)	BAB-C-P	(115V-60HZ)	(230V-50HZ)
13	13	13	13	13	13	13	13	13	13	13	13
BAB-C-Q	(115V-60HZ)	(230V-50HZ)	BAB-C-R	(115V-60HZ)	(230V-50HZ)	BAB-C-S	(115V-60HZ)	(230V-50HZ)	BAB-C-T	(115V-60HZ)	(230V-50HZ)
13	13	13	13	13	13	13	13	13	13	13	13
BAB-C-U	(115V-60HZ)	(230V-50HZ)	BAB-C-V	(115V-60HZ)	(230V-50HZ)	BAB-C-W	(115V-60HZ)	(230V-50HZ)	BAB-C-X	(115V-60HZ)	(230V-50HZ)
13	13	13	13	13	13	13	13	13	13	13	13
BAB-C-Z	(115V-60HZ)	(230V-50HZ)	BAB-C-A	(115V-60HZ)	(230V-50HZ)	BAB-C-B	(115V-60HZ)	(230V-50HZ)	BAB-C-C	(115V-60HZ)	(230V-50HZ)
13	13	13	13	13	13	13	13	13	13	13	13
BAB-C-E	(115V-60HZ)	(230V-50HZ)	BAB-C-F	(115V-60HZ)	(230V-50HZ)	BAB-C-G	(115V-60HZ)	(230V-50HZ)	BAB-C-H	(115V-60HZ)	(230V-50HZ)
13	13	13	13	13	13	13	13	13	13	13	13
BAB-C-K	(115V-60HZ)	(230V-50HZ)	BAB-C-L	(115V-60HZ)	(230V-50HZ)	BAB-C-M	(115V-60HZ)	(230V-50HZ)	BAB-C-N	(115V-60HZ)	(230V-50HZ)
13	13	13	13	13	13	13	13	13	13	13	13
BAB-C-Q	(115V-60HZ)	(230V-50HZ)	BAB-C-R	(115V-60HZ)	(230V-50HZ)	BAB-C-S	(115V-60HZ)	(230V-50HZ)	BAB-C-T	(115V-60HZ)	(230V-50HZ)
13	13	13	13	13	13	13	13	13	13	13	13
BAB-C-U	(115V-60HZ)	(230V-50HZ)	BAB-C-V	(115V-60HZ)	(230V-50HZ)	BAB-C-W	(115V-60HZ)	(230V-50HZ)	BAB-C-X	(115V-60HZ)	(230V-50HZ)
13	13	13	13	13	13	13	13	13	13	13	13
BAB-C-Z	(115V-60HZ)	(230V-50HZ)	BAB-C-A	(115V-60HZ)	(230V-50HZ)	BAB-C-B	(115V-60HZ)	(230V-50HZ)	BAB-C-C	(115V-60HZ)	(230V-50HZ)
13	13	13	13	13	13	13	13	13	13	13	13
BAB-C-E	(115V-60HZ)	(230V-50HZ)	BAB-C-F	(115V-60HZ)	(230V-50HZ)	BAB-C-G	(115V-60HZ)	(230V-50HZ)	BAB-C-H	(115V-60HZ)	(230V-50HZ)
13	13	13	13	13	13	13	13	13	13	13	13
BAB-C-K	(115V-60HZ)	(230V-50HZ)	BAB-C-L	(115V-60HZ)	(230V-50HZ)	BAB-C-M	(115V-60HZ)	(230V-50HZ)	BAB-C-N	(115V-60HZ)	(230V-50HZ)
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BAB-C-Q	(115V-60HZ)	(230V-50HZ)	BAB-C-R	(115V-60HZ)	(230V-50HZ)	BAB-C-S	(115V-60HZ)	(230V-50HZ)	BAB-C-T	(115V-60HZ)	(230V-50HZ)
13	13	13	13	13	13	13	13	13	13	13	13
BAB-C-U	(115V-60HZ)	(230V-50HZ)	BAB-C-V	(115V-60HZ)	(230V-50HZ)	BAB-C-W	(115V-60HZ)	(230V-50HZ)	BAB-C-X	(115V-60HZ)	(230V-50HZ)
13	13	13	13	13	13	13	13	13	13	13	13
BAB-C-Z	(115V-60HZ)	(230V-50HZ)	BAB-C-A	(115V-60HZ)	(230V-50HZ)	BAB-C-B	(115V-60HZ)	(230V-50HZ)	BAB-C-C	(115V-60HZ)	(230V-50HZ)
13	13	13	13	13	13	13	13	13	13	13	13
BAB-C-E	(115V-60HZ)	(230V-50HZ)	BAB-C-F	(115V-60HZ)	(230V-50HZ)	BAB-C-G	(115V-60HZ)	(230V-50HZ)	BAB-C-H	(115V-60HZ)	(230V-50HZ)
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BAB-C-K	(115V-60HZ)	(230V-50HZ)	BAB-C-L	(115V-60HZ)	(230V-50HZ)	BAB-C-M	(115V-60HZ)	(230V-50HZ)	BAB-C-N	(115V-60HZ)	(230V-50HZ)
13	13	13	13	13	13	13	13	13	13	13	13
BAB-C-Q	(115V-60HZ)	(230V-50HZ)	BAB-C-R	(115V-60HZ)	(230V-50HZ)	BAB-C-S	(115V-60HZ)	(230V-50HZ)	BAB-C-T	(115V-60HZ)	(230V-50HZ)
13	13	13	13	13	13	13	13	13	13	13	13
BAB-C-U	(115V-60HZ)	(230V-50HZ)	BAB-C-V	(115V-60HZ)	(230V-50HZ)	BAB-C-W	(115V-60HZ)	(230V-50HZ)	BAB-C-X	(115V-60HZ)	(230V-50HZ)
13	13	13	13	13	13	13	13	13	13	13	13
BAB-C-Z	(115V-60HZ)	(230V-50HZ)	BAB-C-A	(115V-60HZ)	(230V-50HZ)	BAB-C-B	(115V-60HZ)	(230V-50HZ)	BAB-C-C	(115V-60HZ)	(230V-50HZ)
13	13	13	13	13	13	13	13	13	13	13	13
BAB-C-E	(115V-60HZ)	(230V-50HZ)	BAB-C-F	(115V-60HZ)	(230V-50HZ)	BAB-C-G	(115V-60HZ)	(230V-50HZ)	BAB-C-H	(115V-60HZ)	(230V-50HZ)
13	13	13	13	13	13	13	13	13	13	13	13
BAB-C-K	(115V-60HZ)	(230V-50HZ)	BAB-C-L	(115V-60HZ)	(230V-50HZ)	BAB-C-M	(115V-60HZ)	(230V-50HZ)	BAB-C-N	(115V-60HZ)	(230V-50HZ)
13	13	13	13	13	13	13	13	13	13	13	13
BAB-C-Q	(115V-60HZ)	(230V-50HZ)	BAB-C-R	(115V-60HZ)	(230V-50HZ)	BAB-C-S	(115V-60HZ)	(230V-50HZ)	BAB-C-T	(115V-60HZ)	(230V-50HZ)
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BAB-C-U	(115V-60HZ)	(230V-50HZ)	B								

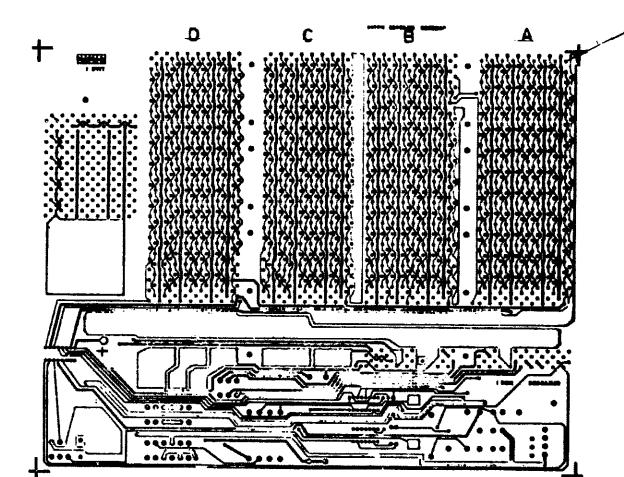


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NOTES



SOLDER SID
(SIDE 2)

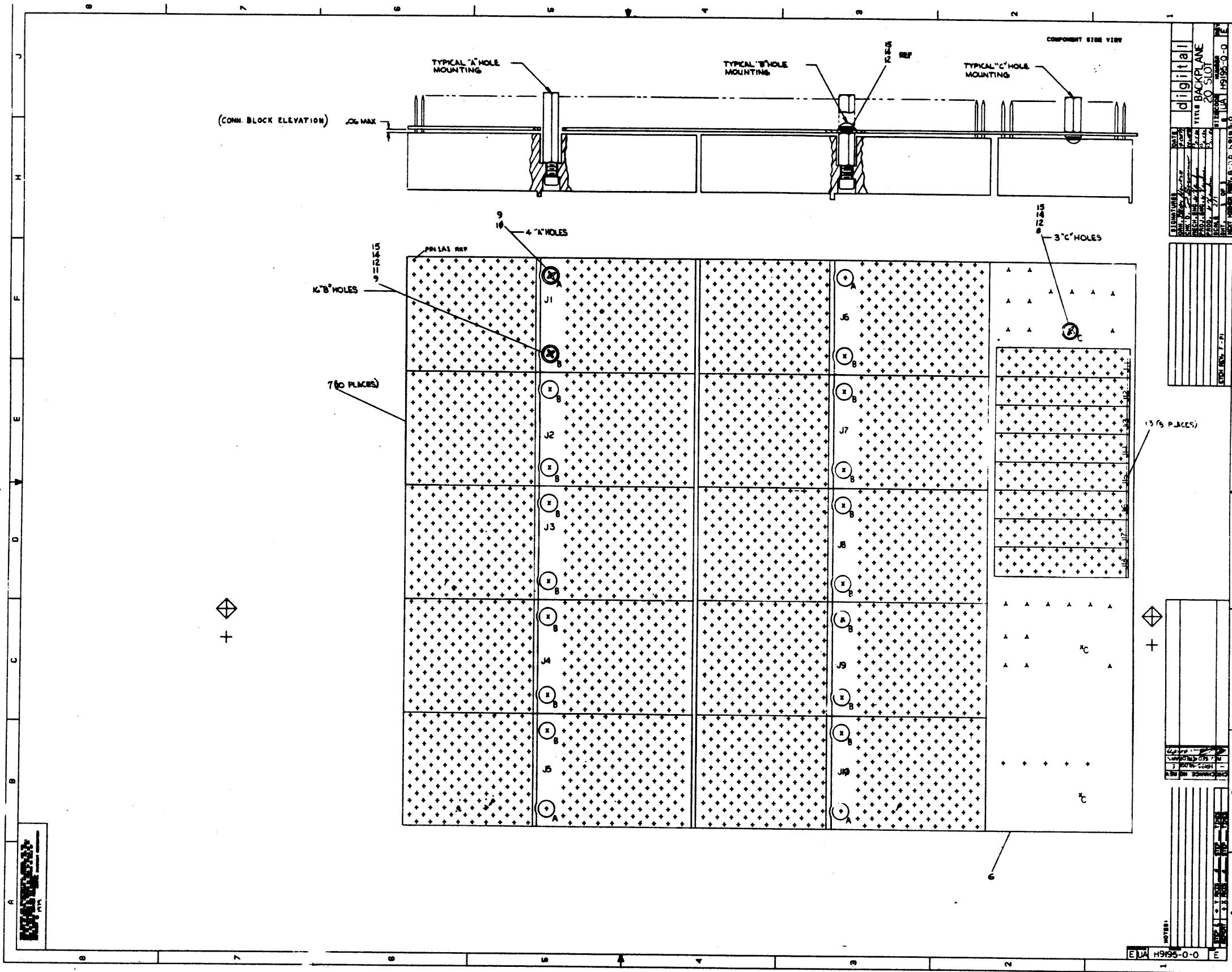


BLOCK SIDE
(SIDE 1)
AS SEEN THRU SIDE

5V ARE USUALLY PIN 7 AND 14
Y EXCEPTIONS ARE STATED ABOVE

IC PIN LOCATIONS

REF	X-Y- COORDINATE HOLE LOCATION	K-CO-H9194-0-4	1
REF	ASSY/DRILLING HOLE LAYOUT	E-AH-H9194-0-5	2
REF	MODULE ECO HISTORY	D-MH-H9194-0-6	3
REF	ETCHED CIRCUIT BOARD	SO11505	4
REF	CONNECTOR BLOCK ASSY	D-AD-H9194-0-0	5



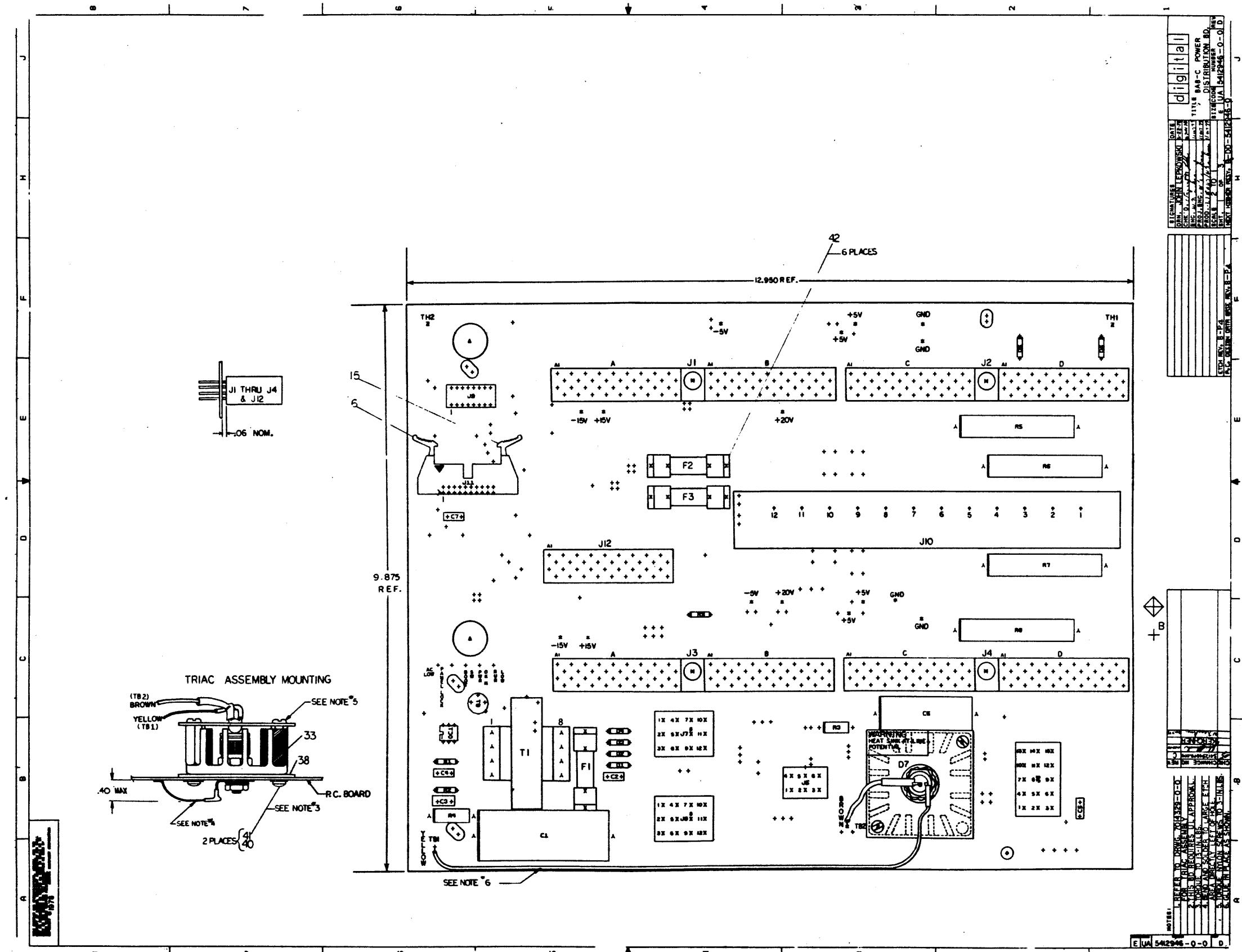
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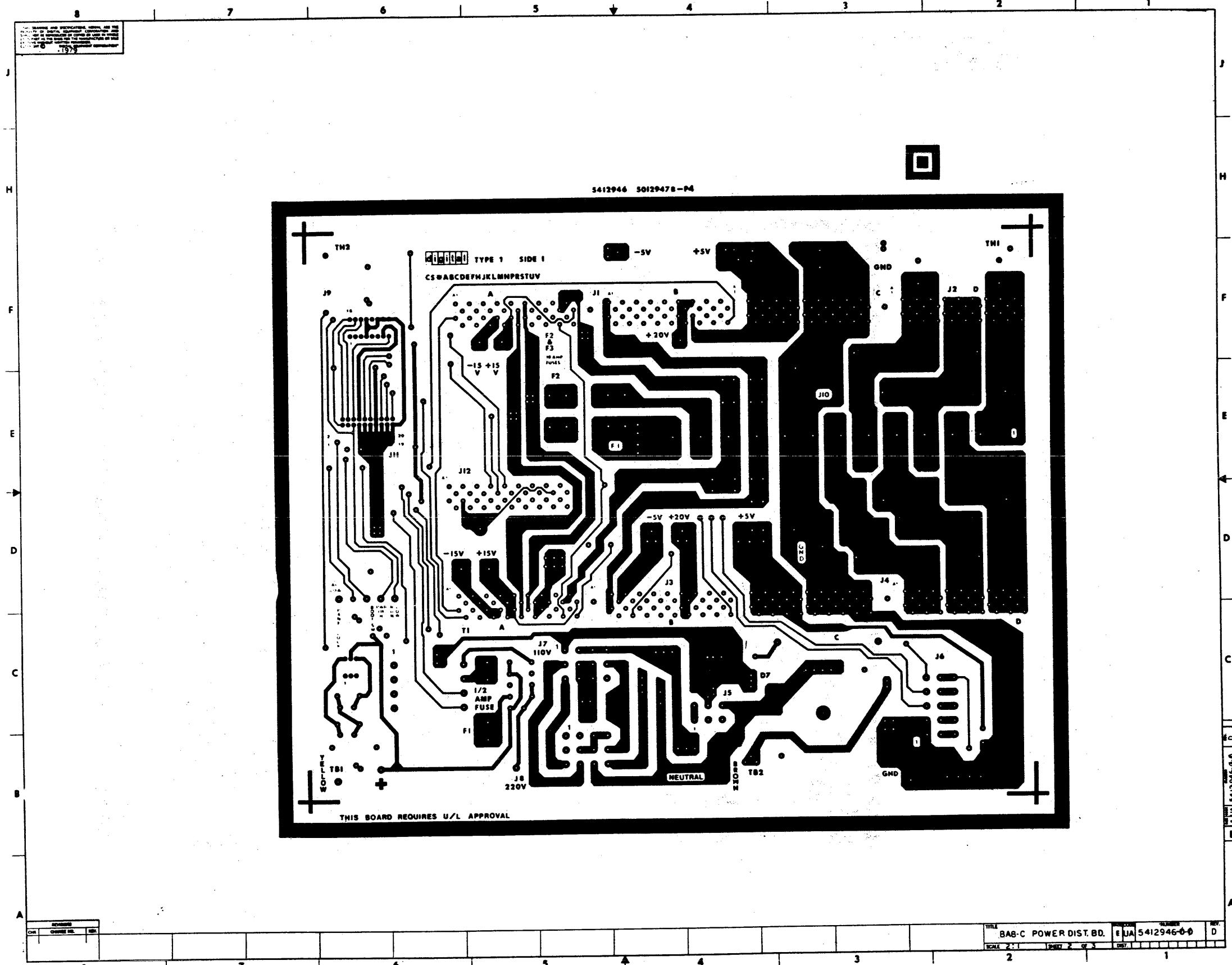
PIN	A1	A2	B1	B2	C1	C2	D1	D2	E1	E2
A	A01=+5V ALL OTHERS = TF	+5V	B02 & B03= BATTERY EMPTY, ALL OTHERS = TP	+5V	C01=+5V, ALL OTHERS = TP	+5V	D02 & D03= PANEL LOCK, ALL OTHERS = TP	+15V	TEST POINT	+20V
B	TEST POINT	-15V	B02 & B03= AC LOW, ALL OTHERS = TP	-15V	TEST POINT	-15V	TEST POINT	-15V	TEST POINT	BANK SEL Ø
C	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND
D	MAØ L	EMAØ L	MA4 L	INT STROBE H	I/O PAUSE L	TP 1 H	MA8 L	IRØ L	TEST POINT	BANK SEL 1
E	MA1 L	EMAIL	MA5 L	BREAK IN PROG L	CØ L	TP 2 H	MA9 L	IR1 L	TEST POINT	+20V
F	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND
H	MA2 L	EMA2 L	MAG L	MA, MS, LOAD CONT L	C1 L	TP 3 H	MA1Ø L	IR2 L	TEST POINT	MEMORY REFRESH
J	MA3 L	MEM START L	MA7 L	OVERFLOW L	C2 L	TP 4 H	MA11 L	FL	TEST POINT	MEMORY REFRESH
K	MDØ L	MD DIR L	MD4 L	BREAK DATA CONT L	BUS STROBE L	TS 1 L	MD 8 L	D L	TEST POINT	+20V
L	MD1 L	SOURCE H	MD5 L	BREAK CYCLE L	INTERNAL I/O L	TS 2 L	MD 9 L	E L	TEST POINT	BANK SEL 2
M	MD2 L	STROBE H	MD6 L	LOAD ADD ENABLE L	NOT LAST XFER L	TS 3 L	MD1Ø L	USER MODE L	TEST POINT	-5V
N	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND
P	MD3 L	INITIATE H	MD7 L	INT IN PROG H	INT REQUEST L	TS 4 L	MD11 L	F SET L	TEST POINT	+20V
R	DATA 7 L	RETURN H	DATA 4 L	NTS STALL L	INITIALIZE H	LINK DATA L	DATA 8 L	PULSE LA H	TEST POINT	BANK SEL 3
S	DATA 1 L	WRITE H	DATA 5 L	RES 2	SKIP L	LINK LOAD L	DATA 9 L	STOP L	UNUSED	UNUSED
T	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	GROUND	JUMPER	GROUND
U	DATA 2 L	ROM ADDRESS L	DATA 6 L	RUN L	CPMA DISABLE L	IND 1 L	DATA 10 L	KEY CONTROL L		UNUSED
V	DATA 3 L	LINK L	DATA 7 L	POWER OK H	MS, IR DISABLE L	IND 2 L	DATA 11 L	SW	UNUSED	UNUSED

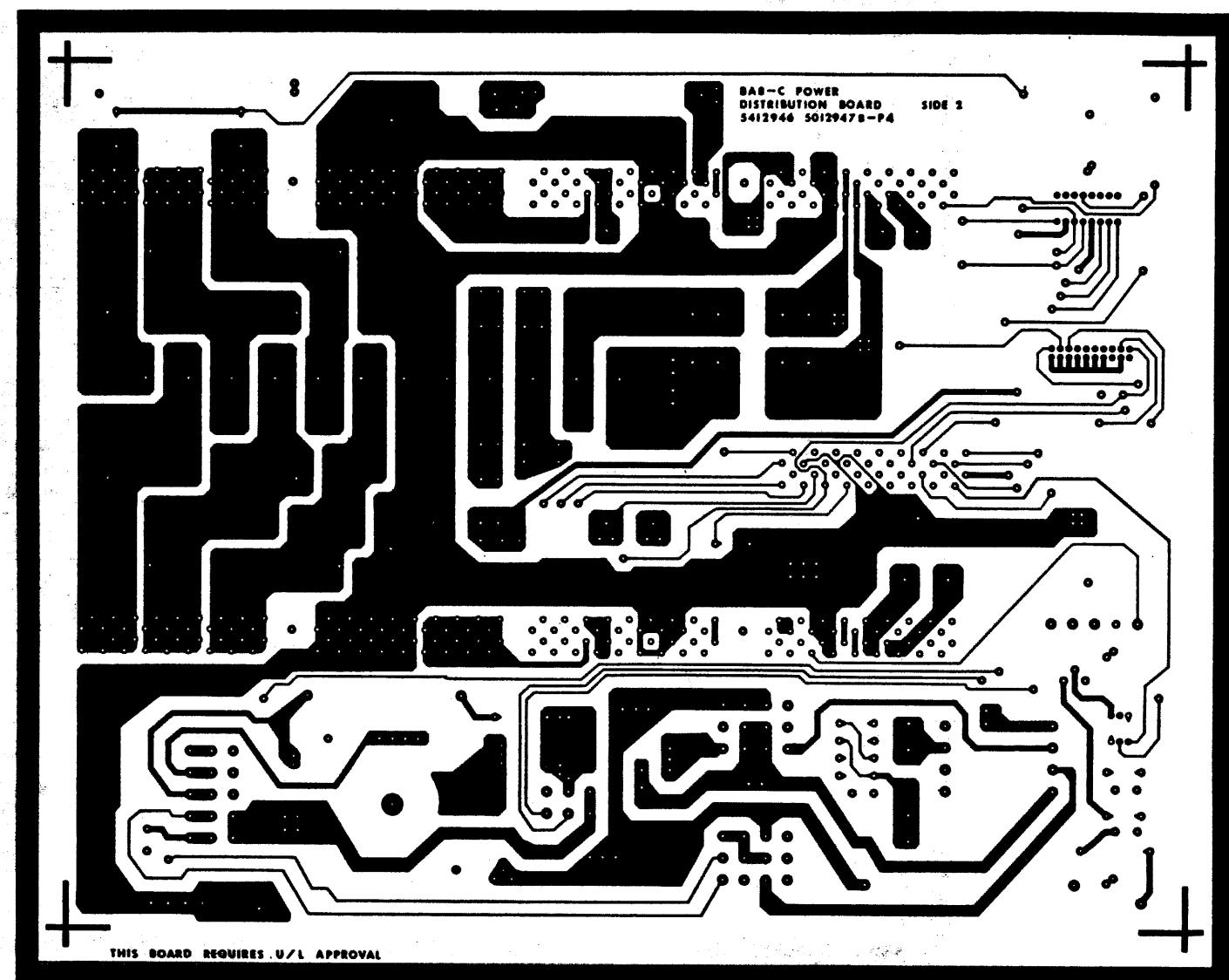
REV.	1
DRN	2-1676
CHG	1
CHG NO.	21-H9195-0-0
DATE	11/10/76
ENG	Augie Tellez
PROJ. ENG.	John J. Hart
PROD.	16
NEXT HIGHER ASSY.	
D-1A-H9195-0-0	
SCALE	1:1
SHEET	1 OF 1
SIZE	D C/S
CODE	H9195-0-1
NUMBER	
REV.	E

W. KERCHNER
11/10/76

DRN	2-1676	FIRST USED ON	BA8-C	digital
CHKD	11/10/76	TITLE		
ENG	Augie Tellez	20 SLOT BACK PLANE		
PROJ. ENG.	John J. Hart			
PROD.	16			
NEXT HIGHER ASSY.				
D-1A-H9195-0-0				
SCALE	1:1	SHEET	1 OF 1	SIZE
CODE	D C/S	NUMBER	H9195-0-1	REV.
NUMBER		REV.	E	





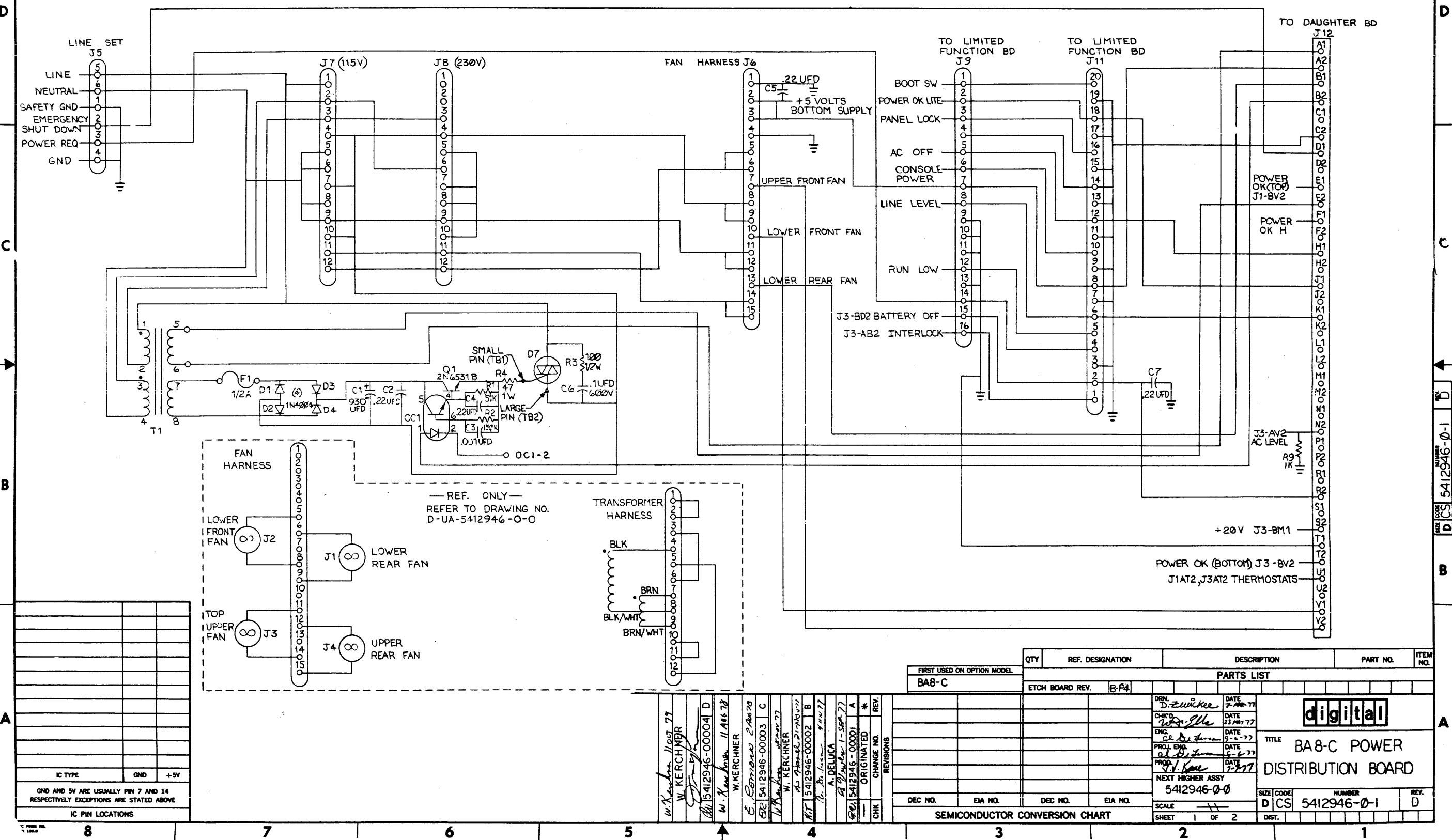


REVISION	CHARGE NO.	REV.
1		

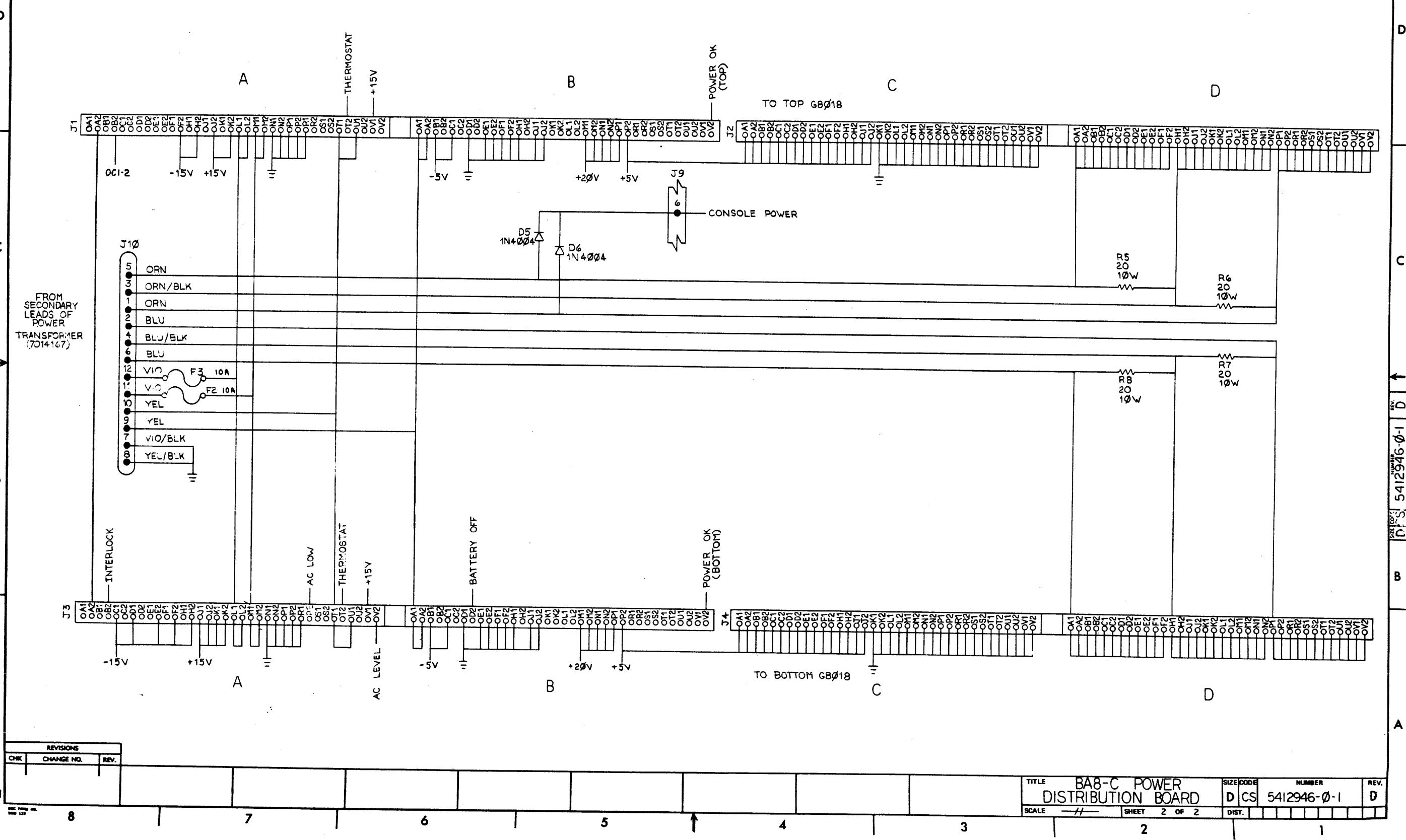
TITLE: BAB-C POWER DIST. BD. DRAWING NUMBER: 5412946-00 D
SCALE: 2:1 SHEET 3 OF 3 DATE: 10/1/94 BY:

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AUTOMATED BY PRTLST.2D(16)

PARTS LIST

SHEET A1 OF A2

LINE ITEM DOCUMENT NUMBER

PART NUMBER

DESCRIPTION

QTY PER VARIATION
00 RE

REFERENCE DESIGNATOR

1	E-MD-5012947-0-0	5012947-00	BABC POWER DISTRIBUTION BOARD	1	
2		1000043-00	1000.0 MMF 250V 20X Y5F DISC	1	C3
3		1000033-00	.1 MFD 600V 10X MYLR	1	C6
4		1010509-00	.930 MFD 30V +75-10% AL EL	1	C1
5		1105796-00	IN 4004 PIV=400 I= 1A D041 SP	6	D1-D6
6		1209941-04	HEADER RT ANGLE, RIGHT	1	
7		1211425-00	CONN,CARD 72PIN SLOTTED DOUBLE	4	J1-J4
8		1211029-00	CONN,CARD 36PIN SLOTTED	1	J12
9		1211813-02	SKT,IC 16PIN DIP GOLD PLATE	1	J9
10		1209941-06	HEADER.100 20POS RT ANGLE	1	J11
11		1211905-01	TERM BLOCK 12POS 7/16 SPACING	1	J10
12		1212297-09	MATE-N-LOK 15PIN UNIV HEADER	1	J6
13		1212297-05	MATE-N-LOK 6PIN UNIV HEADER	1	JS
14		1212297-08	MATE-N-LOK 12PIN UNIV HEADER	2	J7,JB
15		1209941-03	HEADER RT ANGLE LEFT L	1	
16		1302199-00	47.0 1.0 W 5.0 % CC	1	R4
17		1300228-00	100.0 .50 W 5.0 % CC	1	R3
18		1304839-00	51.0 K .25 W 5.0 % CC	1	R1
19		1305416-00	20.0 10.0 W 1.0 % WW	4	R5-R8
20		1509338-00	DEC6531B NPN 310MW SI 40 90 P	1	Q1
21		1914194-00	OPTP-COUPLED ISOLATOR	1	OC1
22		9006707-00	*** THIS ITEM IS NOT USED ***	-	
23		1613282-00	XFMR P=AB S=14.50 12A	1	T1
24		9006023-01	*** THIS ITEM IS NOT USED ***	-	
25		9008185-00	*** THIS ITEM IS NOT USED ***	-	
26		9007203-00	*** THIS ITEM IS NOT USED ***	-	
27		9007208-00	FUSE,REG BLO 1/2 A,250V GLASS	1	F1
28		9009000-00	*** THIS ITEM IS NOT USED ***	-	
29		9008838-00	FUSE,REG BLO 10 A, 32V GLASS	2	F2,F3
30		9107560-01	*** THIS ITEM IS NOT USED ***	-	

AUTOMATED BY PRTLST.2D(16)

PARTS LIST

SHEET A2 OF A2

LINE ITEM DOCUMENT NUMBER

PART NUMBER

DESCRIPTION

QTY PER VARIATION
00

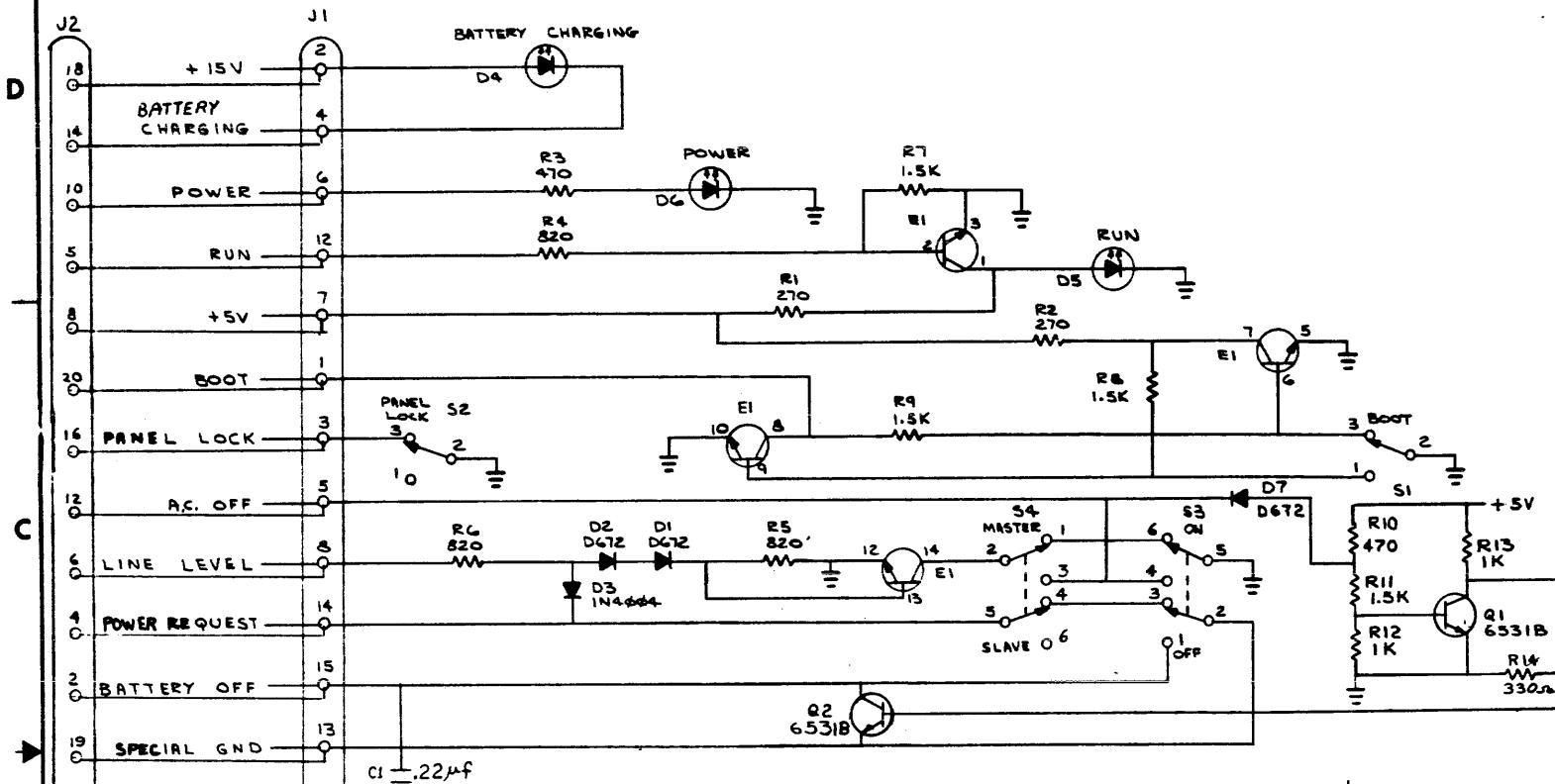
REFERENCE DESIGNATOR

31	31	1210929-01	*** THIS ITEM IS NOT USED ***	-	
32	32	1302396-00	.150.0 K .25 W 5.0 X CC	1	R2
33	33	7014329-00	TRIAC ASSY		D7
34	34	1010274-00	.22 MFD 50V +80-20% ZSU CER	4	C2,C4,C5,C7
35	35	9105740-55	*** THIS ITEM IS NOT USED ***	-	
36	36	1300365-00	1.0 K .25 W 5.0 X CC	1	R9
37	37	9107256-11	*** THIS ITEM IS NOT USED ***	-	
38	38	7420187-00	PLATE LABEL	1	
39	39	9006431-0E	*** THIS ITEM IS NOT USED ***	-	
40	40	9007801-00	WASHER LOCK S.S. #6	2	
41	41	9006024-01	SCREW, PAN PHIL 6-32X 1/2 SS	2	
42	42	9009513-03	CLIP, FUSE, WITH STOP, FOR PC BO	6	

DIGITAL TITLE			SECTION A OF A		SIZE CODE DOCUMENT NUMBER REV						
D	I	G	I	T	A	L	BAB-C POWER DISTRIBUTION BOARD	K	PL	5412946-0-DBP	D

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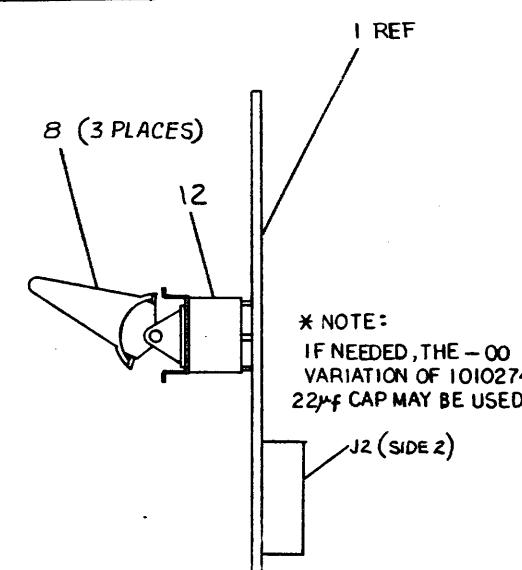


IC TYPE	GND	+5V
GND AND 5V ARE USUALLY PIN 7 AND 14 RESPECTIVELY. EXCEPTIONS ARE STATED ABOVE		
IC PIN LOCATIONS		

19 (2 PLACES)

4.50 F.B.S.

3.00 F.B.S.



FIRST USED ON OPTION MODEL		PARTS LIST	
ETCH BOARD REV. C		digital	
DATE 1-27-75	DATE 1-27-75	CHG'D. Robert Kugend	TITLE
REV. 0	DATE 1-27-75	ORIGINATED 1-27-75	FUNCTION BOARD
PROD. 0	DATE 1-27-75	CHG'D. P. Gardner	SIZE code DCS 5411507-0-1
NEXT HIGHER ASST. D-AD-7610033-P-0	DATE 1-27-75	REVISION	REV. F
DEC NO. 5411507-0-1	EIA NO. 5411507-0-1	DEC NO. 5411507-0-1	BRAND. D
SEMICONDUCTOR CONVERSION CHART			
SHORT 1 OF 1	CHG'D.	REV.	DATE

H 9300

ETCH BOARD REV. C

digital

TITLE

FUNCTION BOARD

SIZE code

DCS 5411507-0-1

REV. F

DATE

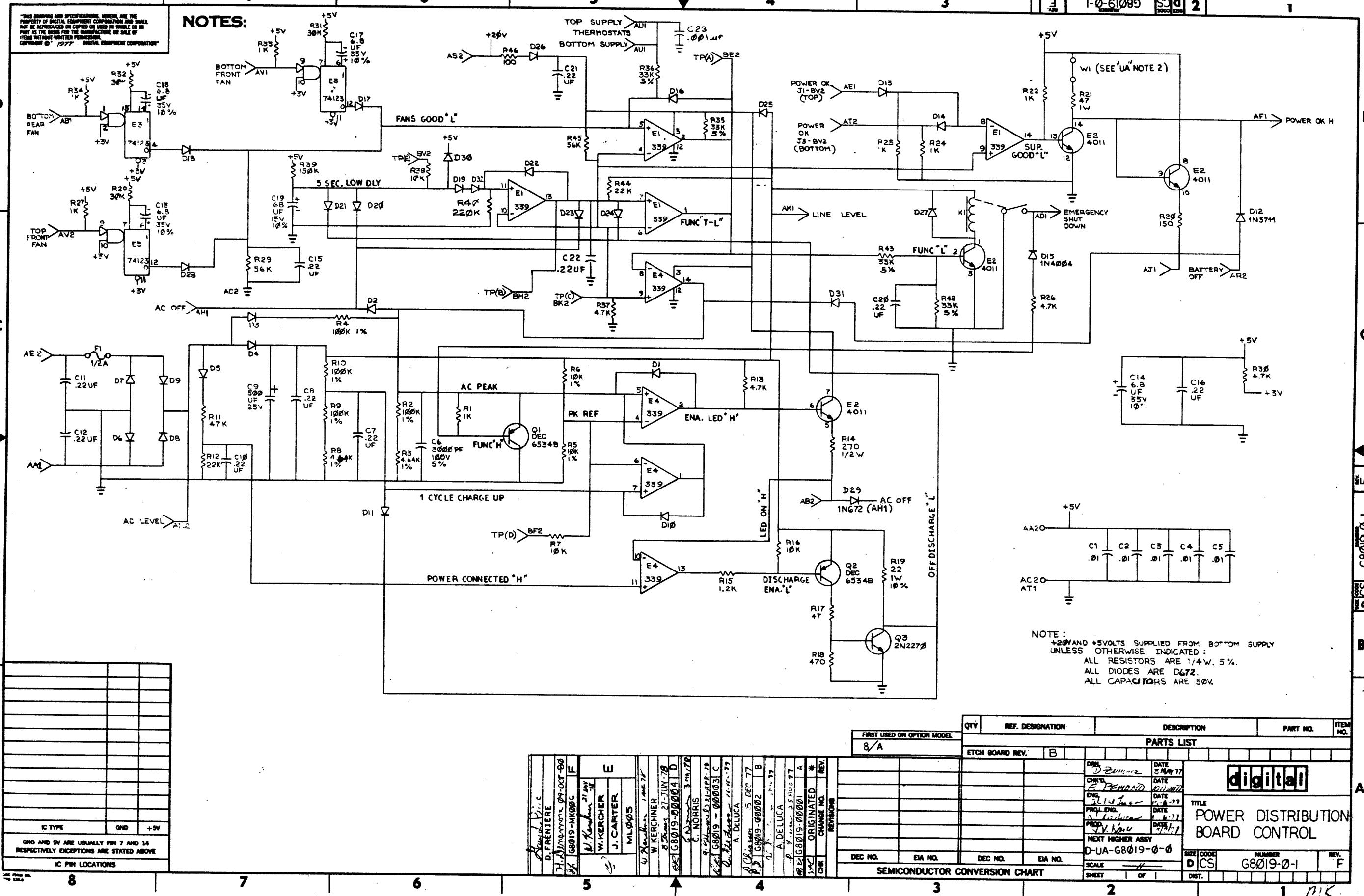
1-27-75

CHG'D.

P. Gardner

REVISION

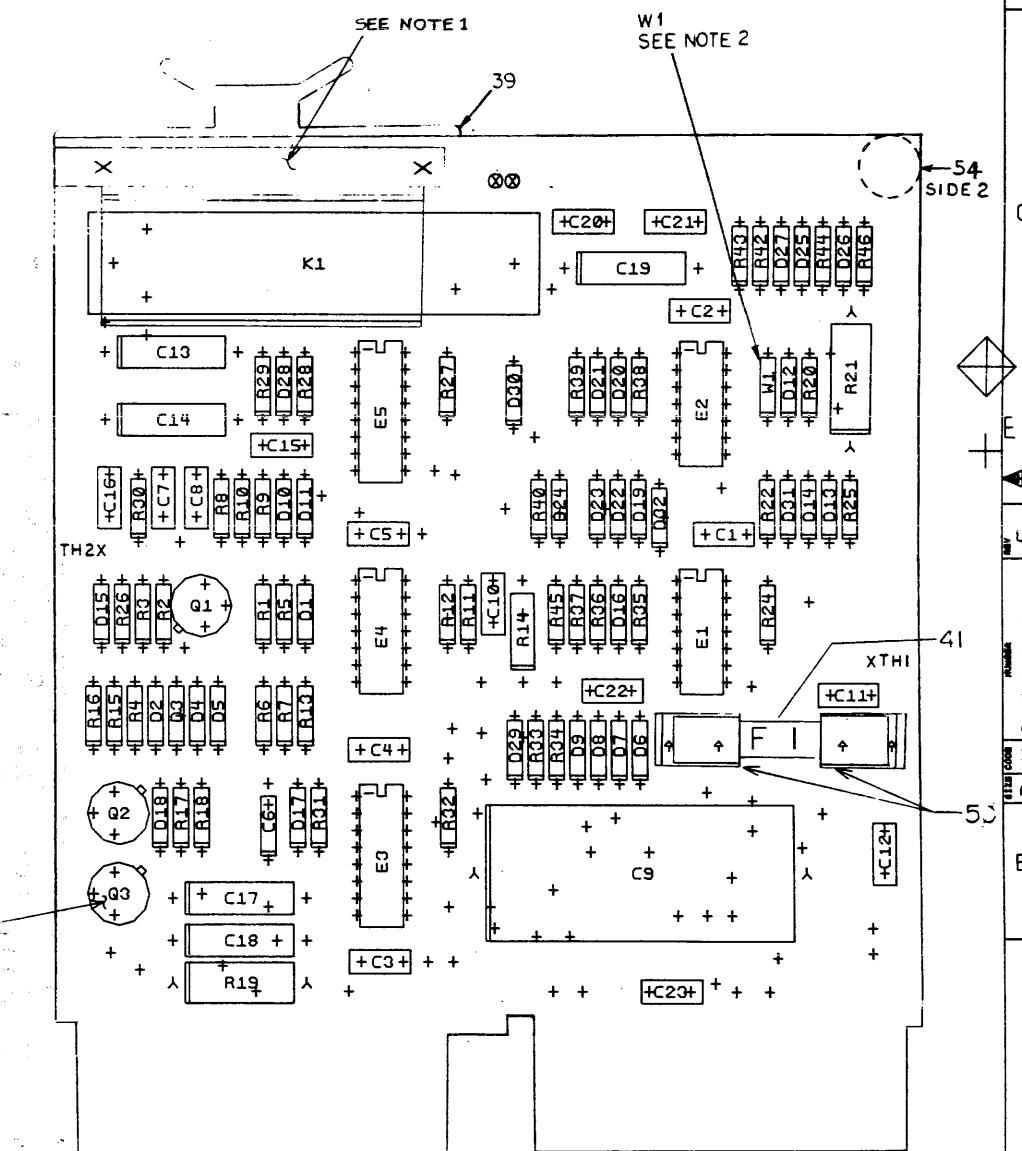
1-27-75



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COMPONENT SIDE VIEW



NOTES:
1. INSTALL A.Y.O. BRACKET (7415122-02)
USED TO SECURE RELAY K1 BY USING
SCREWS (9006611-2-1) AND KEP NUTS
(9006557-0-0); WASHERS (9006655-00).
2. POWER UK W1 IS ONLY REMOVED WHEN 5813
IS AN EXPANDED UNIT.

CHANGE NO	REV	E
MIL-205		
J.CARTER		F
KEN J. HIER		
H. KUNZ		
O. FRENIE		F

ETCH REV. B-P1
P.C. DESIGN DATA BASE REV. B

SIGNATURES	DATE	digital
DRN. D. D.	5-15-78	
CHK'D. /	1/23/78	
ENG. /	1/23/78	
PROJ. ENG. /	1/23/78	
PROD. /	1/23/78	
SCALE 2X		
SHT. 10F3		
SIZE CODE D		
NUMBER UA G8019-0-0		
REV F		

NEXT HIGHER ASSY. B-DD-G8019-0-0

1 MS#

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1

DUA G8&19-9-5
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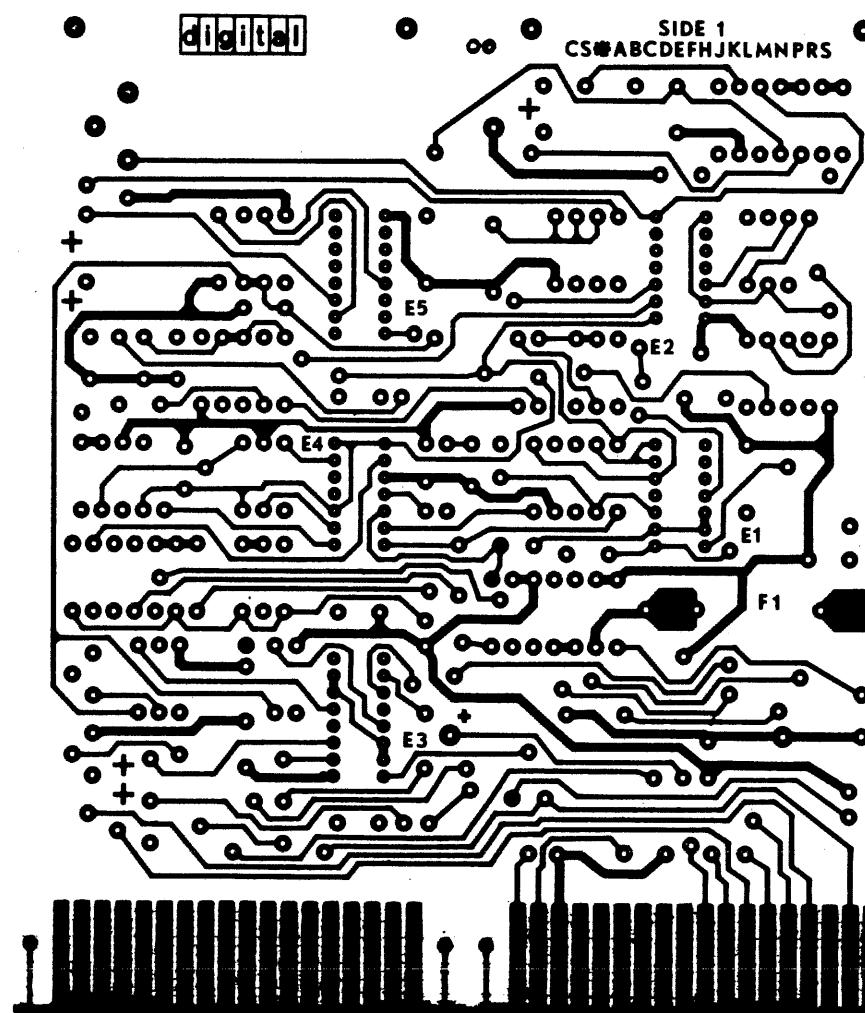
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G8019 5012948 B-P



REVISIONS		
CHK	CHANGE NO.	REV.

DEC FORM NO
DRG 138

TITLE	BA8-C POWER DISTRIBUTION BOARD CONTROL		
SCALE	2 to 1	SHEET	2 OF 3

2

SIZE	CODE	NUMBER
D	UA	G8019-0-0
DIST		

3000

8 7 6 5 ↑ 4 3 2 1

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NOTES:

1. TRANSISTOR DEC 2N5362, 1510196 MAY BE USED AS A SECOND SOURCE FOR ITEM # 62 TRANSISTOR DEC D44H8 1511654.
2. ALL COMPONENTS SHOWN BY A BROKEN LINE MOUNT ON SIDE 2.
3. TRANSISTOR PAD MOUNTS UNDER Q2, AND HEAT SINK MOUNTS ON Q2. REFER TO PARTS LIST, ITEM # 25 AND 77.
4. ITEM # 24 BRKT IS MOUNTED ON BOARD WITH ITEM # 6873 AND # 81 (2 PL).
5. Q6, Q7, Q11 AND Q12 ARE MOUNTED WITH ITEM # 73, 78, 81, 92 AND 101.
6. S1 MOUNTS WITH ITEM 73, 81 AND 92 (1 PLACE).
7. Q26 AND Q27 ARE MOUNTED WITH ITEM # G7, # 71, 74, 92 AND 103.

8. PIN 1 IS MADE IN REVERSE, SIDE 1 IS LIGHT, SIDE 2 IS DARK.
9. INSERT JUMPER W3 FOR 50MHz OPERATION.
10. JUMPERS W1, W2 SHOULD BE INSTALLED AFTER TEST.

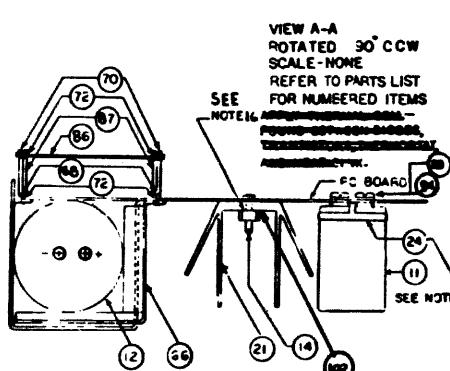
11. JUMPER W1, W2

12. JUMPER W3

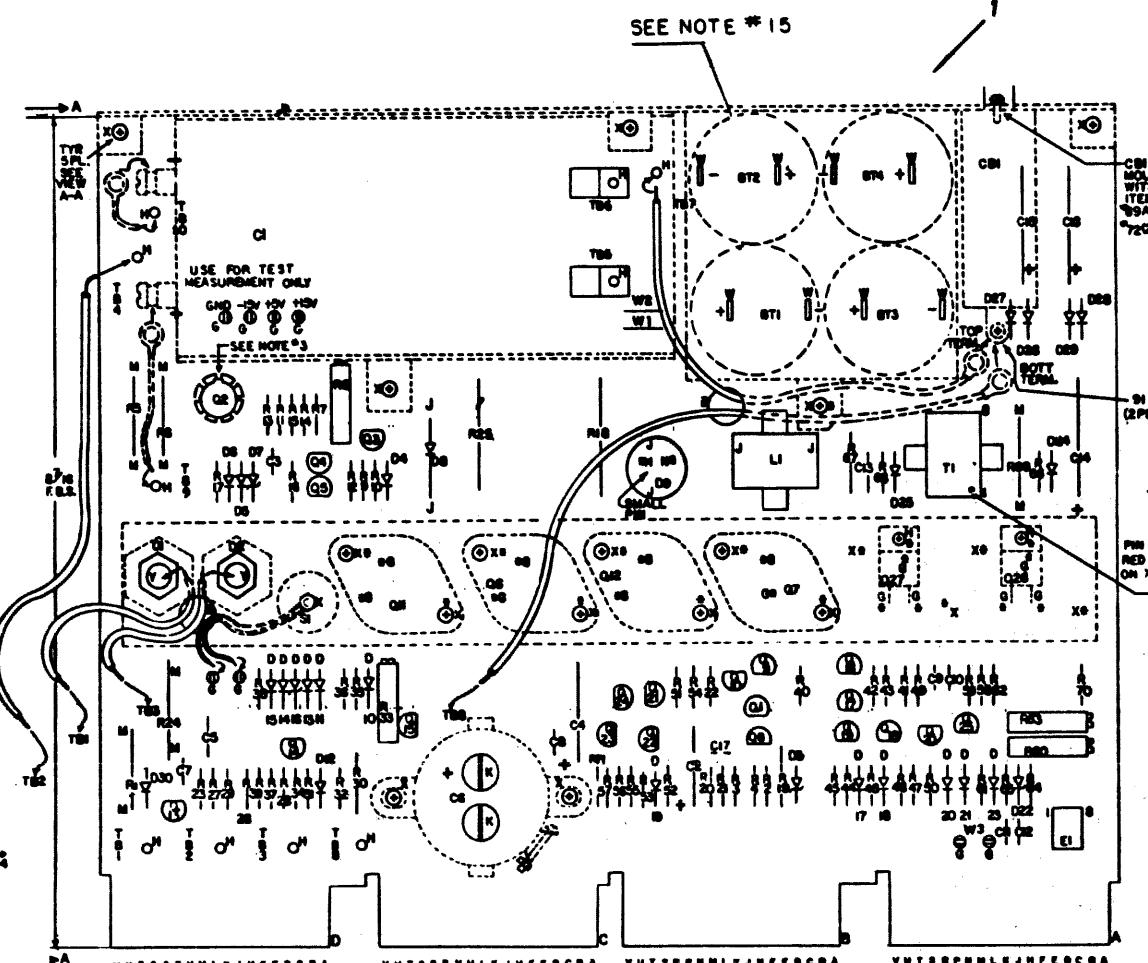
13. JUMPER W4

14. JUMPER W5

15. FOR HANDLING AND SOLDERING REQUIREMENTS OF 12V/170 LEAD ACID BATTERIES (ITEM # 23) SEE A-SP-765252-D-0



16. APPLY THERMAL COMPOUND TO SIDES OF ALL THERMAL INSULATORS AND TO BOTTOM OF THERMOSTAT.



17. BELOW IS LISTED TORQUE VALUES TO BE USED IN ASSEMBLY:

SIZE OF SCREW	INCH/LB
10-32 SCREWS SECURING PC BOARD TO C6	14
10-32 SCREWS SECURING THE CRIMPS TO C1	14
6-32 SCREWS HOLDING PC BOARD TO THE CHASSIS	14
6-32 SCREWS ON THE TRANSISTORS Q6, Q3, Q11, Q12	14
4-40 SCREWS ON THE TRANSISTORS Q26, Q27	10
NUTS HOLDING THE DIODES, D1, D2	20
NUT HOLDING THERMOSTAT, S1	10
6-32 SCREWS HOLDING THE CIRCUIT BREAKER TO CHASSIS	14
6-32 SCREWS HOLDING BATTERY SHIELD TO CHASSIS	14
SCREWS HOLDING CRIMPS TO CIRCUIT BREAKER	14

NOTE: TO ACHIEVE TRUE TORQUE-READINESS, ALWAYS USE A NUT DRIVER ON ONE SIDE AND A TORQUE DRIVER ON THE OTHER WHERE A SCREW AND A NUT ASSEMBLY IS INVOLVED.

Q.C. TOLERANCE: INSPECT TO MINUS (-) 2 INCH/LB OF ASSEMBLED TORQUE VALUES.

CAUTION:
OFF SHEET P/L
DATA BASE PER
G8016-MK006

PDP8A

FIRST USED ON OPTION MODEL

REF	X-Y COORDINATE/HOLE LOCATION	REF NO.
REF	ASSEMBLY DRILLING HOLE LAYOUT	REF NO.
REF	MODULE ECO HISTORY	REF NO.
1	ETCHED CIRCUIT BOARD	REF NO.
2	CAP .1 UF 100V 205 DISC	REF NO.
3	CAP .1UF 50V 205 STANT	REF NO.
4	CAP .01 UF 100V 205 DISC	REF NO.
5	C1, C8, C10, C11, C12	REF NO.
6	CAP .01 UF 25V -105 + 75 ELECT	REF NO.
7	C2	REF NO.
8	C7, C8	REF NO.
9	C8	REF NO.
10	C1	REF NO.
11	B6, B8, B11, B12, B13, B14, B15	REF NO.
12	D1, D2	REF NO.
13	D10, D11, D12, D13, D20, D21, D22, D24, D25, D30	REF NO.
14	D4, D28, D29, D28, D29	REF NO.
15	D10, D11, D12, D13, D14	REF NO.
16	D16, D17, D18, D19, D20, D21, D22, D23, D24	REF NO.
17	D18, D19	REF NO.
18	D19, D20	REF NO.
19	D21, D22	REF NO.
20	D21, D22	REF NO.
21	D21, D22	REF NO.
22	D21, D22	REF NO.
23	D21, D22	REF NO.
24	D21, D22	REF NO.
25	R5, R6, R80	REF NO.
26	R20, R43	REF NO.
27	R31	REF NO.
28	R71, R4, R22, R26, R30, R36, R57	REF NO.
29	R12, R47	REF NO.
30	R12, R43	REF NO.
31	R10, R17, R23, R64	REF NO.
32	R3, R15, R37, R64, R56	REF NO.
33	R11, R13, R20, R21, R29, R40, R41, R42, R52, R56, R60, R76	REF NO.
34	R14, R56	REF NO.
35	R23, R44, R46, R48, R51, R60	REF NO.
36	R30	REF NO.
37	R40	REF NO.
38	R24	REF NO.
39	R34	REF NO.
40	R7	REF NO.
41	R31	REF NO.
42	R56	REF NO.
43	R100 1/40 15 MF	REF NO.
44	R1216 1/40 15 MF	REF NO.
45	R100 1/40 15 MF	REF NO.
46	R464 1/40 15 MF	REF NO.
47	R150 105 RR	REF NO.
48	R1000 1/40 15 MF	REF NO.
49	R303 1/40 15 MF	REF NO.
50	R27E 1/40 15 MF	REF NO.
51	R6, R33, R80	REF NO.
52	R5, R34, R60	REF NO.

REF	X-Y COORDINATE/HOLE LOCATION	REF NO.
REF	ASSEMBLY DRILLING HOLE LAYOUT	REF NO.
REF	MODULE ECO HISTORY	REF NO.
1	ETCHED CIRCUIT BOARD	REF NO.
2	CAP .1 UF 100V 205 DISC	REF NO.
3	CAP .1UF 50V 205 STANT	REF NO.
4	CAP .01 UF 100V 205 DISC	REF NO.
5	C1, C8, C10, C11, C12	REF NO.
6	CAP .01 UF 25V -105 + 75 ELECT	REF NO.
7	C2	REF NO.
8	C7, C8	REF NO.
9	C8	REF NO.
10	C1	REF NO.
11	B6, B8, B11, B12, B13, B14, B15	REF NO.
12	D1, D2	REF NO.
13	D10, D11, D12, D13, D20, D21, D22, D23, D24	REF NO.
14	D4, D28, D29, D28, D29	REF NO.
15	D10, D11, D12, D13, D14	REF NO.
16	D16, D17, D18, D19, D20, D21, D22, D23, D24	REF NO.
17	D18, D19	REF NO.
18	D19, D20	REF NO.
19	D21, D22	REF NO.
20	D21, D22	REF NO.
21	D21, D22	REF NO.
22	D21, D22	REF NO.
23	D21, D22	REF NO.
24	D21, D22	REF NO.
25	R5, R6, R80	REF NO.
26	R20, R43	REF NO.
27	R31	REF NO.
28	R71, R4, R22, R26, R30, R36, R57	REF NO.
29	R12, R47	REF NO.
30	R12, R43	REF NO.
31	R10, R17, R23, R64	REF NO.
32	R3, R15, R37, R64, R56	REF NO.
33	R11, R13, R20, R21, R29, R40, R41, R42, R52, R56, R60, R76	REF NO.
34	R14, R56	REF NO.
35	R23, R44, R46, R48, R51, R60	REF NO.
36	R30	REF NO.
37	R40	REF NO.
38	R24	REF NO.
39	R34	REF NO.
40	R7	REF NO.
41	R31	REF NO.
42	R56	REF NO.
43	R100 1/40 15 MF	REF NO.
44	R1216 1/40 15 MF	REF NO.
45	R100 1/40 15 MF	REF NO.
46	R464 1/40 15 MF	REF NO.
47	R150 105 RR	REF NO.
48	R1000 1/40 15 MF	REF NO.
49	R303 1/40 15 MF	REF NO.
50	R27E 1/40 15 MF	REF NO.
51	R6, R33, R80	REF NO.
52	R5, R34, R60	REF NO.

REF	REF DESIGNATION	DESCRIPTION	PART NO.
PARTS LIST			
ETCH BOARD REV	E		
DEC NO.	EIA NO.	DEC NO.	EIA NO.
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WIRE TABLE				
PART NO.	FROM	TO	TERMINATION	AIRE LENGTH
91-07380-22	TB1	D1	SOLDER AT D1 END.	5.00
91-07380-22	TB3	D2	SOLDER AT D2 END. TAB AT TB3 END	5.00
91-07380-00	TB2	TB4	SOLDER AT TB2 END SOLDER AT TB4 END	7.00
91-07380-00	+ TERMINAL OF CI	TB9 HOLE	SOLDER WIRE AT HOLE END TB9. SOLDERLESS CONNECTOR AT CI END 90-07926	2.50
91-07380-00	- TERMINAL OF CI	TB10 HOLE	SOLDER WIRE AT HOLE END TB10 SOLDERLESS CONNECTOR AT CI END 90-07926	1.25
91-07380-00	TB7	TOP TERMINAL OF CBI	SOLDER AT TB7 END. SOLDERLESS CONNECTOR AT CBI END 90-07926	7.00
91-07380-00	TB8	BOTTOM TERMINAL OF CBI	SOLDER AT TB8 END. SOLDERLESS CONNECTOR AT CBI END 90-07926	11.25
91-07350-22	SI	SPLIT LUG	SOLDER AT SPLIT LUG END	4.00
	SI	SPLIT LUG	SOLDER AT SI END	4.00

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QTY	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM
1	R30	RES. 100 1/2W NTC	1341760	57
1	R67	RES. 2.7 1/2W 10%	1300444	64
2	Q7, Q12	TRANS. DEC 2N3715	1302988	55
1	Q2	TRANS. 2N3762	1300649-01	56
16	Q1, Q3, Q4, Q5, Q6, Q10, Q13, Q15, Q17, Q19, Q20, Q21, Q22, Q23, Q24, Q25	TRANS. DEC A05	1518705	57
1	R58	RES. 5.6K, 1/4W, 5%	1301879	58
4	Q8, Q14, Q16, Q18	TRANS. DEC A55	1518706	59
1	D8	DIODE SCR C32AX135	1518628	60
2	Q8, Q11	TRANS. 2N5663	1518647	61
2	Q20, Q27	TRANS. D44H0	1610707-01	62
1	T1	TRANSFORMER	1611758	63
1	L1	CHOKE	1611759	64
1	E1	I.C. DEC 72741	1610296	65
1		BRACKET REG. B.D.	7411478	66
2		SCREW 4-40 X .50 PH	9000013-1	67
4		SCREW 6-32 X .25 PH	9000020-1	68
2		SCREW 6-32 X .31 PH	9000021-1	69
3		SCREW 6-32 X .75 PH	9000026-1	70
2		KNOB 4-40	9006557	71
7		WASHER #8 INTERNAL	9006633	72
9		WASHER #8 FLAT	9006636	73
2		WASHER #4 FLAT	9006772	74
8		SPLIT LUG	9006735	75
2	TB5, TB6	TAB FAST-ON (OFF SET)	9007112	76
1		TRANSI PAD #10134	9007200	77
6		SCREW 6-32 X .56 PH	9C07793-1	78
REF		G8016 REG. BOARD SPEC	G8016-0-E	79
4		SOLDERLESS CONNECTOR	9007920-01	80
11		KEPNUK 8-32	9008185	81
A/R		WIRE #12 AWG	9107300-00	82
A/R		WIRE #12 AWG	9107300-22	83
A/R	W3	BUS WIRE #22 AWG (SEE NOTE "H")	9107500-01	84
A/R	W1, W2	REEL JUMPER (SEE NOTE "H")	9107560	85
1		SHIELD BATTERY	7411693-0-0	86
3		SPACER #6-38 LG.	9006801	87
2	TB5, TB6	EYELET	9009000	88
8	TB1, TB2, TB3, TB4, TB7, TB8, TB9, TB10	EYELET GS4-3	9007836	89
A/R		WIRE #22 AWG	9107350-22	90
2		WASHER #8 INTERNAL	9006634	91
A/R		THERMO COMPOUND	90068268	92
2		SCREW 10-32 X .31	9000070-01	93
2		WASHER #10 INTERNAL	9006635	94
1	C17	CAP .47UF 25V 20% CER.	1010279	95
				96
1		DECAL	A-DC-7413109-0	97
REF		FINAL INSPE. PROC. FOR G8016	A-SP-G8016-C-9	98
REF		POWER SUPPLY TESTER	B-DD-G8016-TA	99
/		PACKAGING INSTRUCTION	A-SP-3700175-0-0	100

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REVISIONS		
CHK	CHANGE NO.	REV
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REC FORM NO.

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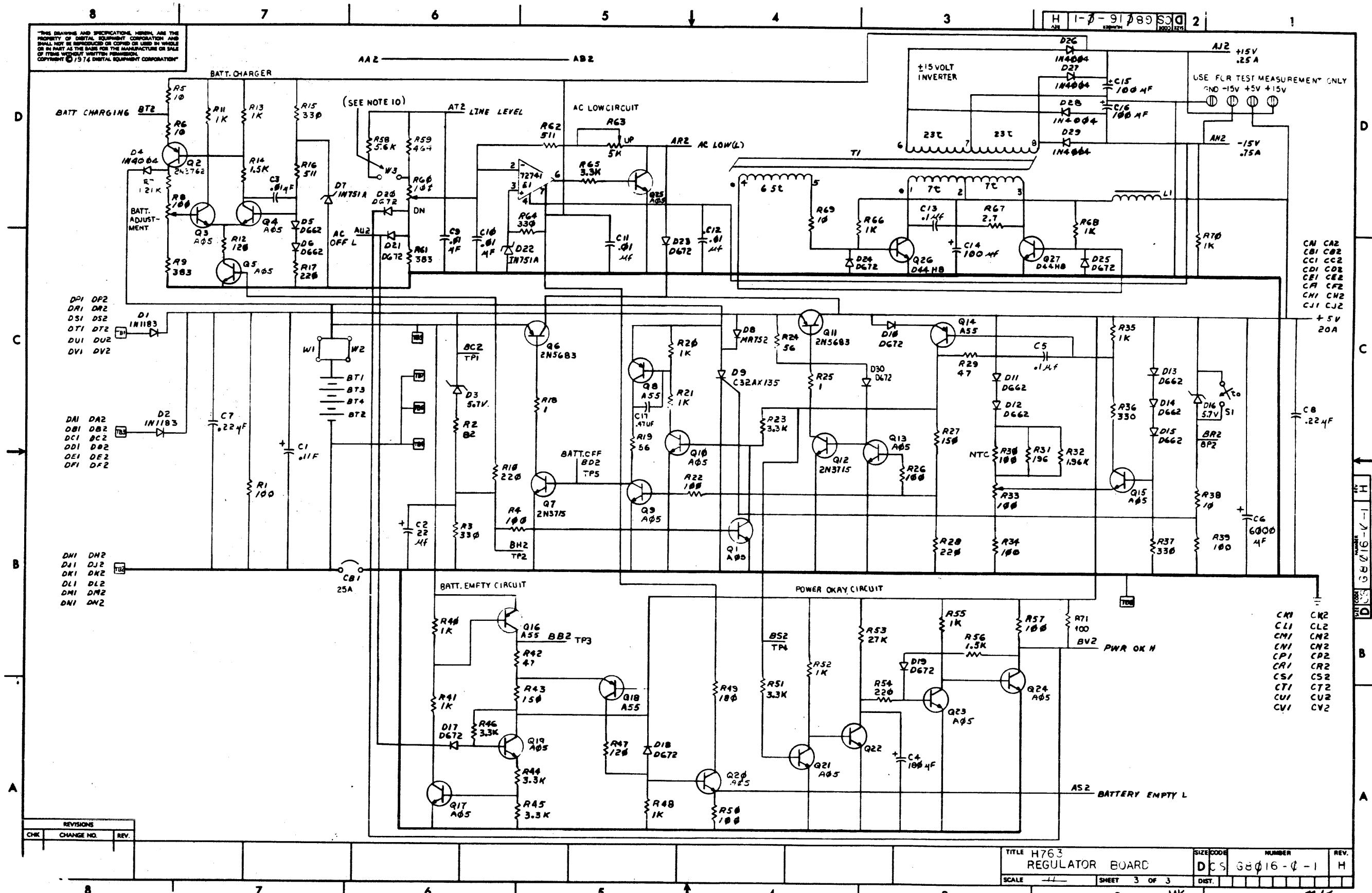
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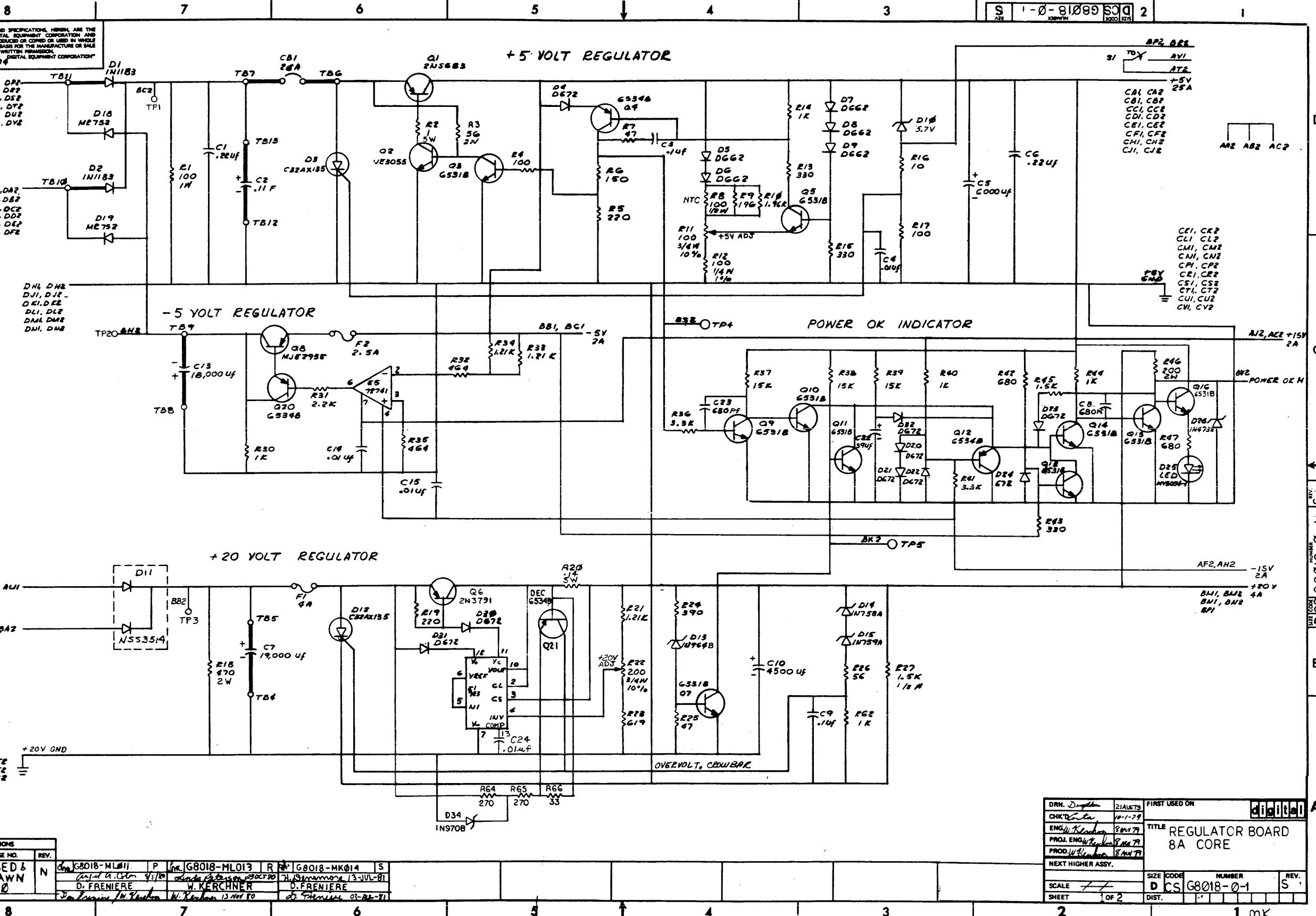
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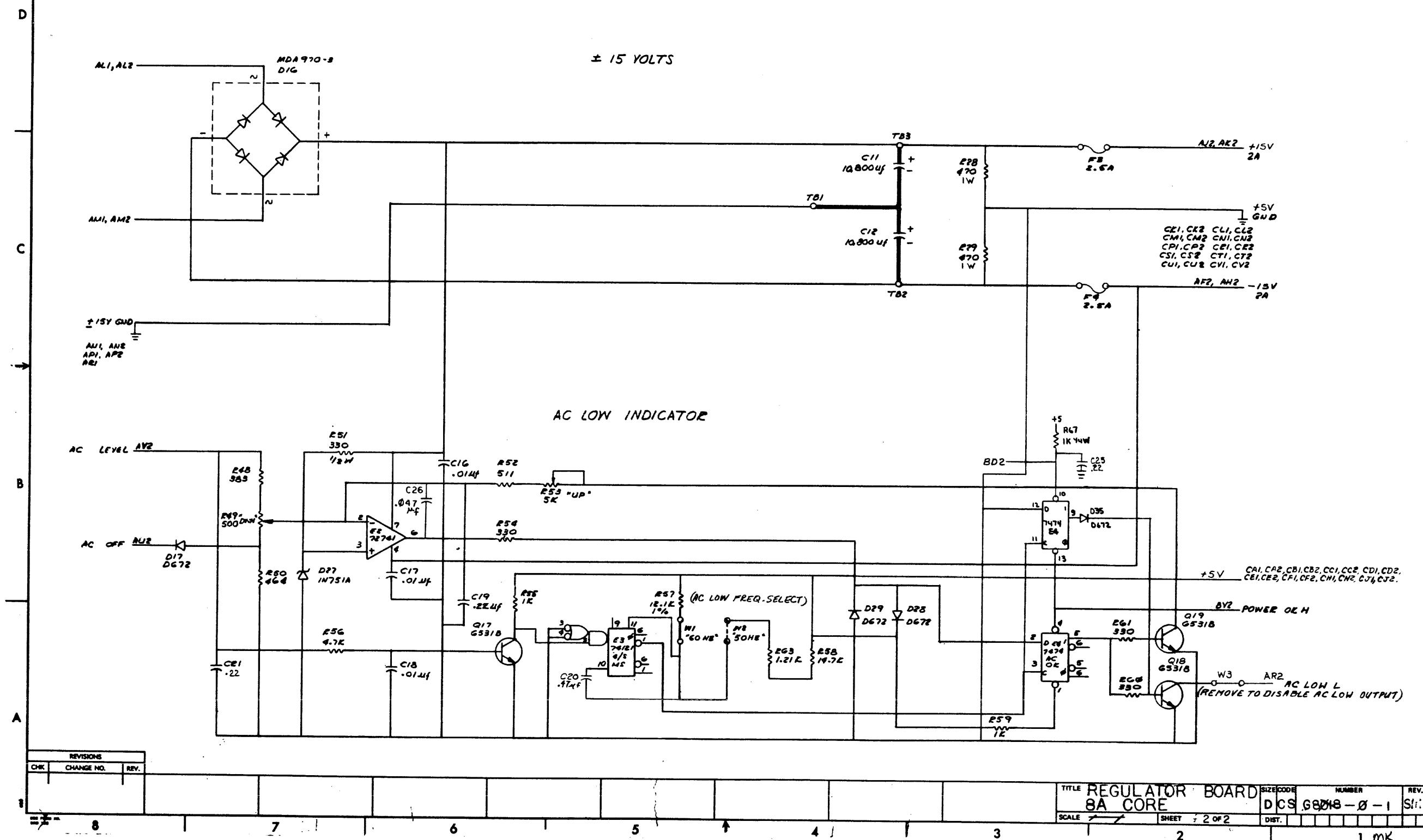
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REGULATOR BOARD
SIZE CODE DCS G8016-0-1
NUMBER REV H
SCALE DIST.

2 OF 3 MK 1





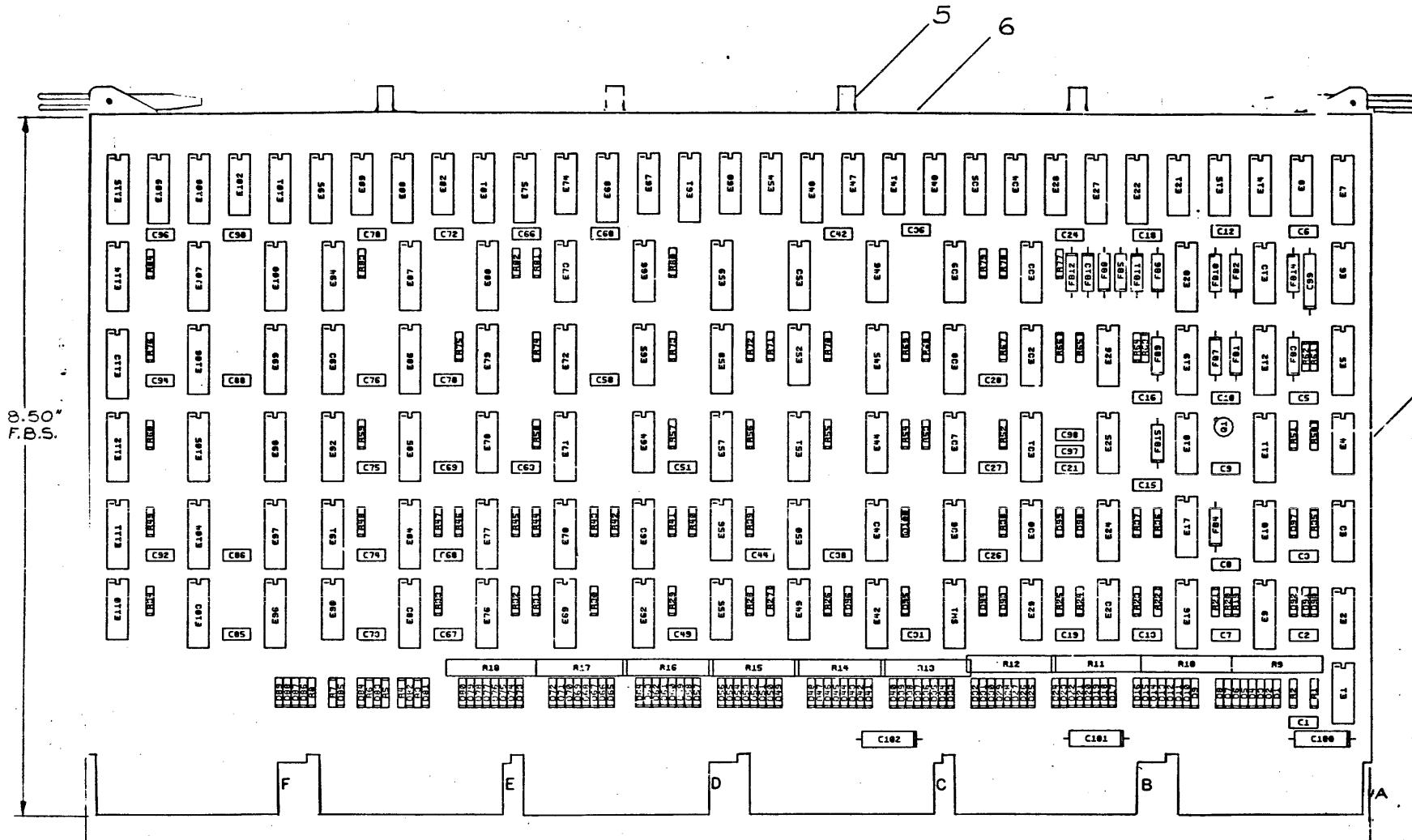
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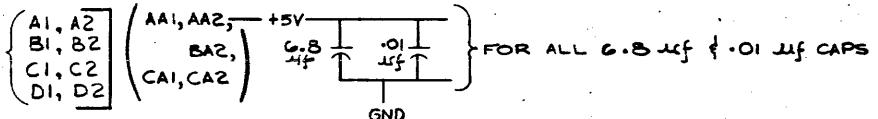
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NOTES:

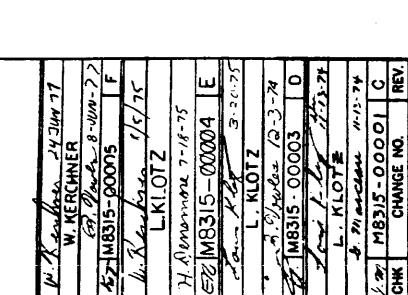
- ALL UNLABELED DIODES ARE DEC TYPE DGC4
- +3V, +3VA AND +3VB GENERATION ON SHEET #8
- FOR ETCH CUTS REFER TO D-AH-MB315-0-2
- NO ECHAN CUTS



GND.
CONNECTION—PINS C,F,N,T ON CONNECTORS



IC 74157	8	16
74S159	8	16
74163	8	16
74S175	8	16
74S194	8	16
380	1	8
8097	8	16
8235	8	16
8234	8	16
8271	8	16
74173-1	8	16
256 BIT ROM	8	16
IC TYPE	GND	+5V
GND AND 5V ARE USUALLY PIN 7 AND 14 RESPECTIVELY. EXCEPTIONS ARE STATED ABOVE		
IC PIN LOCATIONS		



FIRST USED ON OPTION MODEL		PARTS LIST	
ETCH BOARD REV.		PARTS LIST	
E		DRL 101-14 CHRD 101-14 ENG 101-14 PCBL ENG 101-14 PROD. 101-14	digital
		DATE 101-14 DATE 101-14 DATE 101-14 DATE 101-14 DATE 101-14	
		NEXT HIGHER ASSY A-PL-KK8-A-0	
DEC NO.	EIA NO.	DEC NO.	EIA NO.
SCALE 1/10	SHEET 1 OF 10	DIST. 1	REV. F
SIZE CODE DCS	NUMBER M8315-0-1		

SEMICONDUCTOR CONVERSION CHART

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8 7 6 5 ↓ 4 3 2 DCS M8315-0-1 F 1

1	E51	IC DEC 74157	1910055	40
7	E60, E70, E80, E90, E107, E100, E115	IC DEC 748150	1910540	40
3	E75, E93, E94	IC DEC 74163	1911713	50
2	E5, E42	IC DEC 748175	1910067	51
8	E9, E11, E12, E10, E81, E90, E100, E114	IC DEC 748104	1910952	52
1	E38	IC DEC 300	1909405	53
2	E67, E73	IC DEC 8003	1910037	54
4	E77, E84, E104, E105	IC DEC 8007	1911527	55
3	E80, E80, E90	IC DEC 8234	1911315	56
3	E81, E87, E92	IC DEC 8235	1909039	57
1	E31	IC DEC 8271	1909015	58
5	E30, E35, E37, E39, E55	IC DEC 8001	1909705	59
11	E50, E62, E63, E91, E90, E97, E103, E100, E100, E111, E112	IC DEC 74173-1	1911330-OI	60
1	E88	256 BIT ROM (A)	23078A1	61
1	E57	256 BIT ROM (B)	23077A1	62
1	E78	256 BIT ROM (C)	23078A1	63
1	E72	256 BIT ROM (D)	23075A1	64
1	E78	256 BIT ROM (E)	23074A1	65
1	E63	256 BIT ROM (H)	23073A1	66
1	E30	256 BIT ROM (J)	23078A1	67
1	E53	1024 BIT ROM (F)	23080A2	68
1	E54	304K-800M	23080A2	69

SWITCH SELECTION CHART
(FOR AUTO RESTART LOCATION)

SW1-	FIELD 7
2	4000
3	2000
4	1000
5	400
6	200
7	OFF (DISABLES AUTO RESTART)
8	OFF FOR NORMAL OPERATION

ONLY ONE SWITCH
MAY BE CLOSED AT A TIME.

COMPONENT SUBSTITUTION CHART

PART CALLED FOR			SUBSTITUTE PART		
QTY	PART NO	DESC	QTY	PART NO	DESC
1	1909485	IC 380	1	1910392	5380
			1	1909971	5380
			1	1910390	7380
			1	1411464	5340

REVISIONS		
CHK	CHANGE NO.	REV

DCS FORM NO.
Rev. 12

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TITLE HEX OMNIBUS CPU
SIZE CODE DCS M8315-0-1 NUMBER REV. F
SCALE -/- SHEET 2 OF 10 DIST.

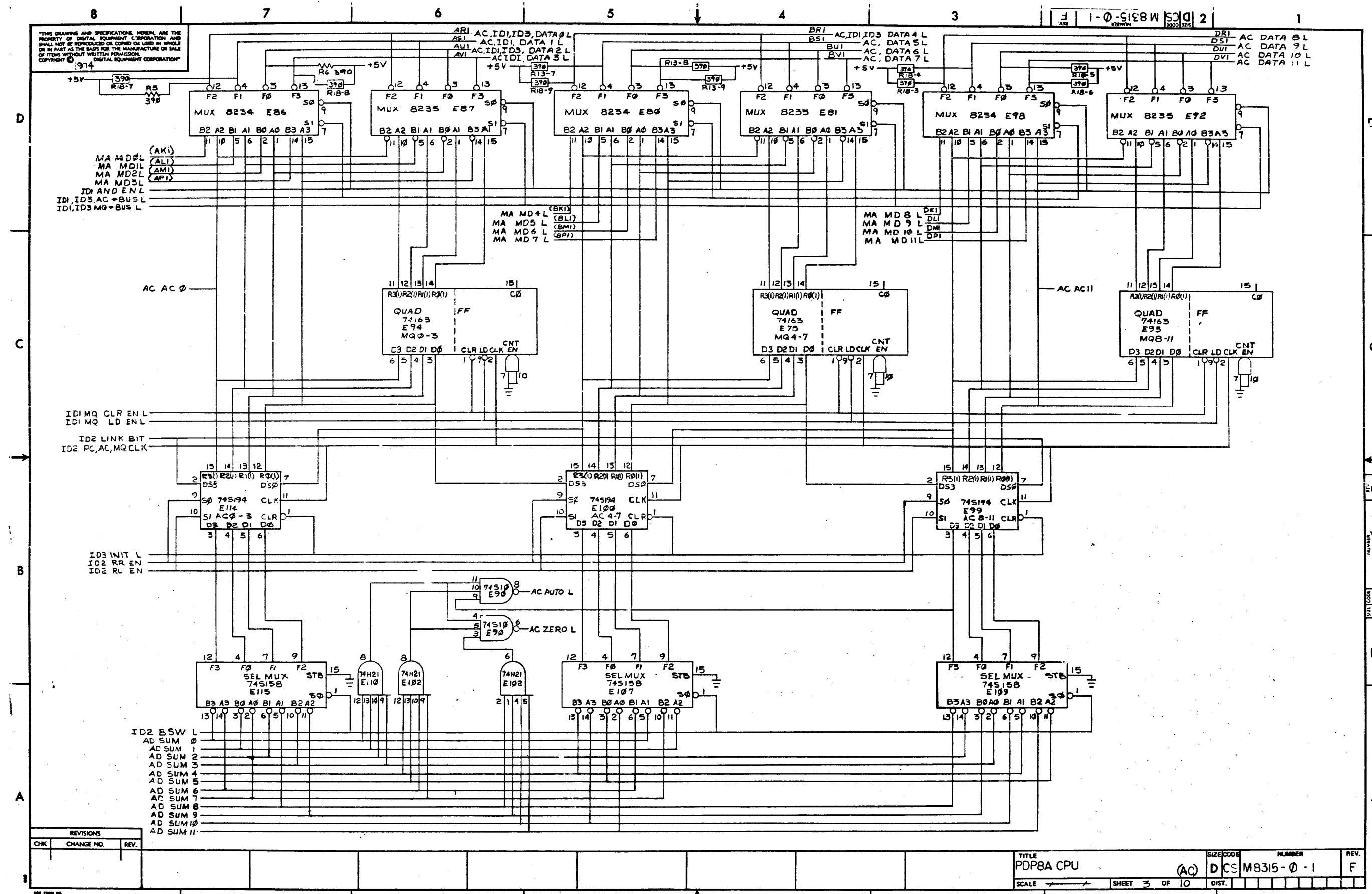
PRINTED BY DCS M8315-0-1 F

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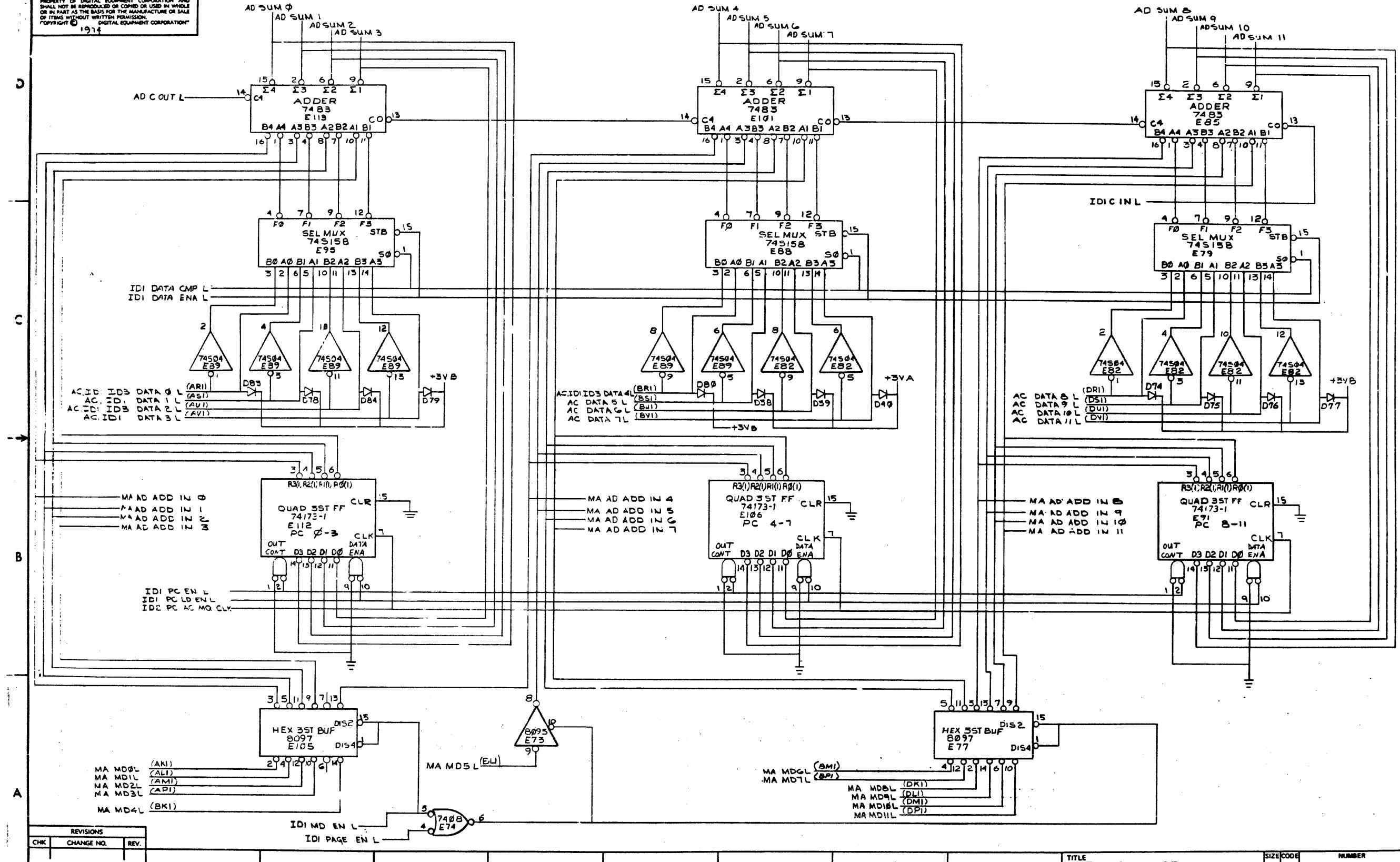
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C



1 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100

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REVISIONS		
CHK	CHANGE NO.	REV.

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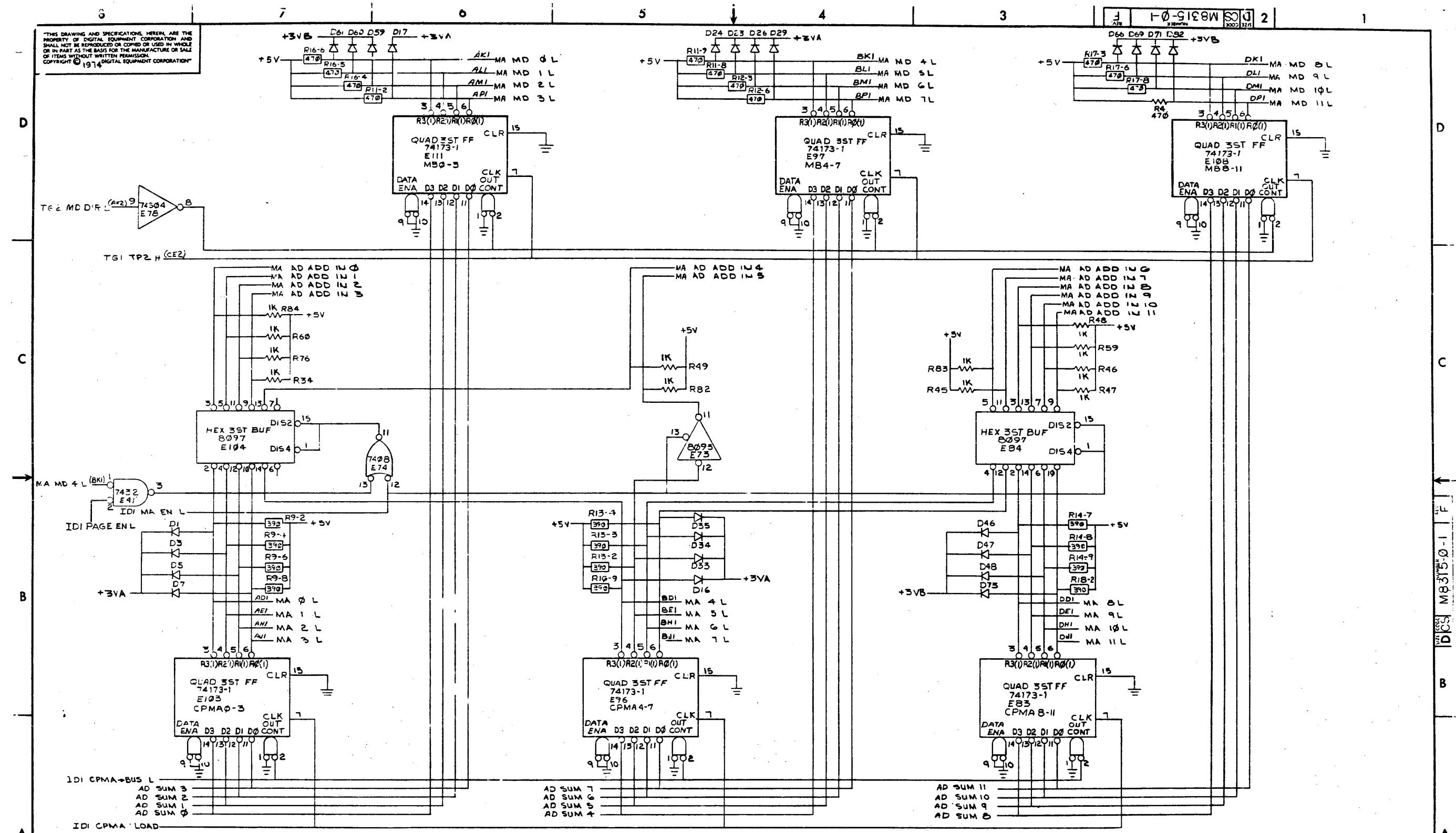
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TITLE: PDP8A CPU (AD) SIZE CODE DCS NUMBER M8315-0-1 REV. F
SCALE ← → SHEET 4 OF 10 DIST.



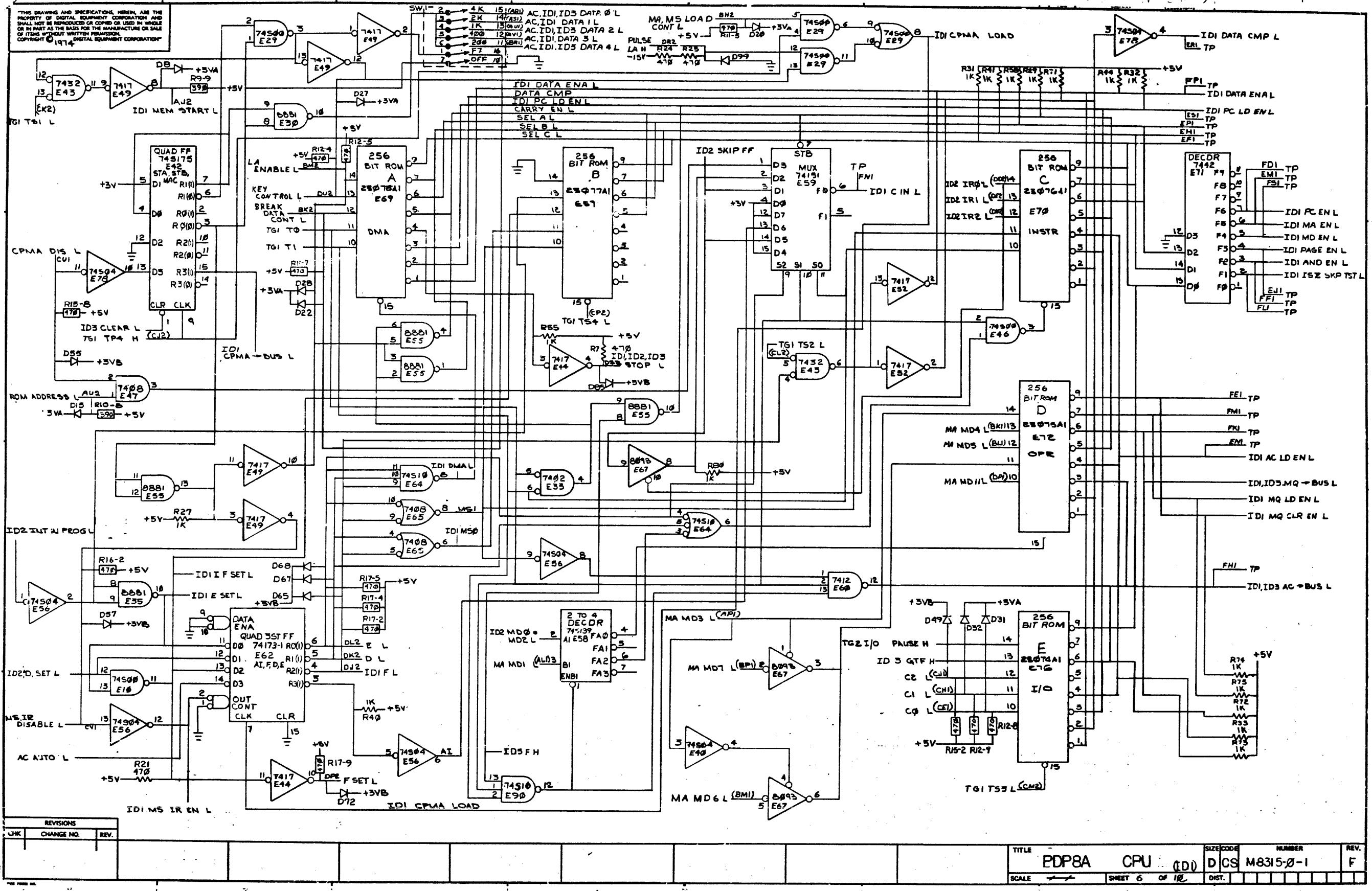
REVISIONS		
CHK	CHANGE NO.	REV

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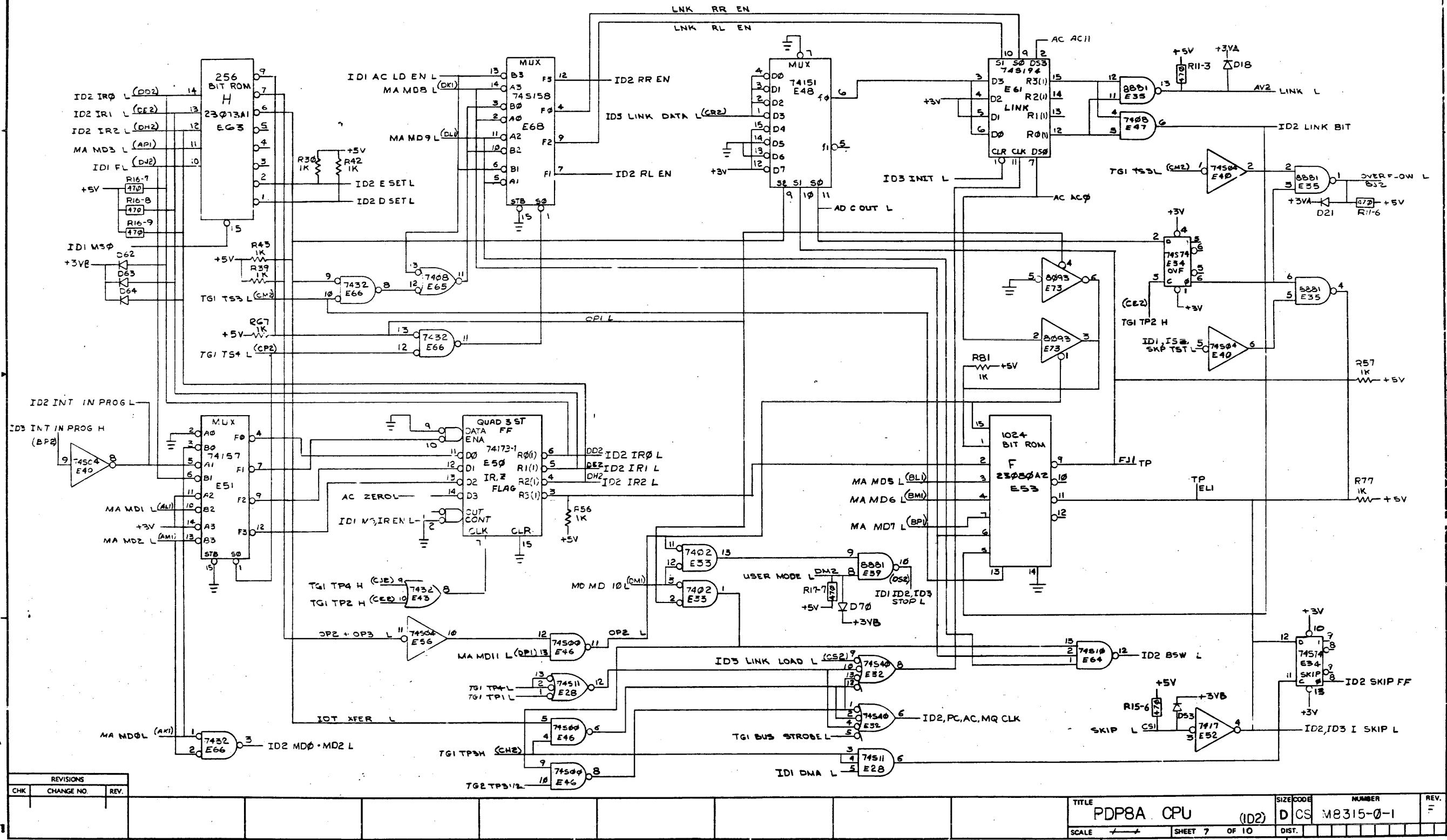
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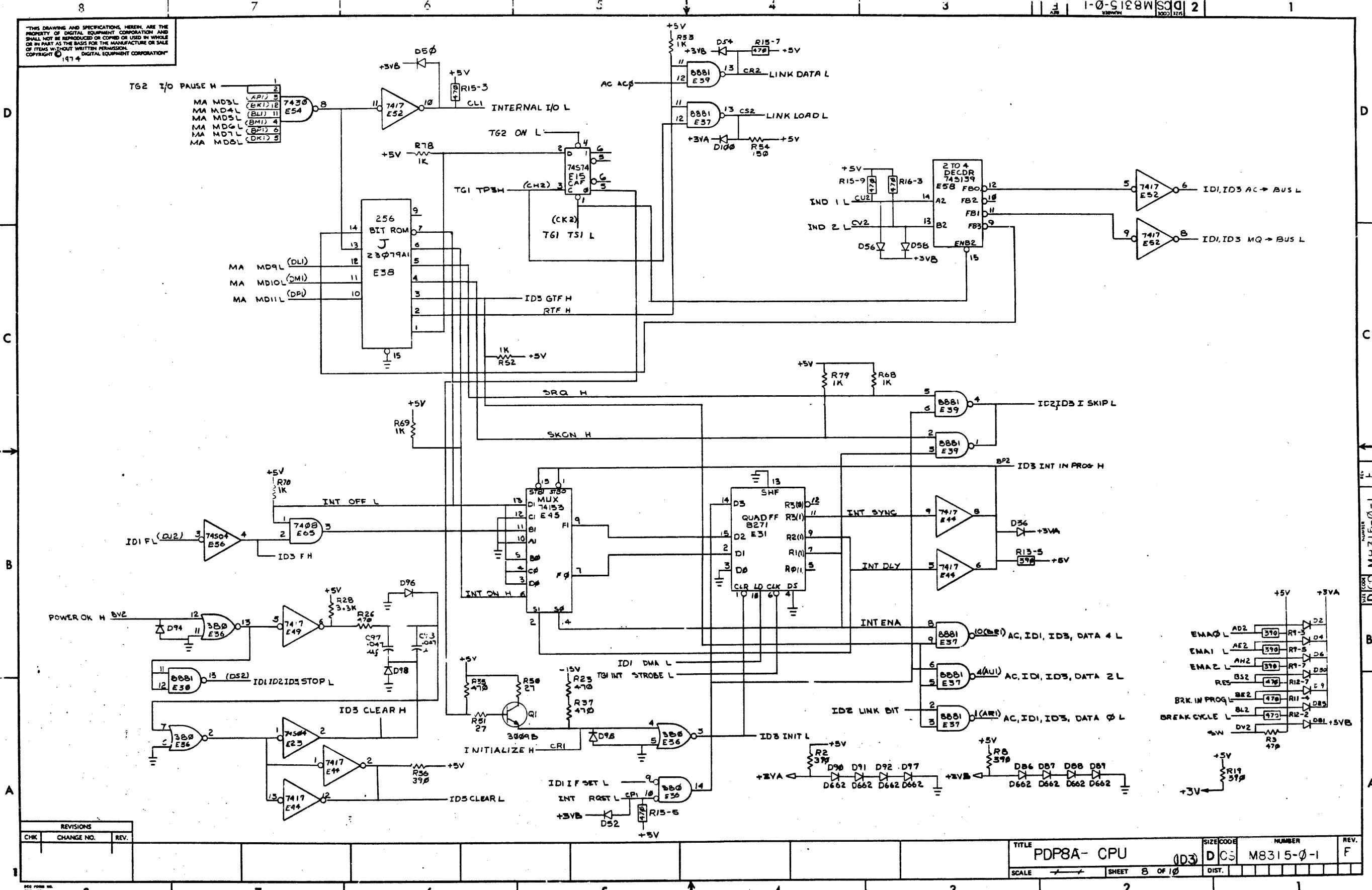
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REVISIONS		
CHK	CHANGE NO.	REV.

REF ID: M8315-0-1

SCALE → → SHEET 10 OF 10 DIST. → →

TITLE PDP8A CPU (TG2) SIZE CODE NUMBER DCS M8315-0-1 REV. F

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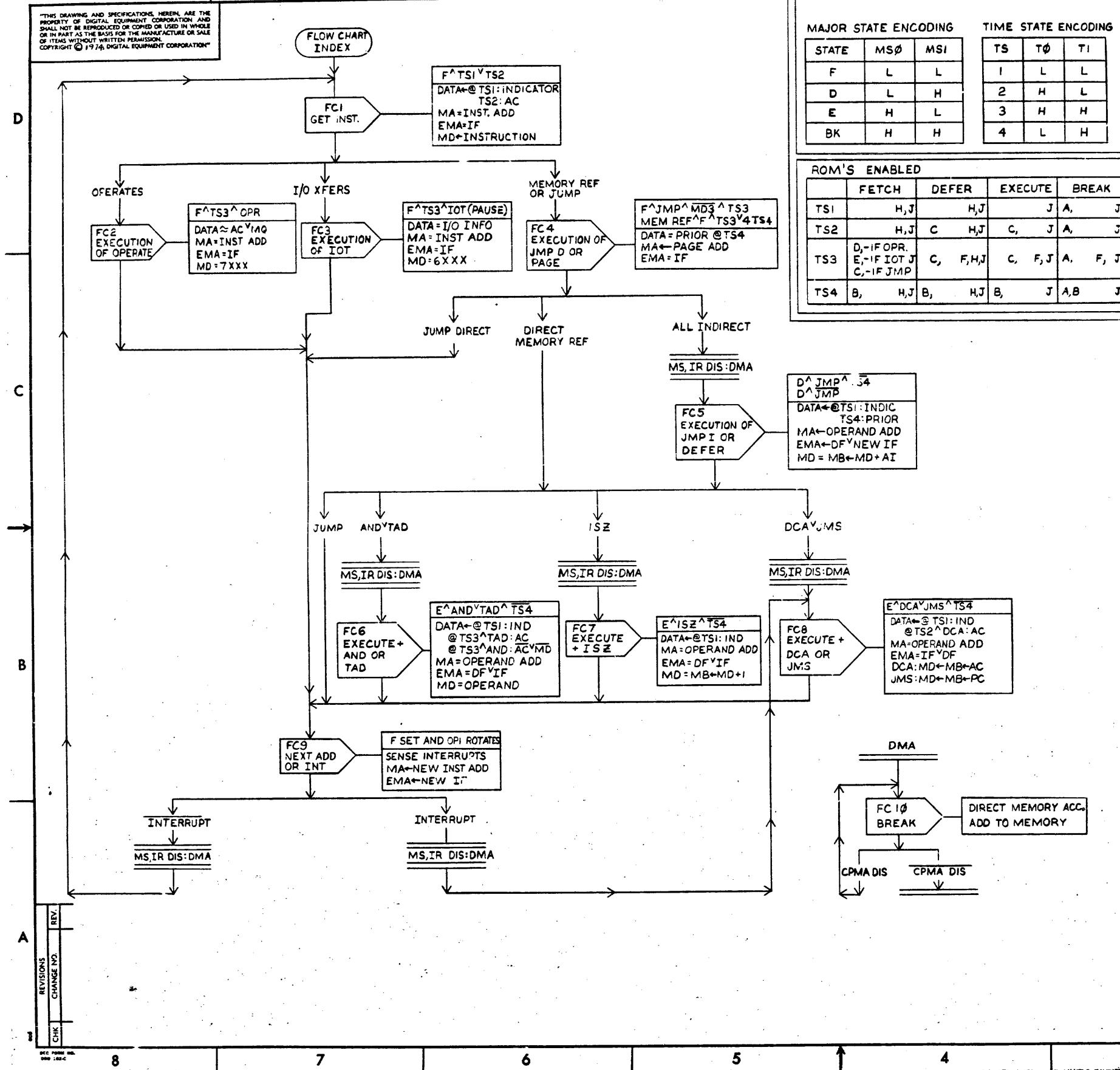
1 2 3 4 5 6 7 8 9 10

1 2 3 4 5 6 7 8 9 10

1 2 3 4 5 6 7 8 9 10</p

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8 7 6 5 4 3 2 1 DE M8315-0-16



NOTES:
THIS IS AN INDEX TO THE 8A FLOW CHARTS.
THE FLOW CHART NUMBER THAT APPEARS WITHIN
THE SYMBOL **FCX** REFERS TO ANOTHER
FLOW WHICH DETAILS THE ACTION WHICH IS BRIEFLY
DESCRIBED IN THE SYMBOL **□**

OPTION FLOW CHARTS WILL USE THE SAME **FCX**
TIME REFERENCE TO SHOW ITS RELATION TO
THE CPU

FLOWS WILL BE NUMBERED AS FOLLOWS
M8315-FCX CPU FLOW FOR TIME X
MABCD-FCX OPTION FLOW FOR CPU TIME X

THE FOLLOWING IS A LIST OF MAJOR OMNIBUS SIGNALS AND THE FLOW CHARTS MOST PERTINENT TO THEM

BUS SIGNAL	FLOW CHARTS	MOST IMPORTANT LOGIC PRINTS
IR ₀₋₂	FC1	ID2
F,D,E	(FC1,FC4),FC5,FC8	ID1
USER MODE	FC2,FC3	ID2,TG2
FSET	FC8	ID1
PULSE LA	FC10	ID1
STOP	FC2,FC10	ID1, ID3, TG2
KEY CONTROL	FC10	ID1
SW	SEE M8317 TIMING & FLOW CHARTS	
I/O PAUSE	FC3	TG2
C0-2	FC3	ID1
BUS STB	FC3	ID2,TG1
NOT LAST XFER	FC3	TG2
INT RQST	FC3	ID3
SKIP	FC7,FC8,FC9	ID2
INITIALIZE	FC3	ID3
CPMA DIS	FC4,FC5,FC9	ID1
MSIR DIS	FC10	ID1
LK LD & DATA	FC3	ID2, ID3
INDI-2	FC1	ID3
MAMS LD CTRL	FC4,FC9,FC10	ID1
OVERFLOW	FC7	ID2
BK DATA CTRL	FC10	ID1
LA ENABLE	FC1,FC10	ID1
INT IN PROG	FC9	ID2, ID3
RUN	FC2,FC10	TG2
PWR OK		ID3
MEM START	FC10	ID1, TG2

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP8A				
DIMENSIONAL TOLERANCE				
DIMENSIONS ARE MILLIMETERS UNLESS OTHERWISE SPECIFIED				
MM	INCHES	ANGLES	ENG.	DATE
X0X = ±0.10	.00X = ±.005	±9° 30'	PROJ ENG. Locality	11-11-74
XX = ±0.5	.00X = ±.02	X = ±.1	PROD.	11-11-74
X = ±2				
THIRD ANGLE PROJECTION REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY ✓ NEXT HIGHER ASSY.				
MATERIAL B-DD-KK8A-0				
FINISH / / DIST.				
SIZE CODE D F D M8315-0-16 REV. 1				
SHEET 1 OF 1				

digital

**FLOW DIAGRAM
M8315 INDEX**

REV. B
M8315-0-16

A

REV. B
M8315-0-16

D

REV. B
M8315-0-16

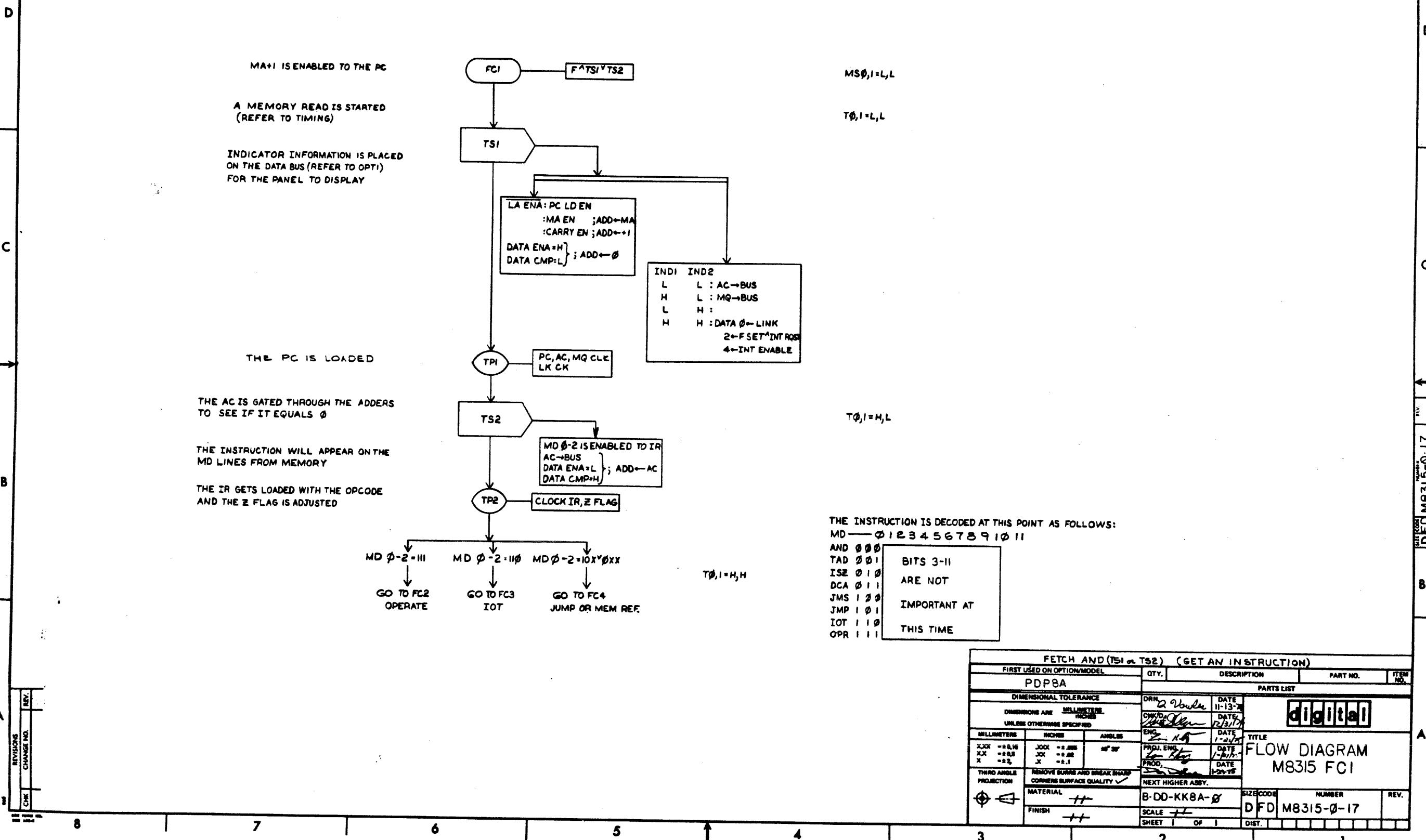
A

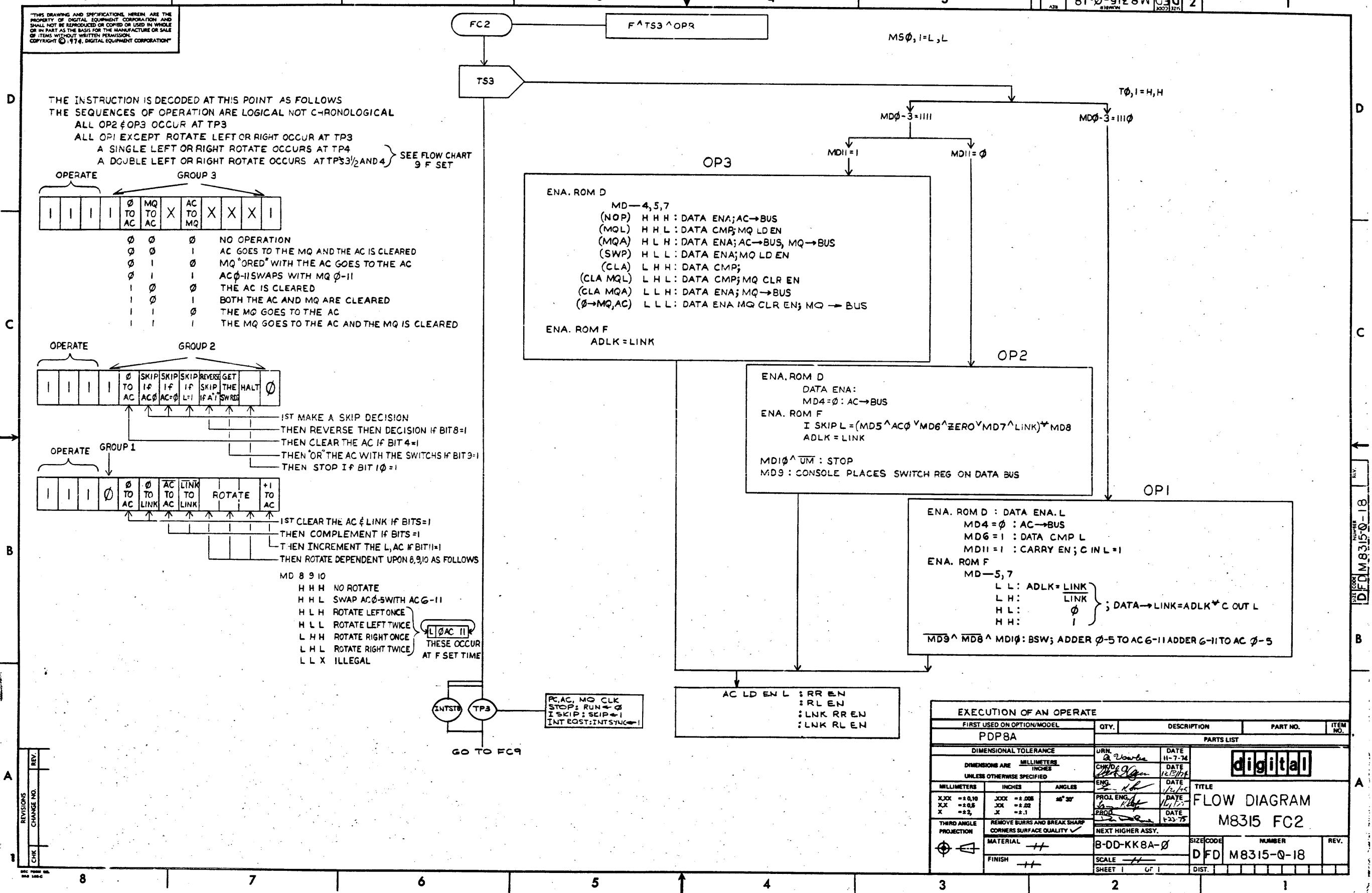
D

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7 6 5 4 3 2 1

DFD M8315-0-17





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THE INSTRUCTION AT THIS POINT IS DECODED AS FOLLOWS: ONLY IF "USER MODE IS NEGATED THUS ALLOWING PAUSE TO BE ASSERTED.

1 1 0 DEVICE CODE COMMAND

FOR DEVICE CODE 008 THE CPU TAKES CONTROL DEPENDING UPON THE COMMAND AS FOLLOWS:

MD-9 10 11
 SKON 0 0 0 SKIP IF INT ON, TURN IT OFF
 ION 0 0 1 TURN INT SYS ON
 IOF 0 1 0 TURN INT SYS OFF
 SRQ 0 1 1 SKIP IF INT RQST
 * GTF 1 0 0 LINK, INT ON, INT RQST TO AC0,2,4
 * RTF 1 0 1 AC0 TO LINK, TURN INT SYS ON
 NOP 1 1 0 NO OPERATION
 CAF 1 1 1 GENERATE INITIALIZE.

* ALSO SEE OPT 2

OTHER DEVICES SEND OR RECEIVE DATA DEPENDING UPON THE C LINES AS FOLLOWS:

AC-DEV H H H THE DEVICE RECEIVES THE AC AT TP3
 RELATIVE JUMP H H L THE DATA LINES + THE PC GO TO PC AT BUS STB
 INPUT OR TO AC H L H THE AC "C'DED" WITH DATA LINES GOES TO THE AC
 BUS STB
 ABSOLUTE JUMP H L L THE DATA LINES GO TO THE PC AT BUS STB
 AC-DEV 0=AC L H H THE DEVICE RECEIVES THE AC AT TP3
 AND THE AC IS CLEARED
 INPUT JUM TO AC L L H THE DATA LINES GO TO THE AC BUS STB
 NOTE ALL I/O XFRS TAKE PLACE OVER THE DATA LINES.

IN REALITY ALL XFRS TAKE PLACE ON THE LEADING EDGE OF BUS STB IN ACCORDANCE WITH THE "C" LINES AT THAT TIME.
 ASSERTING NOT LAST XFER CAUSES THE CPU TO WAIT FOR A BUS STROBE TO DO THE NEXT XFER.
 THE CPU WILL NOT ADVANCE TO TS4 UNTIL IT SEES A BUS STROBE WITH NOT LAST XFER NEGATED - THIS IN TURN CAUSES INTERRUPT STROBE.

LINK LOAD SHOULD BE GIVEN IN SYNC WITH BUS STROBE AND CAUSES LINK DATA TO GO TO THE LINK.

FC3 → F^TS3^MD0-2=110

MSB,1=L,L

TP3,I=H,H

TS3

PAUSE
PAUSE
DO NOTHING
MD0-8=0

EN A ROM J
MD-9,10,11

L L L : CAF-1; CAF
 L H L : LINK DATA=AC0,EN LNK LD, INTENA=1; RTF
 L H H : DATA 4=INTENA, DATA 2=INT RQST, DATA 0=LINK; GTF
 H L L : I SKIP=INT RQST; SRQ
 H L H : INT DLY=0; IOF
 H H L : INTENA=1; ION
 H H H : I SKIP=INT ENA, INT DLY=0; SKON

EN A ROM E

C2,C1,C0
 L L X: DATA EN, PC LDEN;
 L H X: DATA EN, PC LDEN, SEL C;
 H L L: DATA EN, AC LDEN;
 H L H: DATA EN, AC LDEN, AC-BUS;
 H H L: AC LDEN, AC-BUS;
 H H H: DATA EN, AC LDEN, AC-BUS;
 GTF^C0-2=0: DATA EN, AC LDEN;
 ENABLE LINK-DATA → LINK
 ENABLE SKIP → I SKIP

TP3

PAUSE
PAUSE

LAST XFER
(GEN BY TP3)
INT STROBE

IF GEN BY DEVICE
LINK LOAD
E/S XFR

LAST XFER

IF GEN BY DEVICE
LINK LOAD
BUS STROBE

LAST XFER
PC,AC,MQ CLK
I SKIP: SKIP=1

EN A ROM E
C2,C1,C0
 L L X: DATA EN, PC LDEN;
 L H X: DATA EN, PC LDEN, SEL C;
 H L L: DATA EN, AC LDEN;
 H L H: DATA EN, AC LDEN, AC-BUS;
 H H L: AC LDEN, AC-BUS;
 H H H: DATA EN, AC LDEN, AC-BUS;
 GTF^C0-2=0: DATA EN, AC LDEN;
 ENABLE LINK-DATA → DATA
 ENABLE SKIP → I SKIP

GO TO FC4

LINK LOAD
E/S XFR
(GEN BY BUS STROBE) 0=PAUSE
INT RQST, INT SAV=1

INT STROBE
(GEN BY BUS STROBE)

FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION		PART NO.	ITEM NO.		
PDP8A								
DIMENSIONAL TOLERANCE								
DIMENSIONS ARE	MILLIMETERS	INCHES	ANGLES	DRW. S. Charter	DATE 1/6/74			
UNLESS OTHERWISE SPECIFIED								
MM	INCHES			CDW/DK O. Elgin	DATE 1/23/74			
X.XX	=0.10	.0039	XX	=0.08	ENG. 1/1/74			
XX	=0.05	.0020	X	=0.04	PROD. 1/1/74			
X	=0.02	.0008		X = 0.1	TEST. 1/23/74			
THIRD ANGLE PROJECTION								
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY ✓								
NEXT HIGHER ASSY.								
MATERIAL	++	B-DD-KK8A-0	FINISH	++	SCALE ++	SHEET 1 OF 1		
						REV. DFD M8315-0-19		

digital

FLOW DIAGRAM
M8315 FC3

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IF A JMP; THE PAGE IS
ENABLED TO THE PC

THE PC IS LOADED

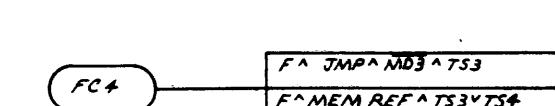
IF JMP DIRECT; GO TO F SET TIME

IF MEM REF; ENABLE PAGE TO MA

LOAD MAS
TEST BREAK SYSTEM.

MS, IR DIS:DMA; GOTO FC 10

D=1 : GO TO FC5; DEFER
E=1 : IR=00X: GO TO FC6; AND TAD DIRECT
IR=010: GO TO FC7; JSZ DIRECT
IR=011: GO TO FC8; DCA DIRECT
IR=100 : GO TO FC8; JMS DIRECT

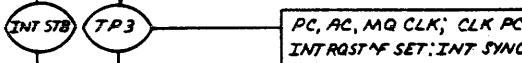
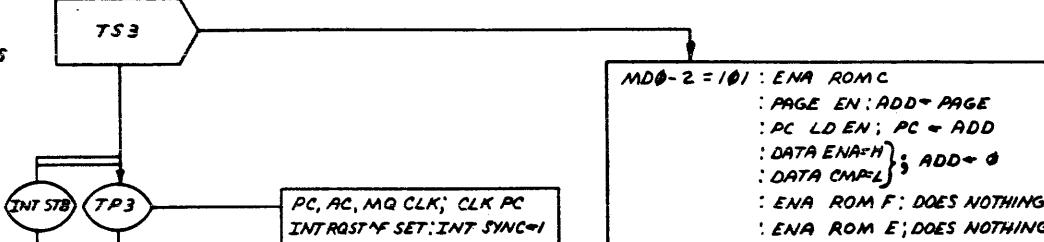


MS0,I = L,L

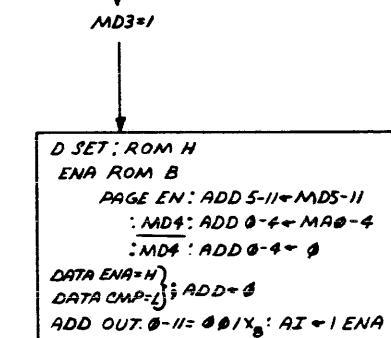
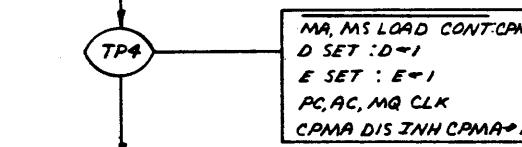
T0, I = H,H

THE INSTRUCTION IS DECODED AT THIS POINT
AS FOLLOWS:

MD- 01234567891011
=IR LOC 0-177
0= PAGE 0
1= CURRENT PAGE
0=DIRECTLY
1=INDIRECTLY
FOR AND, TAD, JSZ
DCA, JMS OR JMP

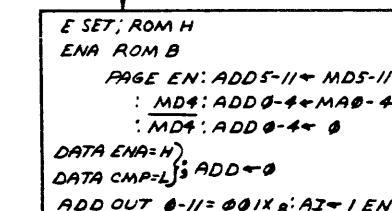


GOTO FC9; JMP DIR.



T0, I = L,H

IR=101 ^ MD3=0



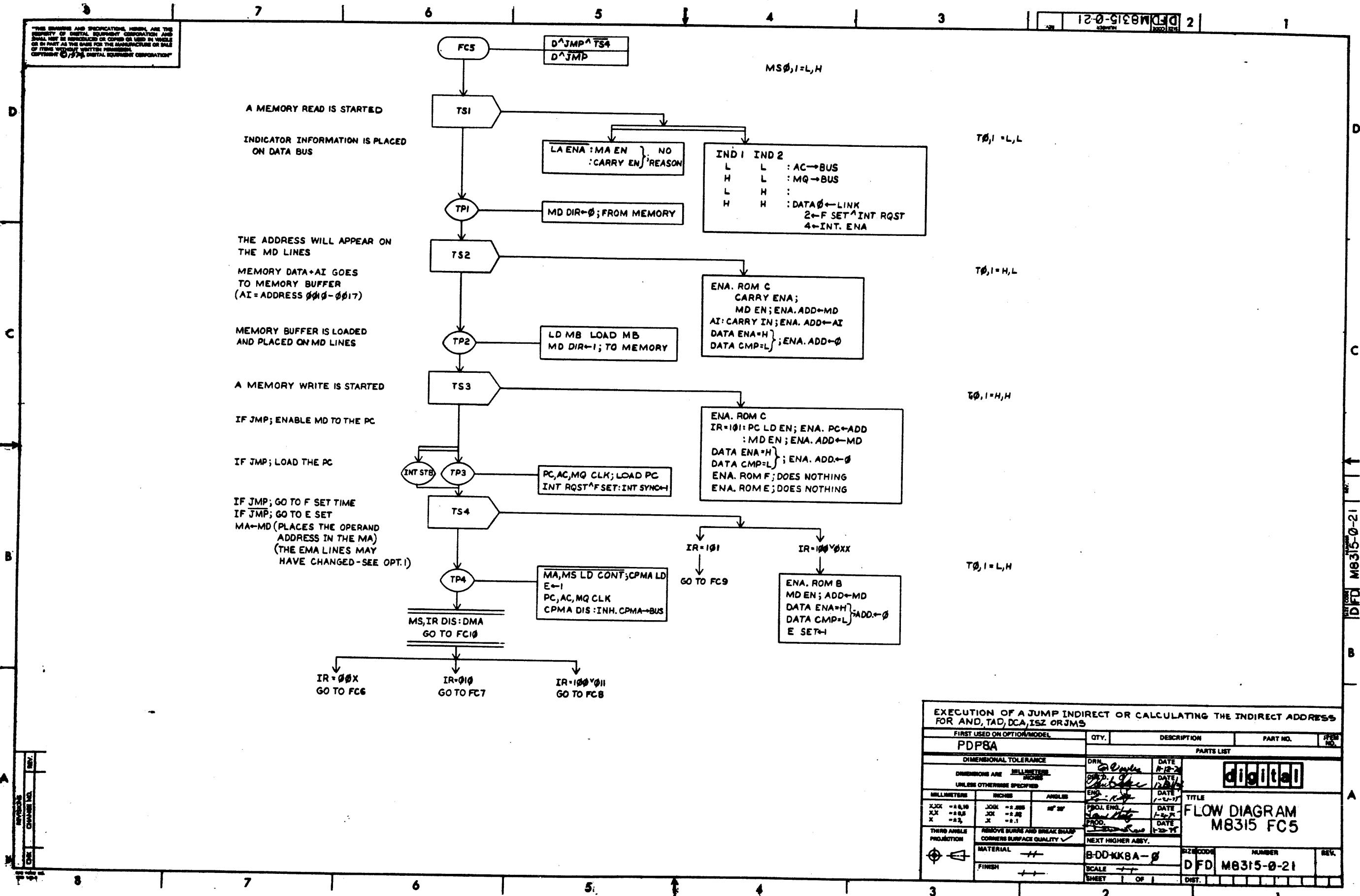
EXECUTION OF A JMP DIRECT OR THE PAGE OPERATION FOR A MEMORY
REFERENCE INSTRUCTION

FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP8A					
PARTS LIST					
DIMENSIONAL TOLERANCE					
DIMENSIONS ARE MILLIMETERS INCHES UNLESS OTHERWISE SPECIFIED					
MILLIMETERS	INCHES	ANGLES	ENG.	DATE	
X0X ± 0.10	.004 ± .005	45° 20'	PRINT. ENG.	1/2/79	
XJX ± 0.05	.002 ± .002	JX ± .1	PRINT. ENG.	1/2/79	
X ± .2	.008 ± .008		PRINT. ENG.	1/2/79	
THIRD ANGLE PROJECTION REMOVE SURFS AND BREAK SHAPES CORNERS SURFACE QUALITY ✓					
NEXT HIGHER ASSY.					
MATERIAL		B-00-KK8A-0	SIZE CODE	NUMBER	REV.
FINISH		SCALE	OF	DIST.	
REV.	CHG. NO.	DATE	DATE	DATE	
A					
1					

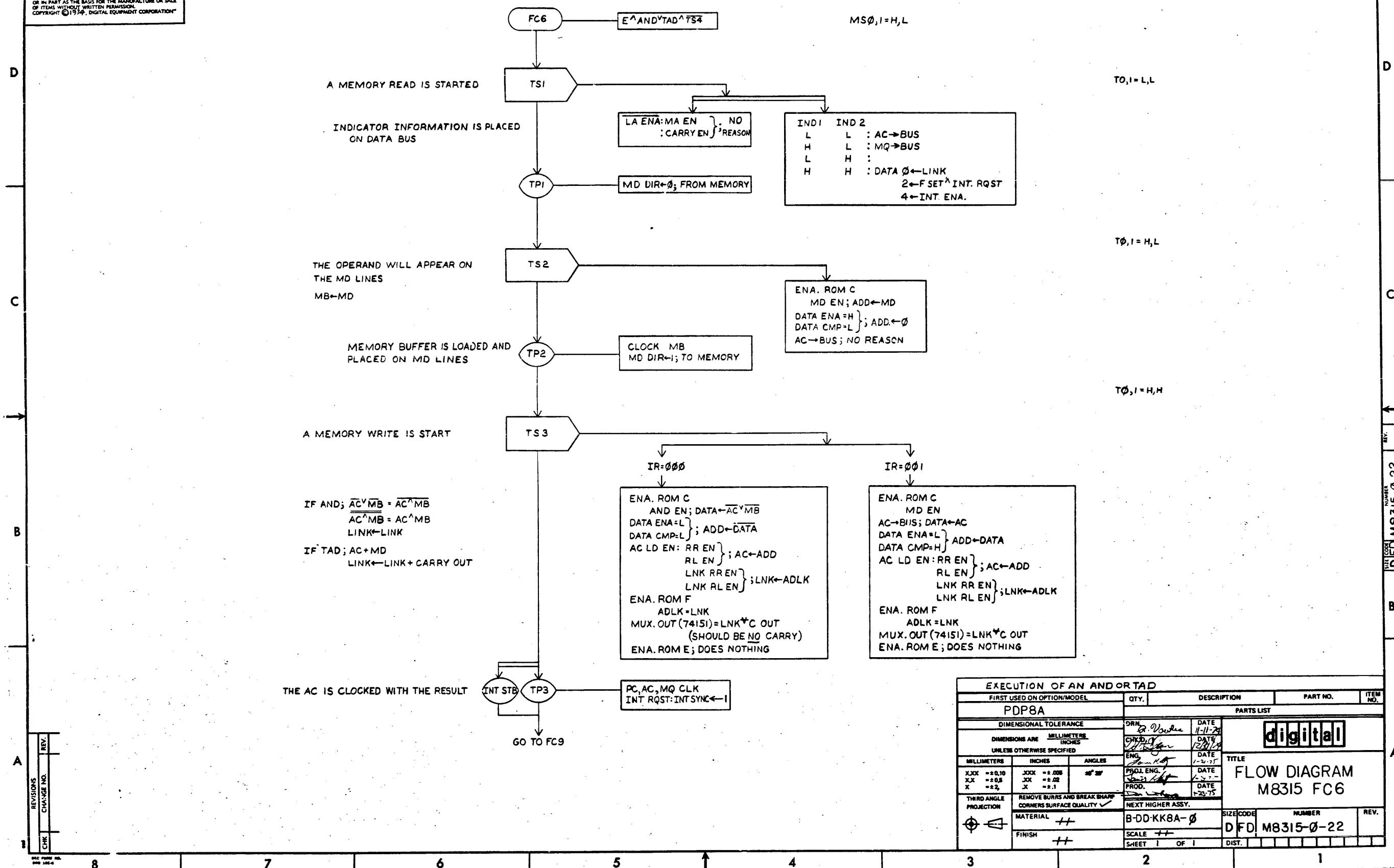
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FLOW DIAGRAM
M8315 FC4

D FD M8315-0-20



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8 7 6 5 4 3 2 1

DFD M8315-0-23

D

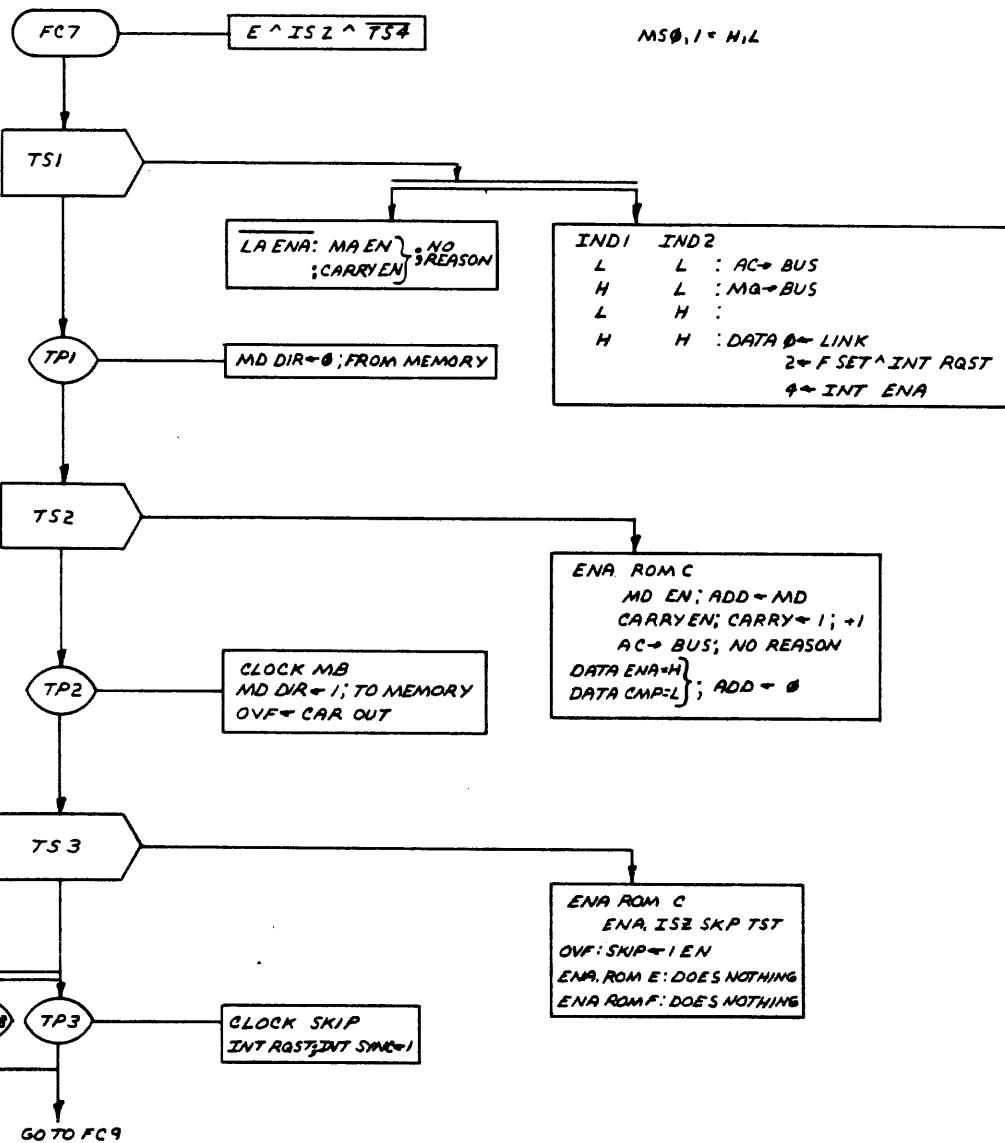
A MEMORY READ IS STARTED

INDICATOR INFORMATION IS PLACED
ON DATA BUS

THE OPERAND WILL APPEAR ON THE MD LINES
 $MB = MD + 1$

THE INCREMENTED MD IS
SAVED IN THE MB AND PLACED
ON THE MD LINES; THE CARRY IS SAVED.

SET SKIP=OVER FLOW



EXECUTION OF AN ISZ		PARTS LIST			
FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP8A					
DIMENSIONAL TOLERANCE		DRN.	DATE		
DIMENSION AND UNLESS OTHERWISE SPECIFIED		CMC.D.	DATE		
MILLIMETERS INCHES ANGLES		ENG.	DATE		
X0X = 0.10	XX = +.005	40° 30'	PROJ. ENG.		
XX = 0.05	XX = +.002		PROJ.		
X = 2.1	X = -.1		PROD.		
THIRD ANGLE PROJECTION		REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY ✓	NEXT HIGHER ASSY.		
MATERIAL		B-00-KK8A-0		SIZE CODE	NUMBER
FINISH		SCALE		REV.	
SHEET 1 OF 1		SHEET		DIST.	

digital

FLOW DIAGRAM
M8315 FC7

REVISIONS
1
REV.
CKH
VAN CHAMBERS
DATE

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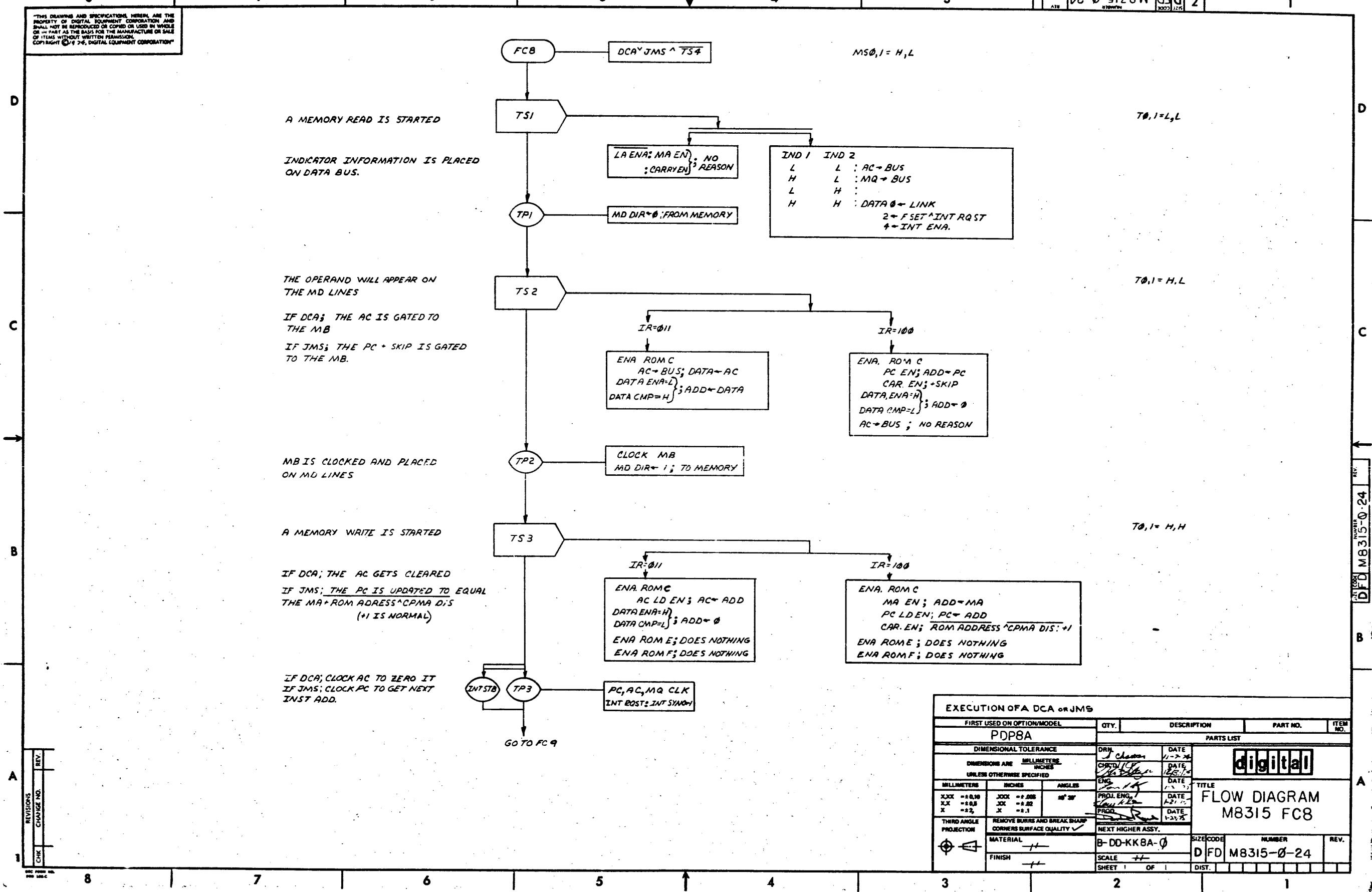
DFD M8315-0-23

A

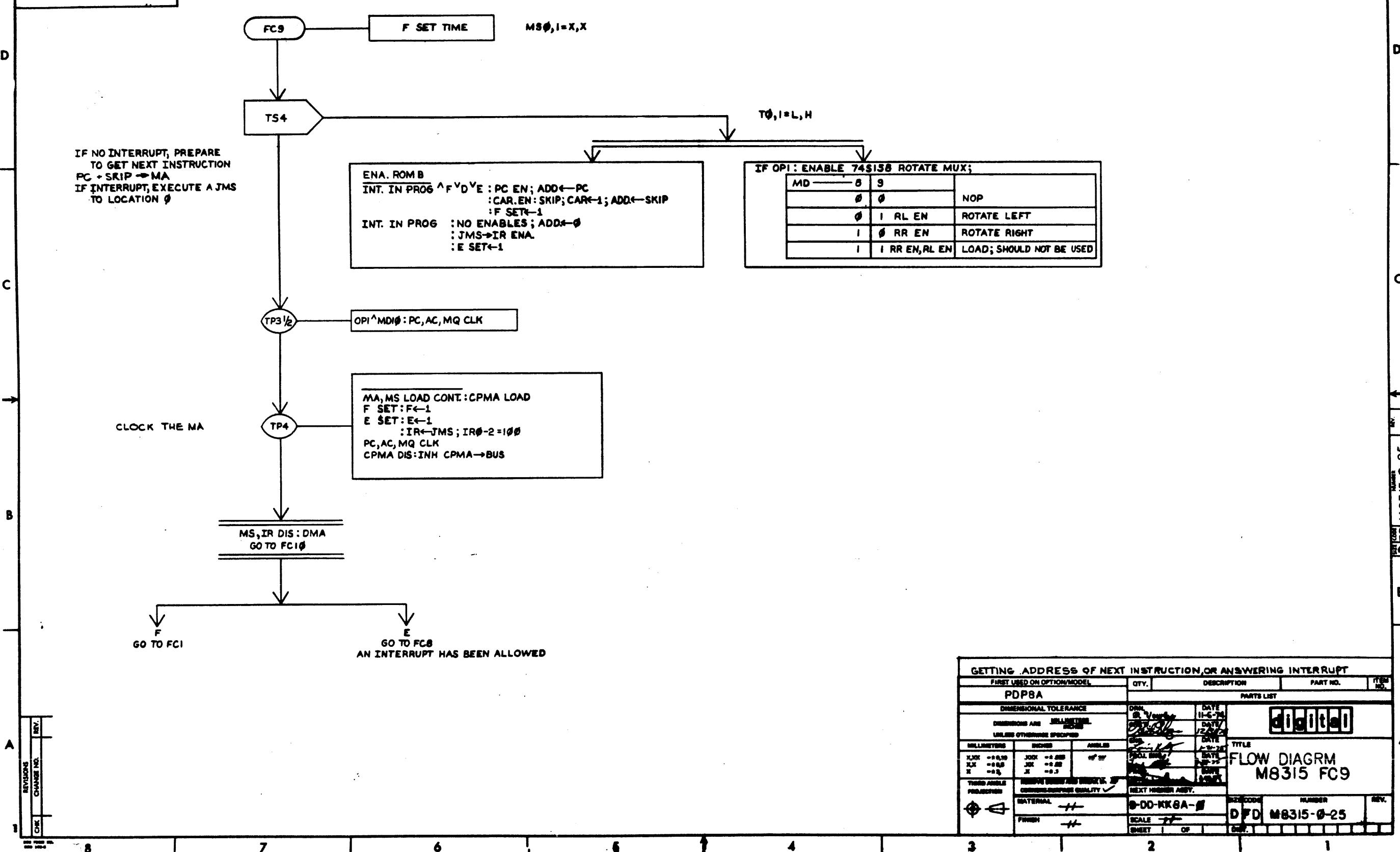
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GETTING ADDRESS OF NEXT INSTRUCTION OR ANSWERING INTERRUPT			
FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.
PDP8A			ITEM NO.
PARTS LIST			
DIMENSIONAL TOLERANCE			
DIMENSIONS ARE IN MILLIMETERS UNLESS OTHERWISE SPECIFIED			
MILLIMETERS	INCHES	ANGLES	
X0X = ±0.10	X0X = ±0.005	±5°	DATE 11-6-78
X ₁ = ±0.05	X ₁ = ±0.02	X ₂ = ±0.5	DATE 12-4-78
THIRD ANGLE PROJECTION			
PRINTED SHEET AND DRAWING COMMERCIAL QUALITY ✓			
NEXT HIGHER ASST.			
MATERIAL ✓			
FINISH ✓			
SCALE ✓			
SHEET 1 OF 1			

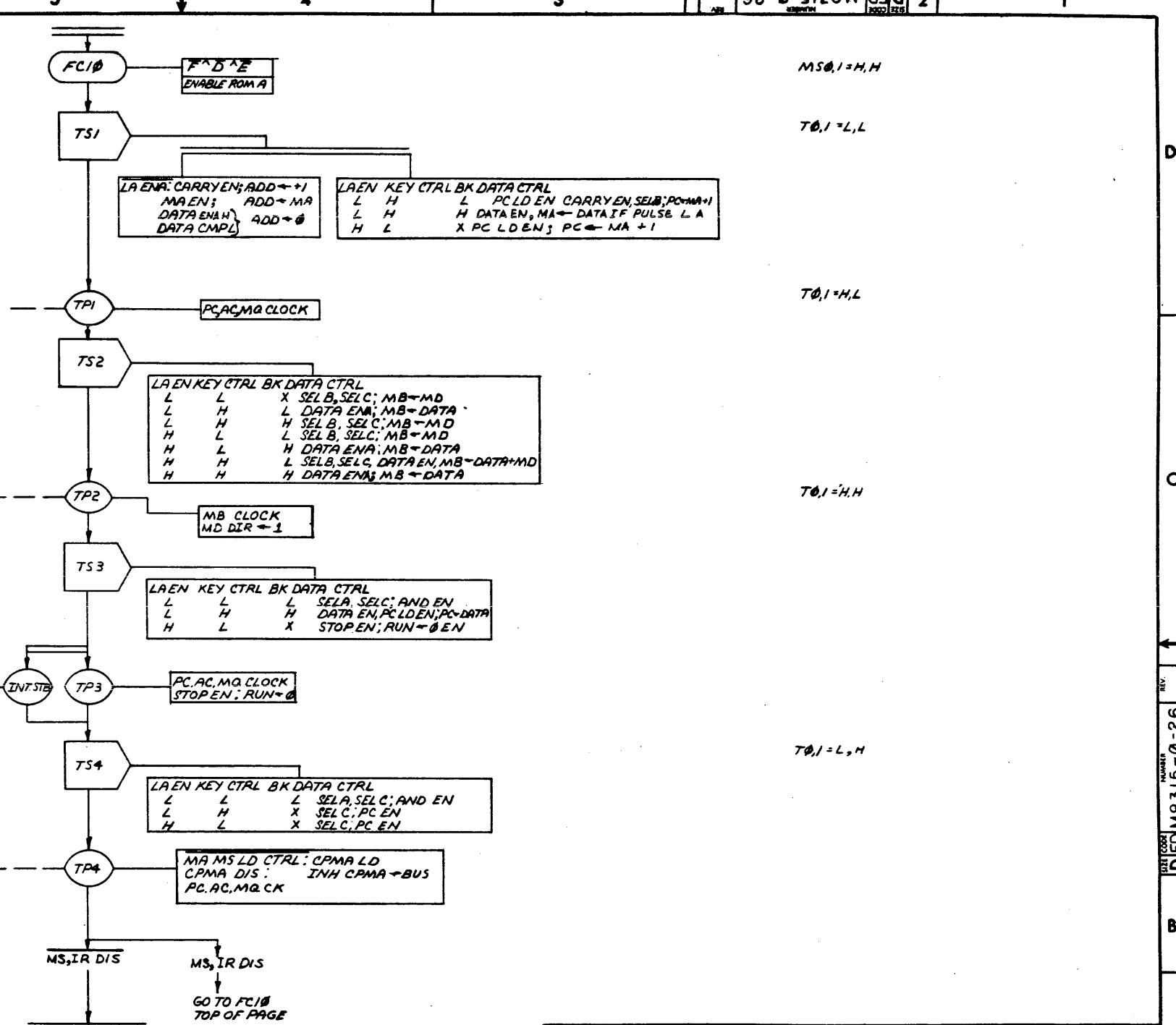
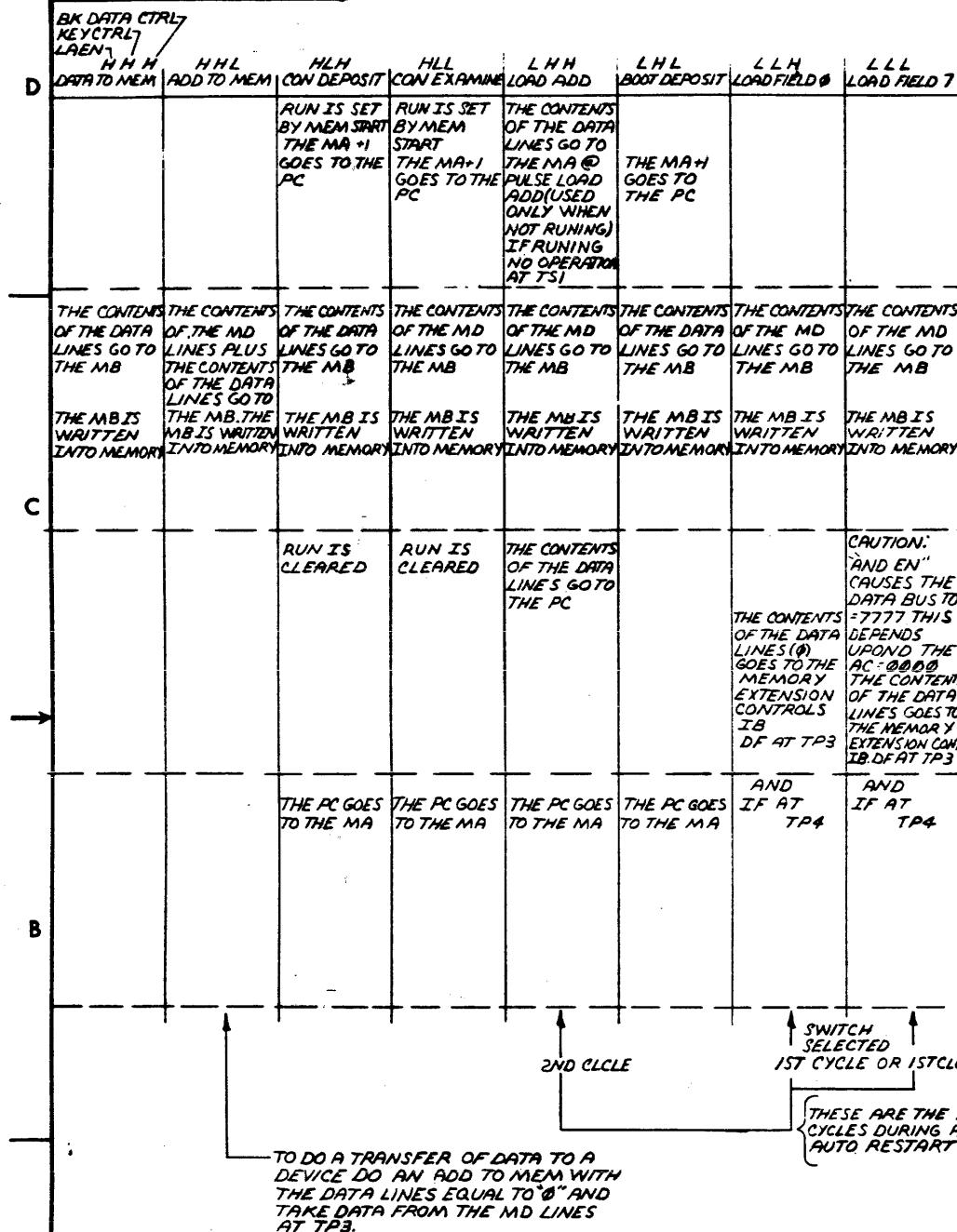
digitel

**FLOW DIAGRM
M8315 FC9**

1

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THE BREAK CONTROL WORD(LA,EN KEYCTRL,BK DATA CTRL)
IS NORMALLY SET UP AT THE BEGINNING OF THE DMA CYCLE
AND REMAINS STABLE FOR THE ENTIRE CYCLE.
IT DEFINES OPERATIONS AS FOLLOWS.



DATA BREAK/CONSOLE OPERATIONS/AUTO RESTART			
FIRST USED ON OPTION/MODEL	GTY.	DESCRIPTION	PART NO.
PDP8A			ITEM NO.
PARTS LIST			
DIMENSIONAL TOLERANCE DRN. 1/1-7-74			
DIMENSIONS ARE MILLIMETERS INCHES DATE 1/26/74			
UNLESS OTHERWISE SPECIFIED			
MILLIMETERS	INCHES	ANGLES	ENG. 1/2-7-74
X0X = ±0.10	X0X = .008	45° 30'	PROJ. ENG. 1/2-7-74
X1X = ±0.5	X1X = .020	X = ±.1	PROJ. 1/2-7-74
X = ±2			DATE 1/2-7-74
THIRD ANGLE PROJECTION REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY ✓ NEXT HIGHER ASSY.			
MATERIAL B DD KK 8A-0			
FINISH SCALE 1/1 SHEET 1 OF 1 DIST. 1			

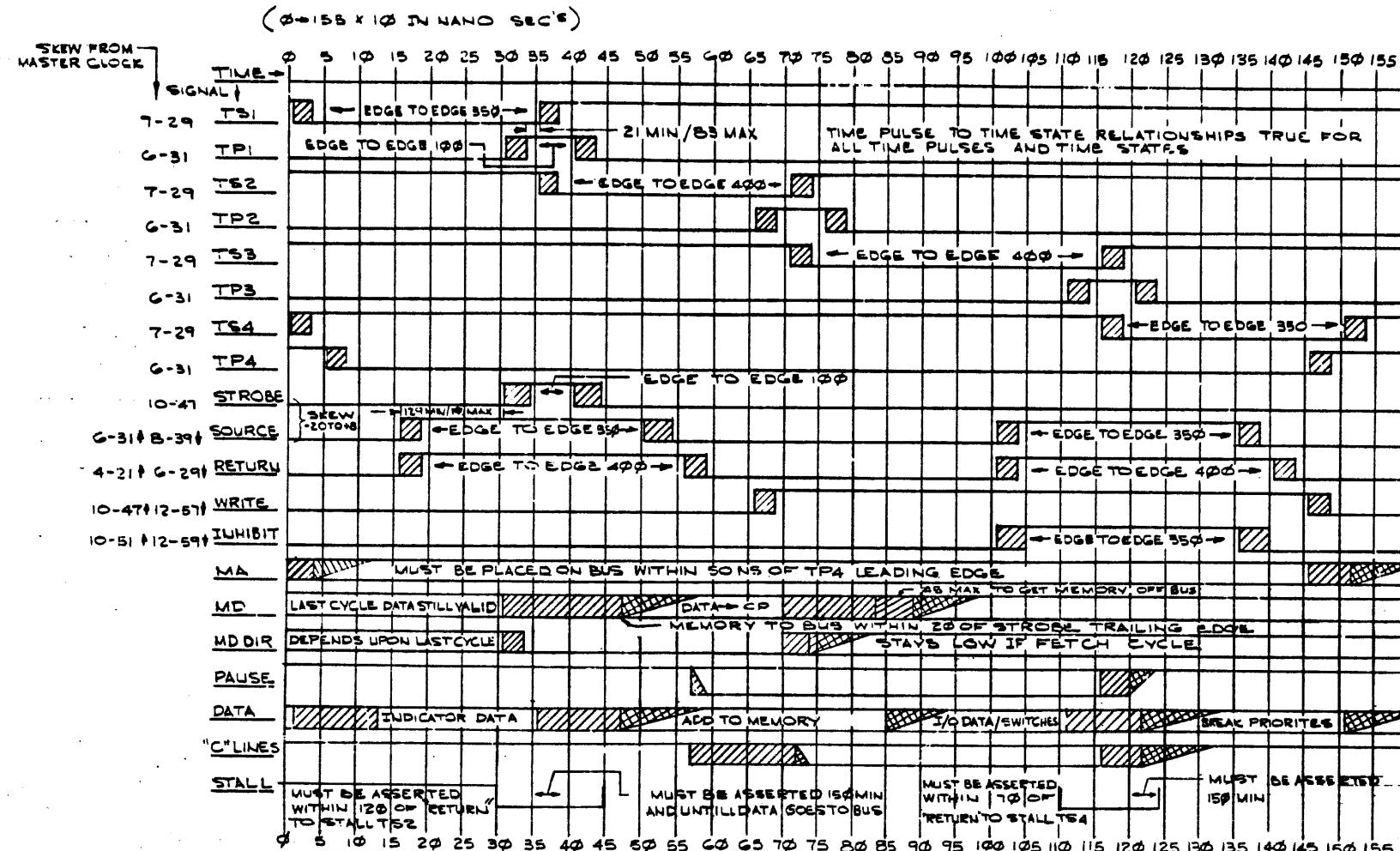
digital

FLOW DIAGRAM
M8315 FC10

SIZE CODE NUMBER REV. DFD M8315-0-26

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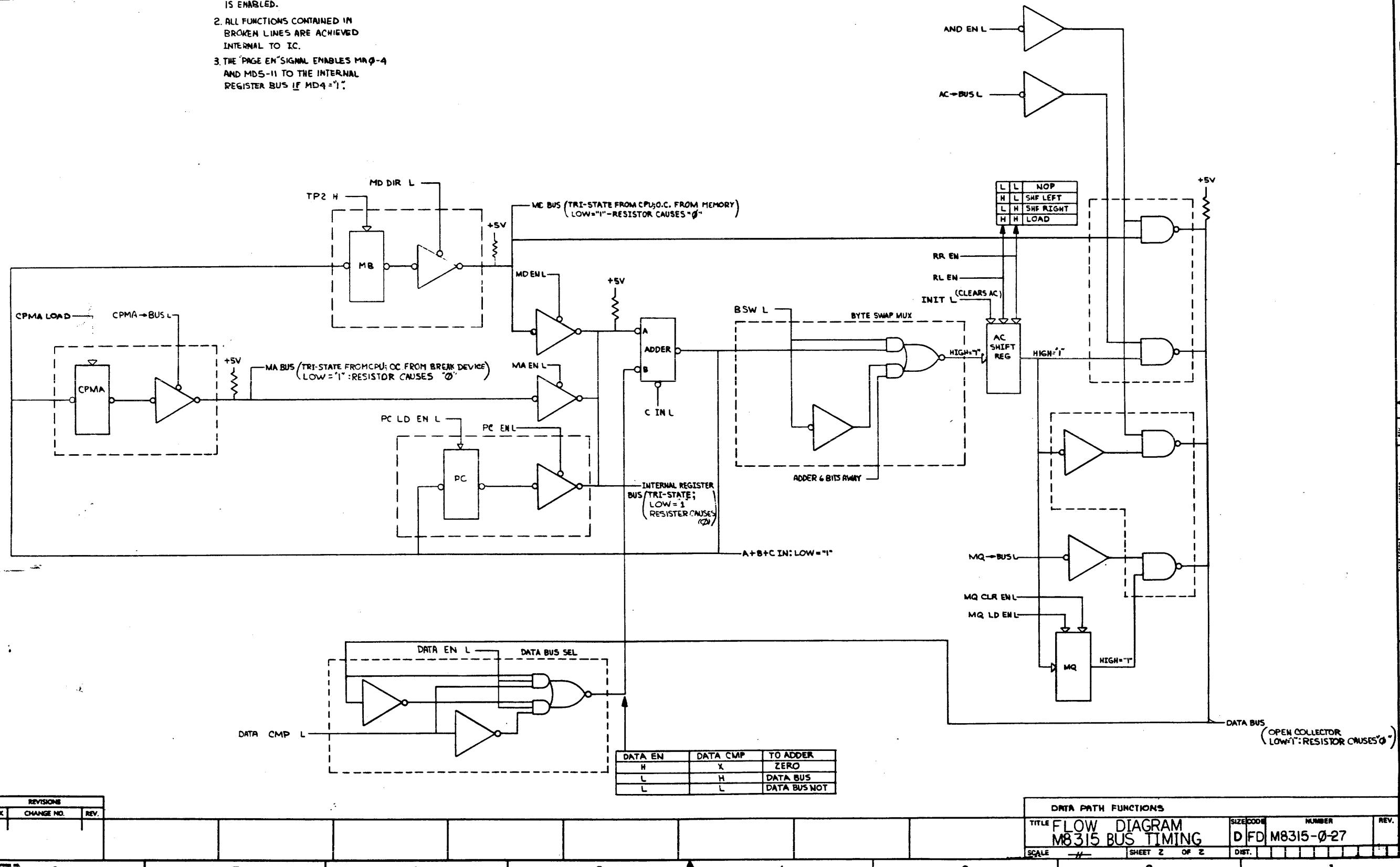


8A DATA PATH FUNCTION & TIMING			
FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.
PDPBA			ITEM NO.
DIMENSIONAL TOLERANCE		DBW7 Charge Spec'd CHN# 100-1000 DATE 10/5/74	PARTS LIST
DIMENSIONS ARE <u>MILLIMETERS</u> <u>INCHES</u> UNLESS OTHERWISE SPECIFIED		ENG. 100-1000 DATE 10/5/74	digital
MILLIMETERS	INCHES	ANGLES	TITLE
XXX = ±0.10 XX = ±0.05 X = ±2.	J00X = ±.005 J0X = ±.002 X = ±.1	20° 30°	FLOW DIAGRAM M8315 BUS TIMING
THIRD ANGLE PROJECTION		PROJ. ENG. DATE 10/5/74 PROD. DATE 10/23/75	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY ✓		NEXT HIGHER ASSY.	
MATERIAL		B-DD-KK8A-3	SIZE CODE NUMBER REV.
FINISH		SCALE	D FD M8315-0-27
		SHEET OF 2	DIST.

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NOTES:

1. THE PC, AC AND MQ ARE LOADED BY PC, AC, MQ CLK IF THE LOAD IS ENABLED.
2. ALL FUNCTIONS CONTAINED IN BROKEN LINES ARE ACHIEVED INTERNAL TO IC.
3. THE 'PAGE EN' SIGNAL ENABLES MQ4-4 AND MDS-II TO THE INTERNAL REGISTER BUS IF MD4 = '1'.



REVISIONS		
CHK	CHANGE NO.	REV.

8

7

6

5

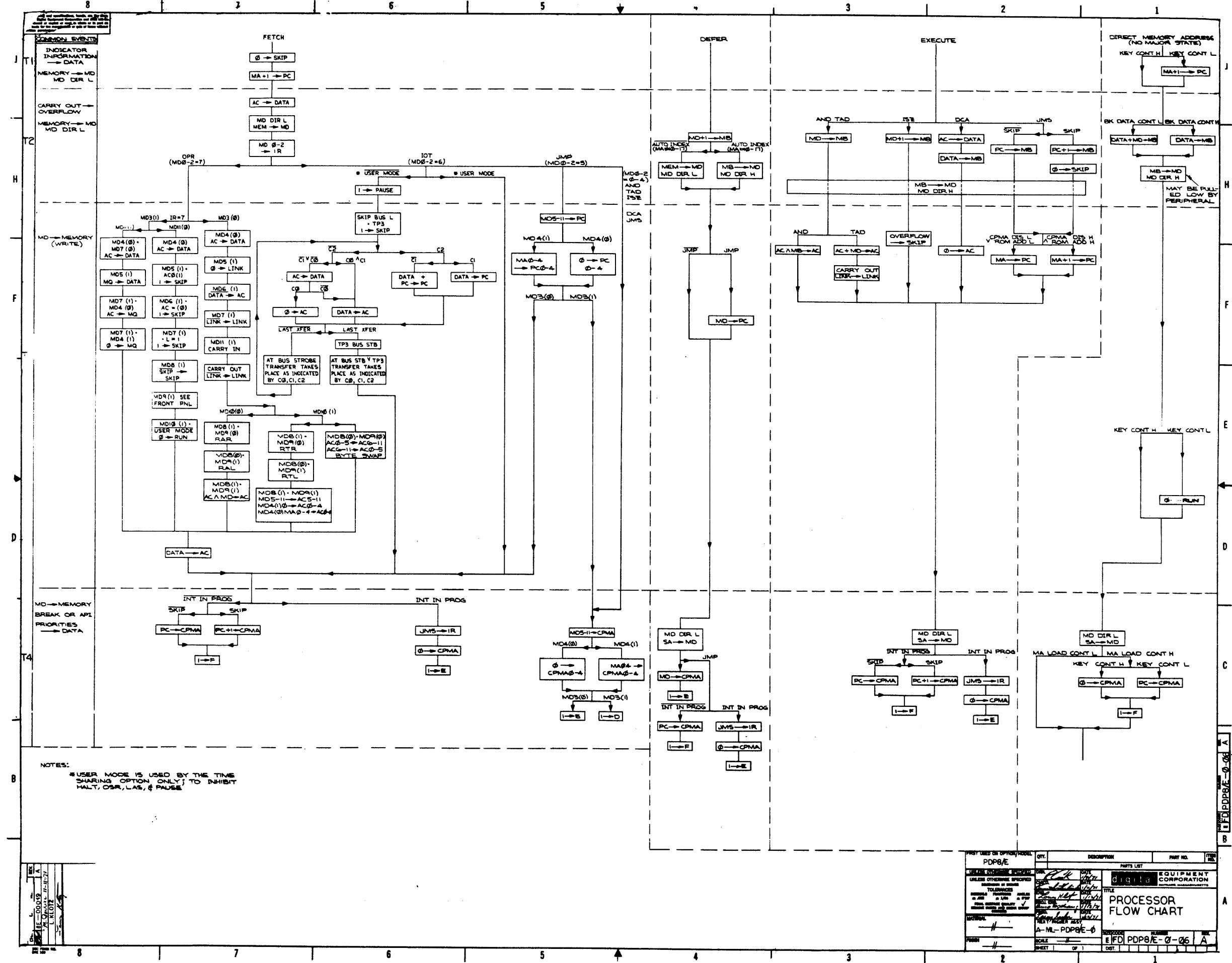
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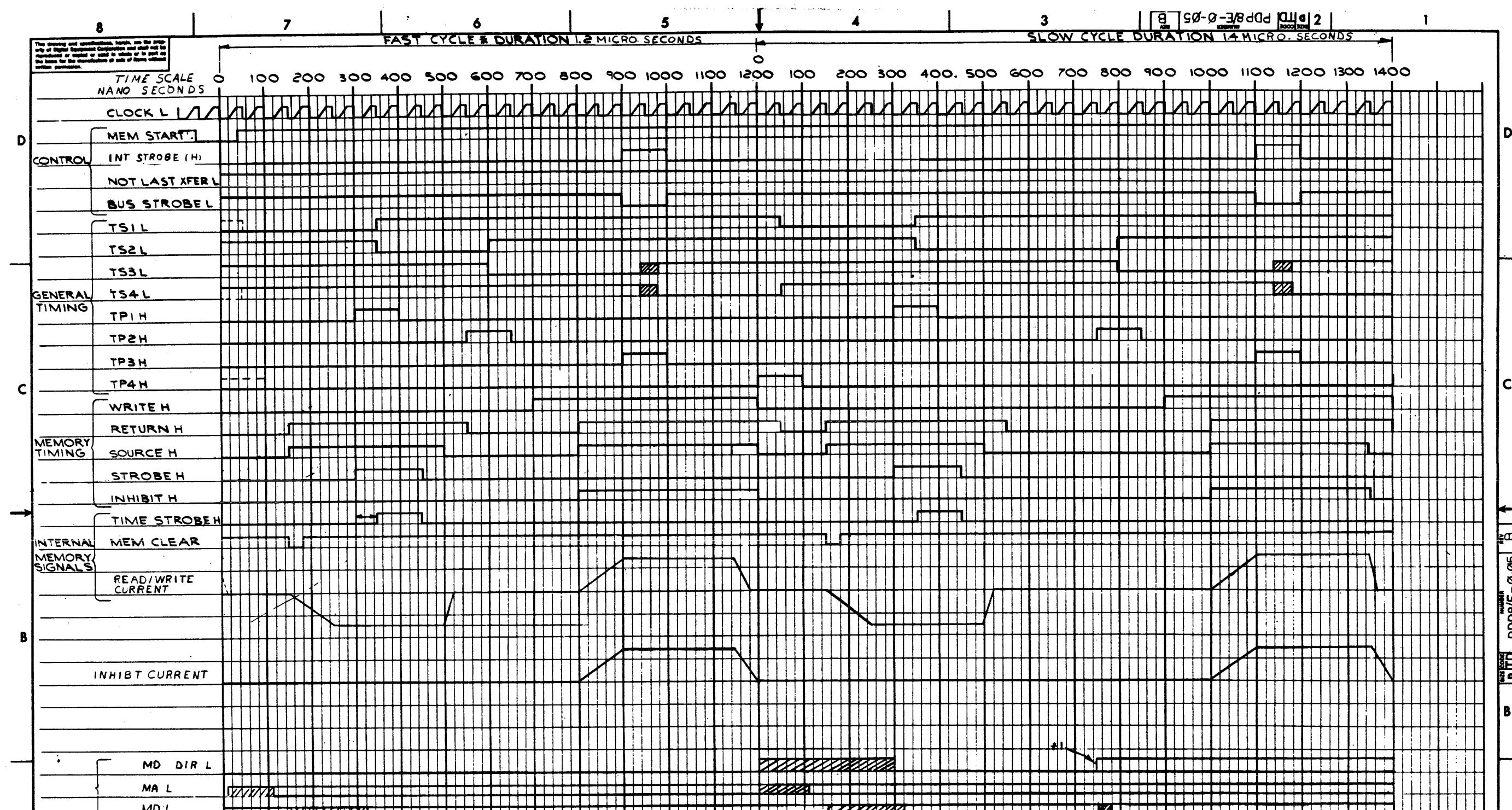
3

2

1

DATA PATH FUNCTIONS		
TITLE FLOW DIAGRAM M8315 BUS TIMING		
SCALE	SHEET 2 OF 2	REV.
D	FD	M8315-0-27



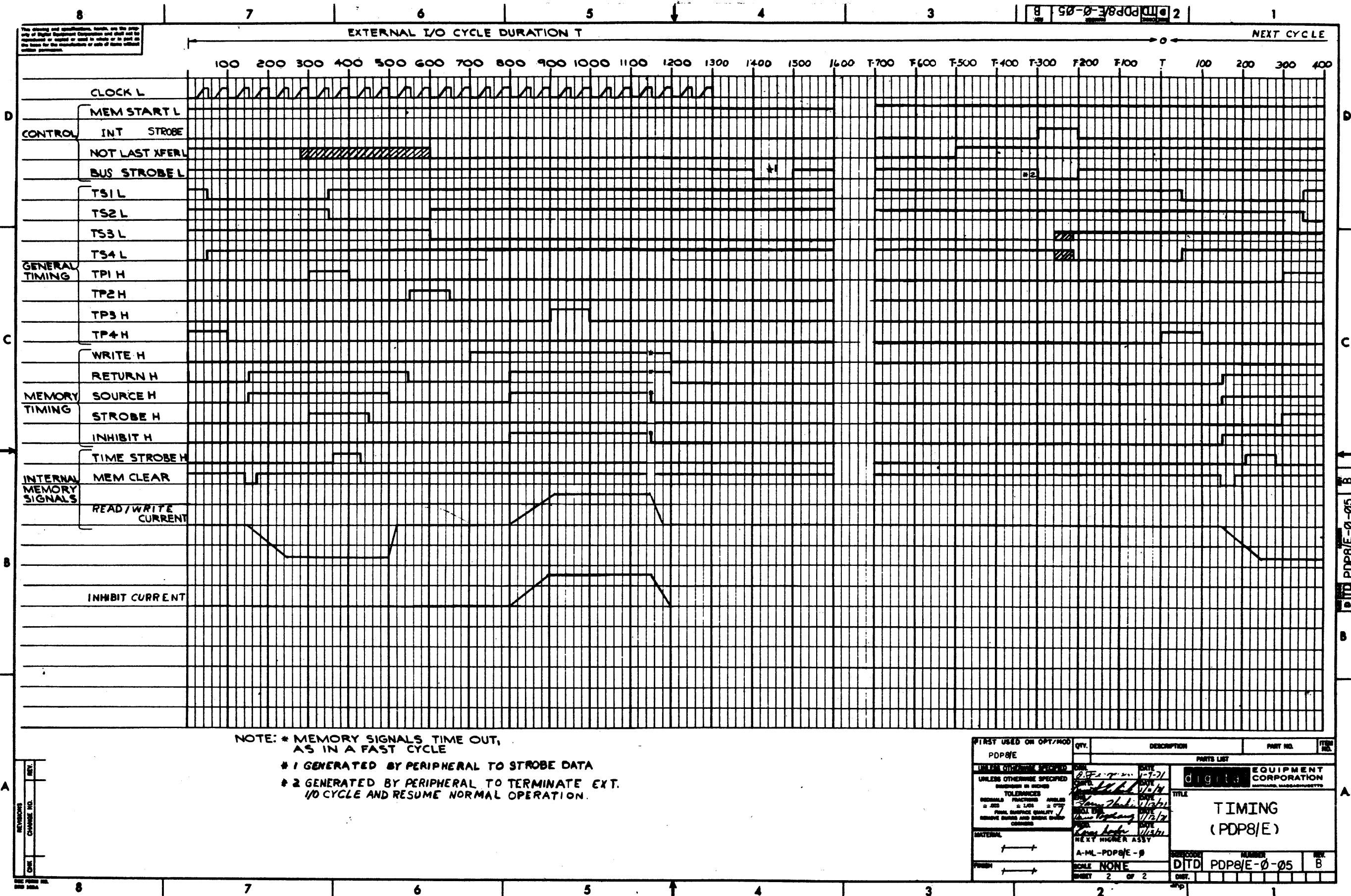


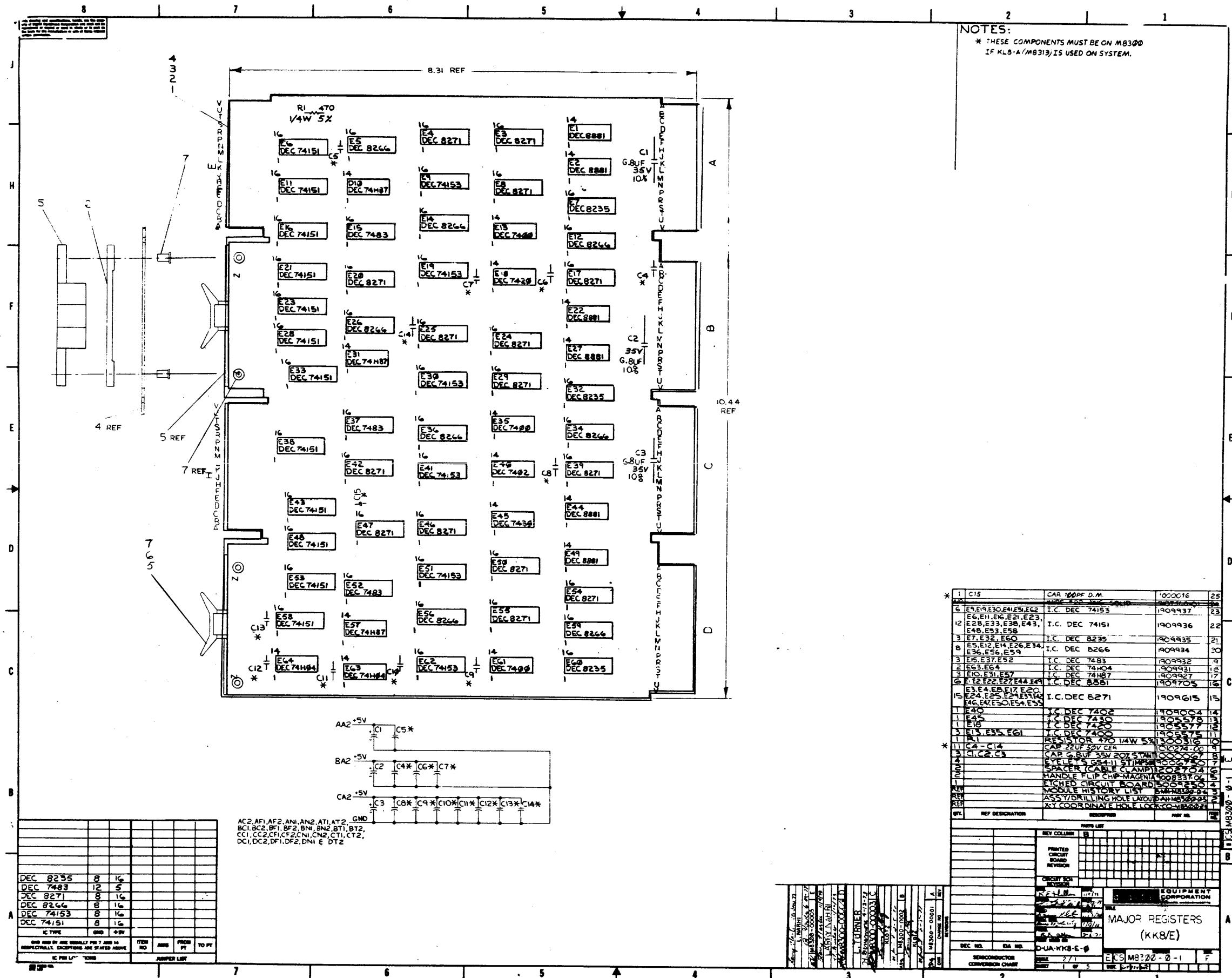
*THIS PLOT SHOWS AN INITIAL FAST CYCLE
THE DOTTED LINES INDICATE A REGULAR CYCLE
#1-MD DIR GOES LOW ONLY IF F+ [D-AUTO INDEX]

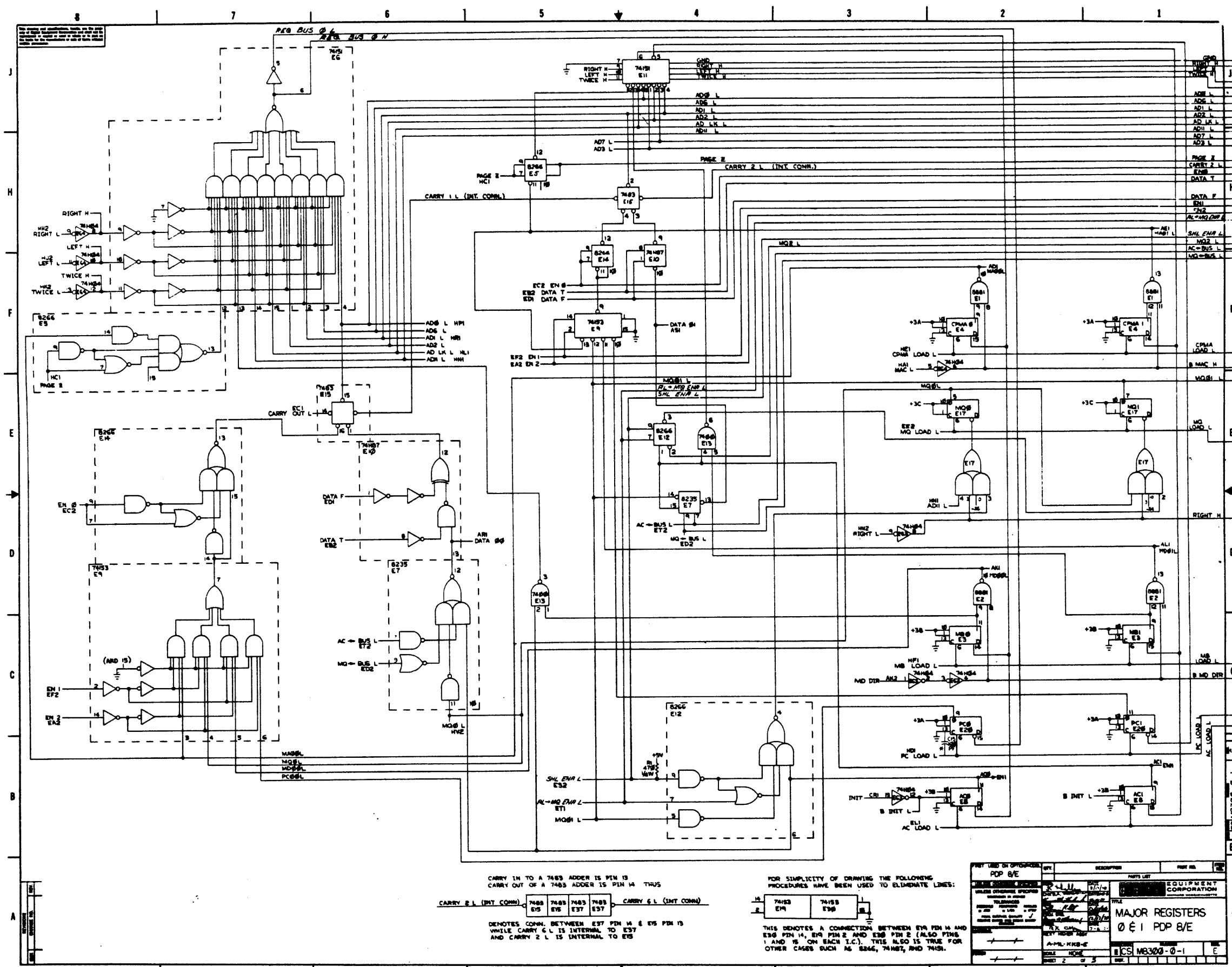
CIRCUIT DELAYS ARE NEGLECTED IN
THIS TIMING DIAGRAM

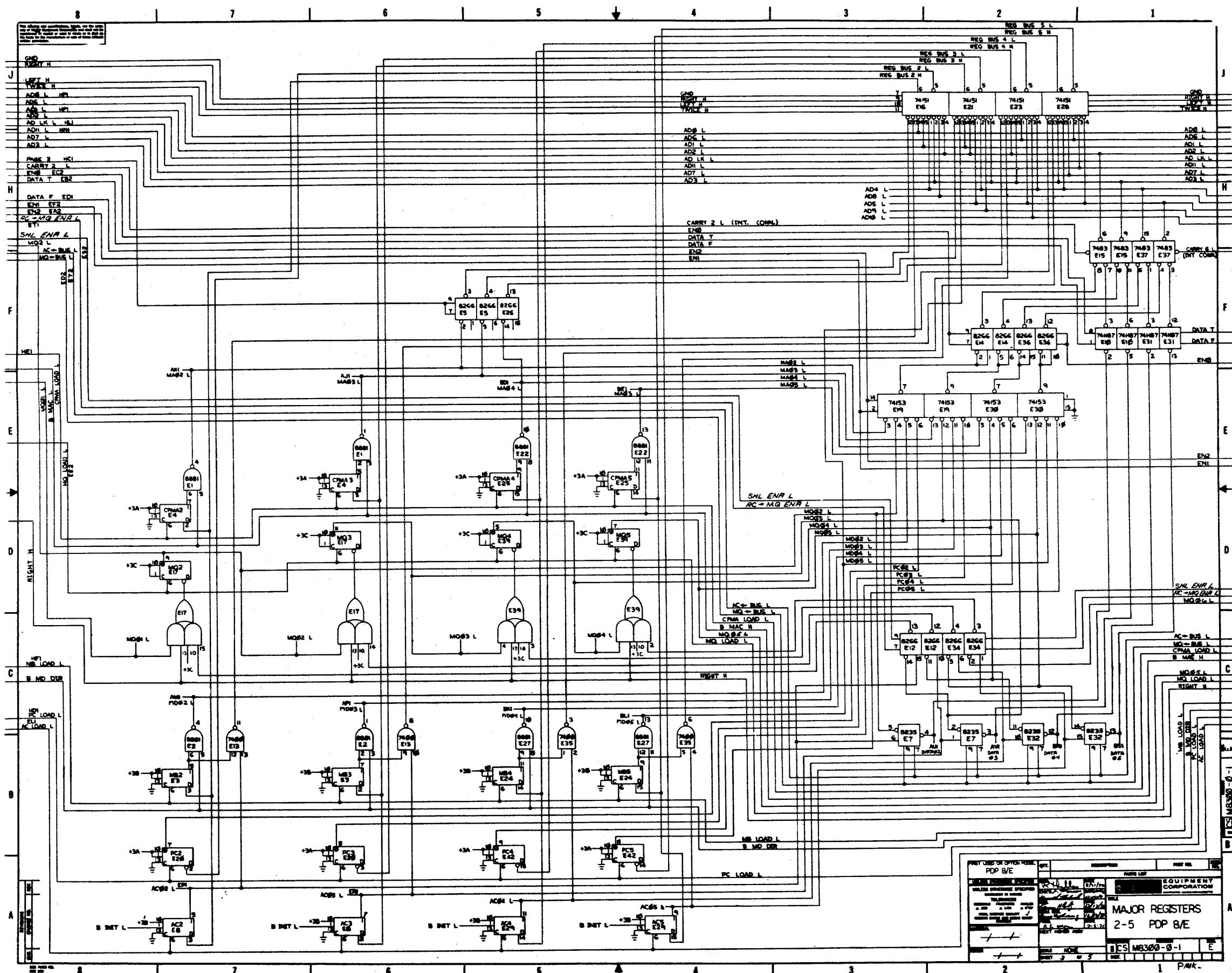
REVISIONS		CHANGE NO.	REV.
C-1	6 E - 01012	A	
MARCH 1			
T BE-00049 B			
M. GOWAN 11-10-71			
L.KLOTZ			

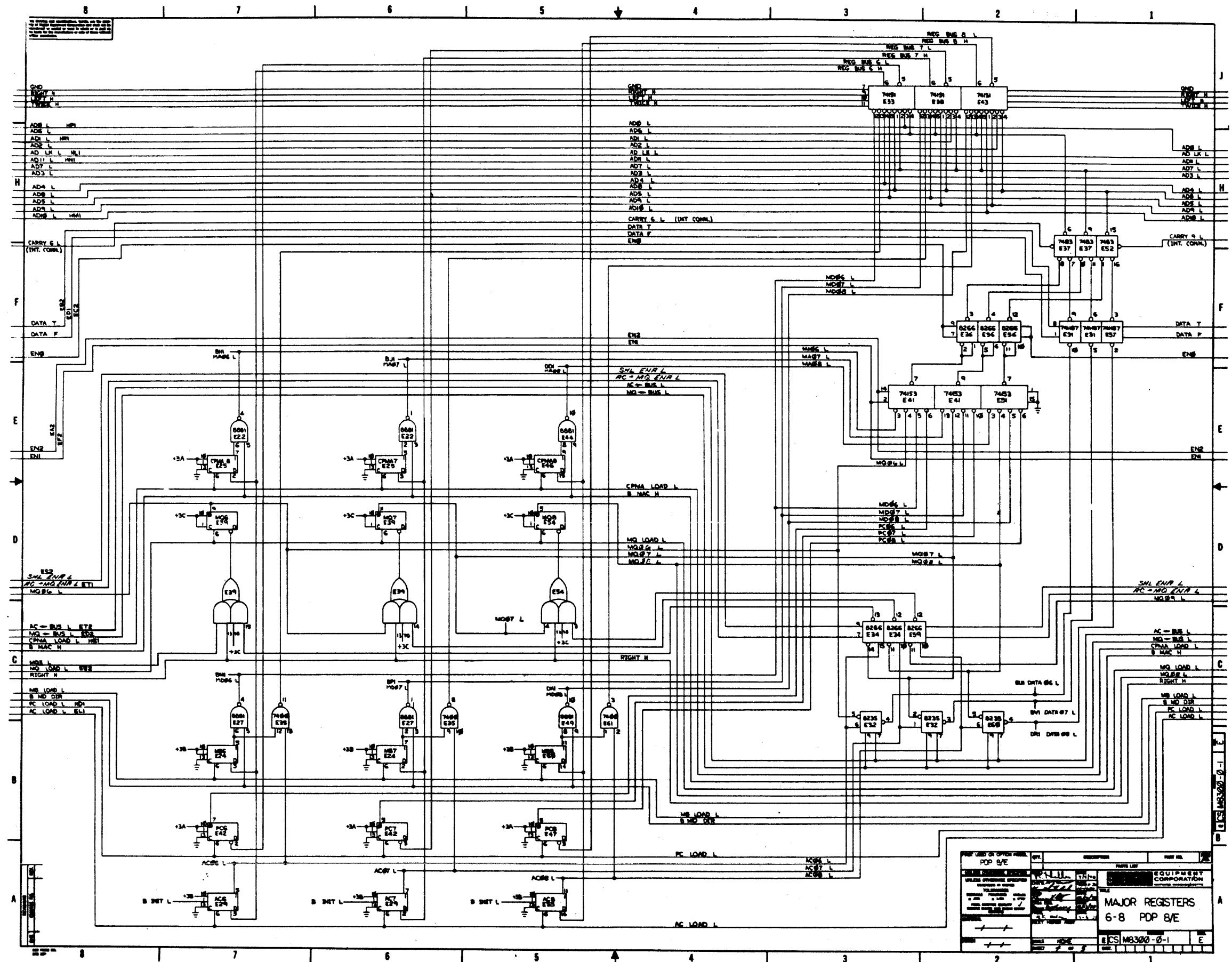
FIRST USED ON OPT/MOD PDP8/E	QTY.	DESCRIPTION	PART NO.	ITEM NO.
				PARTS LIST
UNLESS OTHERWISE SPECIFIED				
DIMENSIONS IN INCHES				
TOLERANCES				
DECIMALS	FRACTIONS	ANGLES		
$\pm .005$	$\pm 1/16$	$\pm 0^{\circ}30'$		
FINAL SURFACE QUALITY				
REMOVE BURRS AND BREAK SHARP CORNERS				
MATERIAL	NEXT HIGHER ASSY			
+	A-ML-PDP8/E-0			
FINISH	SCALE NONE			
+	SHEET 1 OF 2			
DRAFTER <i>[Signature]</i>	DATE <i>[Signature]</i>	REVISER <i>[Signature]</i>	SIZE CODE D T D	NUMBER PDP8/E-0-05
			DIST.	REV. B

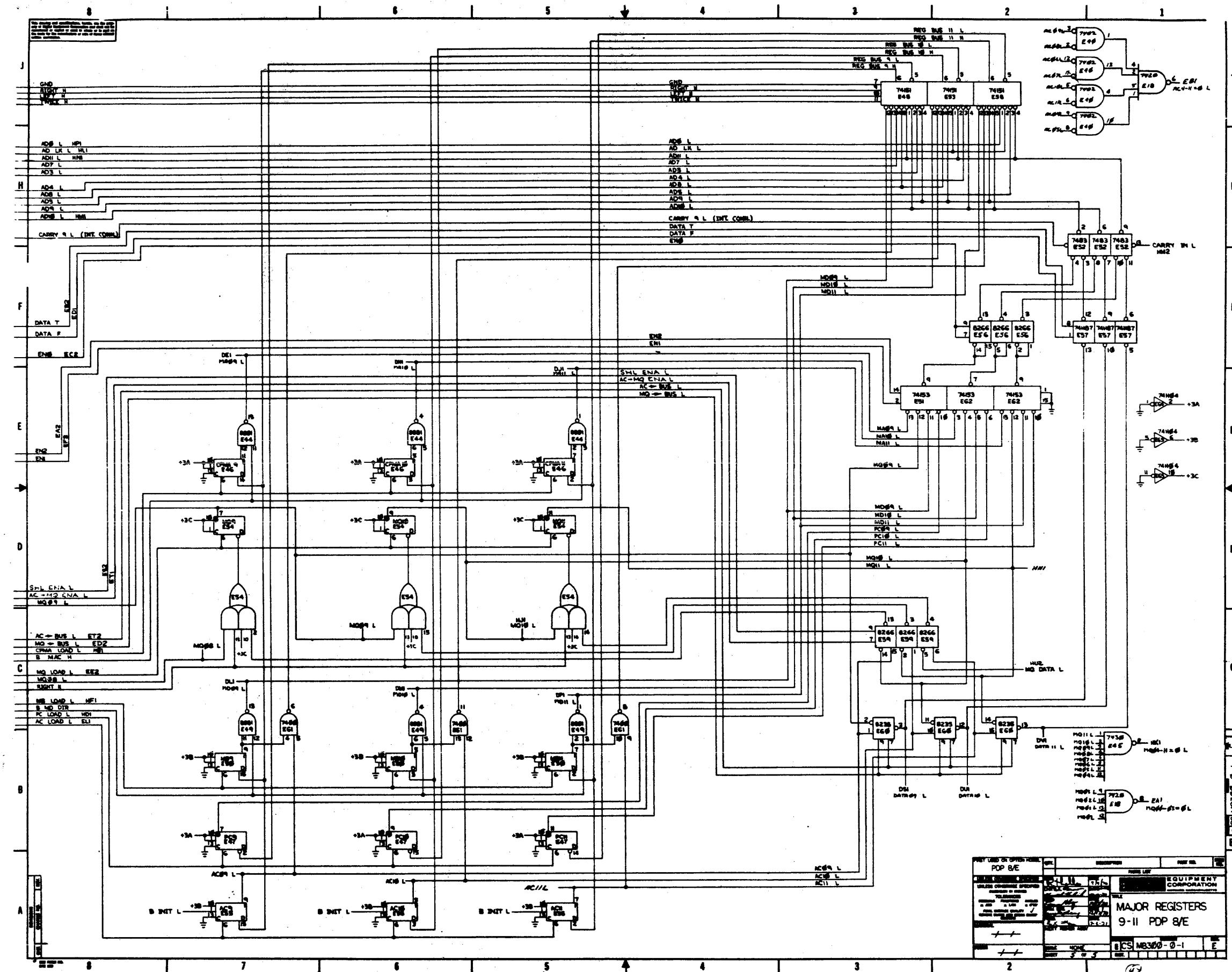


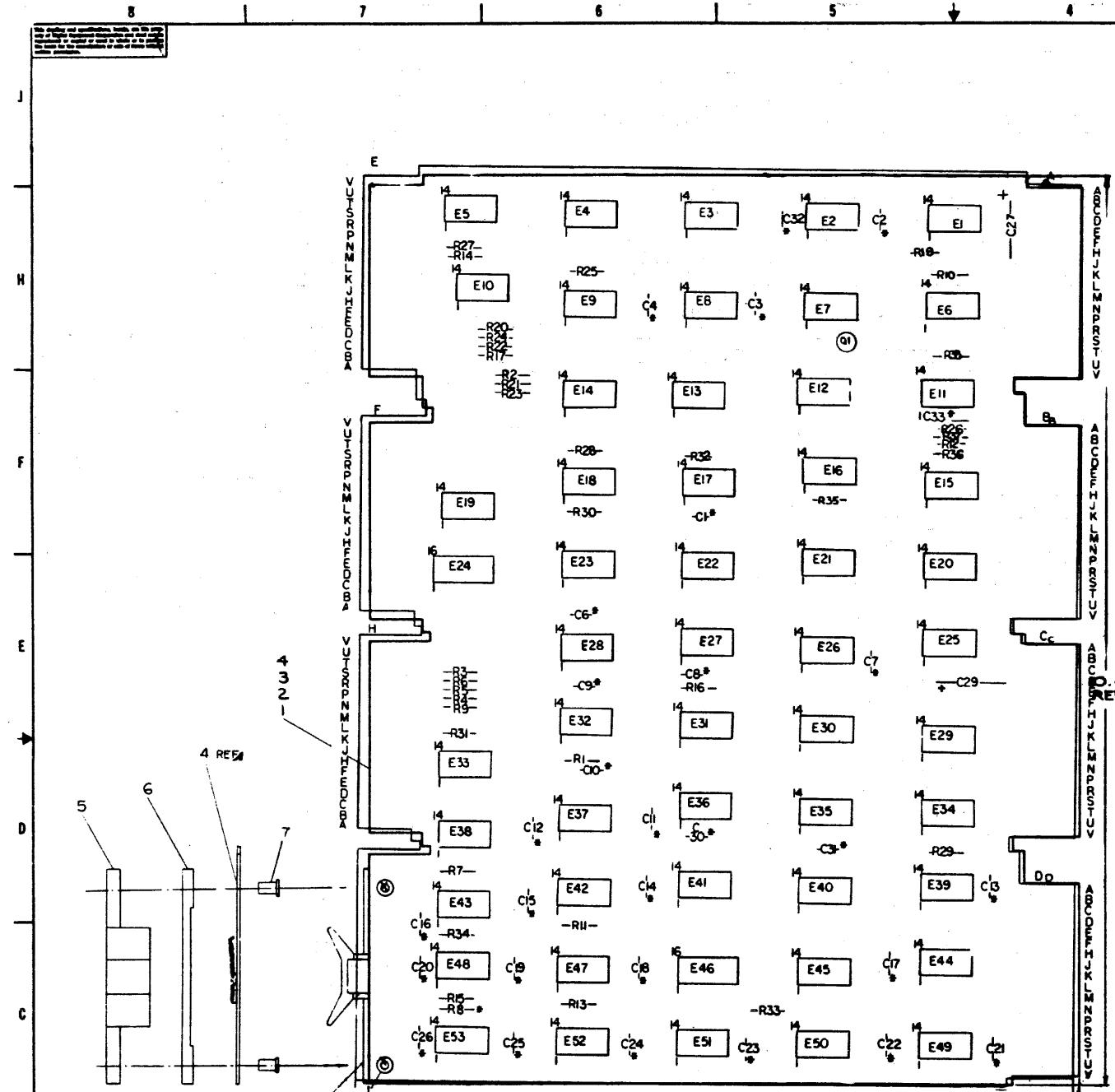




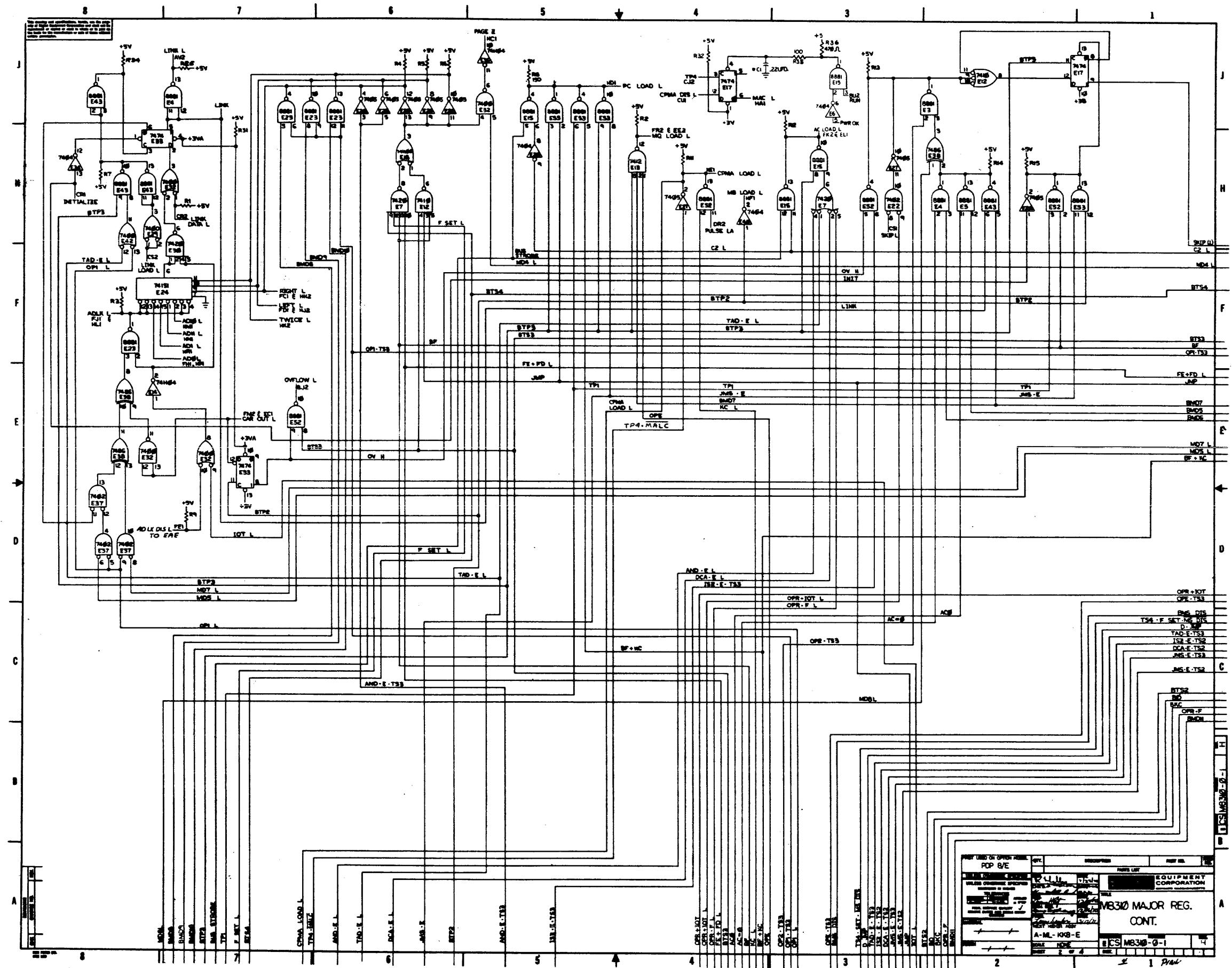


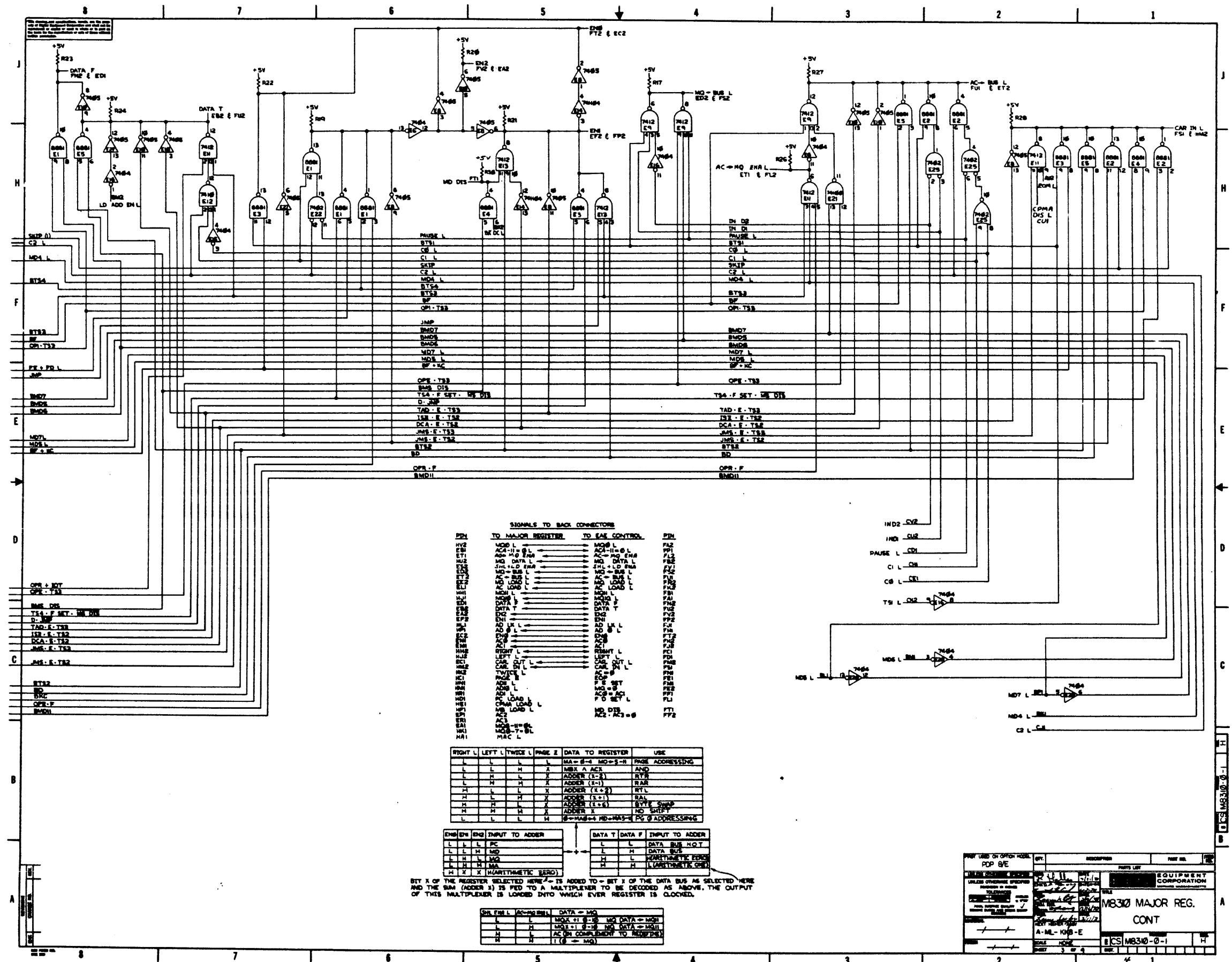


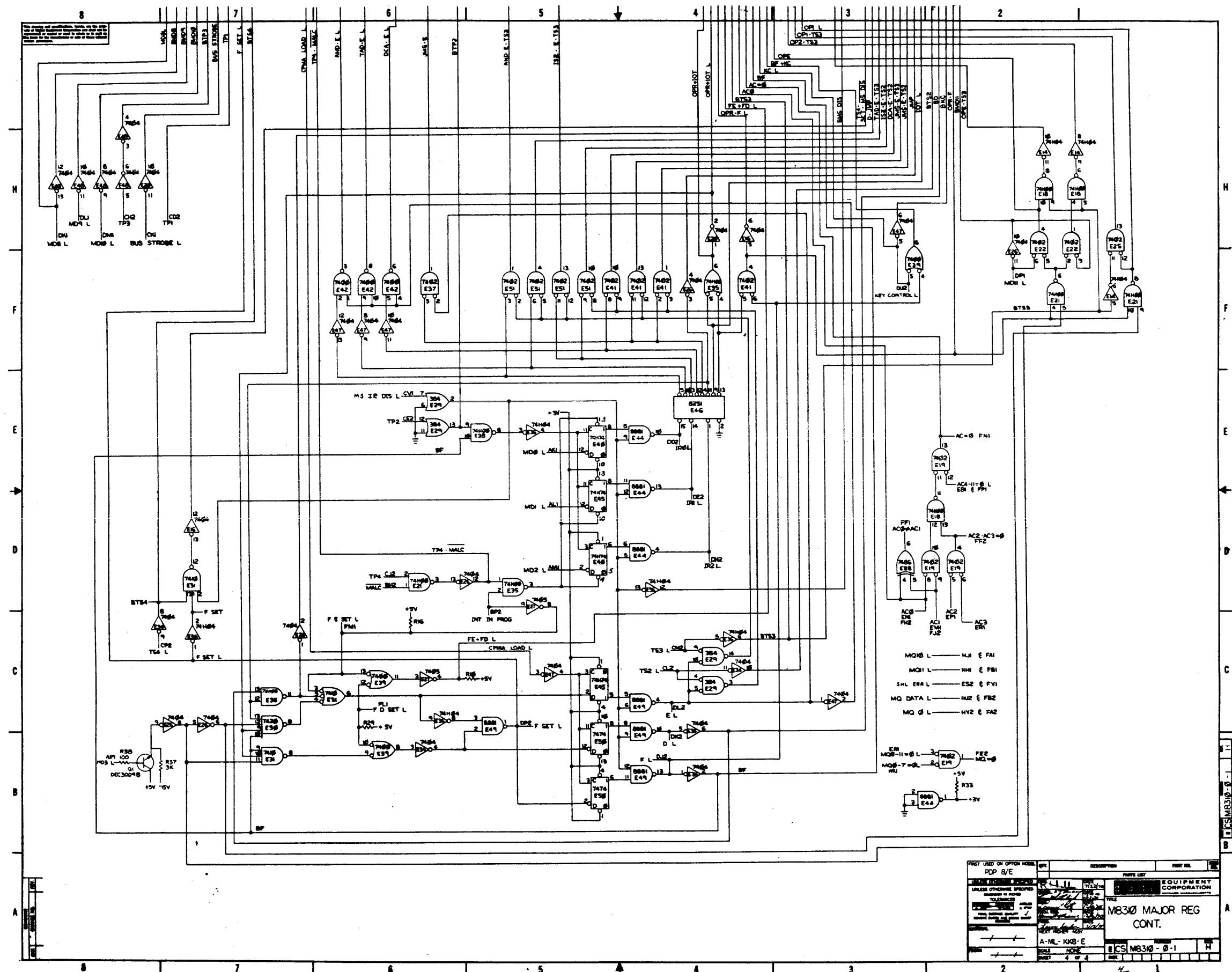


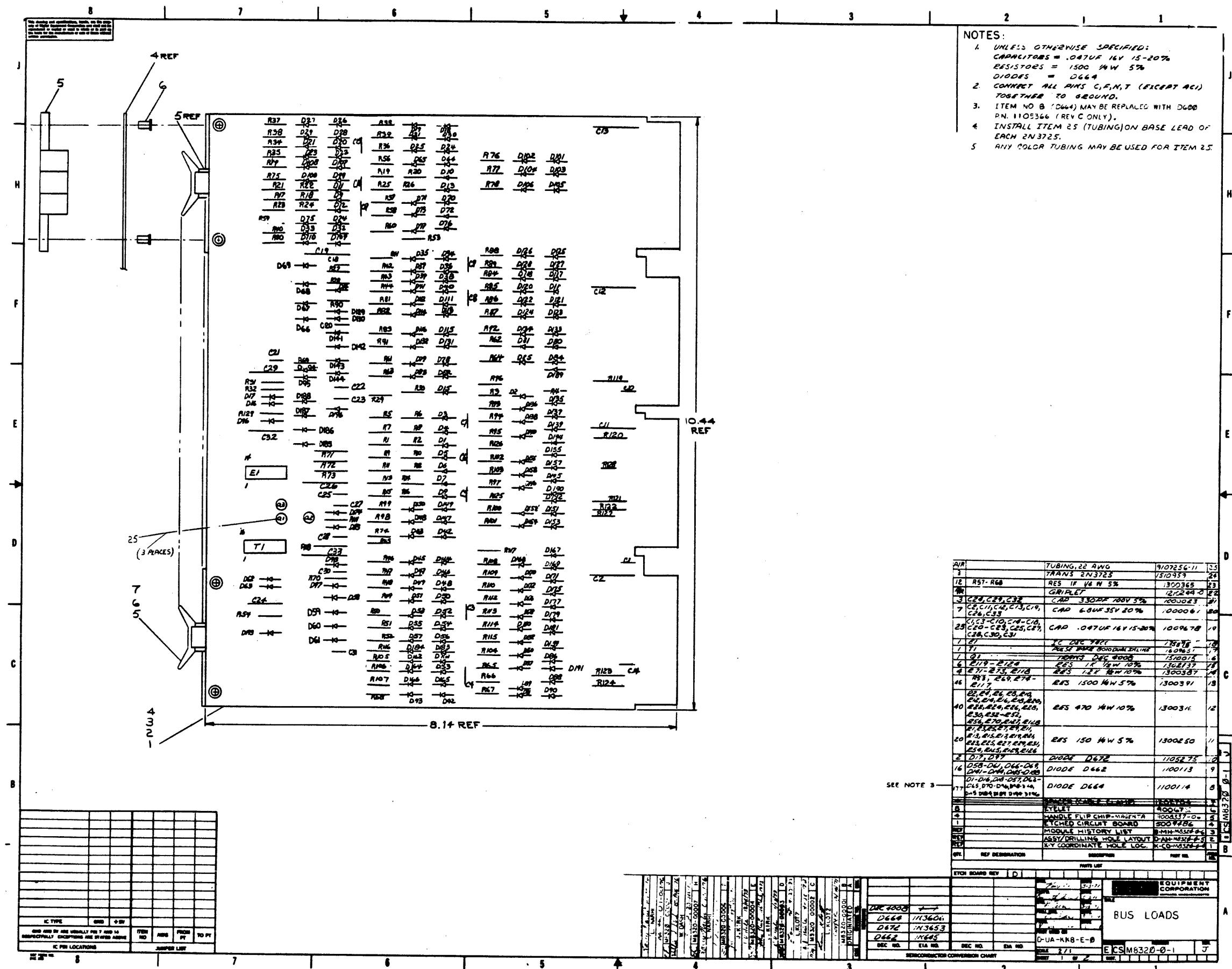


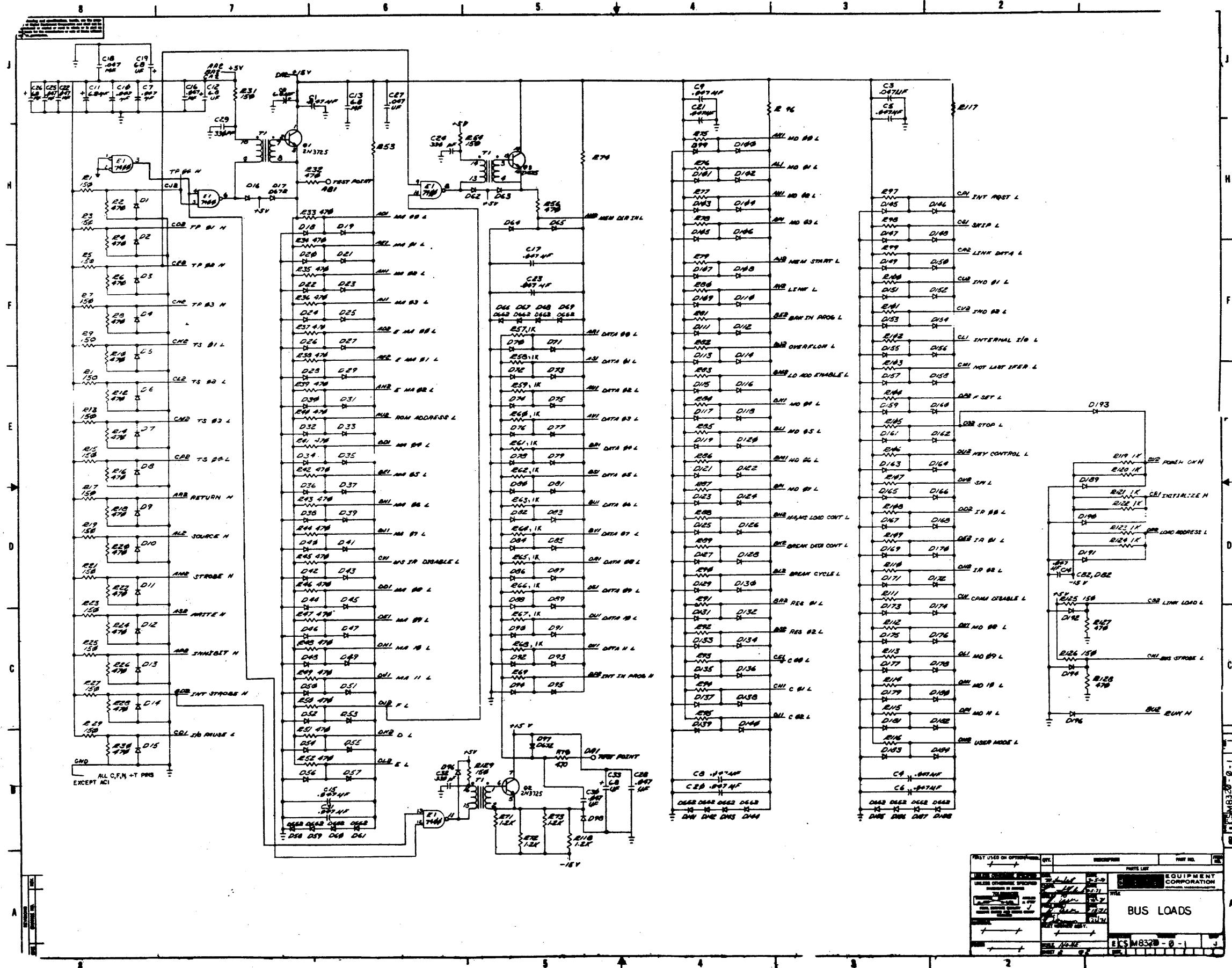
NOTES:
* THESE COMPONENTS MUST BE ON THE M831Q IF THERE IS A KLB-A(M8319) ON THE SYSTEM.

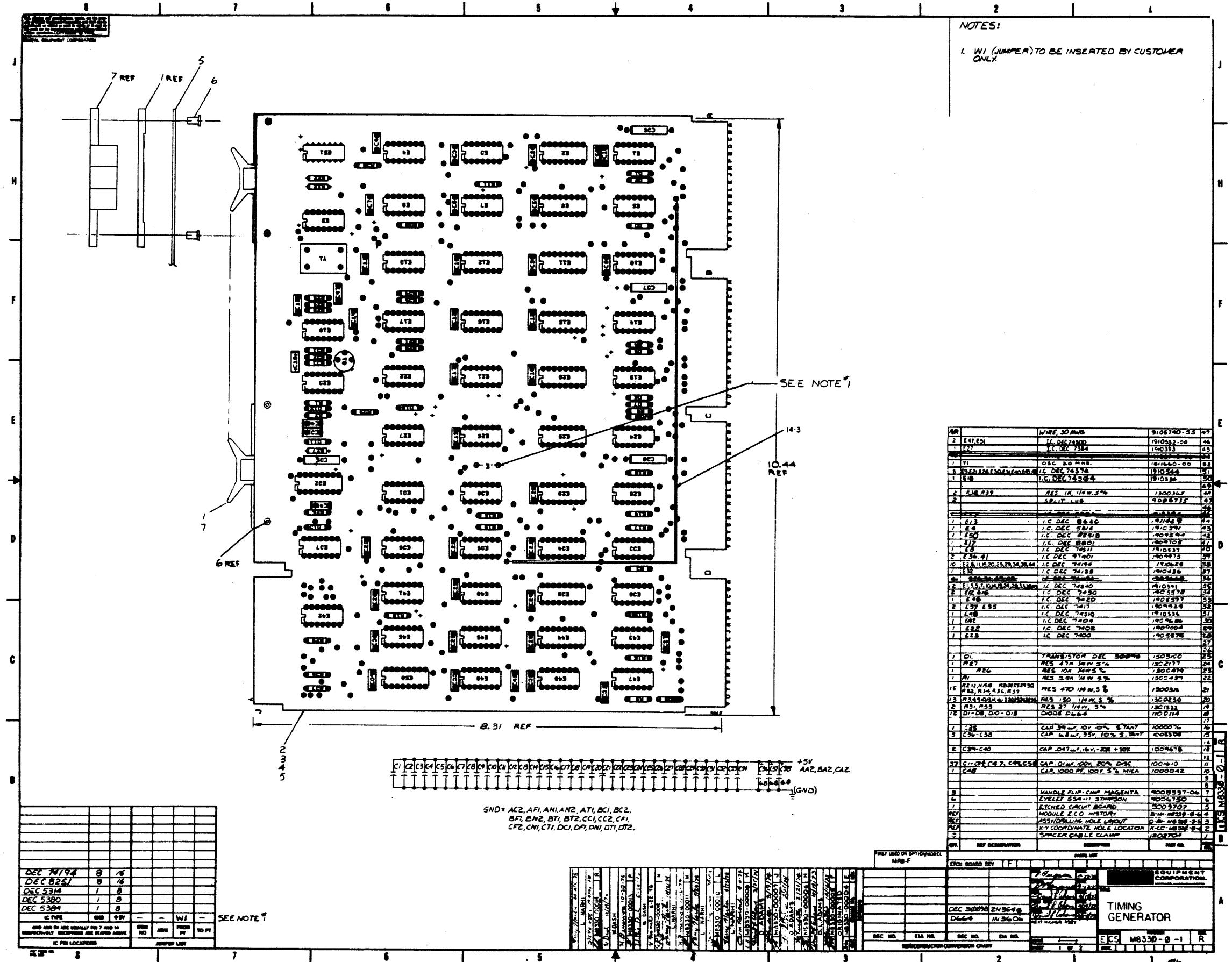


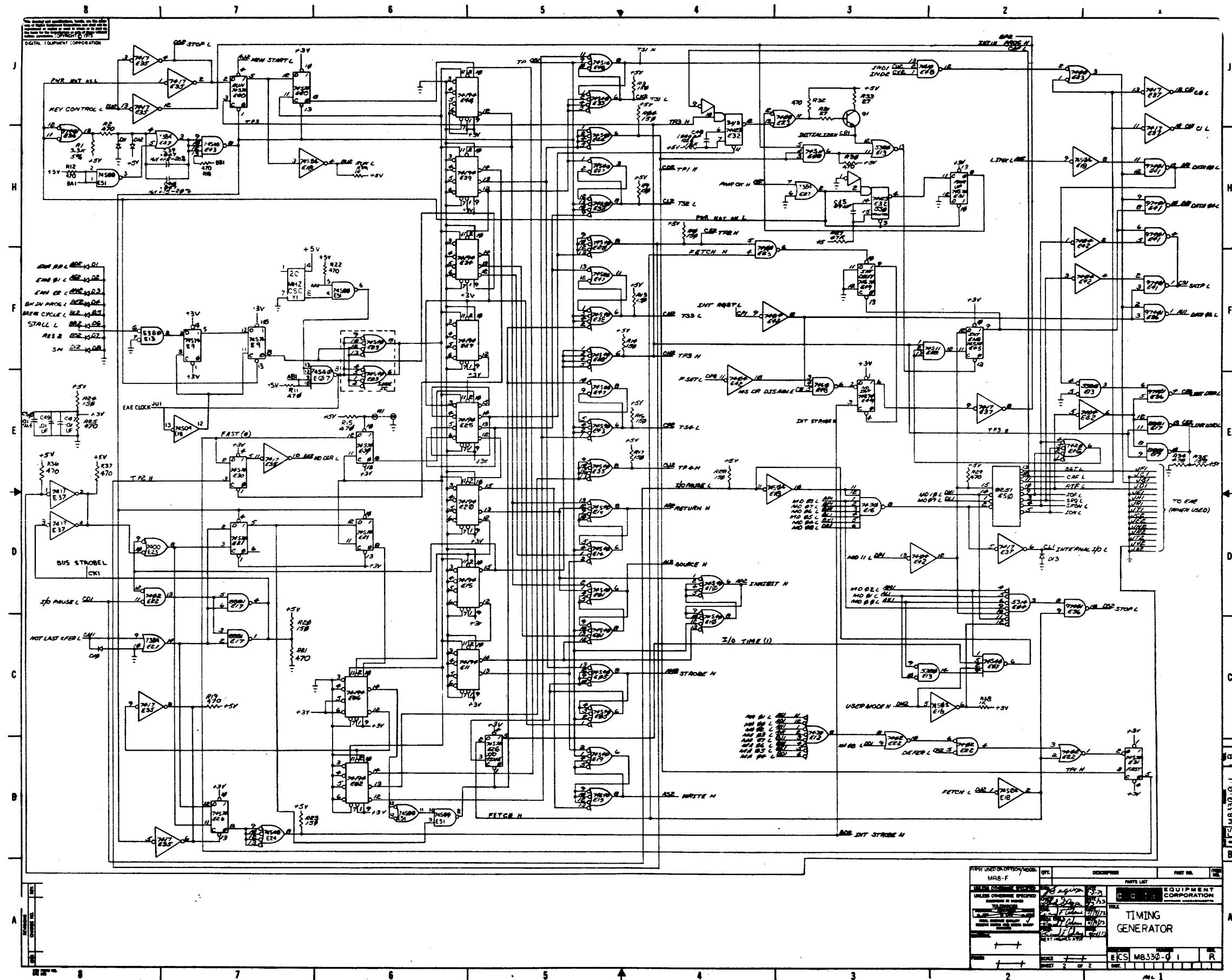












DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

ENGINEERING SPECIFICATION

DATE 11/19/74

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TITLE FIELD INSTALLATION & ACCEPTANCE PROCEDURE FOR KM8-A

REVISIONS

REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE

ENG <i>Larry Martin</i>	APPD <i>Gil Chivie</i>	SIZE A	CODE SP	NUMBER KM8-A-1	REV
DEC 16-(392)-1079-N971					
DRA 107					

SHEET 1 OF 3

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE FIELD INSTALLATION & ACCEPTANCE PROCEDURE FOR KM8-A

I GENERAL

This procedure defines the performance standards required of the KM8A*, option board #2. This procedure refers to both system and add-on acceptance.

NOTE: If KM8A was shipped as part of a PDP-8A system, then proceed to installation procedure.

- * Memory Extension & Time Share
- Bootstrap Loaders
- Power Fail/Auto Restart

II INSPECTION

After removing the KM8A from the packing material, inspect the module for the following:

1. Inventory hardware against shipping list.
2. Inventory software against software list, if ordered.
3. Inventory prints against shipping list, if ordered.
4. Check module for loose or broken components.

III INSTALLATION PROCEDURE

Install the equipment using the following procedure:

1. Set the switches as indicated by the diagnostic write up.

NOTE: Refer to Operator's Handbook for switch setting descriptions.

2. Insure that the PDP-8A power is removed from the OmnibusTM.
3. Insert the KM8A into the second or third slot of the OmnibusTM.
4. Turn the power back "ON".

IV ACCEPTANCE PROCEDURE

Perform the acceptance procedure defined in Table A. If abnormal indications are encountered, refer to the diagnostic listing for the type of error. Reference the diagnostic write ups and Operator's Manual for instructions for loading diagnostics.

	SIZE A	CODE SP	NUMBER KM8-A-1	REV
DEC FORM NO DEC 16-(381)-1022-N370				
DRA 108				

SHEET 2 OF 3

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE FIELD INSTALLATION & ACCEPTANCE PROCEDURE FOR KM8-A

IV ACCEPTANCE PROCEDURE (continued)

Equipment required:

1. PDP-8A with 1K min. R/W Memory
2. Paper Tape Input Device
3. Diagnostic and Listings
4. Programmer's Console (KC8-A & DKC8-A)
5. W987 Quad Extender

NOTE: If the programmer's console and paper tape input device are not available as part of the system being used, they must be supplied in good working order by the customer.

TABLE A

Acceptance of KM8A with 4K of R/W Memory

<u>Program Name</u>	<u>Maindec #</u>	<u>Accept Time</u>	<u>Restrictions</u>
KM8A Option Test #2	08-DJKMA-PB	30 min	4K R/W Memory Min

Acceptance of KM8A with Less than 4K R/W Memory

KM8A Option Test #2 Segment #1 (RIM)	08-DJKMA -PM1	10 min	1K R/W memory min
KM8A Option Test #2 Segment #2 (RIM)	08-DJKMA -PM2	10 min	1K R/W Memory Min
KM8A Option Test #2 Segment #4 (RIM)	08-DJKMA -PM4	10 min	1K R/W Memory Min

	SIZE A	CODE SP	NUMBER KM8-A-1	REV

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BOOTSTRAP/AUTO RESTART FUNCTIONAL SWITCH SETTINGS							
DESIRED FUNCTION	ACTIVATING SIGNAL	SI-4	SI-5	SI-6	SI-7	SI-8	S2-1
BOOTSTRAP ENABLED	"BOOT" SW	*	SPARE	OFF	OFF	ON	N/A
AUTO-RESTART DISABLED	N/A						
BOOTSTRAP ENABLED	"BOOT" SW	*		ON	ON	ON	N/A
AUTO-RESTART DISABLED	"AC LOW"	*		ON	ON	OFF	N/A
BOOTSTRAP ENABLED	N/A	*		ON	ON	OFF	N/A
AUTO-RESTART DISABLED	"AC LOW"	*		ON	ON	OFF	N/A
EOTSTRAP ENABLED	"AC LOW"	*		ON	OFF	OFF	N/A
AUTO-RESTART DISABLED	N/A	*		ON	OFF	OFF	N/A
BOOTSTRAP ENABLED	"AC LOW" OR "BOOT" SW	*	SPARE	ON	OFF	ON	N/A
AUTO-RESTART DISABLED	N/A						
TIME SHARE DISABLED	N/A	N/A	N/A	N/A	N/A	N/A	ON
TIME SHARE DISABLED	N/A	N/A	N/A	N/A	N/A	N/A	OFF

NOTES: * SI-4 "OFF"-BOOTSTRAP CAN BE ACTIVATED BY "BOOT" SW EITHER IN THE RUN OR "RUN" STATE.
SI-4 "ON"-BOOTSTRAP CAN BE ACTIVATED BY "BOOT" SW IN THE RUN STATE.

1. "AC LOW" WILL CAUSE AUTO-RESTART OR EOTSTRAP DEPENDING ON SWITCH SETTINGS TO OCCUR ONLY IN THE "RUN" OR STOPPED STATE SI-6,7,8 "OFF"-BOOTSTRAP AND AUTO-RESTART DISABLED.
2. E76 AND E61 ARE NOT ON THE YC VARIATION KM8-AD. ALL OTHER PARTS REMAIN THE SAME.
3. IF AUTO-RESTART IS ENALED, THE AUTO-START FEATURE OF THE CPU (M8315) MUST BE DISABLED.

BOOT STRAP SELECT SWITCH SETTINGS FOR 158A2/159A2 ROMS									
PROGRAM	S2-5	S2-6	S2-7	S2-8	SI-1	SI-2	SI-3	ROM ST ADD	MEM ADD START
HI-LO PT RDR	ON	ON	ON	OFF	ON	ON	ON	20	7734
RK8E	ON	OFF	ON	OFF	ON	OFF	ON	124	24
RX8E	ON	OFF	OFF	ON	OFF	ON	ON	150	33
RFQ6/DF32D	OFF	ON	OFF	ON	OFF	ON	ON	252	7750
TABE	OFF	ON	OFF	OFF	ON	OFF		272	4000

3. BOOTSTRAP SELECT SWITCHES ARE DEFINED AS FOLLOWS:

- A. ROM ADDRESS RANGE: 0-377
- B. ON=LOGIC 1 OR LOW; OFF=LOGIC 0 OR HIGH
- C. ORDER OF SIGNIFICANCE

$$S_25 = 2^7 = 128$$

$$S_26 = 2^6 = 64$$

$$S_27 = 2^5 = 32$$

$$S_28 = 2^4 = 16$$

$$S_29 = 2^3 = 8$$

$$S_30 = 2^2 = 4$$

$$S_31 = 2^1 = 2$$

THE LSE OF ADDRESS IS CONTROLLED BY THE BOOTSTRAP/AUTO-RESTART LOGIC

BOOTSTRAP SELECT SWITCH SETTINGS FOR 465A2/469A2 ROMS									
PROGRAM	S2-5	S2-6	S2-7	S2-8	SI-1	SI-2	SI-3	ROM ST ADD	MEM ST ADD
HI-LO PTR	ON	ON	ON	OFF	ON	ON	ON	20	7734
RK8E	ON	OFF	ON	OFF	ON	OFF	ON	124	24
RX8E	ON	OFF	OFF	ON	OFF	ON	ON	150	33
RL8A	OFF	ON	OFF	OFF	OFF	ON	ON	272	1

* RX8E BOOT FOR BOTH RX01 AND RX02

AUTO-RESTART SELECT SWITCH SETTINGS			
RESTART ADDRESS	S2-2	S2-3	S2-4
0	OFF	OFF	OFF
200	OFF	ON	OFF
2000	ON	OFF	OFF
4200	ON	ON	OFF

4. AUTO RESTART SELECT SWITCHES ARE DEFINED AS FOLLOWS:

- A. ROM ADDRESS RANG; 0-16.
- B. ON=LOGIC 1 OR LOW; OFF=LOGIC 0 OR HIGH.
- C. ORDER OF SIGNIFICANCE

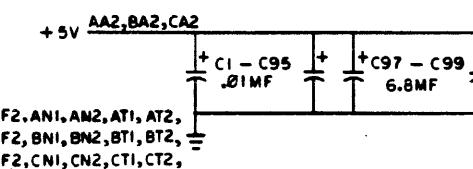
$$S_22 = 2^3 = 8$$

$$S_23 = 2^2 = 4$$

$$S_24 = 2^1 = 2$$

5. TO CONFIGURE MODULE FOR USE WITH KT8-A OPTION,
INSTALL JUMPERS AS SHOWN BELOW.

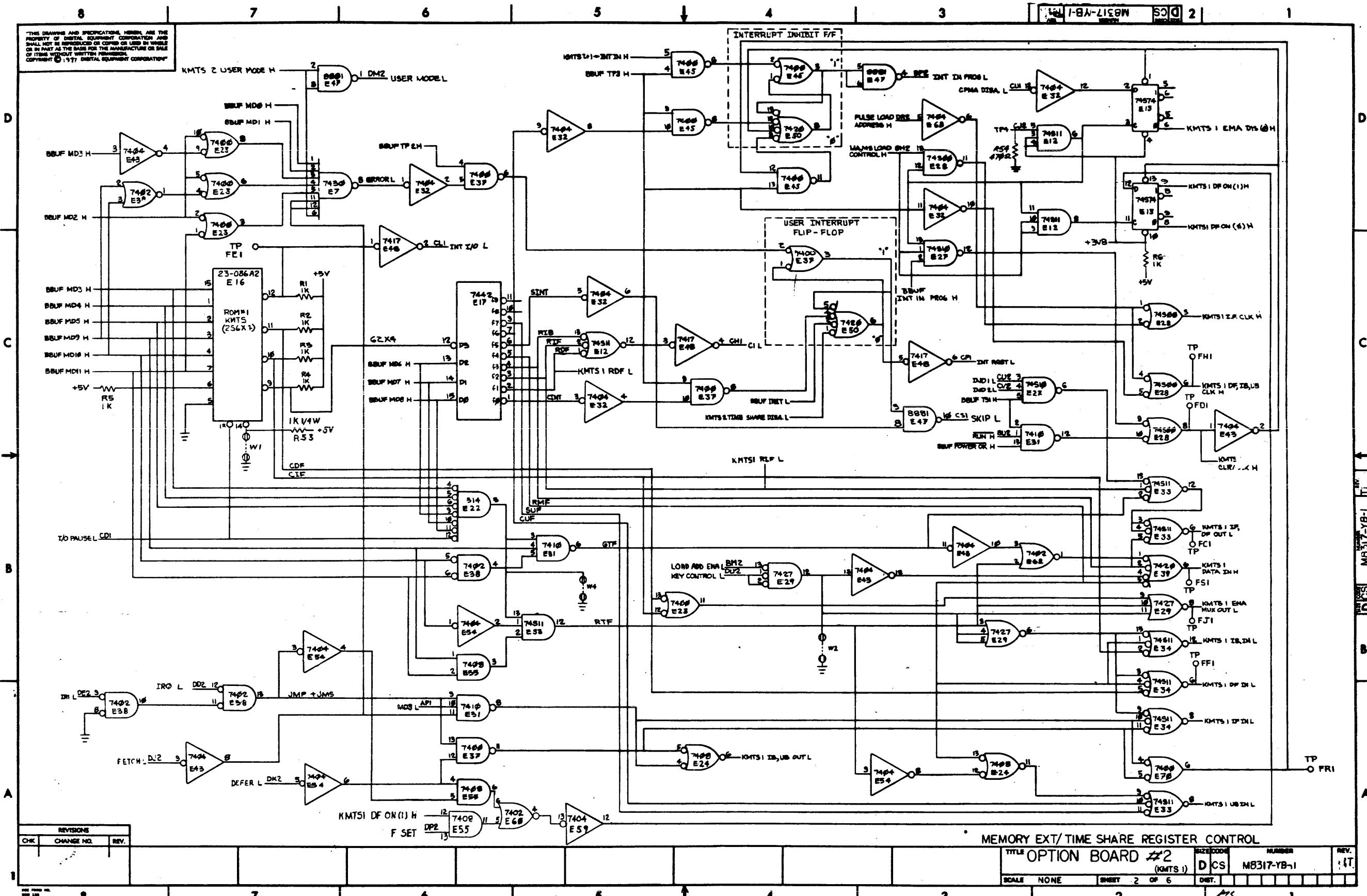
W1	W2	W3	W4
NORMAL	IN	OUT	CUT
WITH KTEA	OUT	IN	IN

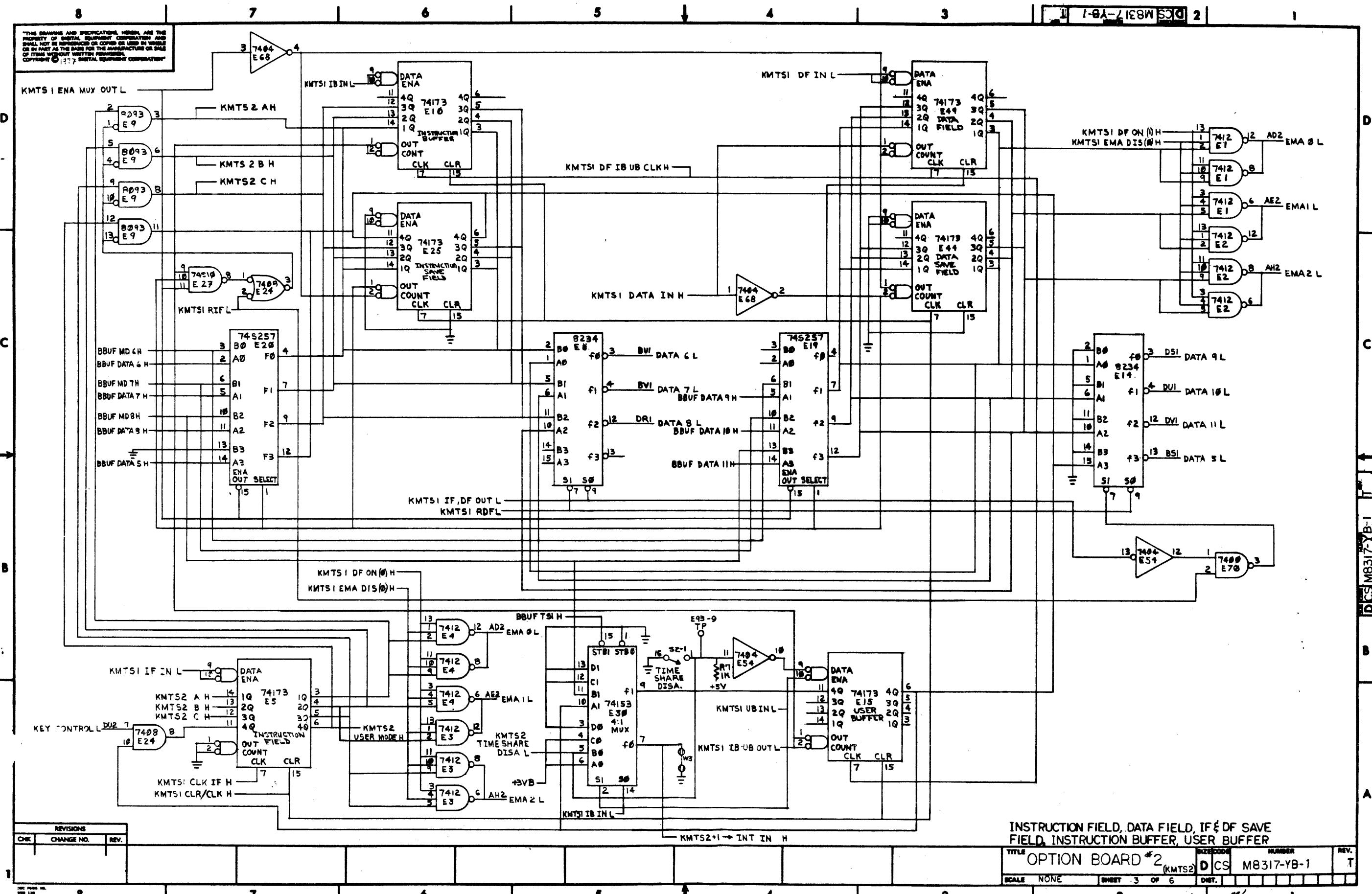


PART CALLED FCR		SUBSTITUTE PART			
QTY	PART NO	DESCRIPTION	QTY	PART NO	DESCRIPTION
96	1001610-01	.01UF DISC	96	10C1610-01	.01UF GLASS
3	1503100	DEC 3009B	3	15C3338	DEC 6531
6	1911330	74173	6	1911711	8TIC
1	1909704	314	1	1910391	314
			1	19C9972	6314
6	19C9705	6661	6	19C9973	97401
1	23158A2	RCM1 (E76)	1	23465A2	RCM1 (E76)
1	23159A2	RCM2 (E81)	1	23469A2	RCM2 (E81)

REV	1
SDO NUMBER	L.NARH
DATE	1-20-81

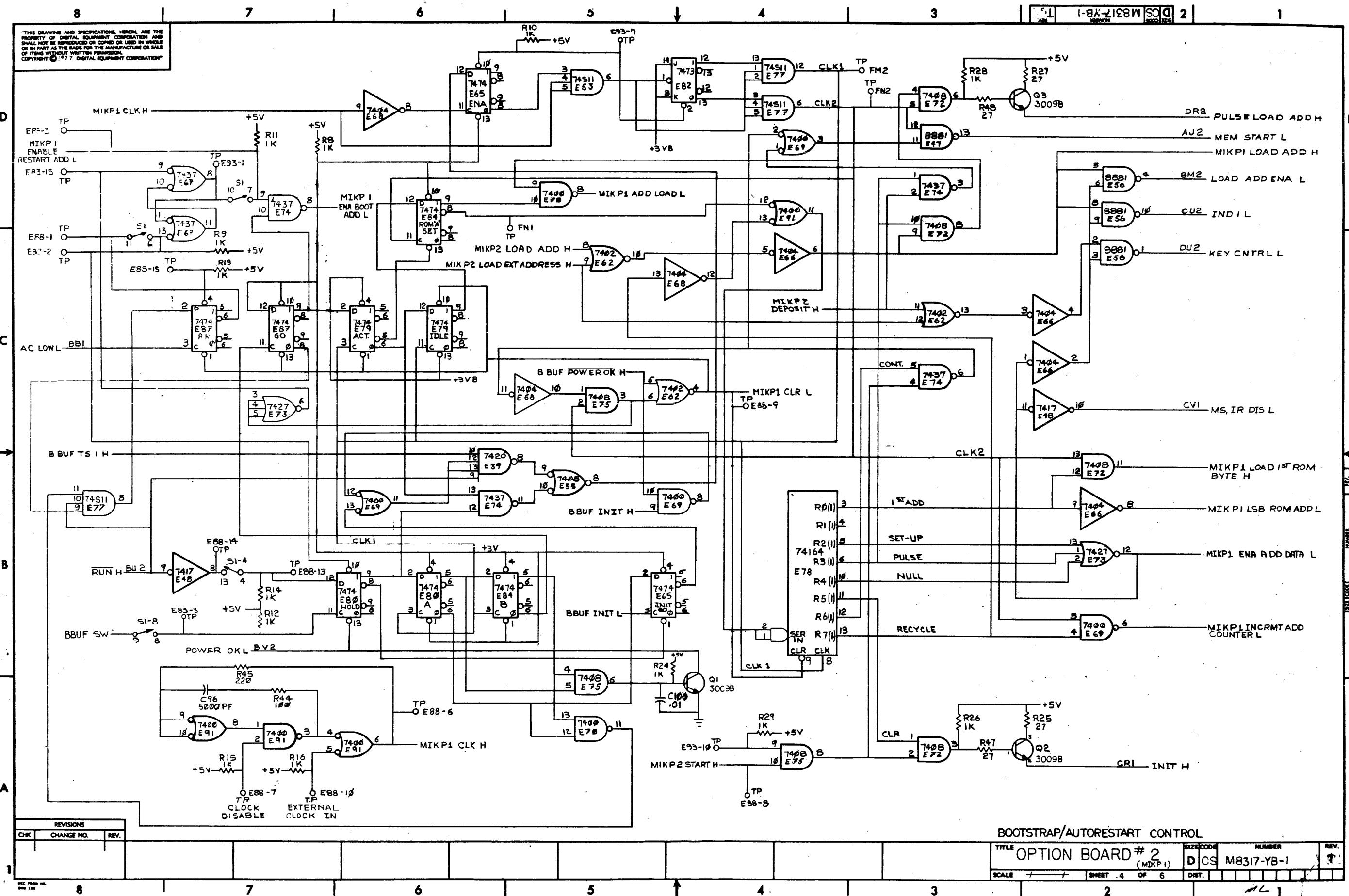
R.16 APPENDIX	07 PAGE 80
St. 80	13-3-81
Vernie Haskin	310CTAD
Vernie Haskin	310CTB0
E. Field	310TB0
OPTION BOARD # 2	
DOCUMENT NUMBER	
B-DO-M8317-YB-1	
DCS	M8317-YB-1
T	6



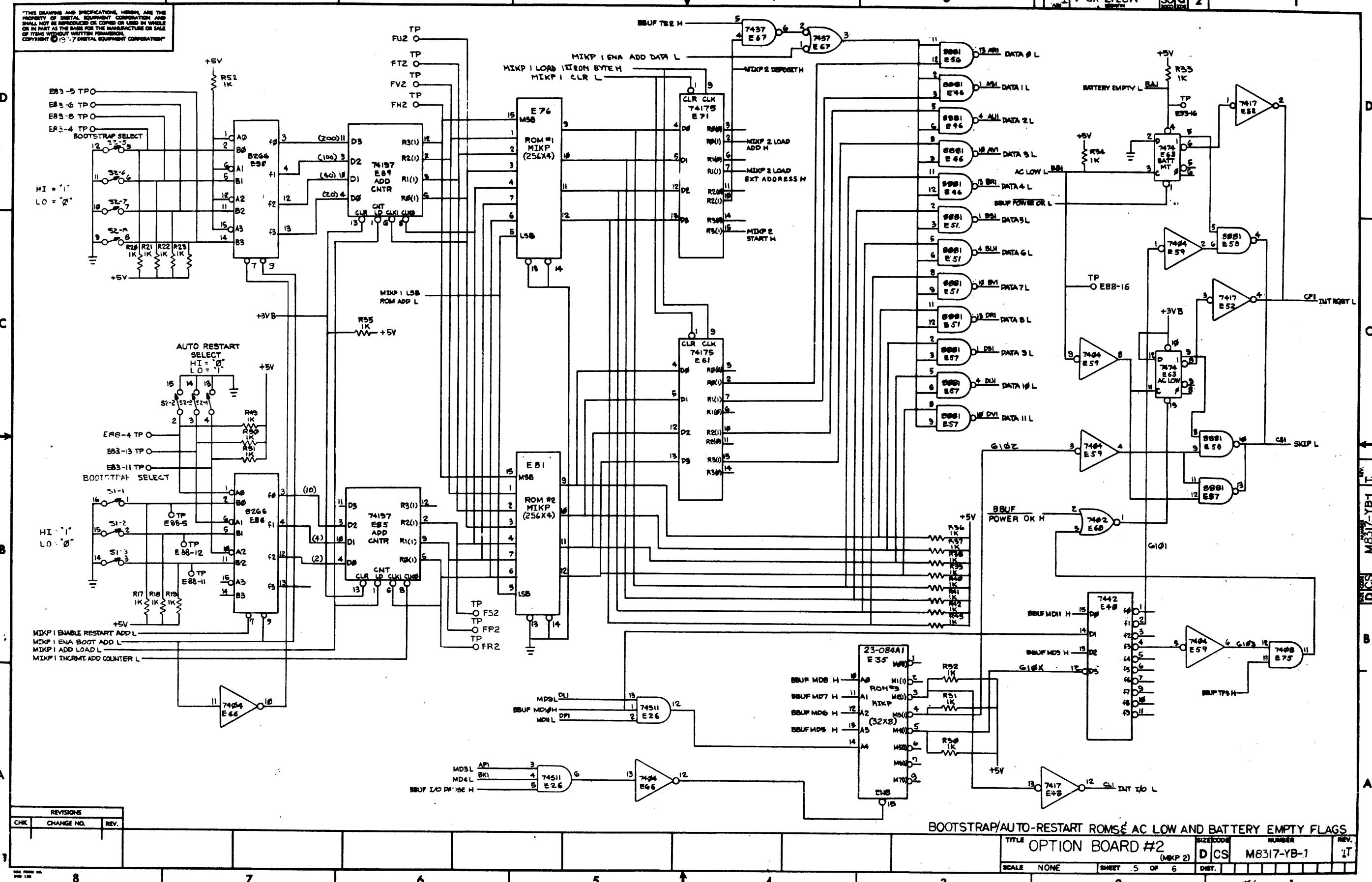


INSTRUCTION FIELD, DATA FIELD, IF & DF SAVE
FIELD, INSTRUCTION BUFFER, USER BUFFER

TITLE	OPTION BOARD #2 (KMTS2)	SIZE/CODE	NUMBER	REV.
SCALE	NONE	INCHES	3 OF 5	DIST.



8 7 6 5 4 3 2 1



REVISIONS
CHG. CHANGE NO. REV.
1

BOOTSTRAP/AUTO-RESTART ROM& AC LOW AND BATTERY EMPTY FLAGS

TITLE OPTION BOARD #2
(M8317-YB-1)

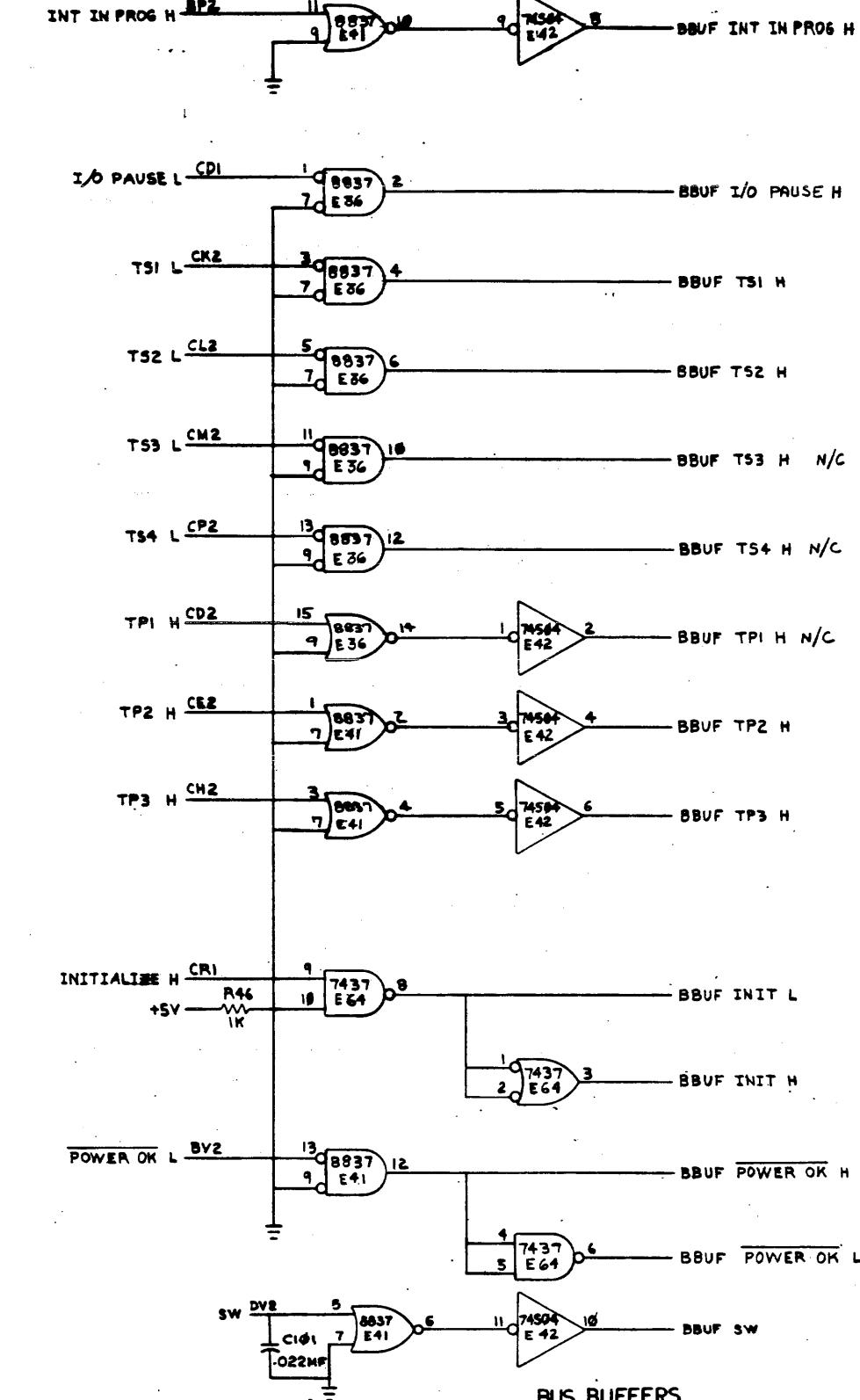
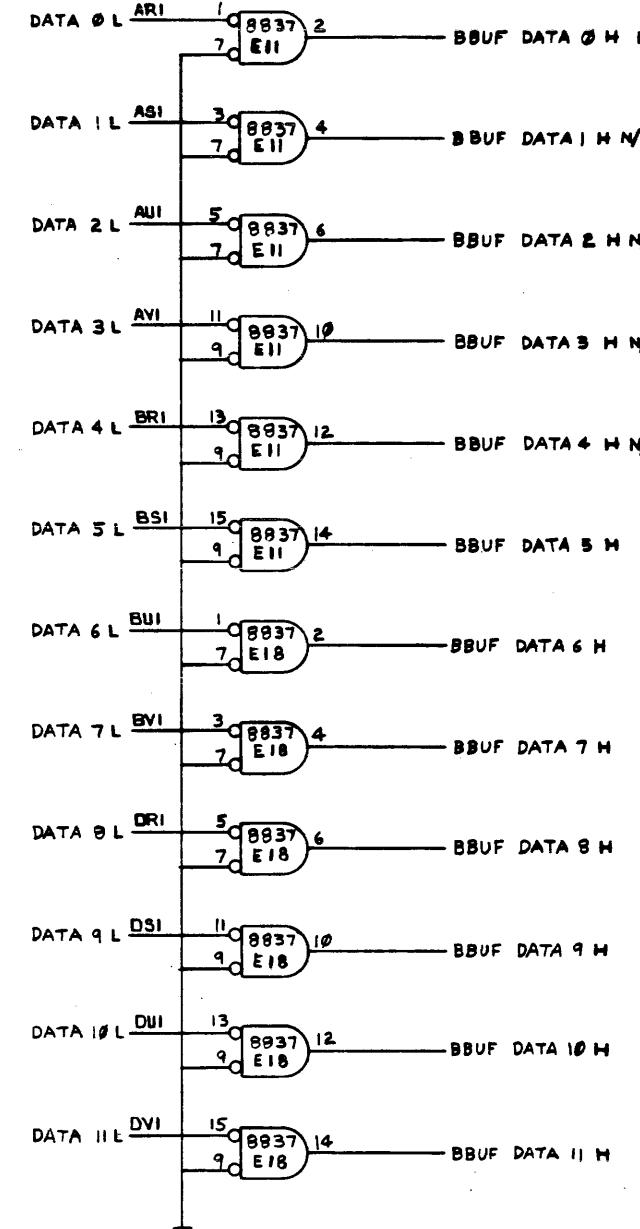
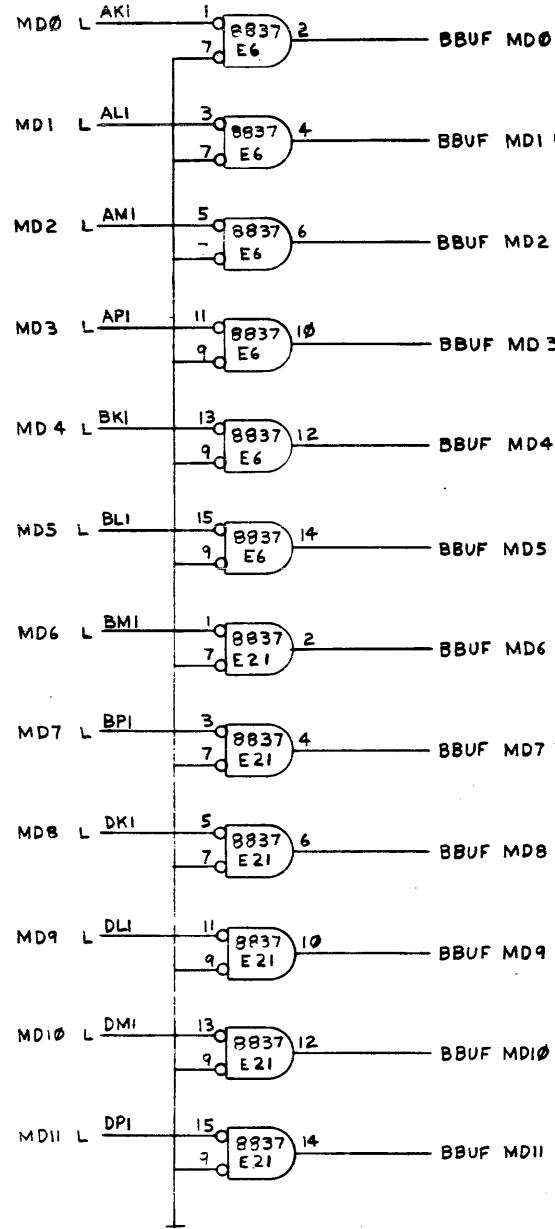
SCALE NONE SHEET .5 OF 6 DIST. 1

SIZE CODE DCS NUMBER M8317-YB-1 REV. 1T

100-1000 8 7 6 5 4 3 2 1

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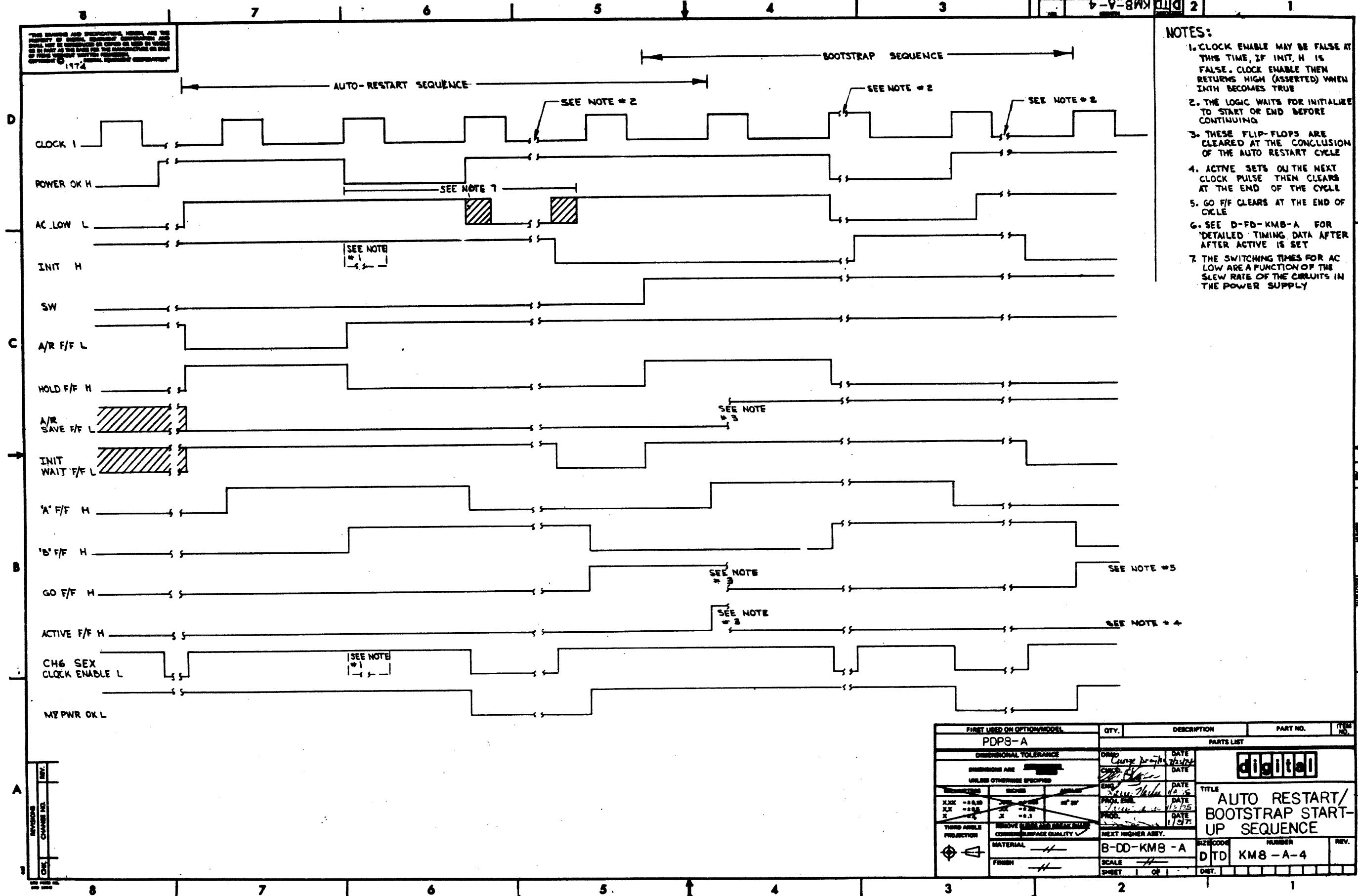
NOTE: SIGNALS WITH N/C
HAVE NO CONNECTION



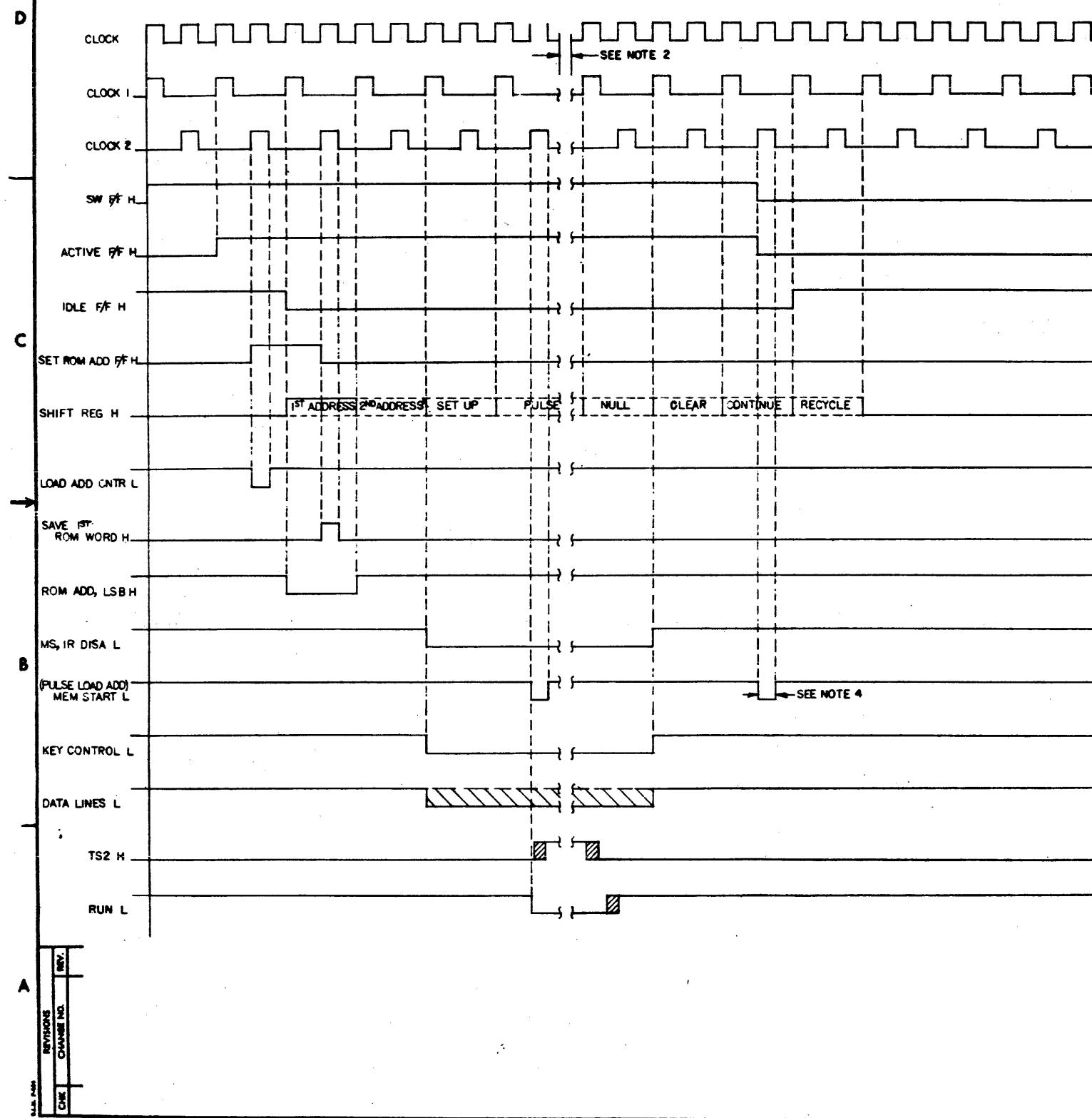
BUS BUFFERS

REVISIONS		
CHK	CHANGE NO.	REV.
1		

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100



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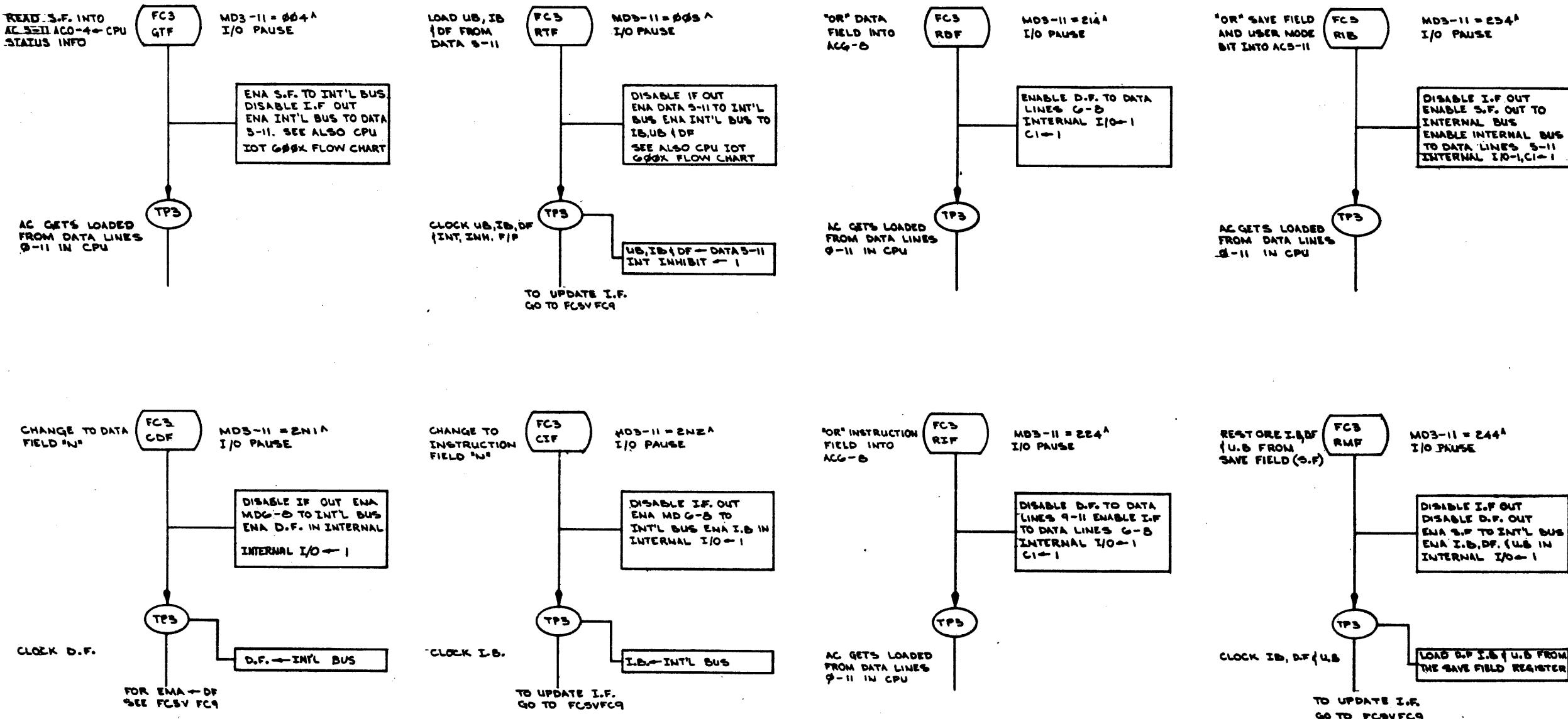


- NOTES:**
- ONE "DEPOSIT" CYCLE IS SHOWN IN DIAGRAM.
 - WHEN "RUN" IS TRUE (LOW) ALL TIMING IS HELD OFF UNTIL THE NEXT CLOCK PULSE AFTER "RUN" GOES FALSE (HIGH).
 - FOR THE "LOAD ADD" CYCLE SIGNALS REMAIN THE SAME AS SHOWN EXCEPT THAT "PULSE LOAD ADD" REPLACES "MEM START" AND "KEY CONTROL" IS NEGATED. FOR "EXT. LOAD ADD" KEY CONTROL IS TRUE.
 - MEM START APPEARS HERE ONLY FOR THE "START" FUNCTION. THE EARLIER MEM START IS FOR DEPOSITS ONLY.

FIRST USED ON OPTION/MODEL		GTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP8-A		PARTS LIST			
DIMENSIONAL TOLERANCE		DRAW.	M. Weller	DATE	
DIMENSIONS ARE MILLIMETERS. INCHES		CONT.	J. Weller	DATE	
UNLESS OTHERWISE SPECIFIED		ENG.	S. Weller	DATE	
MM/MILLIMETERS	INCHES	ANGLE	PROJ. END	DATE	
X	45° 20'	PROJ. END	DATE	
X	X	PROJ. END	DATE	
X	X	PROJ. END	DATE	
THIRD ANGLE PROJECTION		NEXT HIGHER ASY.			
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY ✓		MATERIAL B-DD-KMB-A			
FINISH		SCALE NONE			
		SIZE CODE NUMBER D TD KM8-A-5 REV. .			
		SHEET OF 1 DIST. .			

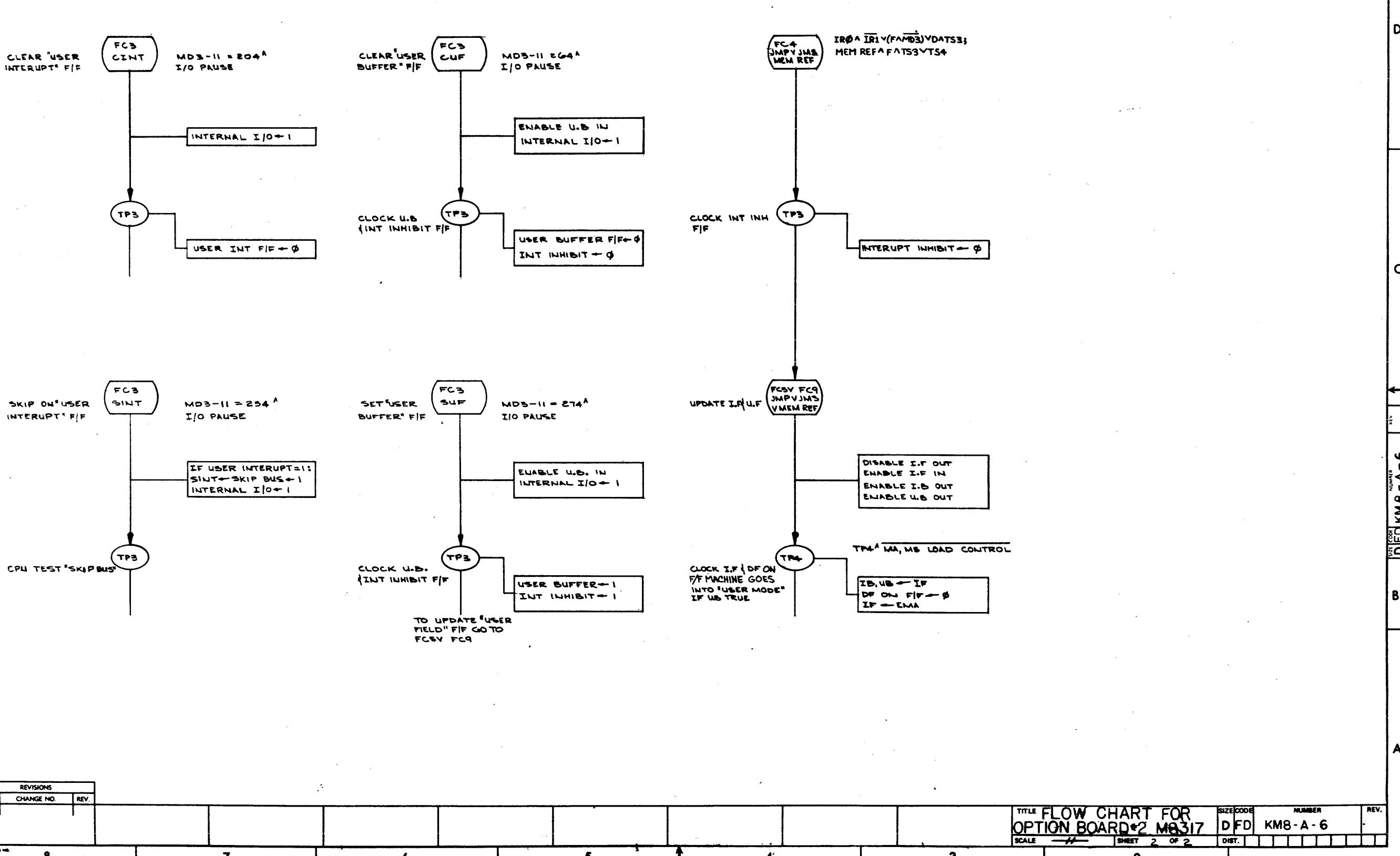
digital

**BOOTSTRAP
TIMING DIAGRAM**



FIRST USED ON OPTION MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
UNLESS OTHERWISE SPECIFIED	DRW	K6	1267-5	
UNLESS OTHERWISE SPECIFIED	DRW	K6	1267-5	
DIMENSIONS IN INCHES	DRW			
TOLERANCES	DRW			
DECIMALS FRACTIONS ANGLES	DRW			
= .000 = 1/1000 = 1° 30'	DRW			
FINAL SURFACE QUALITY	DRW			
REMOVE SURFS AND DEBurr SHARP CORNERS	DRW			
MATERIAL	H			
FINISH	H			
NEXT HIGHER ASSY				
B-DD-KM8-A				
SPEC NUMBER				
DFD KM8-A - 6				
SHEET 1 OF 2				
DRAFT.				

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DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

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ENGINEERING SPECIFICATION

DATE 5/8/74

TITLE ROM PROGRAMMING DIRECTIONS FOR 8A OPTION BOARD #2 KM8-AD (M8317-YC)

REVISIONS

REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE
A	E.C.O. CHANGE	00001	L.NARHI	14 MAY 76	J. Nashi	21-MAY
B	E.C.O. CHANGE	00002	L.NARHI	12-14-77	J. Nashi	5-JAN-78

ENG DEC FORM NO. DRA 107	APPD <i>Larry Nashi 12/24/77</i>	SIZE A	CODE SP	NUMBER KM8-A-7	REV B
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1 of 6

ENGINEERING SPECIFICATION

00001

CONTINUATION SHEET

TITLE ROM PROGRAMMING DIRECTIONS FOR 8A OPTION BOARD #2 KM8-AD (M8317-YC)

1. Introduction

This document describes the organization of the two 256 x 4 ROMs, hereafter called ROM #1 and ROM #2, that control and supply data for the Auto-Restart and Bootstrap portions of Option Board #2.

This information is made available to help users program their own ROMs for their specific Auto-Restart and/or Bootstrap program(s).

2. Organization

The two ROMs are connected as follows: the address lines are connected in parallel; i.e., two corresponding address lines of each ROM are connected together, the outputs are arranged in serial fashion forming an 8 bit word, 4 outputs from each ROM. Because 12 bits are required for data/address information, two sequential addresses must be accessed from the ROMs to form a 16 bit word. Where the first 8 bits are temporarily stored in a register, then the next 8 bits are accessed from the ROMs. At this point the control then decides what to do with 12 of the 16 bits. There are four possible actions that can take place at this time:

- a) Load Address
- b) Load Extended Address, IF AND DF
- c) Deposit
- d) Start

The remaining 4 bits of the 16 actually tell the control which of the four actions are to take place. So the 16 bit word would look like the word in Figure 1.

	SIZE A	CODE SP	NUMBER KM8-A-7	REV B
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DEC FORM NO DEC 16-(381)-1022-N370
DRA 108

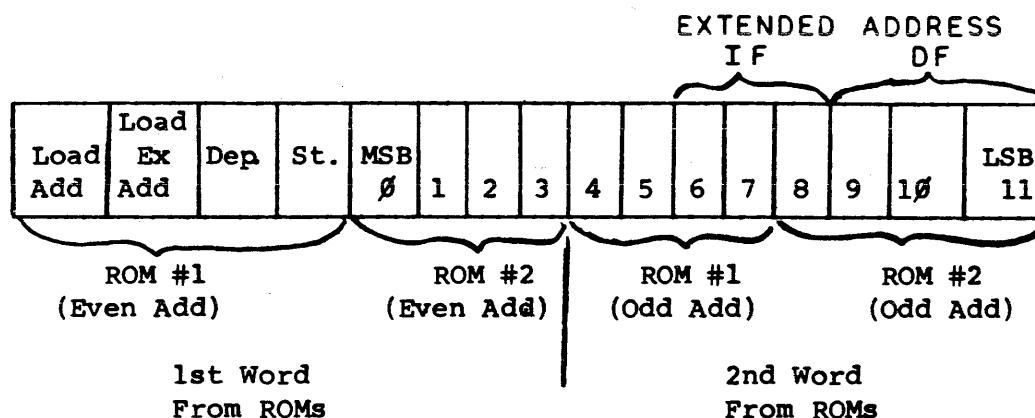
SHEET 2 OF 6

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE ROM PROGRAMMING DIRECTIONS FOR 8A OPTION BOARD #2 KM8-AD (M8317-YC)

Figure 1



The use of ROMs that have 256 addressable locations allows up to 128 words of ROM storage. These 128 locations may be used for Bootstrap and/or Auto-restart programs. Any Auto-restart or Bootstrap program may be located anywhere in the ROMs so long as the program starts in an even address in the ROM. If it is required that both Bootstrap and Auto-restart programs be accessible at the same time, activated by different signals; of course the Auto-restart program(s) must be located in addresses 0 through 15 in the ROMs. This is due to the addressing limits of the Auto-restart select switches.

3. Auto-Restart/Bootstrap Sequence

The following events should take place when an auto-restart is initiated:

- Load a 12 bit address
- LOAD THE IF AND DF AND START.

The following events should take place when the Bootstrap is initiated:

- Load a 12 bit initial address.
- Load the IF AND DF
- Deposit 12 bit data words repeating as required by length of program to be deposited.
- Load a 12 bit starting address and start.

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE ROM PROGRAMMING DIRECTIONS FOR 8A OPTION BOARD #2 KM8-AD (M8317-YC)

The decision to do a Bootstrap or an auto-restart is directed by a set of switches on the module. The Bootstrap may be actuated by the transition of the signal AC Low from a logic low to a logic high or by a similar transition of the SW line on the OMNIBUS.

AN AUTO-RESTART MAY ONLY BE INITIATED BY THE AC LOW SIGNAL. IT SHOULD BE OBVIOUS THAT BOTH THE BOOTSTRAP OR AUTO-RESTART SHOULD NOT BE ACTIVATED BY THE SAME INITIALIZING SIGNAL.

4. ROM Programming Examples

Auto-restart example:

- Load address $\emptyset 0\emptyset 0$
- Load field 0, start

Starting at ROM address $\emptyset 04$

Bootstrap example:

- Load address $\emptyset 023$
- Load field 7 (BOTH IF AND DF)
- Deposit $20\emptyset\emptyset$
- Deposit 6745
- Deposit $\emptyset 023$
- Deposit 7650
- Deposit 5024
- Deposit 6733
- Deposit 5031
- Load address $\emptyset 024$ and start

Starting at ROM address 124.

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE ROM PROGRAMMING DIRECTIONS FOR 8A OPTION BOARD #2 KM8-AD (M8317-YC)

Auto-Restart example:

Bit Add	ROM #1				ROM #2			
	4	3	2	1	4	3	2	1
4	1	0	0	0	0	0	0	0
5	1	0	0	0	0	0	0	0
6	0	1	0	1	0	0	0	0
7	0	0	0	0	0	0	0	0

Load Address
0200
Load Ext. Add 0
and Start

NOTE: Logic one (1) = +3V

Bootstrap example:

Bit Add	ROM #1				ROM #2			
	4	3	2	1	4	3	2	1
124	1	0	0	0	0	0	0	0
125	0	0	0	1	0	0	1	1
126	0	1	0	0	0	0	0	0
127	0	0	1	1	1	1	1	1
130	0	0	1	0	0	1	0	0
131	0	0	0	0	0	0	0	0
132	0	0	1	0	1	1	0	1
133	1	1	1	0	0	1	0	1
134	0	0	1	0	0	0	0	0
135	0	0	0	1	0	0	1	1
136	0	0	1	0	1	1	1	1
137	1	0	1	0	1	0	0	0
140	0	0	1	0	1	0	1	0
141	0	0	0	1	0	1	0	0
142	0	0	1	0	1	1	0	1
143	1	1	0	1	1	0	1	1
144	0	0	1	0	1	0	1	0
145	0	0	0	1	1	0	0	1
146	1	0	0	1	0	0	0	0
147	0	0	0	1	0	1	0	0

Load Add 24 & Start

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE ROM PROGRAMMING DIRECTIONS FOR 8A OPTION BOARD #2 KM8-AD (M8317-YC)

5. ROMs

Unprogrammed ROMs should be purchased by the user from Digital Equipment Corporation. The part number for an unprogrammed 256 x 4 ROM is 23-000A2.

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DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

ENGINEERING SPECIFICATION

DATE 11/19/74

TITLE FIELD INSTALLATION & ACCEPTANCE PROCEDURE FOR DKC8-A

REVISIONS

REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE

ENG *J. Martin* 12/20/74 APPD *Carl Ober* SIZE A NUMBER DKC8-A-1 REV

DEC 16-(392)-1079-N971
DRA 107

SHEET 1 OF 4

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE

FIELD INSTALLATION & ACCEPTANCE PROCEDURE FOR DKC8-A

I GENERAL

This procedure defines the performance standard required of the DKC8*, option board #1. This procedure refers to both system acceptance and add-on acceptance.

NOTE: If DKC8 was shipped as part of a PDP-8A system, proceed to installation procedure.

* Serial Line Unit
Real Time Clock
Parallel I/O
Programmer's Console Logic

II INSPECTION

After removing the DKC8 from the packing material, inspect the module for the following:

1. Inventory hardware against shipping list.
2. Inventory software against software list, if ordered.
3. Inventory prints against shipping list, if ordered.
4. Check hardware for loose or broken components.

III INSTALLATION PROCEDURE

Install the equipment using the following procedure:

1. Set up switches as indicated by the diagnostic write up.

S1-1 thru S1-3	"ON"	9600 baud
S1-4	"ON"	Normally "ON"
S1-5	"ON"	Real Time Clock Enable
S1-6	"ON"	Normally "ON"
S1-7	"ON"	One Stop Bit
S1-8	"OFF"	Disable TTY 20 MA Filter

NOTE: Reference Operator's Handbook for switch setting descriptions.

2. Insert TTY loop back cable (DEC Part #7008517) on DKC8.
3. Insert parallel I/O cable loop back cable (DEC Part # BC08R-1) on DKC8.

SIZE A NUMBER DKC8-A-1 REV

DEC FORM NO DEC 16-(381)-1022-N370
DRA 108

SHEET 2 OF 4

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE FIELD INSTALLATION AND ACCEPTANCE PROCEDURE FOR DKC8-A

III INSTALLATION PROCEDURE (continued)

4. Insert two programmer's console cables on DKC8.
5. Insure that the 8A Power is removed from the Omnibus™.
6. Insert DKC8 into the second or third slot of the Omnibus™.
7. Turn the power back "ON".
8. Check the operation of the programmer's console.

IV ACCEPTANCE PROCEDURE

Perform the acceptance procedure defined in Table A. If abnormal indications are encountered, refer to the diagnostic listing for type of error. Reference the diagnostic write ups and operator's manual for instructions on loading diagnostics.

Equipment Required:

1. PDP-8A with 1K or more R/W Memory
2. Paper Tape Input Device
3. Programmer's Console(KC8-A)
4. Diagnostic and Listings
5. TTY loop Back Cable
6. Parallel I/O Loop Back Cable
7. W987 Quad Extender

NOTE: If the programmer's console and paper tape input device are not available as part of the system being used, they must be supplied in good working order by the customer.

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE FIELD INSTALLATION & ACCEPTANCE PROCEDURE FOR DKC8-A

TABLE A

Acceptance of DKC8 with 4K or More R/W Memory

Program Name	Maindec #	Accept Time	Restriction
DKC8-AA Option Test #1	08-DJDKA-PB	30 min	4K R/W Memory

Acceptance of DKC8 with less than 1K of R/W Memory

DKC8-AA Opteion Test #1 Segment #1 (RIM)	08-DJDKA	-PM1	10 min	1K R/W Memory
DKC8-AA Option Test #1 Segment #2 (RIM)	08-DJDKA	-PM2	10 min	1K R/W Memory
DKC8-AA Option Test #1 Segment #3 (RIM)	08-DJDKA	-PM3	10 min	1K R/W Memory
DKC8-AA Option Test #1 Segment #4 (RIM)	08-DJDKA	-PM4	10 min	1K R/W Memory

	SIZE A	CODE SP	NUMBER DKC8-A-1	REV
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SHEET 3 OF 4DEC FORM NO DEC 16-(381)-1022-N370
DRA 108

	SIZE A	CODE SP	NUMBER DKC8-A-1	REV
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SHEET 4 OF 4DEC FORM NO DEC 16-(381)-1022-N370
DRA 108

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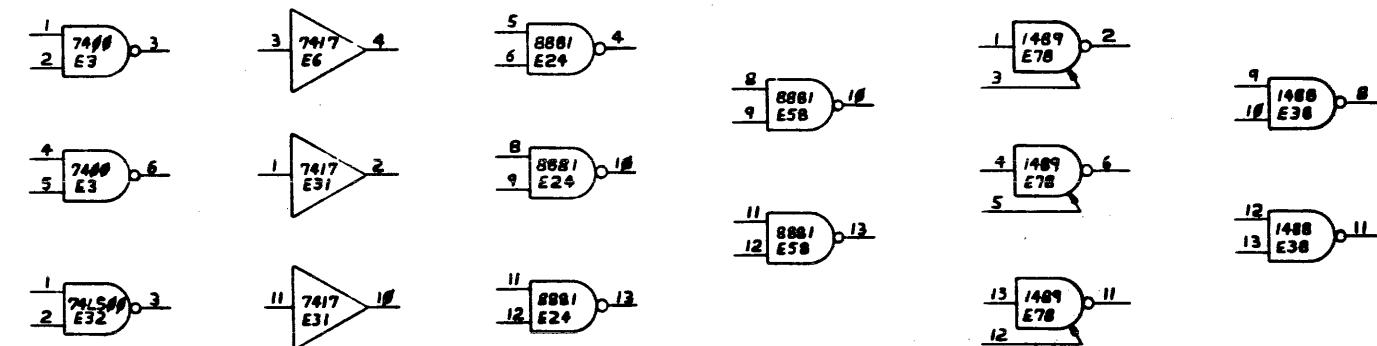
REVISIONS	REV.
DATE	CHANGED BY
ORIGINATED	H
DATA M8316-0-1	1
PC BOARD	1
SCANTLEADER	1

C

M8316 SWITCH SETTINGS

- SI-1
SI-2 } SERIAL LINE BAUD RATE
SI-3
SI-4 } (SEE CHART)
- SI-5 ON = REAL TIME CLOCK ENABLED
OFF = REAL TIME CLOCK DISABLED
SI-6 ON = TEST SWITCH (ALWAYS ON)
SI-7 ON = 1 STOP BIT IN SLU CHARACTER
OFF = 2 STOP BITS IN SLU CHARACTER
SI-8 ON = ASR/KSR 33 DR35 FILTER IN
(ACROSS SLU 20 MA REC'D LEADS)
OFF = FILTER OUT
SI-9 ON = TSI CLEARS "DATA AVAIL" F/F IN
PARALLEL I/O SECTION
OFF = "DATA AVAIL" NOT CLEARED BY TSI

SPARES



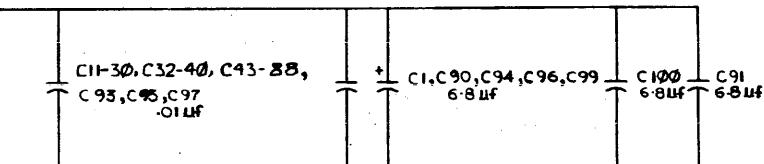
SLU BAUD RATE SELECT CHART

SI-4	SI-3	SI-2	SI-1	BAUD RATE
ON	ON	ON	ON	50 BAUD
ON	ON	ON	OFF	75 BAUD
ON	ON	OFF	ON	110 BAUD
ON	ON	OFF	OFF	134.5 BAUD
ON	OFF	ON	ON	150 BAUD
ON	OFF	ON	OFF	300 BAUD
ON	OFF	OFF	ON	600 BAUD
ON	OFF	OFF	OFF	1200 BAUD
OFF	ON	ON	ON	1800 BAUD
OFF	ON	ON	OFF	2000 BAUD
OFF	ON	OFF	ON	2400 BAUD
OFF	ON	OFF	OFF	3600 BAUD
OFF	OFF	ON	ON	4800 BAUD
OFF	OFF	ON	OFF	7200 BAUD
OFF	OFF	OFF	ON	9600 BAUD
OFF	OFF	OFF	OFF	19.2 K BAUD

* SERIAL LINE WILL NOT RUN AT THIS BAUD RATE.
THIS SETTING IS NOT TO BE USED

*

AA2.BA2.CA2 +5V



AC1, AC2, AF1, AF2, AN1,
AN2, AT1, AT2, BC1, BC2,
BF1, BF2, BN1, BN2, BT1,
BT2, CC1, CC2, CF1, CF2.

COMPONENT SUBSTITUTION CHART

PART CALLED P/N	DESCRIPTION	SUBSTITUTE PART	
		P/N	DESCRIPTION
1001610-01	0.01μF DISC.	1001610-00	0.01μF GLASS
1909705	8881	190973	97401
		1910392	5380
		190971	6380
		1910390	7380
		1911113	11380
1912824	74LS74	1905547	7474
1912799	74LS00	1905575	7400
1912807	74LS10	1905576	7410
1912815	74LS30	1905578	7430
1912801	74LS02	1909004	7402
1912803	74LS04	1909686	7404
1912814	74LS42	1910046	7442
1912805	74LS08	1910155	7408
1912853	74LS175	1910651	74175
1912697	74LS174	1910652	74174

DRA. Hymann	3-4-76	FIRST USED ON	DKC 8A
CHK'D	3-30-76	REVISION	Digitized
ENG.	7-7-76	TITLE	OPTION BD #1
PROD. ENGR.	7-2-76		
NEXT HIGHER ASY.			
D-LA-M8316-0-0		SIZE	CODE
		SCALE	NUMBER
		SHEET	REV.
		1	H

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B

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A

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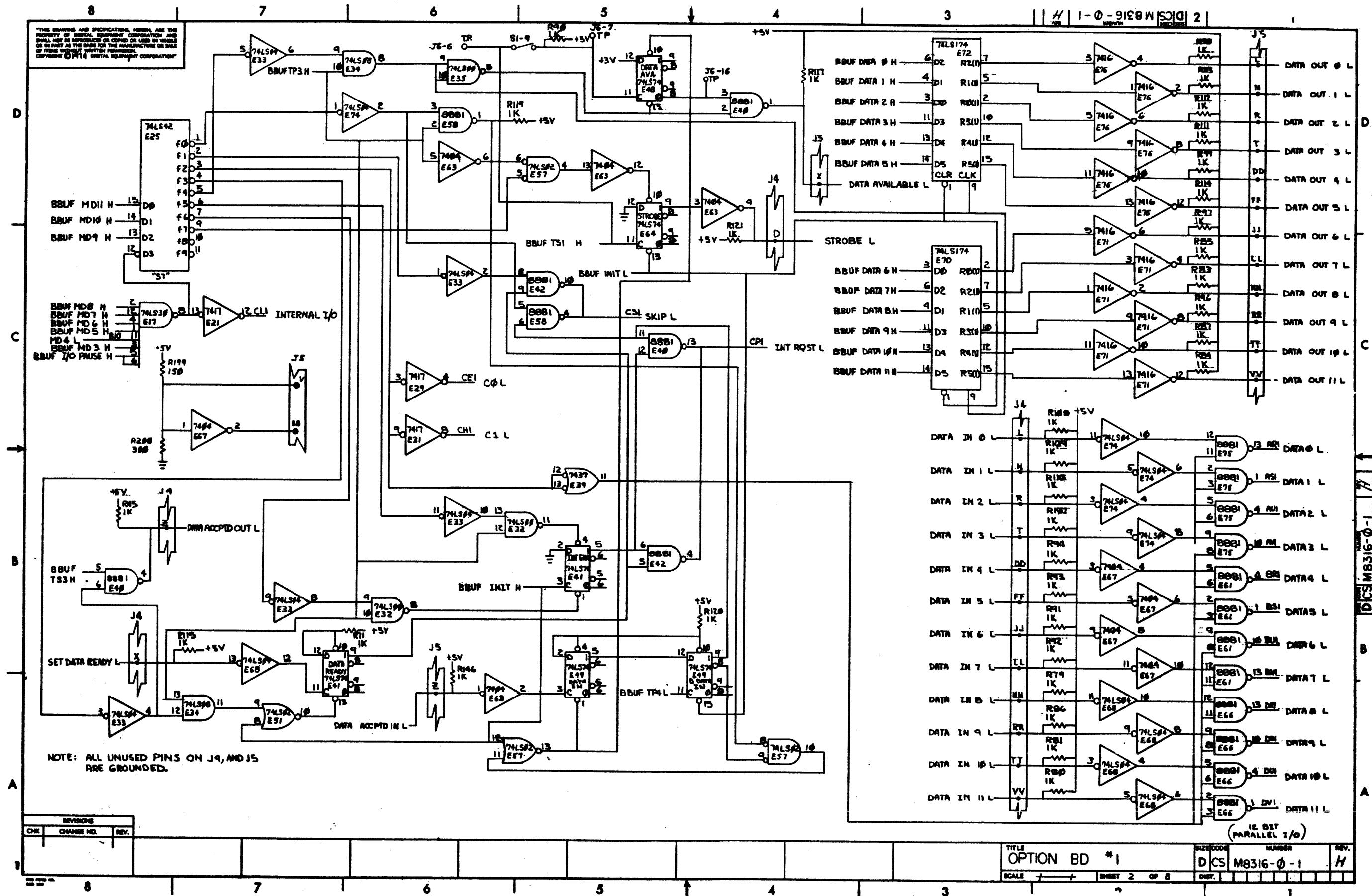
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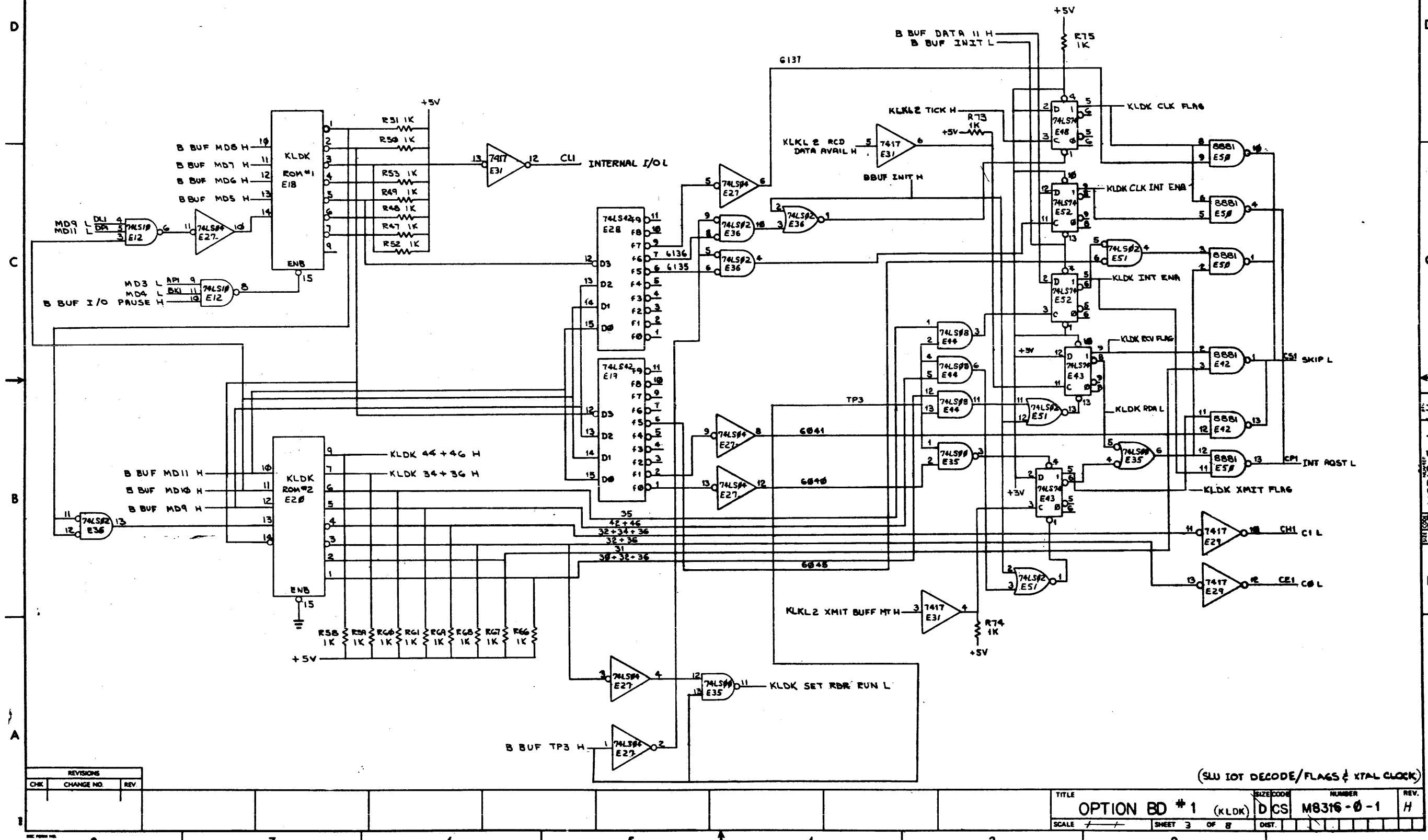
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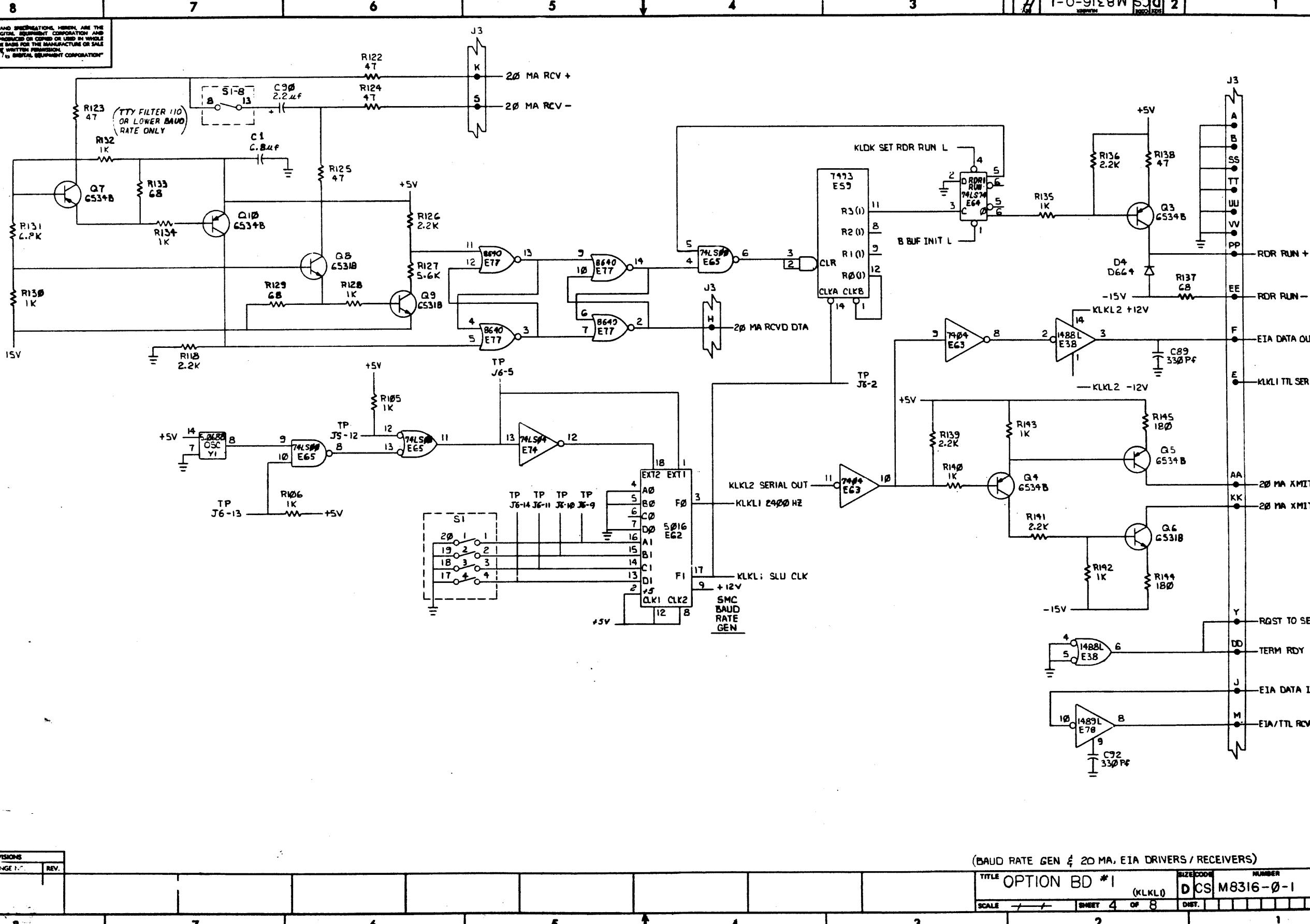
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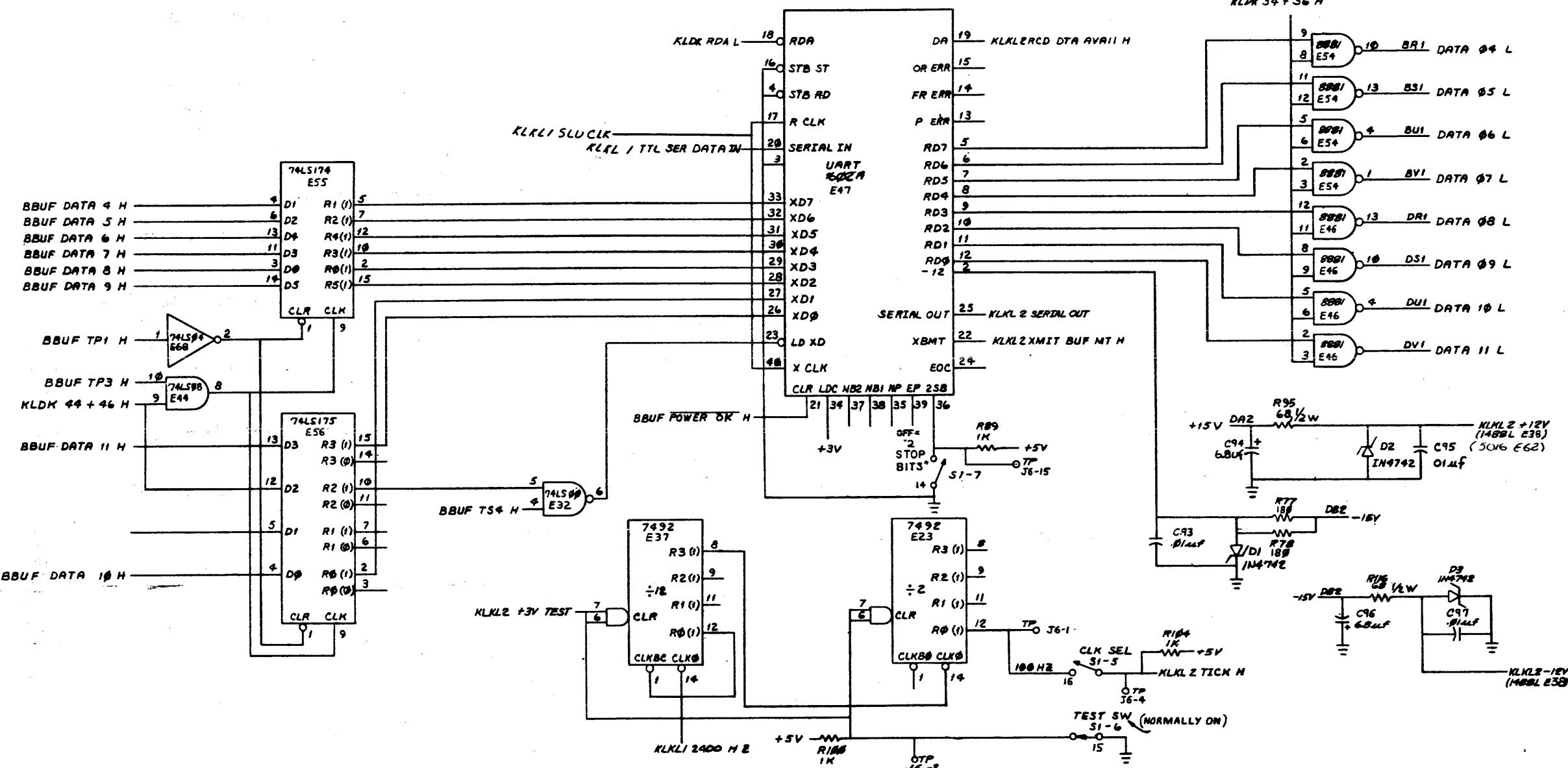
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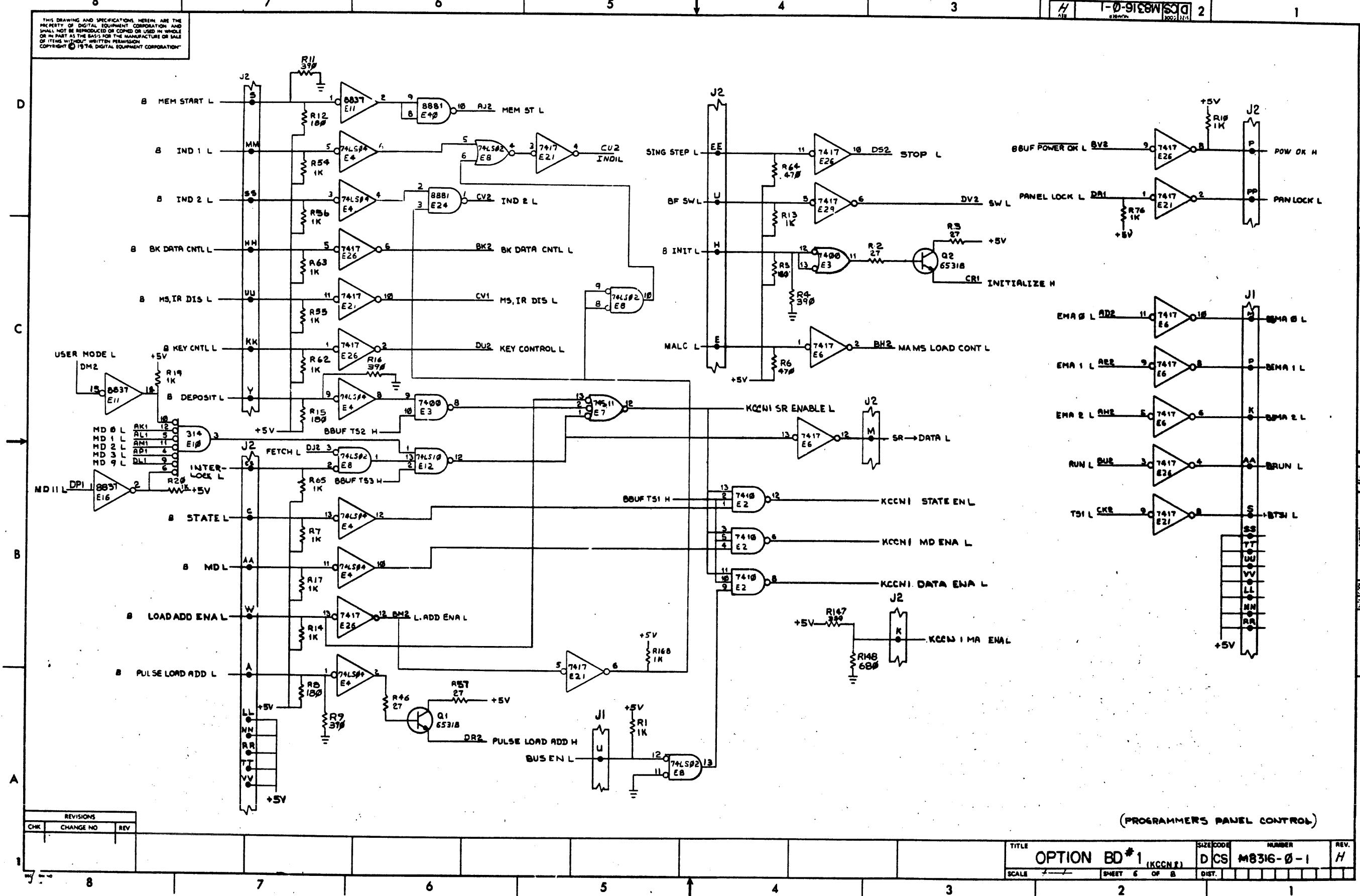


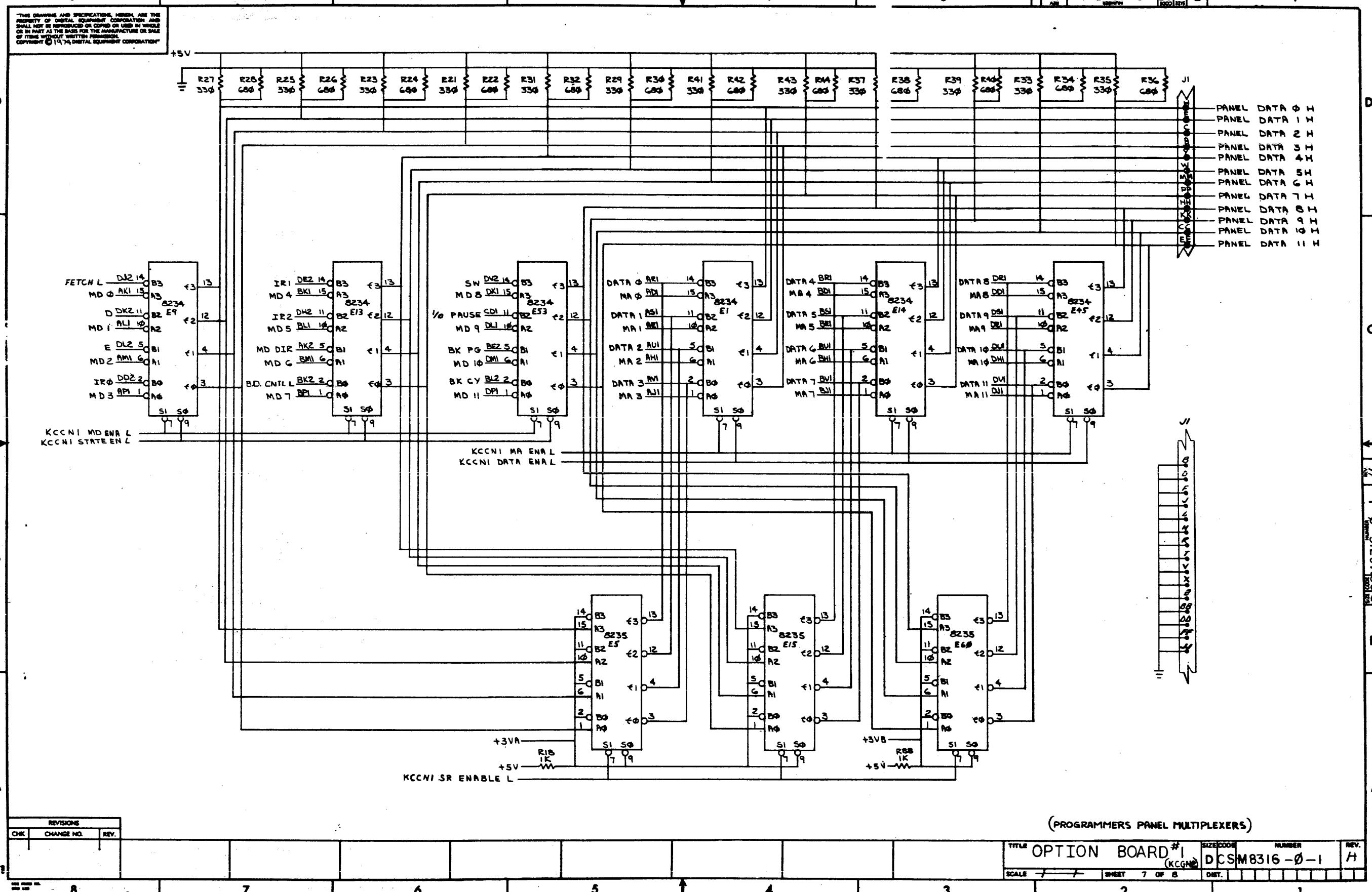
REVISIONS		
CHK	CHANGE NO.	REV.
1		

(UART & XTAL CLK FREQ SOURCE)

TITLE: **OPTION BD #1 (KLKL 2)** SHEET 5 OF 8
SCALE: **1/1** DIST: **1**
SIZE/CODE: **DCS M8316-0-1** REV: **H**

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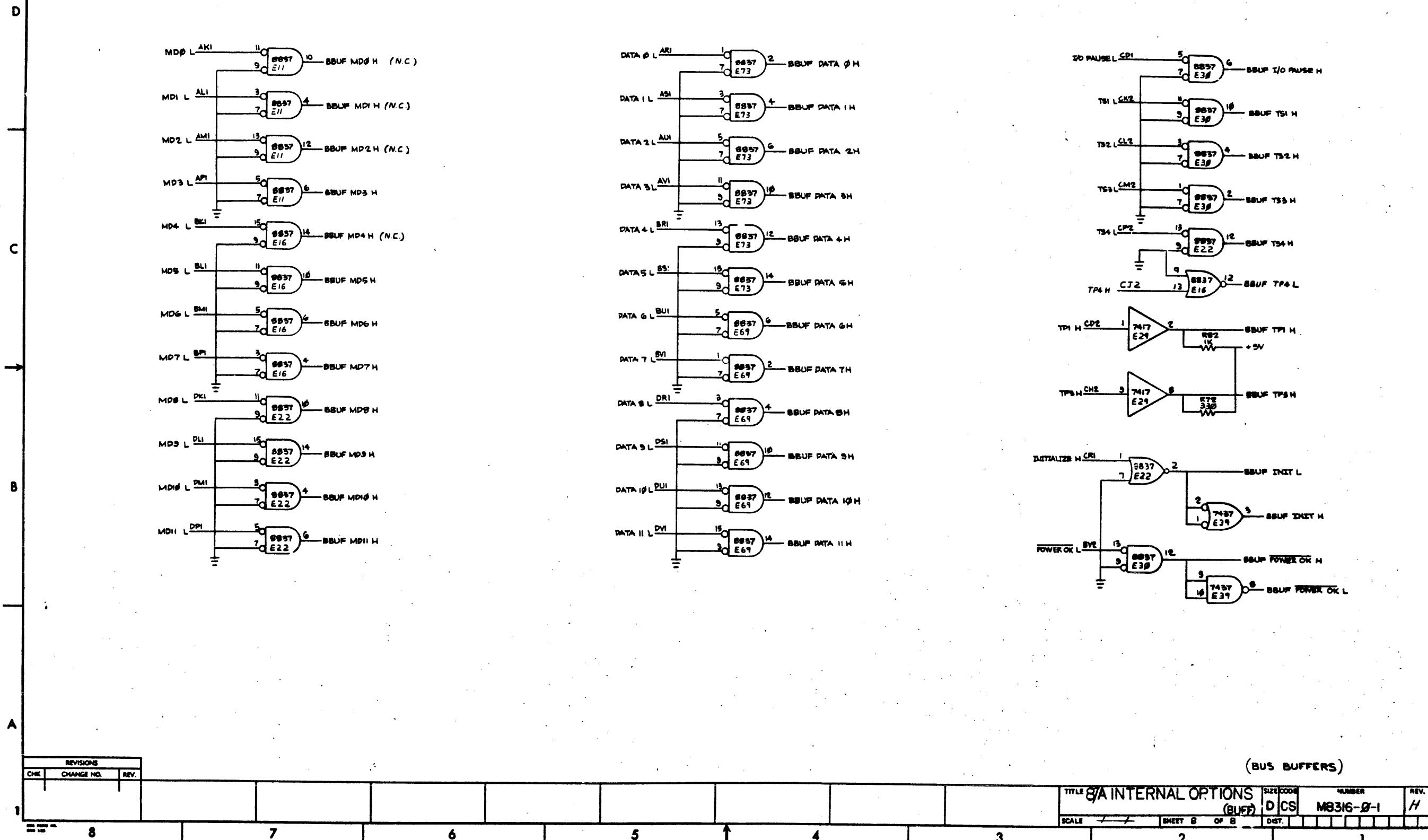


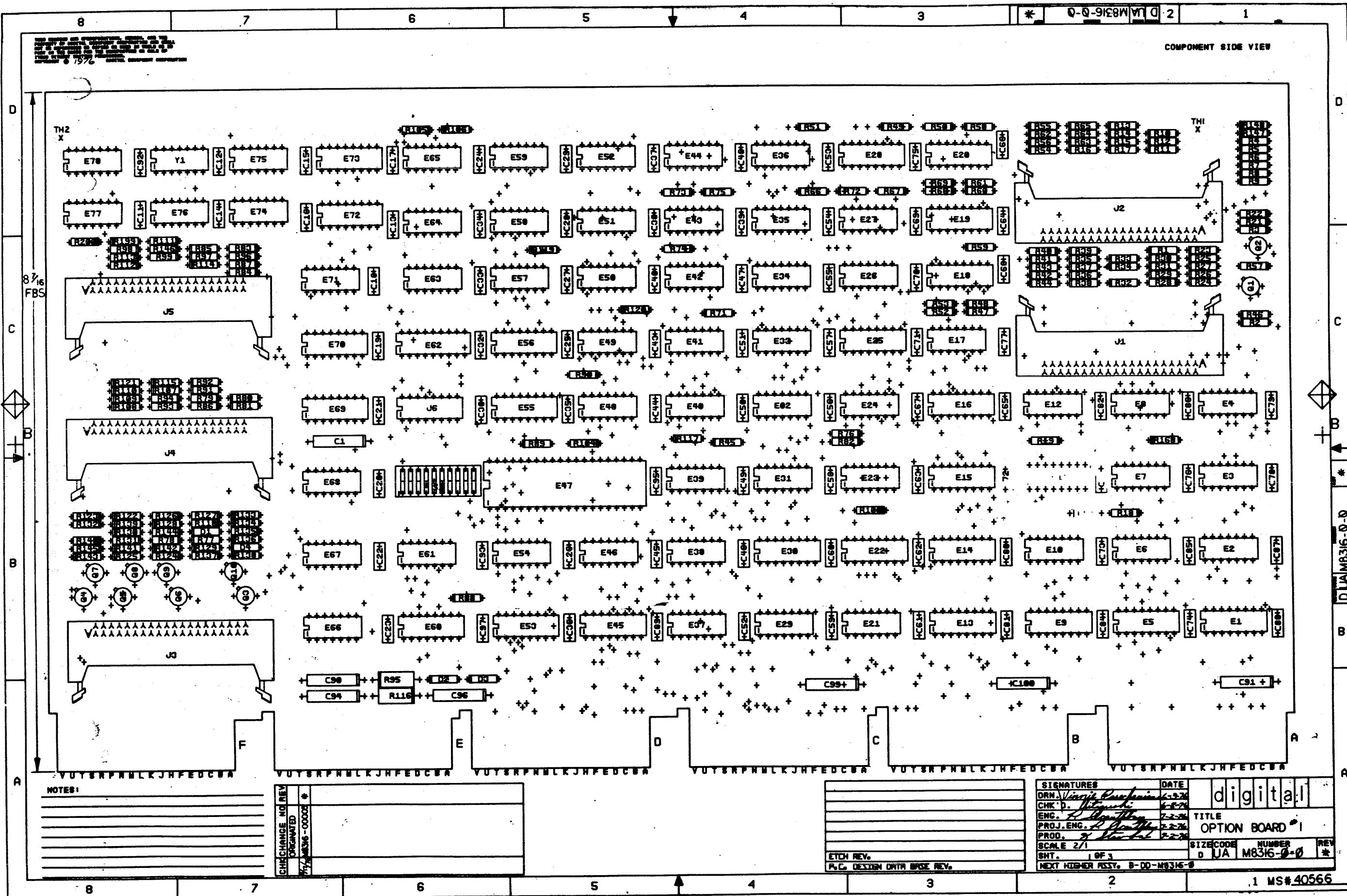


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H 1-0-9189 M3816-0-1 DCS 2

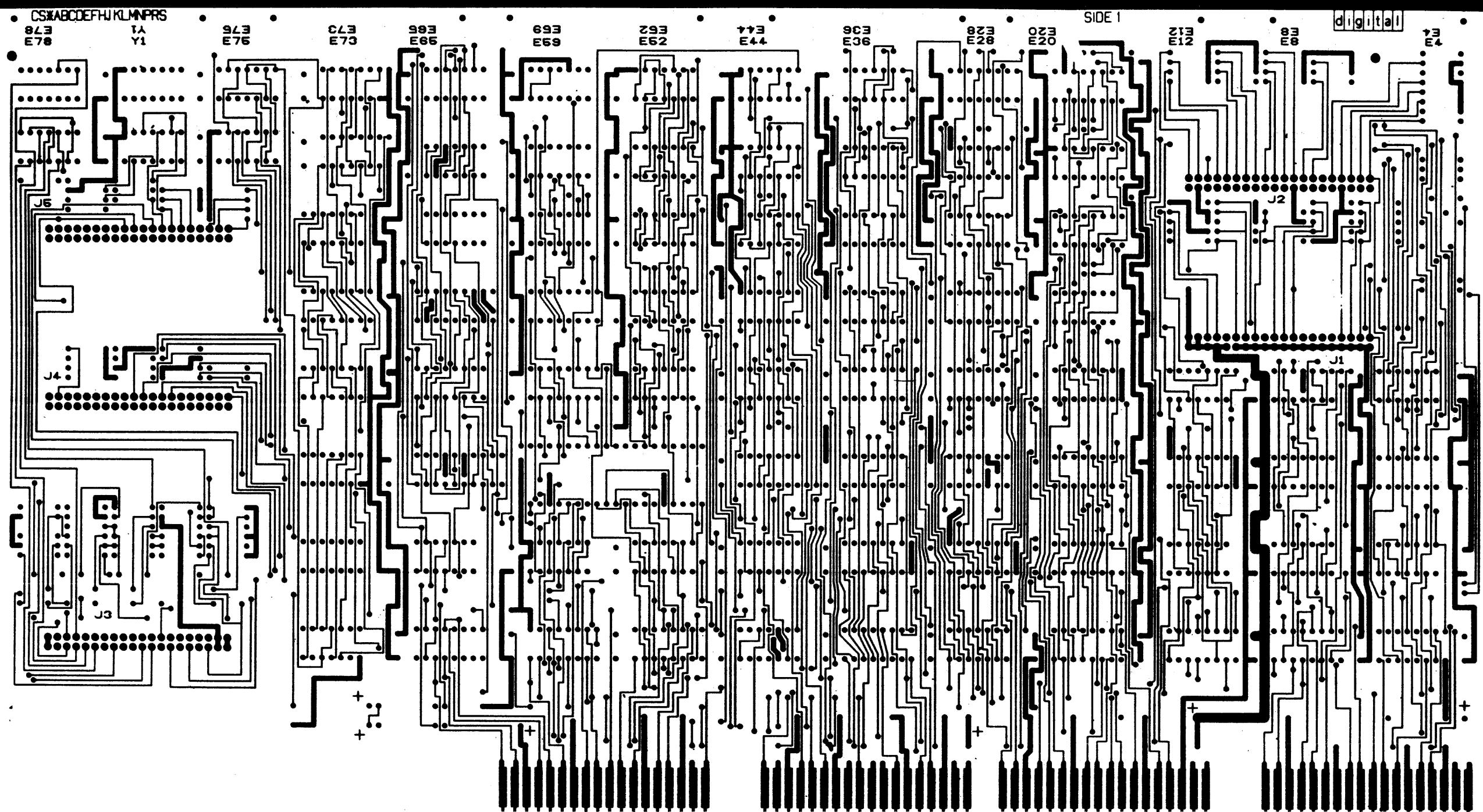




8 7 6 5 4 3 2 1

L1 MS40566 M8316 5010900D P4

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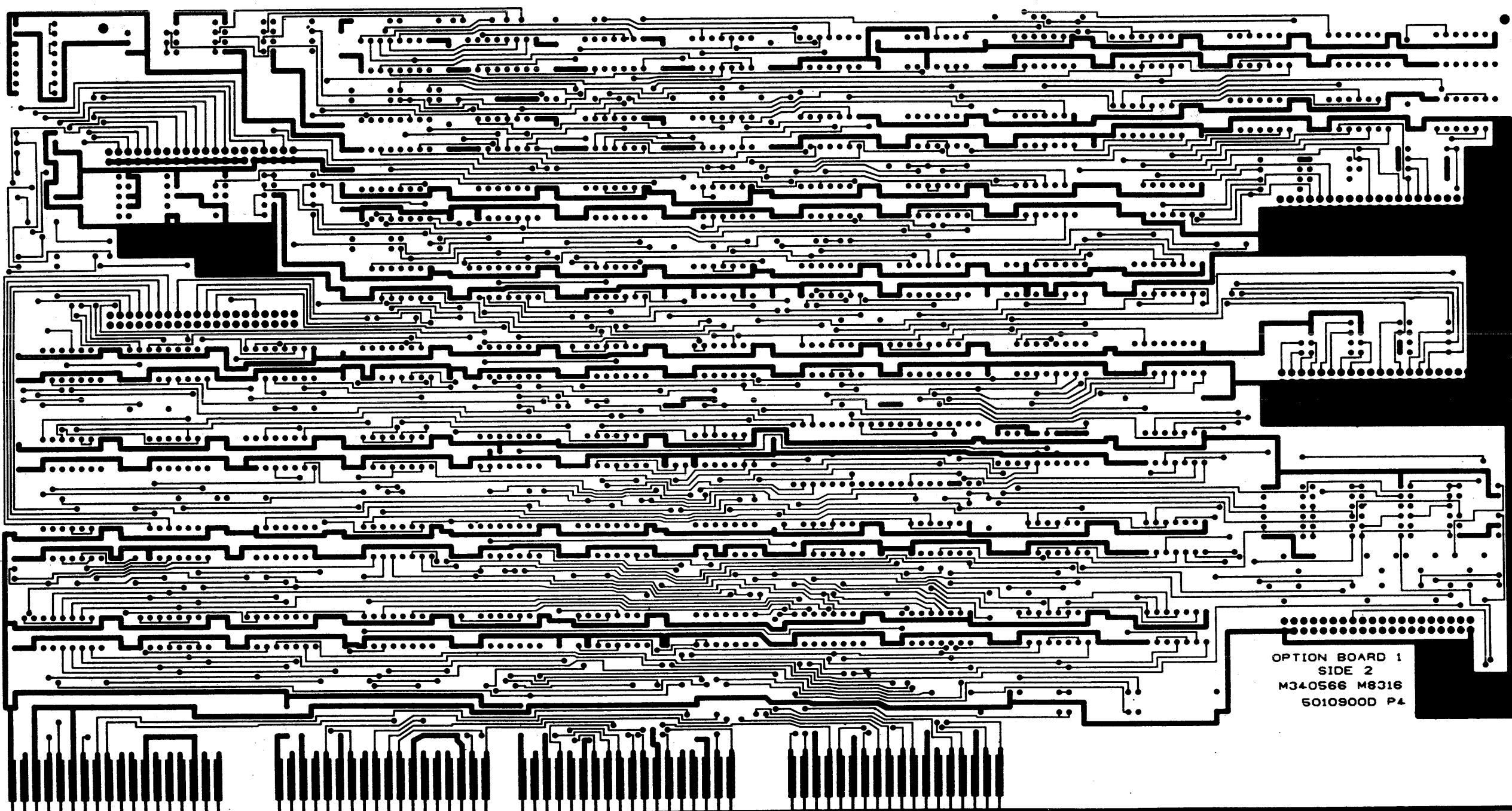
REVISIONS

CHG. CHANGE NO. REV.

TITLE			OPTION BOARD #1	SIZE CODE	NUMBER	REV.
SCALE			2/1	SHEET	2 OF 3	DIST.

1 8 7 6 5 4 3 2 1

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REVISIONS

CHG. CHANGE NO. REV.

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* D U A M8316-0-0 2

1

REV.

TITLE: OPTION BOARD *1
SCALE: 2/1 SHEET: 3 OF 3
SIZE CODE: D U A M8316-0-0
NUMBER: *
DIST:

DIGITAL EQUIPMENT CORPORATION PARTS LIST				QUANTITY / VARIATION								NOTES:		
ITEM NO.	DRAWING NO.	PART NO.	DESCRIPTION	M8316-Ø-Ø										
1	D-MD-5010900-0-0	5010900	ETCHED CIRCUIT BOARD	1										
2		1001610-01	CAP, .01 UF, 100V, 10%	78									C11-C3Ø, C32-C4Ø, C43 - C88, C93, C95, C97	
3		1000023	CAP, 33Ø PF, 100V, 5%	2									C89, C92	
4		1002431	CAP, 2.2 UF, 35V, 10%	1									C9Ø	
5		1005306	CAP, 6.8 UF, 35V, 10%	3									C91, C94, C96, C99-C1ØØ, C1,	
6		1109502	DIODE, IN4742	3									D1-D3	
7		1100114	DIODE, D664	1									D4	
8		1211164-06	SWITCH PACK (1Ø POS DIP)	1									S1	
9		1211813-02	IC SOCKET (16 PIN)	1									J6	
10		1210711-02	HANDLE ASSY	1										
11		1209941-02	CONNECTOR 4Ø P. RT. ANG. HD	5									J1-J5	
12		1209941-03	LATCH LEFT 4Ø P. RT. ANG. HD	5										
13		1209941-04	LATCH RIGHT 4Ø P RT. ANG. HD	5										
14		1300202	RES., 47, 1/4W, 5%	5									R122-R125, R138	
15		1300219	RES., 68, 1/4W, 5%	3									R129, R133, R137	
16		1301424	RES., 68Ø, 1/4W, 5%	13									R22, R24, R26, R28, R3Ø, R32, R34, R36 R38, R4Ø, R42, R44, R148	
17		1300417	RES., 2.2K, 1/4W, 5%	5									R118, R126, R136, R139, R141	
18		1301874	RES., 5.6K, 1/4W, 5%	1									R127	
19		1301423	RES., 6.8K, 1/4W, 5%	1									R131	
20		1301522	RES., 27, 1/4W, 5%	4									R2, R3, R46, R57	
Q.C. NO.														
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EN-01140A-16-R276(325) DRB 125					SHEET 1 OF 4			ASSY NO.	SIZE B CODE PL		NUMBER M8316-Ø-Ø	REV. *	INSERTION PARTS LIST DATA BASE REV D	

DIGITAL EQUIPMENT CORPORATION PARTS LIST				QUANTITY / VARIATION								NOTES:		
MADE BY BOB KOPPENAL DATE 3-31-76		CHECKED DATE	SECTION	M8316-Ø-Ø										
ENG DATE		PROD DATE	ISSUED SECTION											
ITEM NO.	DRAWING NO.	PART NO.	DESCRIPTION									REF DESIGNATION		
21		1301322	RES., 18Ω, 1/4W, 5%	8									R5, R8, R12, R15, R77, R78, R144, R145	
22		1300295	RES., 33Ω, 1/4W, 5%	15									R21, R23, R25, R27, R29, R31, R33, R35, R37, R39, R41, R43, R147, R200, R72	
23		1300309	RES., 39Ω, 1/4W, 5%	4									R4, R9, R11, R16	
24		1300316	RES., 47Ω, 1/4W, 5%	2									R6, R64	
25		1300365	RES., 1K, 1/4W, 5%	83									R1, R7, R10, R13, R14, R17-R20, R45, R47-R56, R71, R58-R63, R65-R69, R73-R76, R79-R81, R82-R94, R117, R96-R100, R104-R115, R119-R121, R128, R130, R132, R135, R140, R142, R143, R146, R168, R134	
26		1309405	RES., 68, 1/2W, 5%	2									R116, R95	
27		1300250	RES., 15Ω, 1/4W, 5%	1									R199	
28		1509338	TRANSISTOR, DEC 6531B	5									Q1, Q2, Q6, Q8, Q9	
29		1503409-01	TRANSISTOR, DEC 6534B	5									Q3, Q4, Q5, Q7, Q10	
30		1811660-02	CRYSTAL OSCILLATOR, 5.0688 MHZ	1									Y1	
31		1912824	IC., 74LS74	6									E41, E43, E52, E64, E48, E49	
32		1912799	IC., 74LS00	3									E32, E35, E65	
33		1912807	IC., 74LS10	1									E12	
34		1912815	IC., 74LS30	1									E17	
35		1912801	IC., 74LS02	4									E8, E36, E51, E57	
36		1909053	IC., 7492	2									E23, E37	
37		1909054	IC., 7493	1									E59	
38		1910537	IC., 74S11	1									E7	
39		1912803	IC., 74LS04	5									E4, E27, E33, E74, E68,	
ECO NO.														

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TITLE

OPTION BOARD #1

ASSY NO.
D-UA-M8316-Ø-Ø

SIZE
B

CODE
PL

NUMBER
M8316-Ø-Ø

REV.
*

SHEET 2 OF 4

INSERTION PARTS LIST DATA BASE REV D

DIGITAL EQUIPMENT CORPORATION
PARTS LIST

MADE BY BOB KOPPENAL	CHECKED	SECTION
DATE 4 NOV 76	DATE	
ENG	PROD	ISSUED SECTION
DATE	DATE	

ITEM NO.	DRAWING NO.	PART NO.	DESCRIPTION	M8316-Ø-Ø	QUANTITY / VARIATION										NOTES:
					1	2	3	4	5	6	7	8	9	10	
40		1909705	IC., 8881		10										E40, E24, E54, E42, E46, E50, E66, E61, E75, E58
41		1909928	IC., 7416		2										E71, E76
42		1909929	IC., 7417		5										E6, E26, E31, E29, E21
43		1909935	IC., 8235		3										E5, E15, E60
44		1912819	IC., 74LS42		3										E19, E28, E25
45		1912805	IC., 74LS08		2										E34, E44
46		1910322	IC., 1488L		1										E38
47		1910323	IC., 1489L		1										E78
48		1909704	IC., 314A		1										E10
49		1911469	IC., 8640		1										E77
50		1910459	IC., 1602A (UART)		1										E47
51		1912853	IC., 74LS175		1										E56
52		1912697	IC., 74LS174		3										E55, E70, E72
53		1910091	IC., 7437		1										E39
54		1911315	IC., 8234		6										E1, E9, E14, E13, E45, E53
55		1911116	IC., 8837		6										E11, E16, E30, E22, E69, E73
56		2112623	IC., 5016 (BAUD RATE GEN.)		1										E62
57		23062A1	ROM #1 KLDK #1, 32 X8		1										E18
58		23063A1	ROM #2 KLDK #2, 32 X 8		1										E20
59		1905575	IC., 7400		1										E3
E.C.O. NO.															

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EN-01140A-16-R276(325)

DRB 125

TITLE

OPTION BOARD #1

ASSY NO.

D-UA-M8316-Ø-Ø

SHEET 3 OF 4

SIZE

B

CODE

PL

NUMBER

M8316-Ø-Ø

REV.

*

INSERTION PARTS LIST DATA BASE REV D

**DIGITAL EQUIPMENT CORPORATION
PARTS LIST**

MADE BY JACK MASON	CHECKED	SECTION
DATE 13 APRIL 76	DATE	1
ENG	PROD	ISSUED SECTION
DATE	DATE	1

ITEM NO.	DRAWING NO.	PART NO.	DESCRIPTION	M8316-Ø-Ø	QUANTITY / VARIATION												NOTES:
					REF DESIGNATION												
60		1905576	IC., 7410	1													E2
61		1909686	IC., 7404	2													E63, E67
62		9006732	EYELET	12													

ECO. NO.

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TITLE

OPTION BOARD #1

ASSY NO.
D-UA-M8316-Ø-Ø

SIZE CODE
B PL

NUMBER
M8316-Ø-Ø

REV.
*

SHEET 4 OF 4

INSERTION PARTS LIST DATA BASE REV D

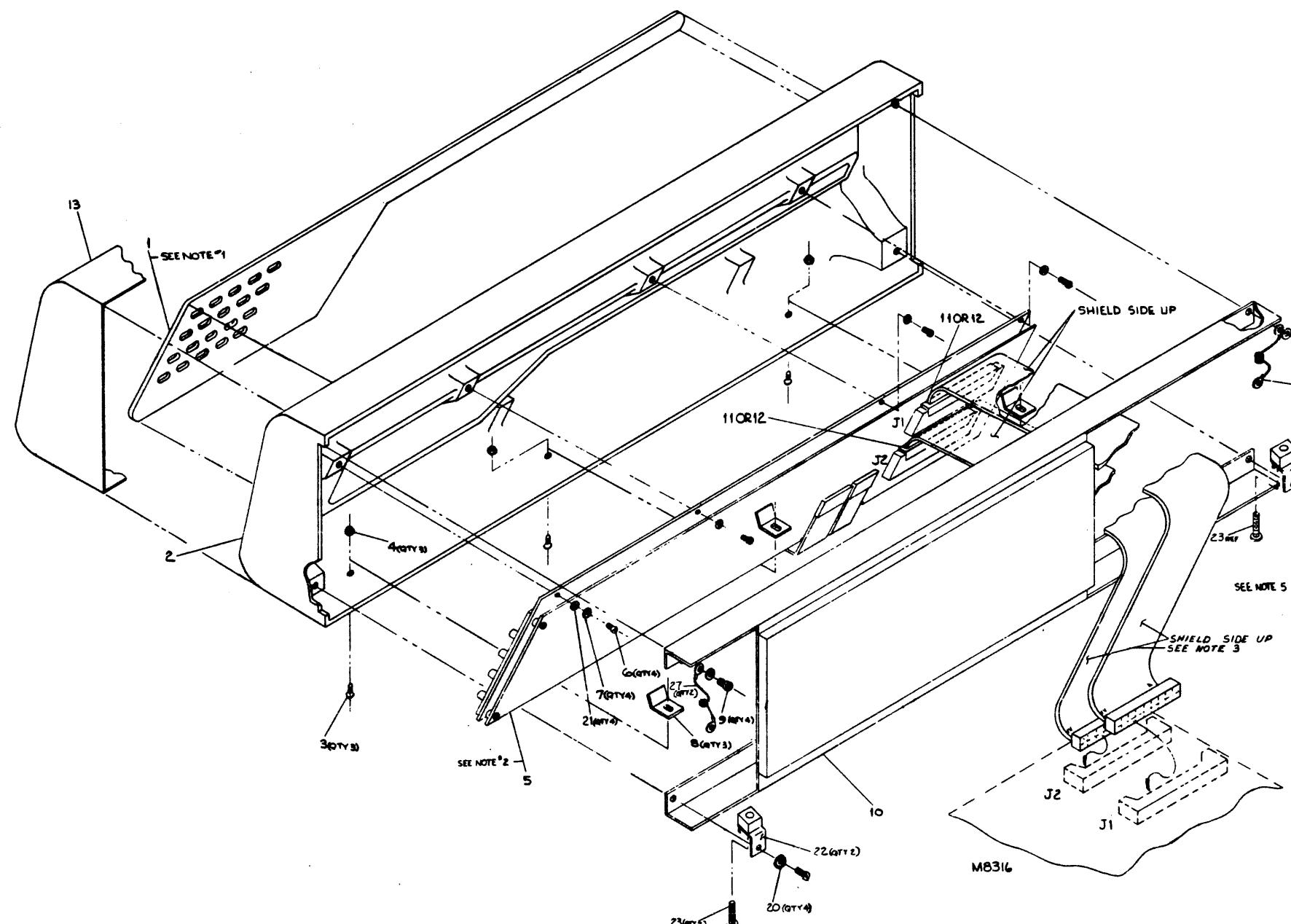
DO NOT SCALE DRAWING

LEGEND

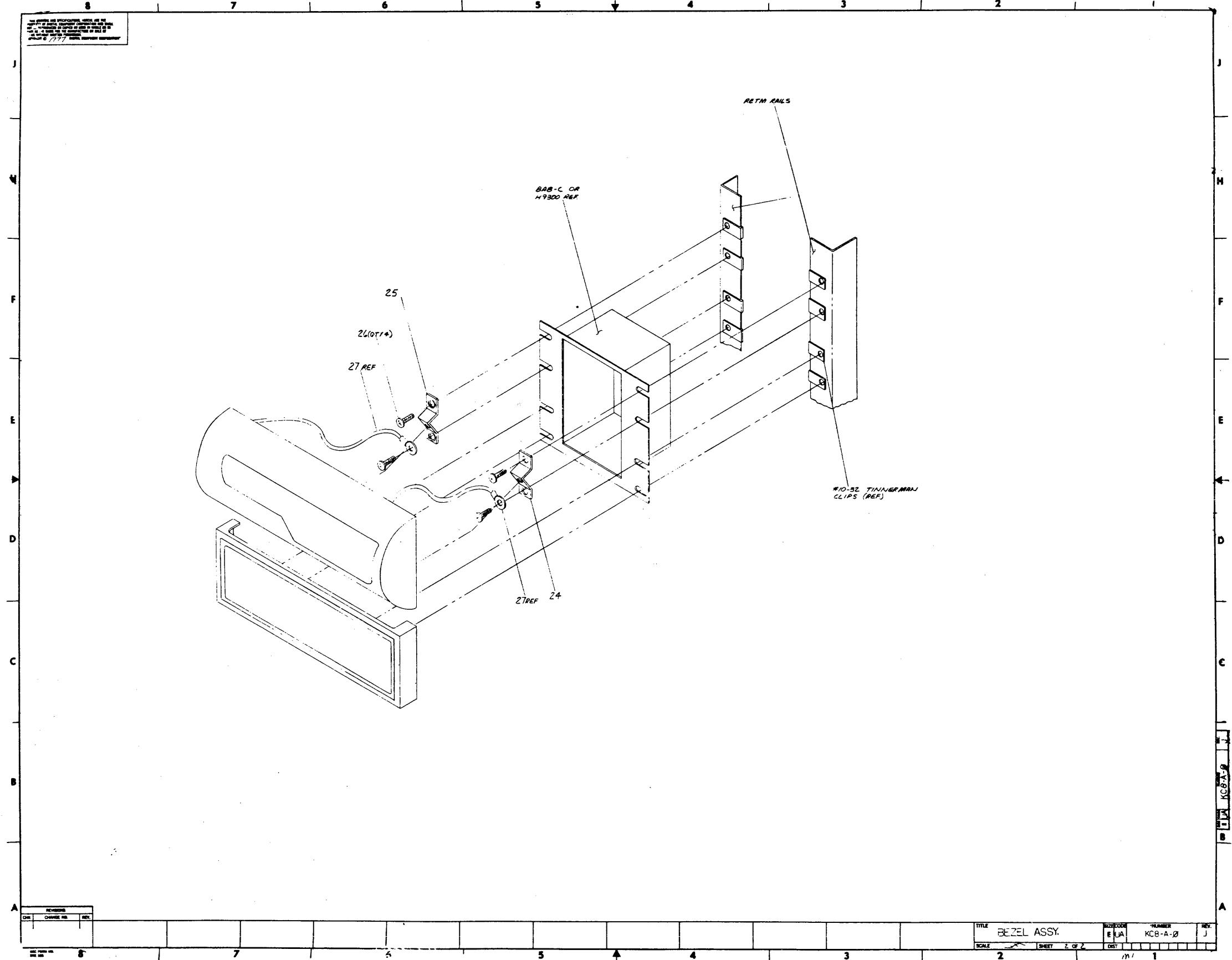
LEGEND	
PART. NO.	VARIATION
KCB-AA	PROG'S CONSOLE(NEUT PL.)
KCB-AB	REMOTE PROG'S CONSOLE(NEUT PL.)

- NOTES:**

 1. ASSEMBLE ITEM "1(PANEL) TO ITEM "2(BEZEL) USING SILASTIC ADHESIVE ITEM "1A.
 2. FOR PROPER ALIGNMENT OF KEYPAD OF MODULE ASSEMBLY (ITEM "5) INTO BEZEL (ITEM "2), DO THE FOLLOWING:
 - PLACE MODULE ASSEMBLY (ITEM "5) INTO BEZEL (ITEM "2).
 - START MOUNTING HARDWARE, ITEMS 6, 7 AND 21 INTO BEZEL.
 - BEFORE TIGHTENING DOWN ITEMS 6, 7 AND 21, PUSH MODULE ASSEMBLY TOWARDS THE TOP OF BEZEL. THE KEYPAD, WHEN VIEWED FROM THE FRONT OF BEZEL, SHOULD JUST TOUCH THE TOP EDGE OF THE KEY PAD CUTOUTS IN BEZEL.
 - TIGHTEN ITEMS 6, 7 AND 21.
 - NOW SECURE MODULE ASSEMBLY BOTTOM TO BEZEL WITH ITEMS 3, 4 AND 8; BEING SURE THAT CLIPS (ITEM "8) ARE PRESSED FIRMLY AGAINST MODULE ASSEMBLY.
 3. MATE BCB01 CABLES
J1(KCB-A) TO J1(MB316)
J2(KCB-A) TO J2(MB316)
 4. SEE SHEET 2 FOR INSTRUCTIONS OF MOUNTING BEZEL TO BOX AND CABINET LOOSE SHIP ITEMS 2A, 2B&2C WITH ASSY
 - 5.



AR	AR	INSTRUCTIONS.PNQ	3700087	28
7	2	CABLE ASSY, FLEX	1212070-01	27
4	4	SCR PFM 10-32X.50	9006673-03	26
1	1	BRIEF CABINET	7811704-1	25
1	1	BRIEF CABINET	7811709-0	24
2	2	SCR, SOC, HD 10 32X.50	9006670-08	23
2	2	BRIEF, BEZEL	C-1A 704524	22
4	4	WASHER FLAT	9006653	21
4	4	WASH. EXT TOOTH LOCK "B"	9008151	20
4	4	SCREW, PAN HEAD	9006650	19
4	4	SCREW, PAN HEAD	9006650	18
4	4	SCREW, PAN HEAD	9006650	17
4	4	SCREW, PAN HEAD	9006650	16
1	1	SHIPPING LIST	A-PL-KCB-A-2	15
A/R	A/R	ADHESIVE, SILASTIC	9009158	14
1	1	COVER, PROTECTIVE, BRIEFL	D-477R3241-0-0	13
2	-	I/O CABLE, BC618R	C-U/A BC618R-1	12
-	2	I/O CABLE, BC618R	C-U/A BC618R-01	11
1	1	BRACKET BEZEL	CIA-7811987-0-0	10
4	4	SCR PNL HPMAN 10-32 X.50	9006671	9
3	3	CLIP	B-10-7472-352-0-0	8
4	4	WASH. EXT TOOTH LOCK "B"	9007659	7
4	4	SCR PNL HD PFM 10-32 X.50	9006020-1	6
1	1	KEYBOARD ASSY	D-AD-7010644-0	5
3	3	NUT KEP'S #4-40	9006657	4
3	3	SCR PNL HD FLAT 10-40 X.375	9006011-2	3
1	1	BEZEL, BRIEF, HD 10 32X.50	LND-7811989-3-0	2
1	1	PANEL	D-LA-7811989-1-0	1

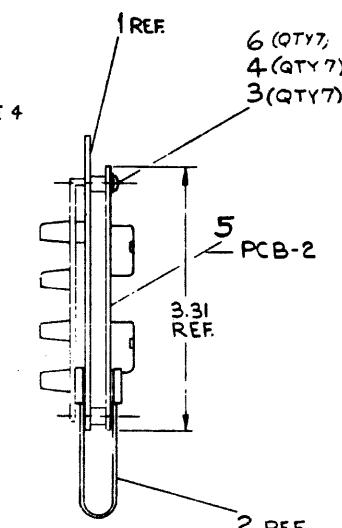
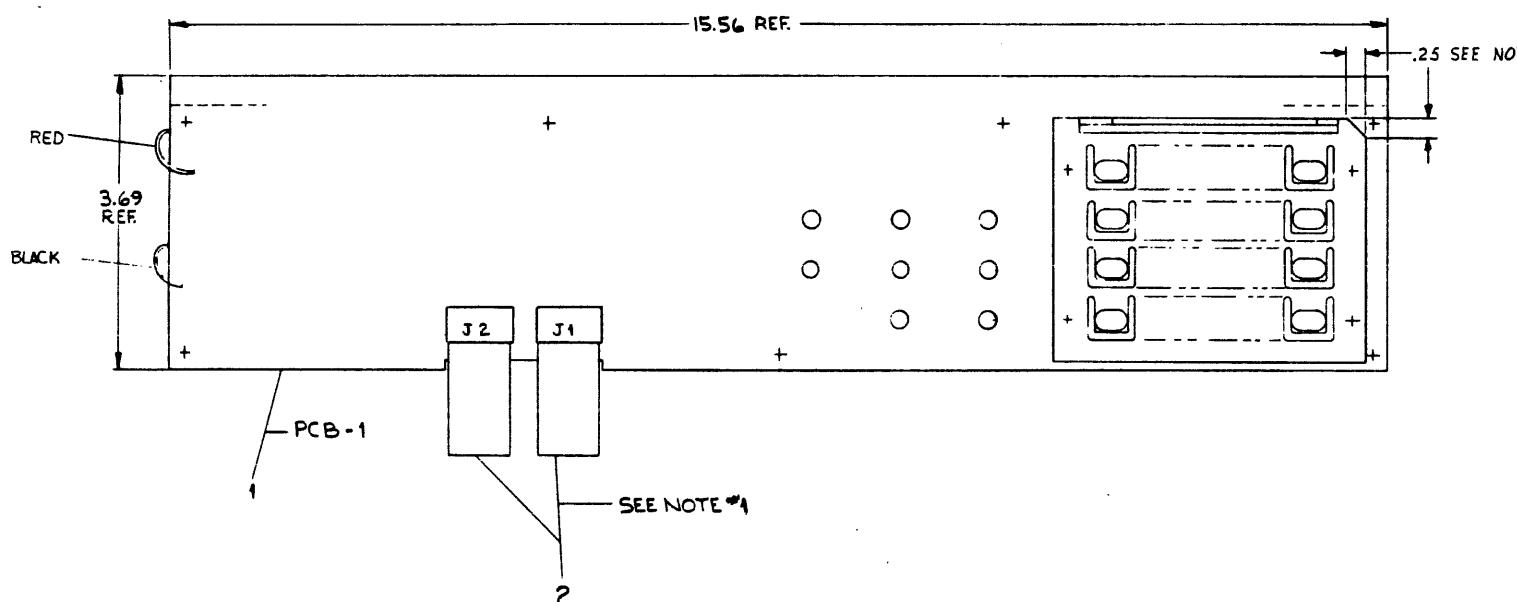
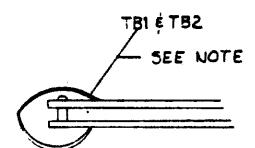


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NOTES:

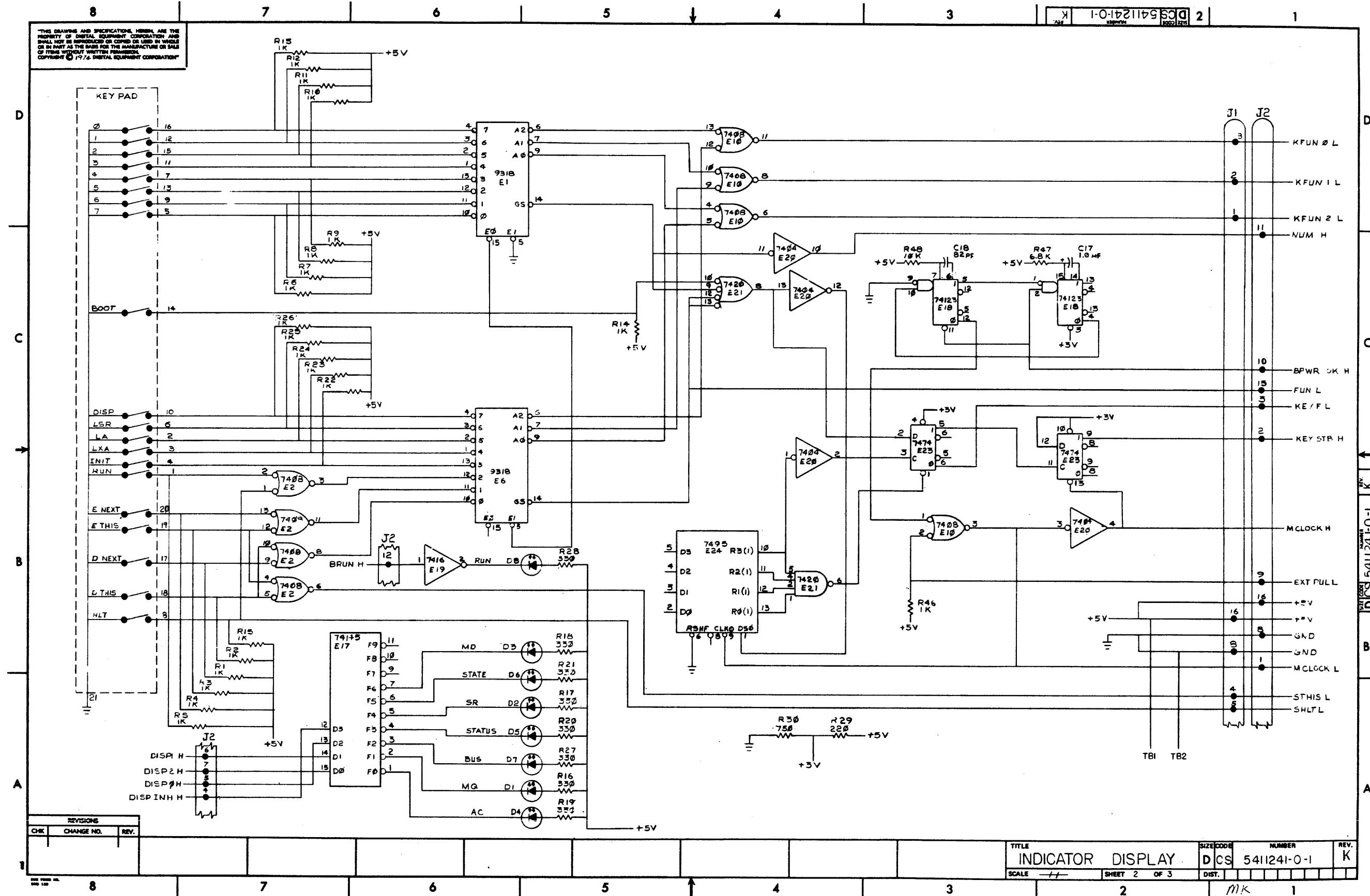
1. INSTALL ITEM #2 (CABLE) AS FOLLOWS.
PCB-1-J1 TO PCB-2-J3
PCB-1-J2 TO PCB-2-J4
 2. CONNECT RED WIRE FROM PCB-1 TO TBI ON PCB-2. CONNECT BLACK WIRE FROM PCB-1 TO TB2 ON PCB-2.
 3. ITEM #7 MAY BE RECYCLED WHEN USED FOR INTERPLANT SHIPMENT.
 4. CLIP UPPER RIGHT HAND LIP OF KEYPAD OFF TO ALLOW PROPER ALIGNMENT INTO BEZEL.



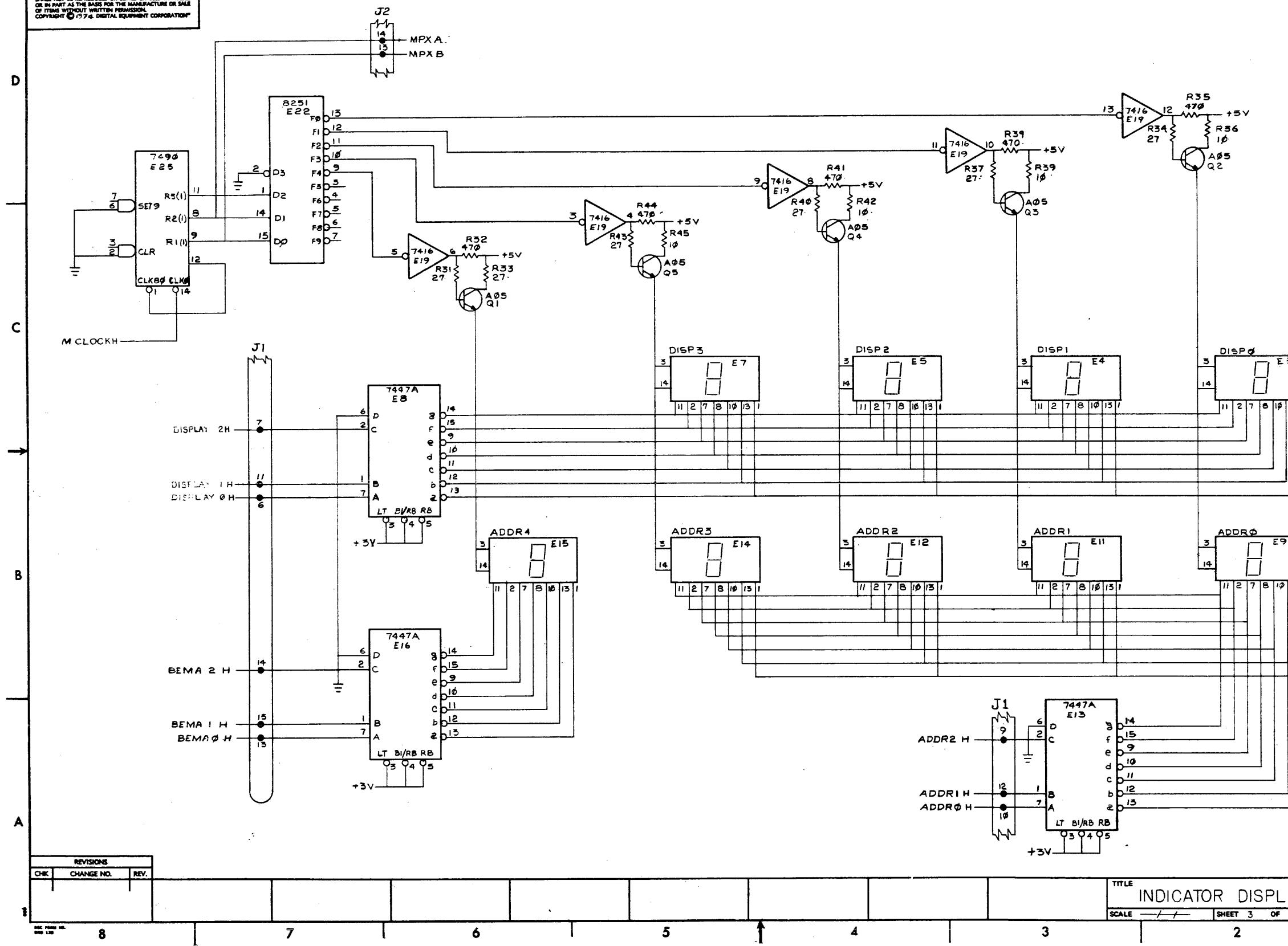
SEE NOTE 3 →	1	PACKAGING INSTRUCTIONS	A-SP-37001B8-0-0	7
	7	WASHER, FLAT #4	9006655	6
	1	REGISTERS AND CONTROL	D-CS-5A11316-0-1	5
	7	WASH INT LOCK#4	9006632	4
	7	SCR PHL HD PAN 4-40X25	9008301-1	3
	2	CABLE, KEYBOARD	C-IA-7008612-ØD	2
	1	INDICATOR DISPLAY	D-CS-5A11241-0-1	1

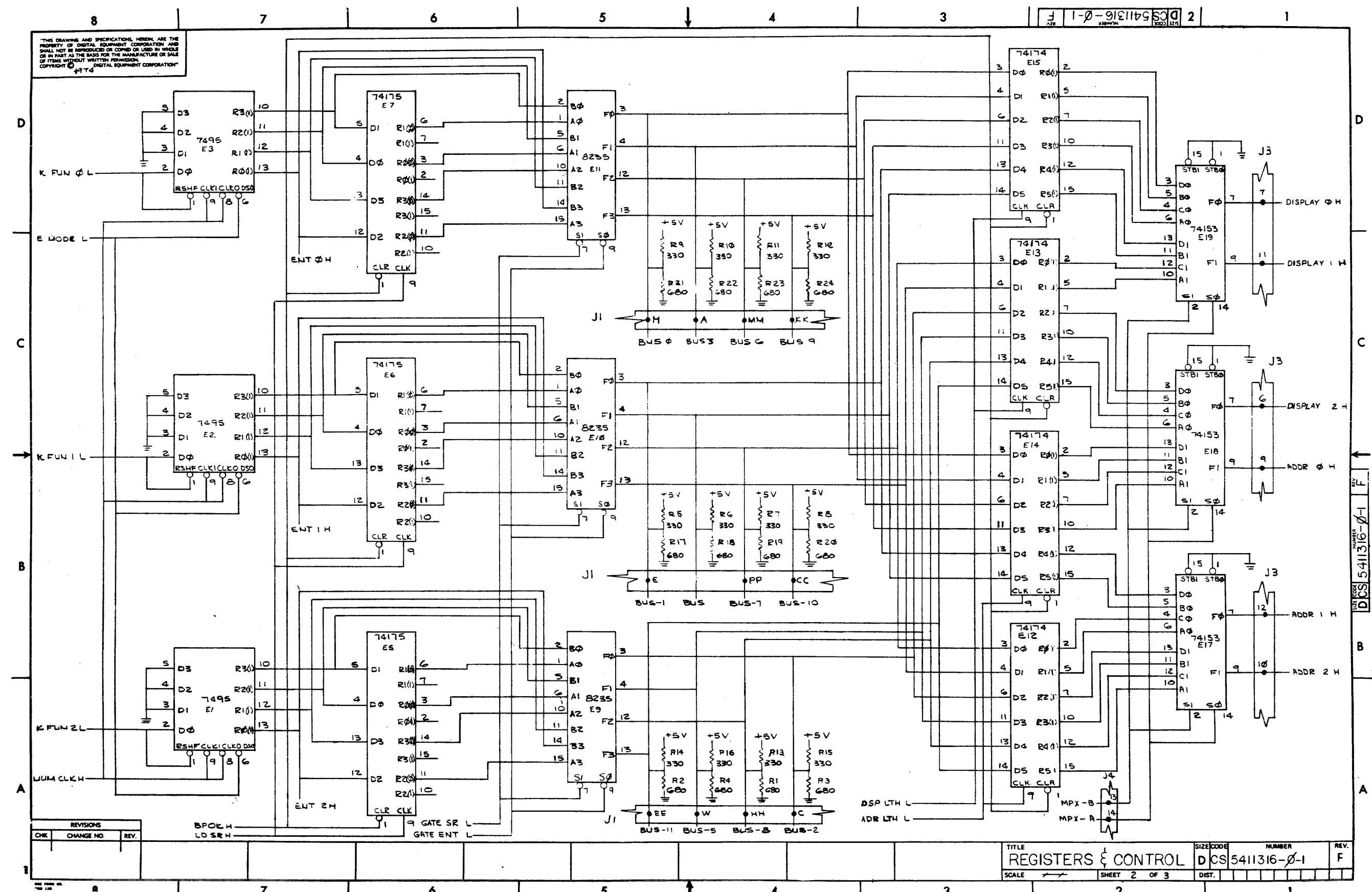
FIRST USED ON OPTION/MODEL 8/A		QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST					
DIMENSIONAL TOLERANCE		BRN. <i>John Clegg</i>	DATE 1-15-74		
DIMENSIONS ARE UNLESS OTHERWISE SPECIFIED		CHK'D <i>John Clegg</i>	DATE 1-23-75		
MILLIMETERS	INCHES	ANGLES	END'D <i>John Clegg</i>	DATE 1-23-75	TITLE
XXX - 5.000	XXX - .200	45° 30'	PROD ENG <i>John Clegg</i>	DATE 1-23-75	KEYBOARD ASSY.
XX - 0.05	XX - .002	X - .1	PROD <i>John Clegg</i>	DATE 1-23-75	
THIRD ANGLE PROJECTION		REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY ✓ NEXT HIGHER ASSY.			
		MATERIAL E-U-KC8-A-0 SEE PARTS LIST			
FINISH		SCALE 1/1	SIZE CODE DAD	NUMBER 70106440-0	REV. C
		SHEET 1 OF	DIST.		

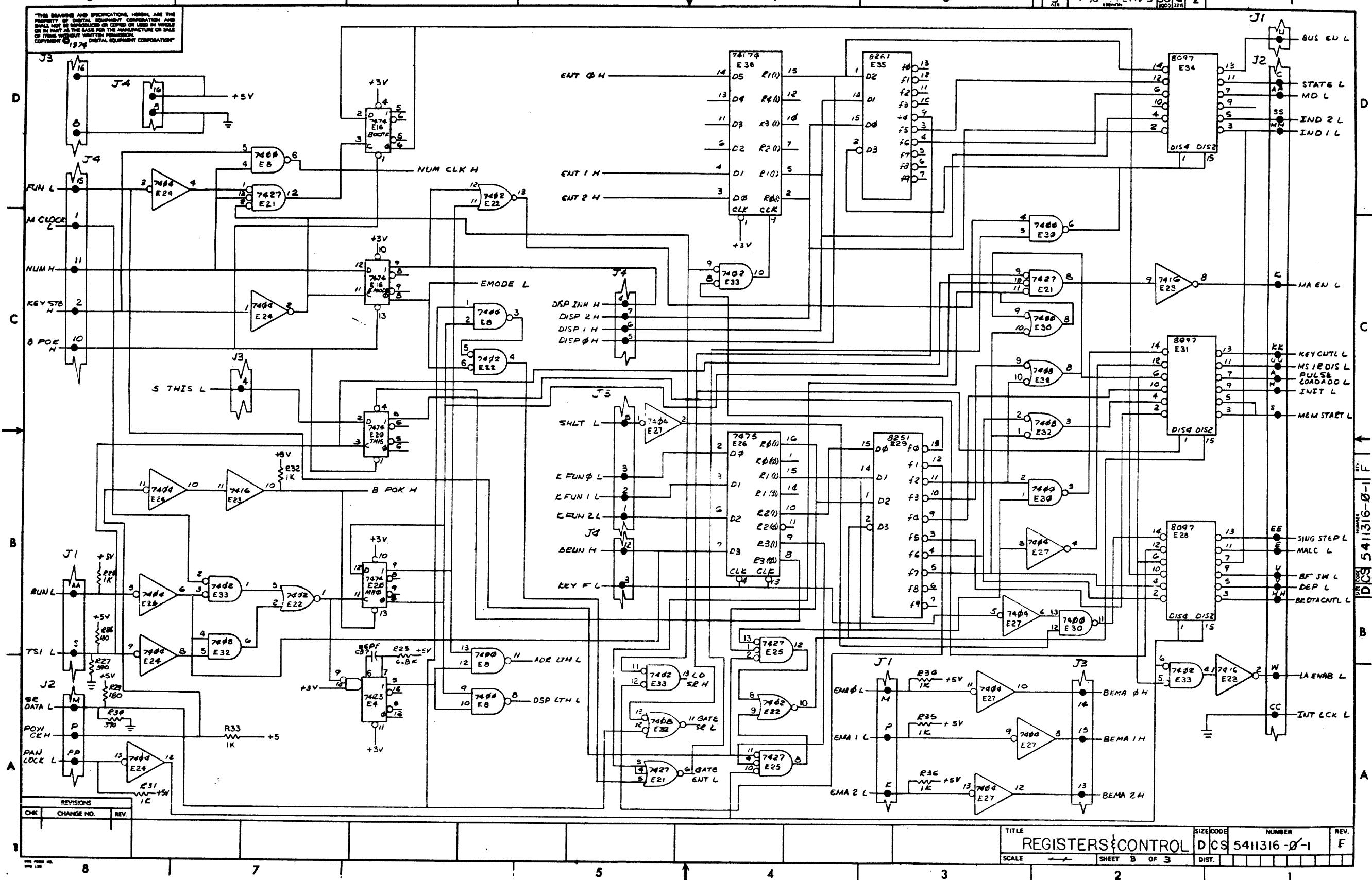
REVISIONS	C/N#	CHANGER NO.	REV.
3/9	7010644-00001	A	
		Scantlebury	
		G. LORDE	
		11-1997-26	
	7010644-00002	B	
		P. SCANTLEBURY	
		11-1997-26	
	7010644-00003	C	
		P. SCANTLEBURY	
		11-1997-26	



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DIGITAL EQUIPMENT CORPORATION						
MAYNARD, MASSACHUSETTS						
ENGINEERING SPECIFICATION						
DATE 22 MARCH 78						
TITLE KT8A FIELD INSTALLATION AND ACCEPTANCE PROCEDURE						
REVISIONS						
REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE
A	ECO CHANGE	00001	ATSHUDY	4-78	7-22-78	5-2-78
B	ECO CHANGE	ML002	PGARDNER	12-78	7-22-78	5-2-78

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ENGINEERING SPECIFICATION																	
CONTINUATION SHEET																	
TITLE KT8A FIELD INSTALLATION AND ACCEPTANCE PROCEDURE																	
I General																	
<p>This document will define the hardware requirements and tests to be performed to: (1) install, (2) configure and (3) accept a KT8-AA system or KT8-AB add-on to an existing system. Because the KT8-A Memory Management options has several possible hardware configurations, the Hardware Rules, Restrictions (Appendix A), General Configuration Guide (Appendix B), and Configuration Examples (Appendix C) should be referenced before installing this option.</p>																	
<p>A. If the KT8-AA was shipped as part of a system, refer only to the Acceptance procedure.</p>																	
<p>B. If the KT8-AB is an add-on installation to upgrade an existing system, then refer to the Installation and Acceptance Procedures.</p>																	
II Hardware																	
<p>This section defines the required hardware to install and accept a KT8-A and also defines the three hardware designations of the KT8-A option.</p>																	
<p>A. The KT can be installed and accepted on any 8A/420 or 620 machine.</p>																	
<p>B. The KCRA Programmer's Console is not required, as the KT diagnostics have a console package.</p>																	
<p>C. Program loading media is via: Paper tape, Floppy, or RK05.</p>																	
<p>D. The Three designations of the KT are as follows:</p>																	
<p>1. KT8-A - the KT Memory Management option shipped as part of a system configured by a DEC Manufacturing facility.</p>																	
<p>2. KT8-B - the required hardware to upgrade an 8A/420 or 620 system. The KMBAC (M8317YB or YC) is part of this option.</p>																	
<table border="1"> <tr> <th>DEC FORM NO</th> <th>ENG-0102216-N370-3811</th> </tr> <tr> <th>DRA (or)</th> <th>KT8A</th> </tr> <tr> <td>SHEET</td> <td>2</td> </tr> <tr> <td>OF</td> <td>12</td> </tr> <tr> <td>REV</td> <td>B</td> </tr> <tr> <td>NUMBER</td> <td>KT8A-3</td> </tr> <tr> <td>CODE</td> <td>SP</td> </tr> <tr> <td>SIZE</td> <td>A</td> </tr> </table>		DEC FORM NO	ENG-0102216-N370-3811	DRA (or)	KT8A	SHEET	2	OF	12	REV	B	NUMBER	KT8A-3	CODE	SP	SIZE	A
DEC FORM NO	ENG-0102216-N370-3811																
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SIZE	A																

ENGINEERING SPECIFICATION																	
CONTINUATION SHEET																	
TITLE KT8A FIELD INSTALLATION AND ACCEPTANCE PROCEDURE																	
IV Acceptance																	
<p>The time to accept a KT8-A configuration depends upon the amount of memory installed.</p>																	
<ol style="list-style-type: none"> Load and run the KT8-A Memory Management Diagnostic, Maindec 08-DIKTA-A, for five min. with NO errors. Load and run the Extended Address Test Maindec 08-DIKMC-C, for one pass with NO errors. Load and run the Extended Memory Data and Checkerboard Test, Maindec 08-DHKWA-D, for one pass with NO errors. To insure system integrity, load and build a DEC/X8 program using version 2, which will exercise up to 128K of memory. It is important that the program is build using the latest DEC/X8 modules. 																	
<p>NOTE: Reference should be made to the latest write-up for DEC/X8 (version 2) as further parameters must be inputted to support break devices.</p>																	
III Installation																	
<p>3. KT8-EX - this option is required any time the memories are located in two separate boxes (2ABC's). If required as part of an add-on, both the KT8-AB and KT8-EX must be ordered as separate line items.</p>																	
Before proceeding with your installation refer to Appendix A and B to familiarize yourself with the rules and configurations. Also refer to the configuration example that most represents your particular installation.																	
<ol style="list-style-type: none"> Install all memory in the system, refer to Configuration guide (Appendix B). Install the KT8-AB in any vacant OMNIBUS slot with an "E" connector. If the system is comprised of two (2) BBC boxes and memory will be located in each box than install the M920, terminator module, in any available "E" connector of the box not containing the KT8-A (M8416). Now connect the cable (78-11411-1) between the two berg connectors of the M8416 and M920. 																	
<table border="1"> <tr> <th>DEC FORM NO</th> <th>ENG-0102216-N370-3811</th> </tr> <tr> <th>DRA (or)</th> <th>KT8A</th> </tr> <tr> <td>SHEET</td> <td>3</td> </tr> <tr> <td>OF</td> <td>12</td> </tr> <tr> <td>REV</td> <td>B</td> </tr> <tr> <td>NUMBER</td> <td>KT8A-3</td> </tr> <tr> <td>CODE</td> <td>SP</td> </tr> <tr> <td>SIZE</td> <td>A</td> </tr> </table>		DEC FORM NO	ENG-0102216-N370-3811	DRA (or)	KT8A	SHEET	3	OF	12	REV	B	NUMBER	KT8A-3	CODE	SP	SIZE	A
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DRA (or)	KT8A																
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<p>NOTE: Reference should be made to the latest write-up for DEC/X8 (version 2) as further parameters must be inputted to support break devices.</p>																	
VI Installation																	
<p>1. Install all memory in the system, refer to Configuration guide (Appendix B).</p>																	
<p>2. Install the KT8-AB in any vacant OMNIBUS slot with an "E" connector.</p>																	
<p>3. If the system is comprised of two (2) BBC boxes and memory will be located in each box than install the M920, terminator module, in any available "E" connector of the box not containing the KT8-A (M8416). Now connect the cable (78-11411-1) between the two berg connectors of the M8416 and M920.</p>																	
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DEC FORM NO	ENG-0102216-N370-3811																
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ENGINEERING SPECIFICATION		CONTINUATION SHEET																															
TITLE KT8A FIELD INSTALLATION AND ACCEPTANCE PROCEDURE																																	
<p>APPENDIX A</p> <p>HARDWARE RULES/RESTRICTIONS</p> <ol style="list-style-type: none"> 1. Any OMNIBUS CPU (KK8A or KK8F) using a BABC box (20 slot box) is acceptable. 2. The KT8-A system can only be configured using any combination of MM8AB (16K core) and MSC1 16K or 32K MOS memories. NOTE: MM8AA, M8A, MM8E, MM8F and MM8F memories cannot be used to configure a KT8A system. 3. If the system is made up of MM8AB core memories (16K), then they must be modified per ECO MM8AB #1, refer to table 1 for instructions. 4. If the system is made up of MS8C type memories (16K or 32K MOS), them refer to table 2 for switch configuration. 5. The PDP/8E chassis cannot be used as part of a KT8-A system. 6. If Power Fail/Auto Restart and/or Bootstraps are required as part of the system, then a KM8-AC (M8317YB or YC) must be used with the Memory Extension and Timeshare option disabled via the Jumper configuration in table 3. <p>NOTE: The M8317 and M8317YA are incompatible with the KT8A system.</p>																																	
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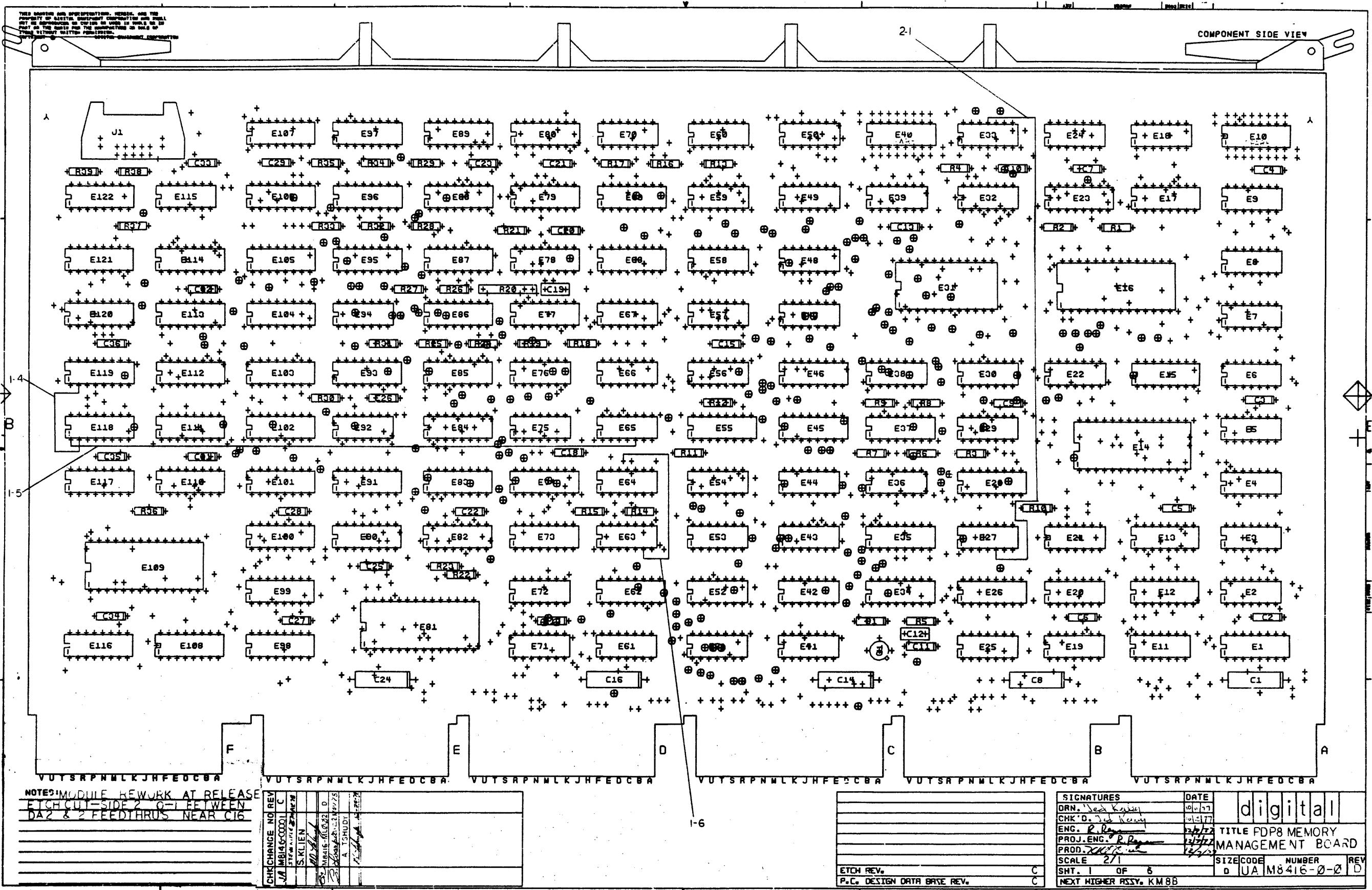
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<ol style="list-style-type: none"> 1. All memories must be physically located in the OMNIBUS where an "E" connector is present. 2. Remembering the above rule, place the memories as far away as possible from the CPU. 3. Direct Memory Address interfaces can only be located between the CPU and the first memory element. With one exception, in a two box system (2 BABC's) where memory is located in both boxes a DMA interface may be located in any vacant slot of the box containing the CPU. 4. Programmed I/O interfaces may be located in any vacant slot of the system. 5. When memories are located in two BABC chassis then the KT8-EX option must be used to extend the memory management option bank bits. The M9020 terminator card must be located in an "E" connector of the BABC not containing the M8416. The 70-1141-1J cable is then connected between the M9020 and the M8416. 																																																																																				
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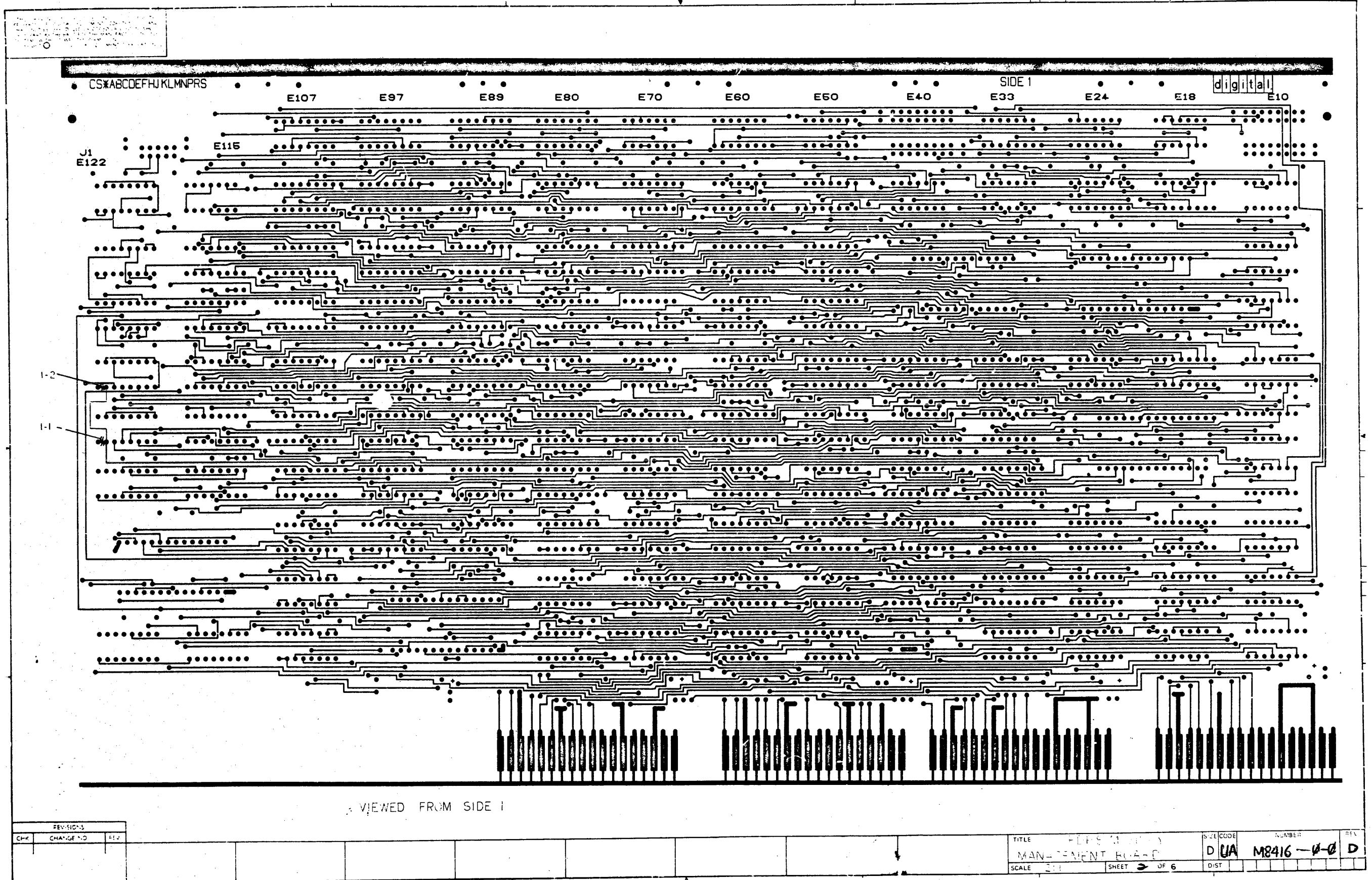
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<p>Because the KT8-A is limited to use in the BA8C chassis 126 slot box) there are only four possible configurations.</p> <p>1. The entire system located in one BA8C with a KT8A CPU as shown below.</p> <table border="1"> <thead> <tr> <th>SLOT</th> <th>OPTION</th> <th>DEFINITION</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>KK8A</td> <td>CPU (M8315)</td> </tr> <tr> <td>2</td> <td>DKC8A</td> <td>OPTION ONE (M8316), IF REQUIRED</td> </tr> <tr> <td>3</td> <td>KM8-AC</td> <td>OPTION TWO (M8317B OR YC), IF REQUIRED</td> </tr> <tr> <td>4</td> <td>KT8-A</td> <td>MEMORY MANAGEMENT OPTION (M8416)</td> </tr> <tr> <td>5</td> <td>-----</td> <td>DMA DEVICES CONFIGURED FROM THIS POINT TOWARD MEMORY</td> </tr> <tr> <td>6</td> <td>-----</td> <td></td> </tr> <tr> <td>7</td> <td>-----</td> <td></td> </tr> <tr> <td>8</td> <td>-----</td> <td></td> </tr> <tr> <td>9</td> <td>-----</td> <td></td> </tr> <tr> <td>10</td> <td>-----</td> <td></td> </tr> <tr> <td>11</td> <td>MEMORY</td> <td>MEMORY CONFIGURED FROM THIS POINT TOWARD THE CPU ONLY I/O INTERFACES</td> </tr> <tr> <td>12</td> <td>-----</td> <td></td> </tr> <tr> <td>13</td> <td>-----</td> <td></td> </tr> <tr> <td>14</td> <td>-----</td> <td></td> </tr> <tr> <td>15</td> <td>-----</td> <td></td> </tr> <tr> <td>16</td> <td>-----</td> <td></td> </tr> <tr> <td>17</td> <td>-----</td> <td></td> </tr> <tr> <td>18</td> <td>-----</td> <td></td> </tr> <tr> <td>19</td> <td>-----</td> <td></td> </tr> <tr> <td>20</td> <td>-----</td> <td>ONLY I/O INTERFACES</td> </tr> </tbody> </table>				SLOT	OPTION	DEFINITION	1	KK8A	CPU (M8315)	2	DKC8A	OPTION ONE (M8316), IF REQUIRED	3	KM8-AC	OPTION TWO (M8317B OR YC), IF REQUIRED	4	KT8-A	MEMORY MANAGEMENT OPTION (M8416)	5	-----	DMA DEVICES CONFIGURED FROM THIS POINT TOWARD MEMORY	6	-----		7	-----		8	-----		9	-----		10	-----		11	MEMORY	MEMORY CONFIGURED FROM THIS POINT TOWARD THE CPU ONLY I/O INTERFACES	12	-----		13	-----		14	-----		15	-----		16	-----		17	-----		18	-----		19	-----		20	-----	ONLY I/O INTERFACES
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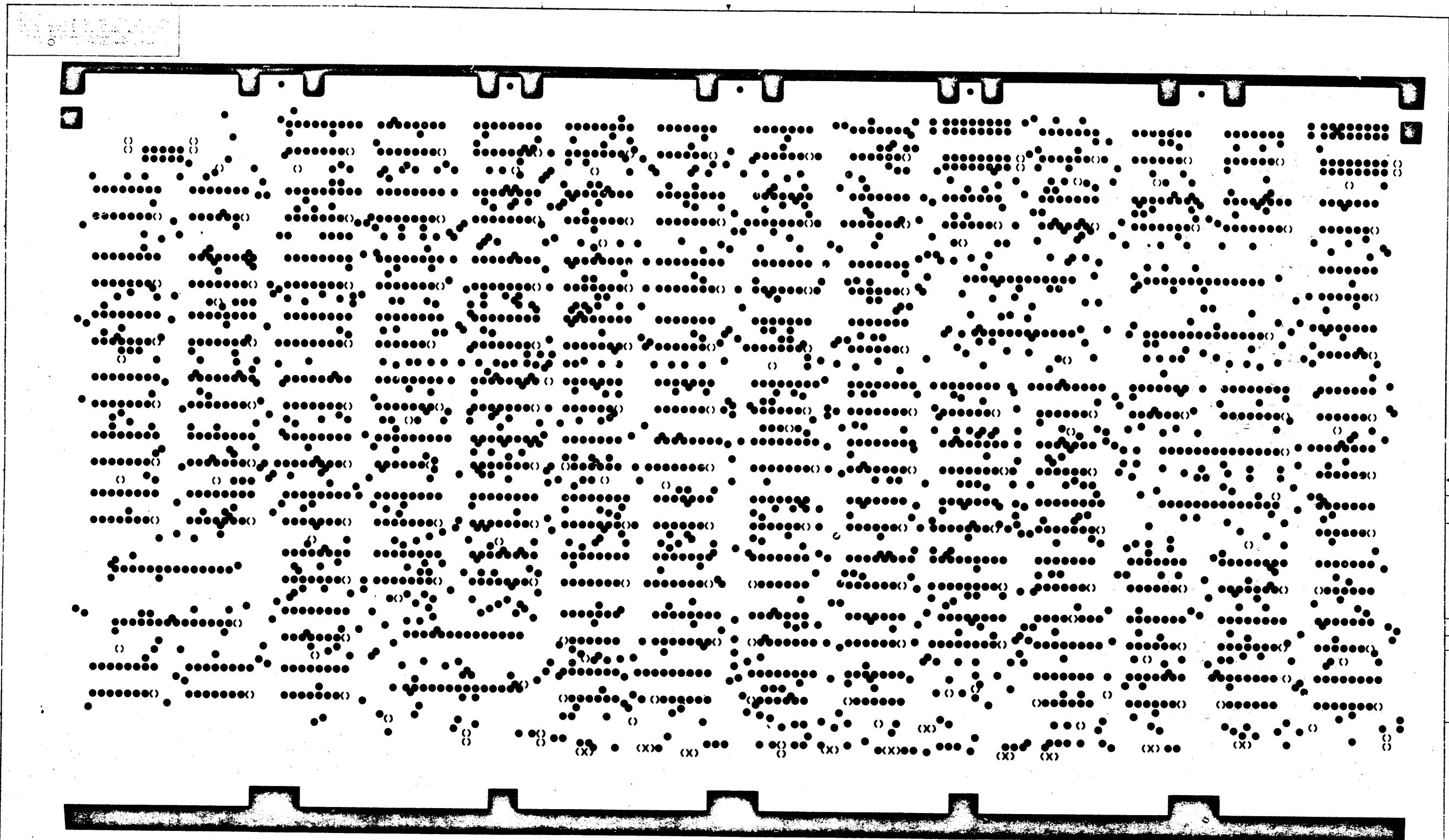
ENGINEERING SPECIFICATION		CONTINUATION SHEET			
TITLE KT8A FIELD INSTALLATION AND ACCEPTANCE PROCEDURE					
3. The KT8-A system made up of two BA8C boxes with the KK8F CPU in one box and all the memory located in the other box as shown below.					
SLOT	OPTION	DEFINITION			
1	KK8F	TERMINATOR, MB320	TOP BA8C)		
2	----	ANY I/O INTERFACE			
3	----	ANY I/O INTERFACE			
4	KT8-A	MEMORY MANAGEMENT OPTION (MB416)			
5	MEMORY	MEMORY CONFIGURED FROM THIS POINT TOWARD THE CPU			
6	----				
7	----				
8	----				
9	----				
10	----	LAST POSSIBLE MEMORY IN THIS CONFIGURATION			
11	----	ANY DMA OR I/O INTERFACES			
12	----				
13	----				
14	----				
15	----				
16	----				
17	----				
18	----				
19	----				
20	BC09H-3	OMNIBUS EXPANDER CABLES	*BOTTOM BA8C)		
1	BC09H-3	OMNIBUS EXPANDER CABLES			
2	DKC8A	OPTION ONE (MB316), IF REQUIRED			
3	KMB-SAC	OPTION TWO (MB317B or YC), IF REQUIRED			
4	----				
5	----				
6	----				
7	----				
8	----				
9	----				
10	----				
11	----				
12	----				
13	----				
14	----				
15	----				
16	----		DMA AND I/O INTERFACES CONFIGURED FROM THIS		
17	----		POINT TOWARD MEMORY		
18	KK8P	CPU, MB310			
19	WRC8P	CPU, MB310			
20	-----				

ENGINEERING SPECIFICATION				CONTINUATION SHEET	
TITLE KT8A FIELD INSTALLATION AND ACCEPTANCE PROCEDURE					
2. The entire system located in one BAC with a KK8F CPU as shown below.					
SLOT	OPTION	DEFINITION			
1	KK8F	TERMINATOR, M8320			
2	DKC8A	OPTION ONE (M8310), IF REQUIRED			
3	KM8-AC	OPTION TWO (M8311B OR CYC), IF REQUIRED			
4	KT8-A	MEMORY MANAGEMENT OPTION (M8416)			
5	MEMORY	MEMORY CONFIGURED FROM THIS POINT TOWARD CPU			
6	----	-----			
7	----	-----			
8	----	-----			
9	----	-----			
10	----	-----			
11	----	-----			
12	----	-----			
13	----	-----			
14	----	-----			
15	----	-----			
16	----	-----			
17	----	DMA AND I/O INTERFACES CONFIGURED FROM THIS POINT TOWARD MEMORY			
18	KK8F	CPU, M8310			
19	KK8F	CPU, M8300			
20	KK8F	CPU, M8330			
3. DMA AND I/O INTERFACES CONFIGURED FROM THIS					
POINT TOWARD MEMORY					
18 KK8F					
19 KK8F					
20 KK8F					
DECK FORM NO. EN-01022-16-N370 (381)					
DEC 1984					
12 OF 10 SHEET					
REV B					
NUMBER KT8A-3					
SIZE A					
CODE SP					

ENGINEERING SPECIFICATION		CONTINUATION SHEET	
TITLE KT8A FIELD INSTALLATION AND ACCEPTANCE PROCEDURE			
4.	The KT8-A system made up of two BA8C boxes with a KKPF in one box and memories located in both boxes as shown below:		
SLOT	OPTION	DEFINITION	(TOP BA8C)
1	KK8F	TERMINATOR, MB320	
2	----	ANY I/O INTERFACE	
3	----	ANY I/O INTERFACE	
4	M9020	KT8A TERMINATOR, LOCATE IN SLOT "E" OF OMNIBUS MEMORY CONFIGURED FROM THIS POINT TOWARD CPU	
5	MEMORY		
6	----		
7	----		
8	----		
9	----		
10	----		
11	MEMORY	LAST MEMORY ELEMENT IN THIS BA8C!	
12	----		
13	----		
14	----		
15	----		
16	----		
17	----		
18	----		
19	----		
20	BC06H-3	ANY I/O INTERFACES OMNIBUS EXPANDER CABLES	
		*BOTTOM BA8C)	
1	BC08H-3	OMNIBUS EXPANDER CABLES	
2	DKC8A	OPTION ONE (MB3116), IF REQUIRED	
3	MR8-AC	OPTION TWO (MB317B OR YC), IF REQUIRED	
4	KT8-A	MEMORY MANAGEMENT OPTION, MB416	
5	MEMORY	CONTINUE CONFIGURING MEMORY FROM THIS POINT TOWARD THE CPU	
6	----		
7	----		
8	----		
9	----		
10	----		
11	----		
12	----		
13	----		
14	----		
15	----		
16	----		
17	----	DMA AND I/O INTERFACES CONFIGURATED FROM THIS POINT TOWARD MEMORY	
18	KK8F	CPU, MB310	
19	KK8F	CPU, MB310	
20	KK8F	CPU, MB310	
		SIZE	CODE
		A	SP
		NUMBER	
		KTB-3	REV B



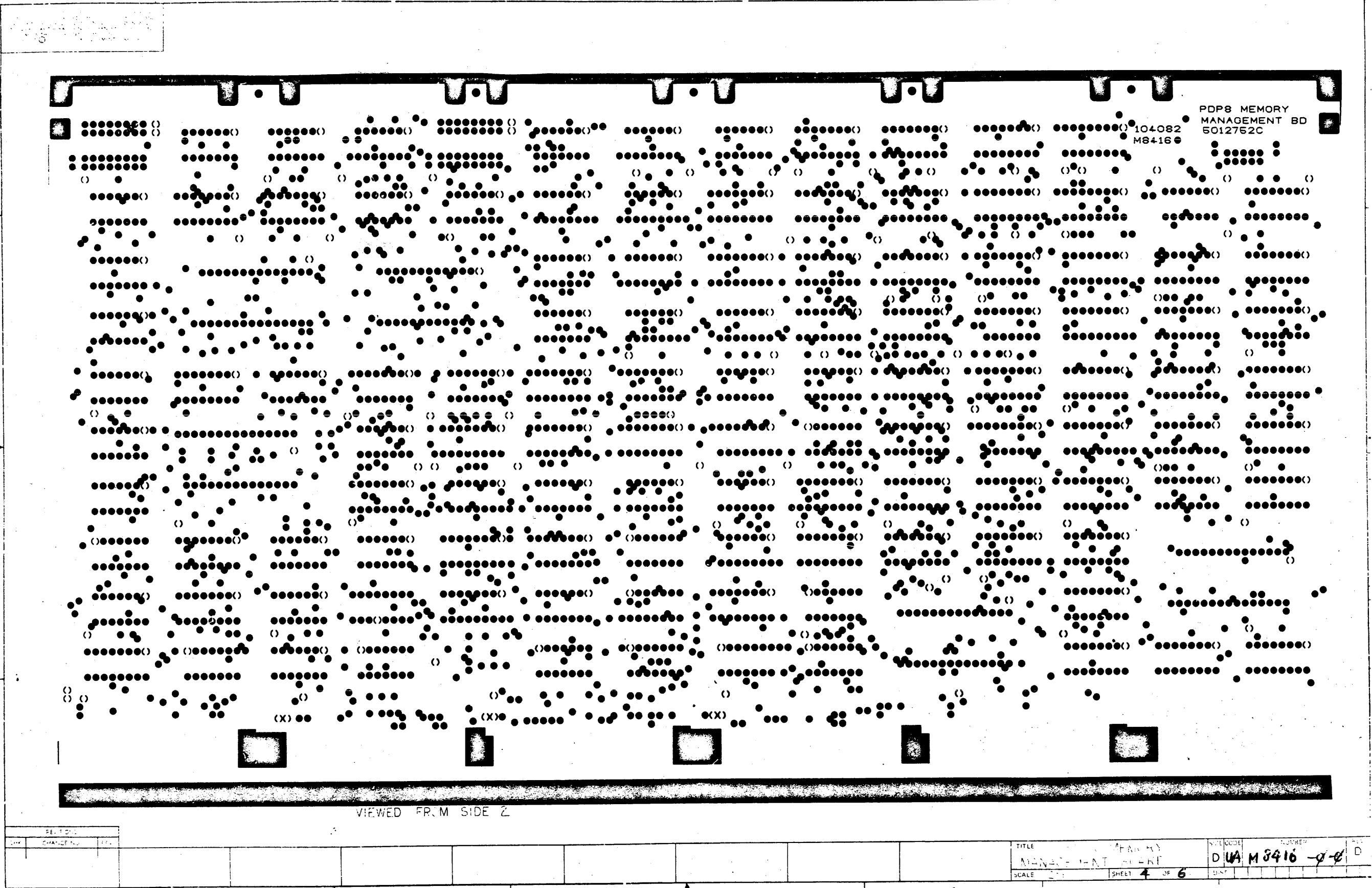


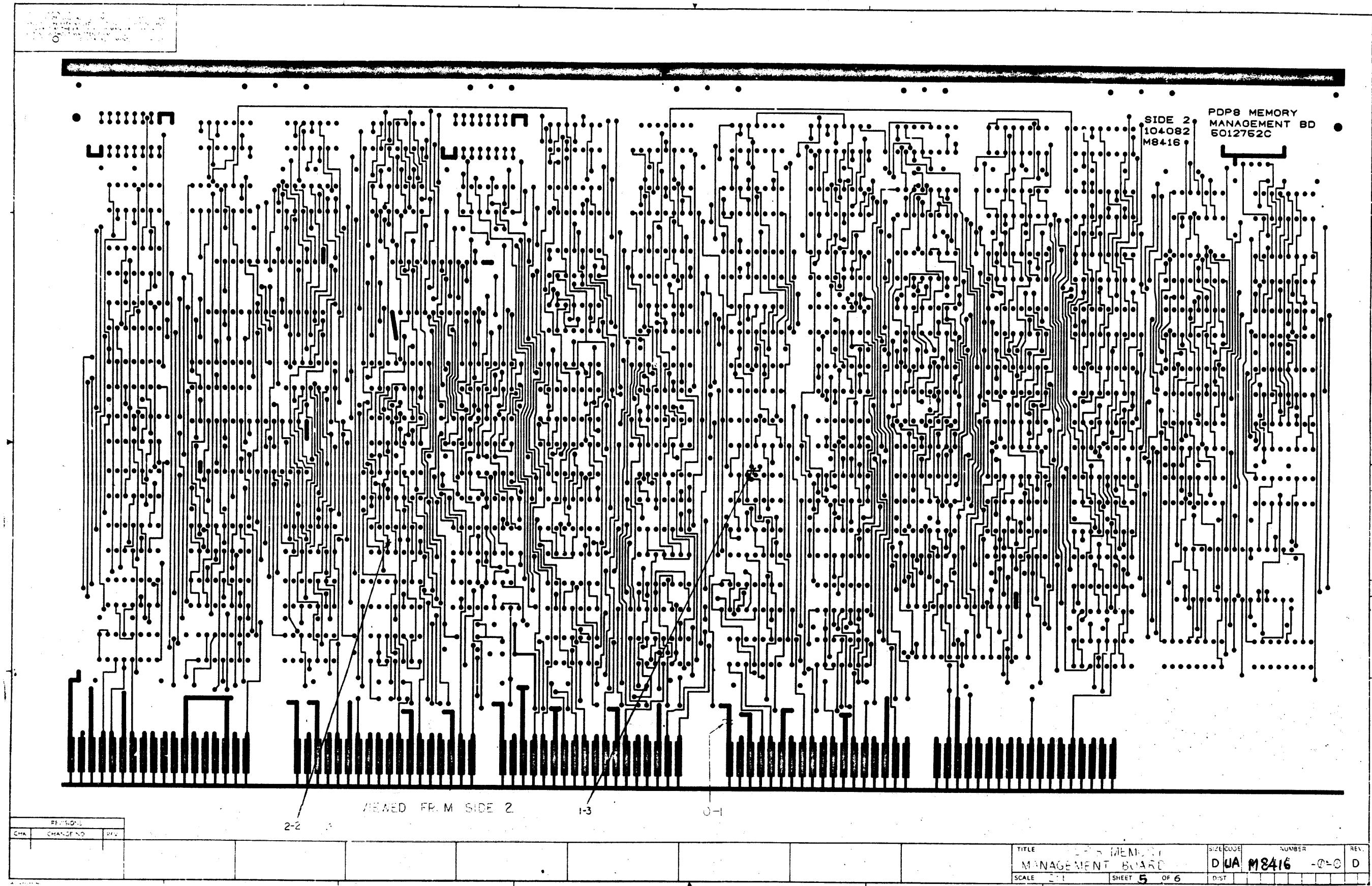


VIEWED FROM SIDE 1

REVISIONS									
LINE	CHARACTER	NO.	1	2	3	4	5	6	7

TITLE: M-8416-0-0 MANAGEMENT BOARD									
SCALE: 1/4 INCH = 1 FT									
SHEET 3 OF 6 DATE: 10-17-00									
DRAWING NUMBER: DUA M-8416-0-0 REV: D									





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REWORK INSTRUCTIONS

E.C.O. #1

ETCH CUTS SIDE 1

- I-1: BETWEEN E118 PIN1 & E118 PIN2
- I-2: BETWEEN E119 PIN1 & E119 PIN2

ETCH CUTS SIDE 2

- I-3: BETWEEN E64 PIN10 & FEED THRU
ABOVE AND BETWEEN E64 PINS 11,12

WIRE ADDS SIDE 1

- I-4: FROM E118 PIN2 TO E119 PIN2
- I-5: FROM E118 PIN1 TO E65 PINS 5-7
- I-6: FROM E65 PIN6 TO FEED THRU
THAT WAS CUT FROM E64-10

E.C.O. #2:

ETCH CUTS SIDE 2

- 2-1: E27-5

WIRE ADDS SIDE 1

- 2-2: E33-11 TO E27-5

REVISIONS		
CHK.	CHANGE NO.	REV.

								TITLE PDP 8 MEMORY MANAGEMENT BOARD	SCALE	1/1	SHEET	6 OF 6	SIZE CODE	D	NUMBER	REV.
													U A	M8416-0-C	D	

8 7 6 5 4 3 2 1

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY	REFERENCE DESIGNATOR
1	1	D-MD-5P12752-0-0	5012752-00	M8416	1	
2	2		1000016-00	100.0 MMF 100V 58200PPM DM155	2	C12,C19
3	3		1005306-00	6.0VFD 35V 10% S.TANT	5	C1,C8,C14,C16,C24
4	4		1012784-00	.047 MFD 50V -20+80 CER	28	C2-C7,C9,C10,C11,C13,C15,C17,C18,C20-C23,
					CONT	C25-C29,C31-C36
5	5		1110603-00	IN 5711 TN=100PS PIV= 70V HMs	1	D1
6	6		1239941-05	HEADER,10P 10POS RT ANGLE	1	J1
7	7		1210711-02	HANDLE,MODULE,HEX	1	
8	8		1300316-00	470 1/4W 5%	CC	R5
9	9		1300365-00	1 K 1/4W 5%	CC	25 R1,R2,R3,R6-R10,R12,R14,R16,R17,R22-R29,
					CONT	R31-R35
10	10		1300479-00	10 K 1/4W 5%	CC	R4,R11,R13,R15,R18,R19,R21,R30,R36-R39
11	11		1302941-00	14.7 K 1/4W 1% PN55D-F 100PPM	1	R20
12	12		1501999-00	DEC3009A NPN 300MW SI 20 25 M	1	Q1
13	13		1909701-00	74154 1 OF 16,PIWA	1	E31
14	14		1909705-00	DEC 8881 NAND GATE-QUAD 2IN 0	4	E4,E8,E19,E42
15	15		1909934-00	8266 MUS 1 OF 2 (QUAD)	2	E69,E79
16	16		1910393-00	DEC 7384 OR GATE-QUAD 2IN,UTI	4	E52,E53,E72,E75
17	17		1910537-00	74S11 AND GATE-TRIPLE 3INP	1	E27
18	18		1910544-00	74874 FF-D DUAL,EDGE TRIGG	3	E32,E63,E65
19	19		1911330-01	74173N FF-D QUAD,TRI-STATE	11	E1,E62,E73,E90,E97,E98,E99,E110,E119,E121,
					CONT	E122
20	20		1911469-00	DEC 8640 RECEIVER,BUS,QUAD,U	7	E3,E11,E25,E41,E51,E61,E71
21	21		1911527-00	8097 BUFFER GATE-HEX 2INP	10	E37,E55,E83,E84,E91,E100,E101,E104,E105,
					CONT	E107
22	22		1911579-00	8641 TRANSCEIVER,BUS,QUA	3	E12,E13,E45
23	23		1911676-00	74S139 DECODER-DUAL TWO-INP	1	E12C
24	24		1912388-00	74802 NOR GATE-QUAD 2IN,PO	1	E64
25	25		1912649-00	LS75 LATCH 4BIT,BISTABLE	1	E26
26	26		1912661-00	74S189 MEMORY READ/WRITE	1	E82

REVISION HISTORY			SECTION 1 OF 1	RESP,ENG., R,REGAN, DATE: 27-OCT-77	D	I	G	I	T	A	L			
ENG	ECO NUMBER	I REV	SECTION, VARIATION INDEX	MADE BY: TED KELLEY DATE: 29-AUG-77	TITLE PARTS LIST									
J.A 00001	IC			PDP8 MEMORY MANAGEMENT BOARD										
J.A,T M8416-ML002	ID	1.00	1.	DSN,ENG., R, REGAN DATE: 14-NOV-77	SIZE	CODE	DOCUMENT NUMBER	REV						
			2.	PROD., MELVIN SCHENKE DATE: 14-NOV-77										
			3.	ASSEMBLY NUMBER: D-UA-M8416-0-0										
			4.	PART NUMBER: M8416 EDIT 36										
			5.											
			6.											
			7.											
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			10.											
			11.											
			12.											

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LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY	REFERENCE DESIGNATOR
27	27		1912697-00	1.8174 FF-D HEX W/CLEAR	5	E15,E17,E85,E88,E103
28	28		1912796-00	74148 EXCODER, PRIORITY, 8 T	1	E89
29	29		1912799-00	LS00 NAND-GATE-QUAD 2IN,P	8	E6,E18,E50,E54,E57,E66,E70,E94
30	30		1912800-00	LS01 NAND-GATE-QUAD 2IN,P	1	E20
31	31		1912801-00	LS02 NOR-GATE-QUAD 2IN	3	E29,E47,E48
32	32		1912803-00	LS04 INVERTER GATE-HEX 1I	8	E2,E5,E7,E22,E39,E44,E58,E115
33	33		1912805-00	LS08 AND GATE-QUAD 2IN,PU	2	E33,E67
34	34		1912807-00	LS10 NAND GATE-TRIPLE 3TN	5	E21,E24,E49,E56,E92
35	35		1912810-00	LS20 NAND GATE-DUAL 4TN	3	E9,E36,E43
36	36		1912815-00	LS30 NAND GATE-SINGLE 8TN	1	E30
37	37		1912817-00	LS37 NAND GATE-QUAD 2IN,P	1	E38
38	38		1912819-00	LS42 DECODER,BCD-DECIMAL	1	E34
39	39		1912824-00	LS74 FF-D DUAL,EDGE TRIGG	3	E59,E60,E76
40	40		1912828-00	LS85 COMPARATOR,4BIT MAGN	2	E68,E78
41	41		1912853-00	LS175 FF-D QUAD	8	E35,E46,E93,E102,E108,E110,E116,E117
42	42		1912858-00	LS221 ONE SHOT-DUAL,SCHMITT	1	E77
43	43		1912859-00	LS256 MUX 1 OF 2 (DUAL),	1	E23
44	44		1914087-00	9098 BUFFER GATE-HEX 2IN,	4	E86,E95,E96,E106
45	45		23211A1-00	A1-07	1	E80
46	46		2344PA2-00	A2-05	1	E28
47	47		23441A2-00	A2-05	1	E74
48	48		23442A2-00	A2-05	2	E87,E113
49	49		23621A9-00	A9-01	3	E111,E112,E114
50	50		23007C6-00	C6-01	1	E14
51	51		23008C6-00	C6-01	1	E16
52	52		23009C6-00	C6-01	1	E81
53	53		23010C6-00	C6-01	1	E109
54	54		9000024-01	EYFLFT, ROLLED FLANGE, .121 OD X .12	12	
55	55		9105740-55	WIRES(WRAP)30AWG		UL1423 A/R

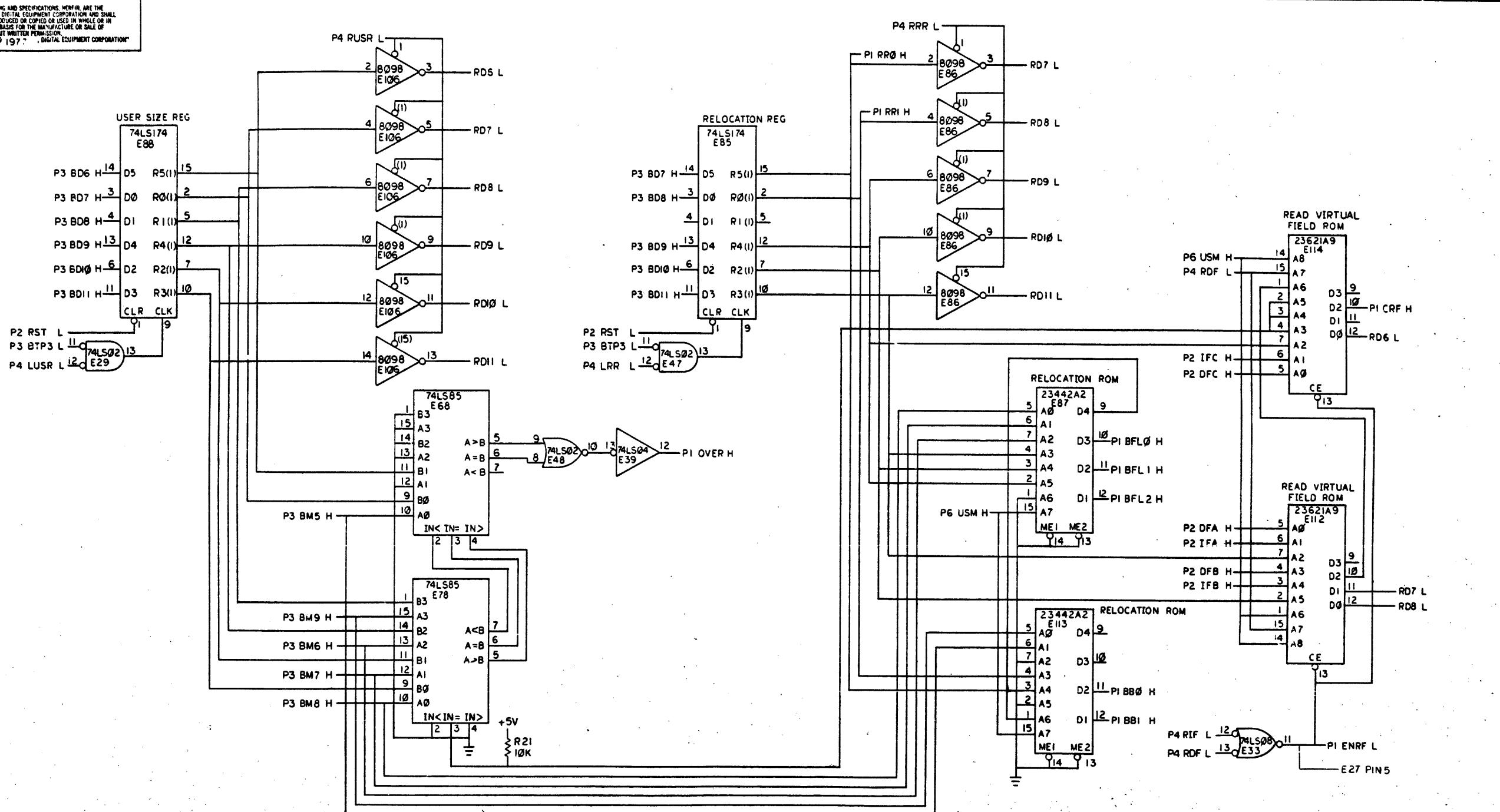
56 NOTE: LINE 181 PARTS SUBSTITUTION LIST

57 NOTE: ITEM #18 1910544-01 74574 FF-D DUAL (60 VERSION) QTY 3

58 NOTE: ITEM #18 1910950-00 74574 FF-D DUAL (45 VERSION) QTY 3

TITLE			SIZE/CODE	DOCUMENT NUMBER	R.					
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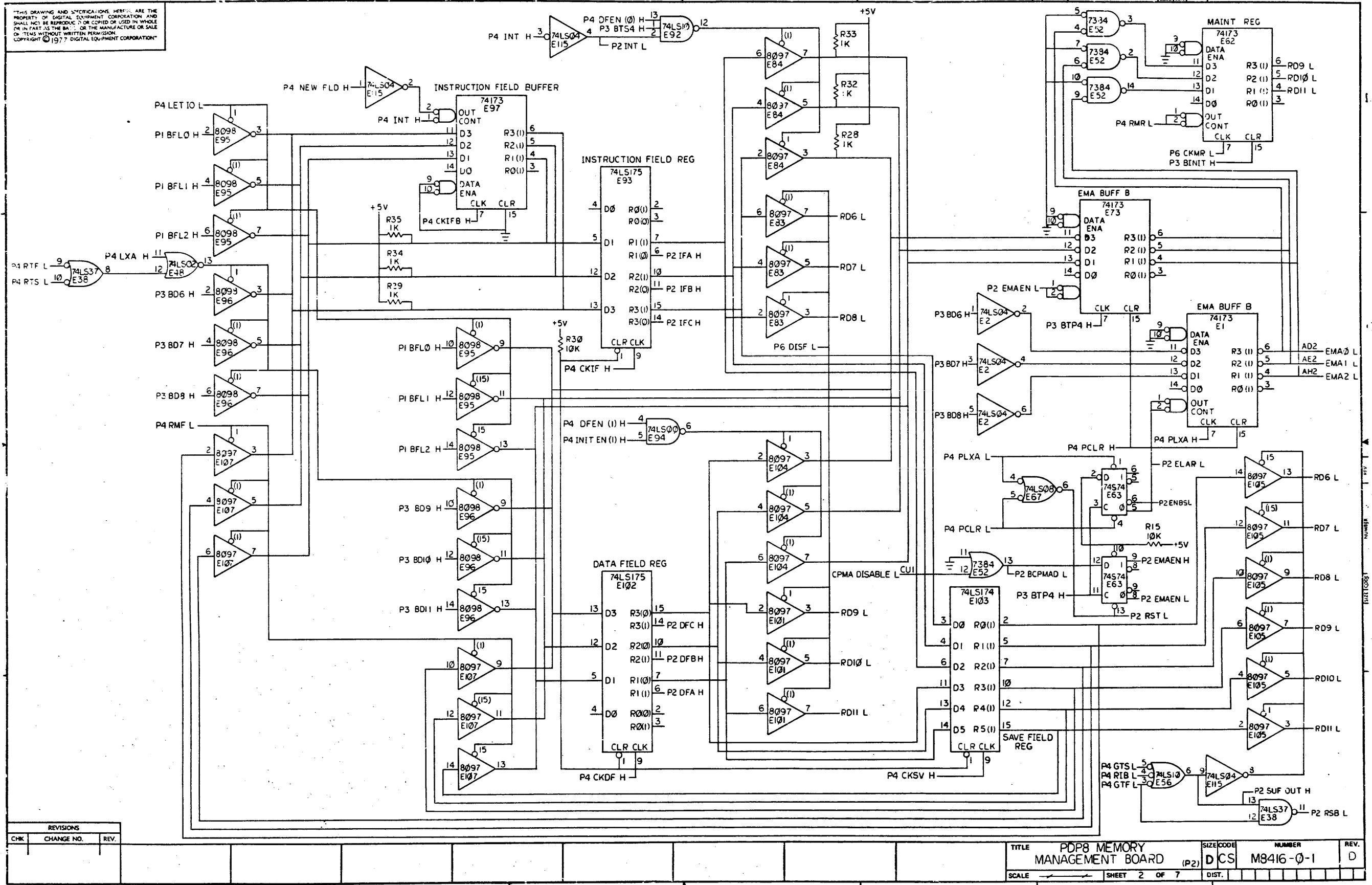
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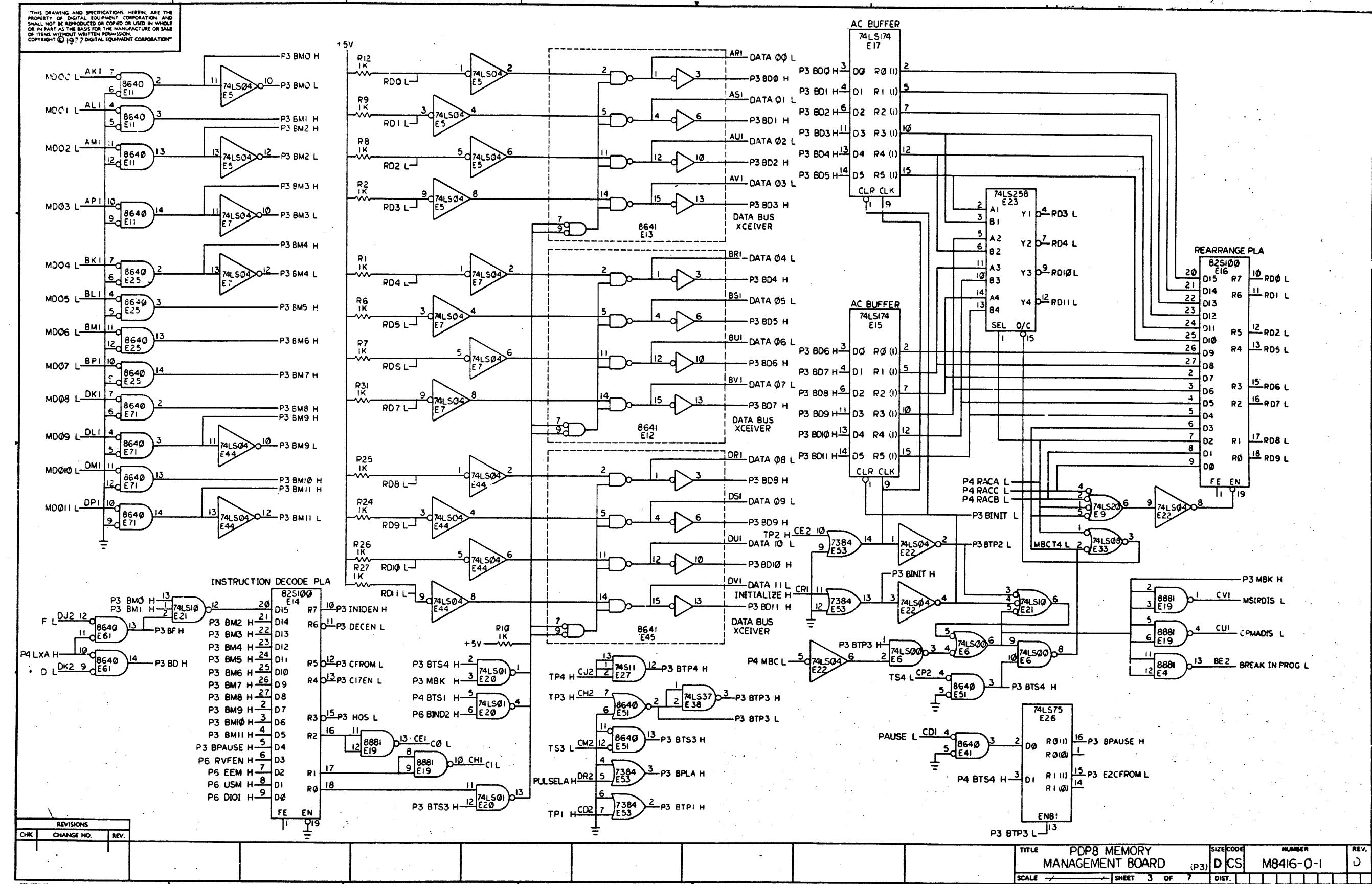


REVISIONS	
CHE	CHANGE NO.
J1	REV.
S. K. F. N.	C
DATA SHEET	12-7-77
DATA SHEET	12-7-77
4. T. SHALOM	12-29-77

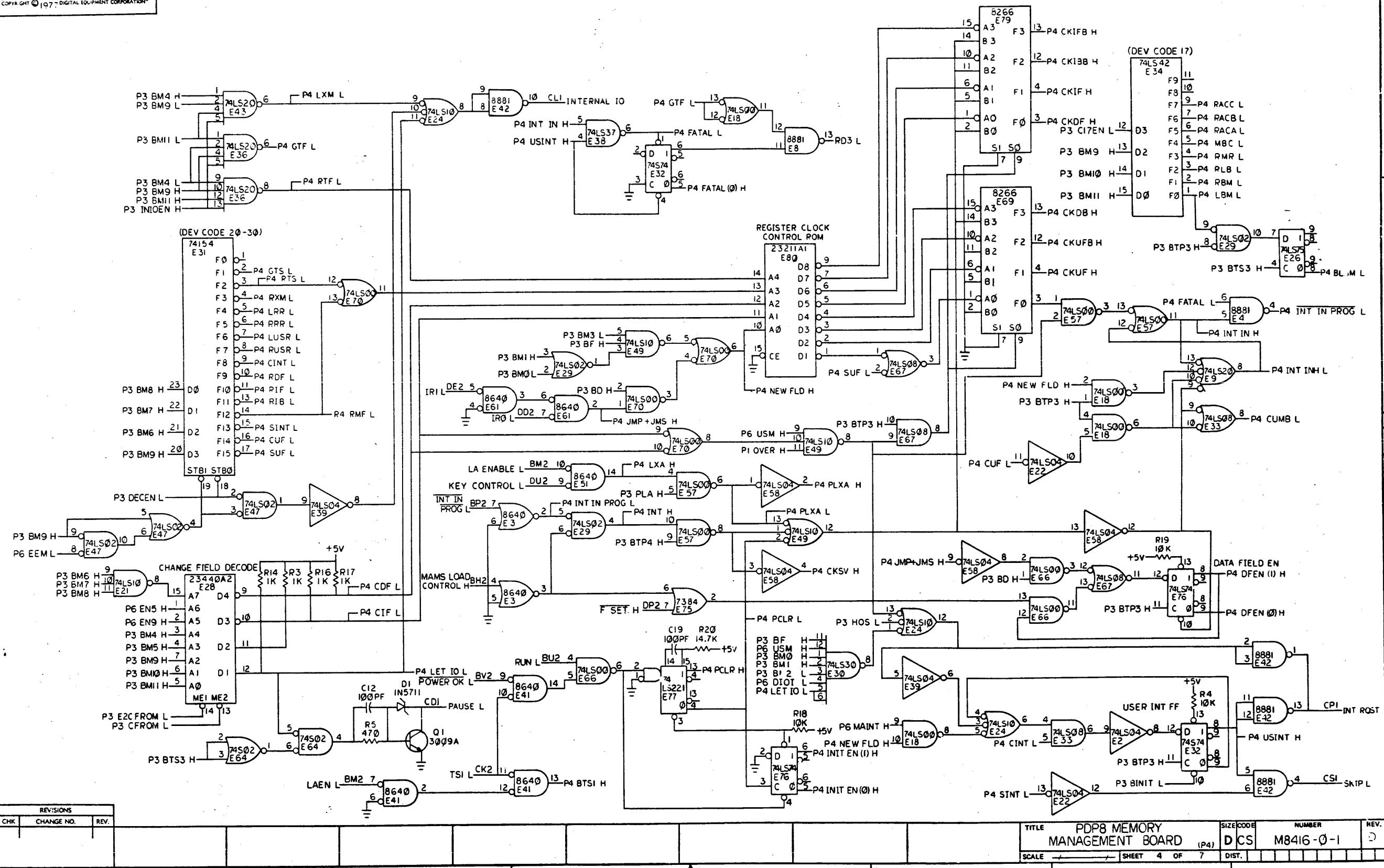
DRN. TESLA	11-4-77	FIRST USED ON	KT8-A	digital
CHG. NO.	12-7-77	TITLE		
ENC. NO.	12-7-77			
PROJ. ENG.	12-7-77			
PROD. V.	12-2-77			
NEXT HIGHER ASSY.				
D-U-A-M8416-0-0	SIZE	CODE	NUMBER	REV.
SCALE	D	CS	M8416-0-1	D
SHEET	1	OF	7	DIST.

PDP8 MEMORY
MANAGEMENT BOARD

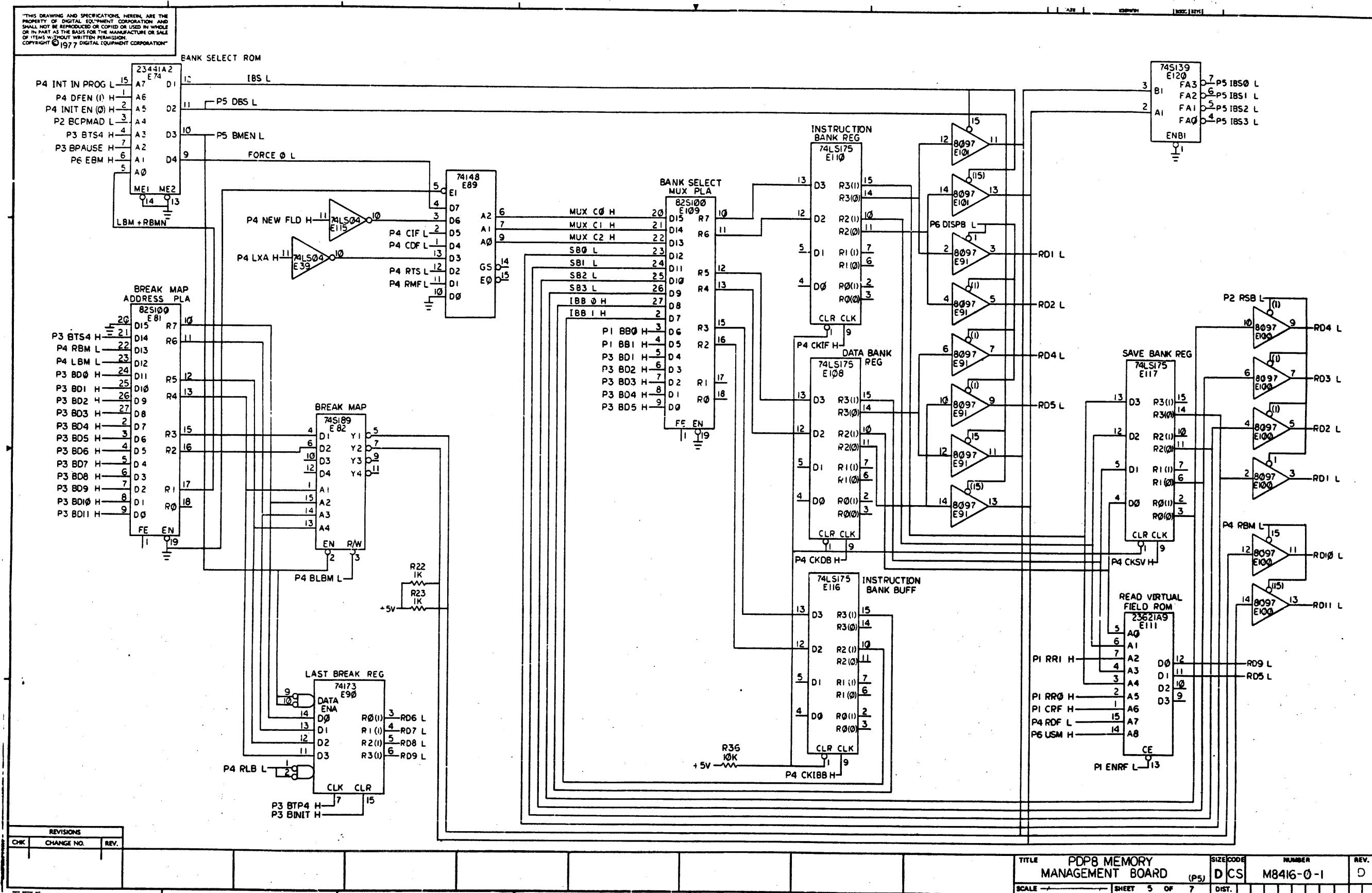


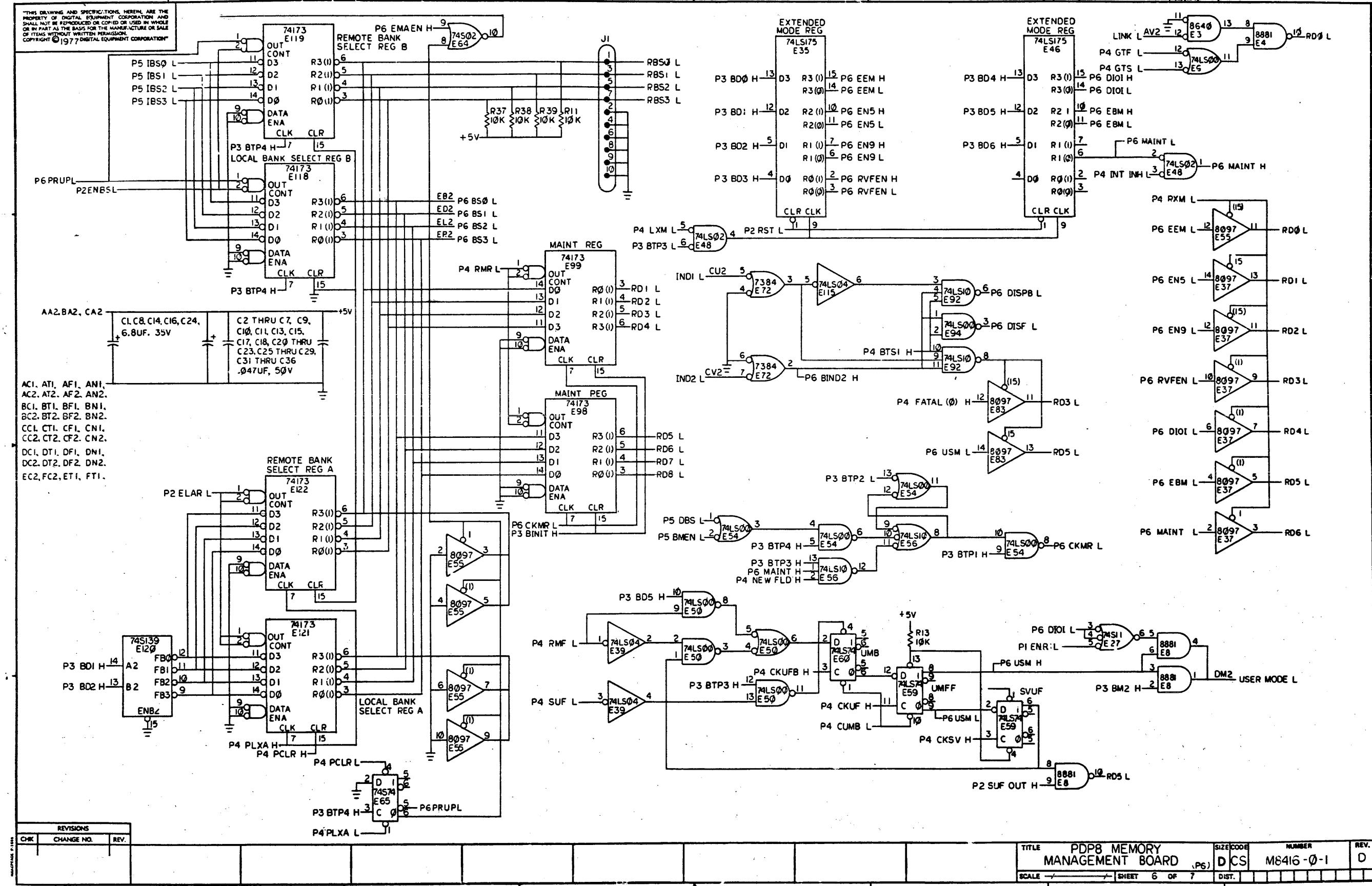


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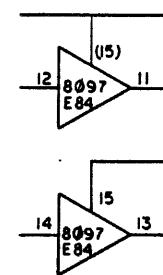
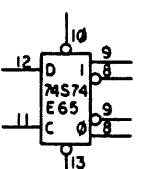
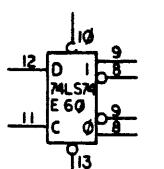
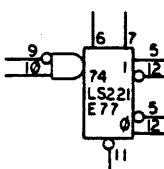
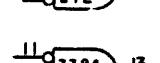
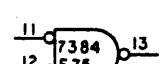
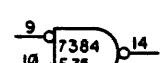
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SPARES



REVISIONS		
CHK.	CHANGE NO.	REV.

TITLE PDP8 MEMORY MANAGEMENT BOARD
SCALE / SHEET 7 OF 7 DIST. 1
SIZE CODE DCS NUMBER M8416-0-1 REV. D

8

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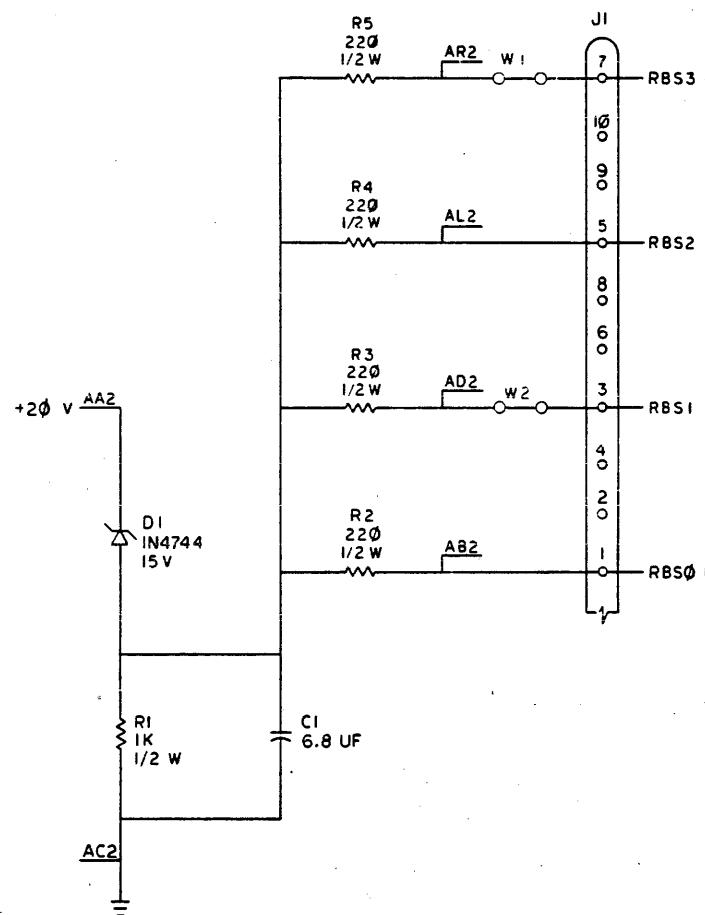
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D

D

C

C

B

B

A

A

REVISIONS
CHANGE NO. REV.
CHK: DATE: 11-9-77
MANUFACTURE: 12-7-77
SCALE: DIST:

DRN: <i>KT8-A</i>	11-9-77	FIRST USED ON	KT8-A	digital
CHK: <i>KT8-A</i>	11-9-77	TITLE	KT8-A	
ENG: <i>E</i>	12-7-77		TERMINATOR	
PROJ. ENG: <i>E</i>	12-7-77			
PROD. <i>KT8-A</i>	12-7-77			
NEXT HIGHER ASSY.				
D-1A-M9020-0-0		SIZE	CODE	NUMBER
SCALE		D	CS	M9020-0-1
SHEET	1	OF	1	REV.
				*

DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS

ENGINEERING SPECIFICATION

DATE 14 JUNE 77

TITLE MS8C Field Installation and Acceptance Procedure

REVISIONS

REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE

ENG	Bull Eng	APPD	Stover	SIZE	A	CODE	SP	NUMBER	MS8-C-3	REV	
-----	----------	------	--------	------	---	------	----	--------	---------	-----	--

DEC 16-(392)-1079A-R873
DRA 107A

SHEET 1 OF 3

ENGINEERING SPECIFICATION

000000

CONTINUATION SHEET

TITLE MS8C Field Installation and Acceptance Procedure

I. General

This procedure defines the performance standards required of the MS8CA and MS8CB Memories. The MS8C can be installed in any PDP8A series computers. The Memory may be an add-on to PDP8E systems if a BA8C expander is used.

II. Inspection Add-on

After removing the M8417 from the packing material, inspect the module for the following:

1. Loose or broken components.
2. Inventory against shipping lists.

III. Installation

1. Switch/Field Selection

MS8-CA (1) One switch(only) must be off.
MS8-CB (2) Two switch(only) must be off.

Set-up switch as defined in the following table.
Switches off are the enable.

Address	Bank	Field
S1-1	0-16K	0
S1-2	16-32K	0
*S1-3	32-48K	1
*S1-4	48-64K	1
*S1-5	64-80K	2
*S1-6	80-96K	2
*S1-7	96-112K	3
*S1-8	112-128K	3

*NOTE: KT8A must be installed for these settings. Refer to the KT8A Installation and Acceptance Procedures for KT8A systems acceptance.

2. Install

Ensure power to PDP8A is off.
Insert the M8417 into slot 4 of the PDP8A.
If two memories are to be installed, install the second in slot 5.

IV. Acceptance

Perform the acceptance test as indicated in the following table. If problems are encountered, refer to the diagnostic listings for the type of error and for information on how to read the error printouts.

	SIZE	CODE	NUMBER	REV
	A	SP	MS8-C-3	

DEC FORM NO DEC 16-(381)-1022-N370
DRA 108

SHEET 2 OF 3

ENGINEERING SPECIFICATION

Digital

CONTINUATION SHEET**TITLE** MS8C Field Installation and Acceptance Procedure**Equipment Required:**

1. 03,04 Console Terminal
2. PDP8A with MS8C Memory
- *3. Input device, either paper tape or OS8.
4. Diagnostics and listings.

*Programmers console is required to make Switch register settings on the paper tape version of the diagnostic.

<u>Program Name</u>	<u>Maindec #</u>	<u>Accept Time</u>
Extended Memory Address Test	08-DHKMA	30 Minutes
Extended Memory Checkerboard	08-DHKMC	30 Minutes

No Errors are Acceptable.

	SIZE A	CODE SP	NUMBER MS8-C-3	REV
--	-------------------------	-------------------	--------------------------	------------

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M8417-AA (16K)
X = SWITCH OPEN OR OFF

	E62-1	E62-2	E62-3	E62-4	E62-5	E62-6	E62-7	E62-8
0-16	X							
16-32		X						
32-64			X					
48-64				X				
64-80					X	X		
80-96						X	X	
96-112							X	X
112-128								X

M8417-BA (32K)
X = SWITCH OPEN OR OFF

	E62-1	E62-2	E62-3	E62-4	E62-5	E62-6	E62-7	E62-8
0-32	X	X						
16-48		X	X					
32-64			X	X				
48-80				X	X			
64-96					X	X		
80-112						X	X	
96-128							X	X

NOTES:

1. MODULE DESIGNATION: M8417-AA = M8417-AB,-AC,-AD,-AE, ETC.
16K MOS MEMORY

REFERENCE DESIGNATIONS NOT USED:

E101 E201 E301 E401
E103 E203 E303 E403
E105 E205 E305 E405
E107 E207 E307 E407
E109 E209 E309 E409
E111 E211 E311 E411
E113 E213 E313 E413
E115 E215 E315 E415
E117 E217 E317 E417
E119 E219 E319 E419
E121 E221 E321 E421
E123 E223 E323 E423

2. MODULE DESIGNATION: M8417-BA = M8417-BB,-BC,-BD,-BE, ETC.
32K MOS MEMORY

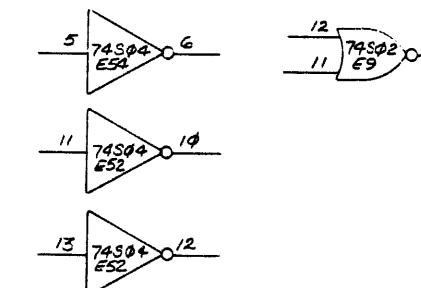
3. ALL 8837'S HAVE PINS 7 & 9 GROUNDED.

4. TIMING RESISTORS R87 AND R92 MAY BE REMOVED AT MODULE TEST FOR TIMING ADJUSTMENTS.

5. TIMING RESISTORS R88, R100, R140 MAY BE INSTALLED AT MODULE TEST FOR TIMING ADJUSTMENTS.

SPARES

R88	3.16K 1/4W 1% MF	13-03945-00
R100	6.04W 1/4W 1% MF	13-13155-00
R140	1.21K 1/4W 1% MF	13-02671-00



JUMPER CONFIGURATIONS		
JUMPER	M8417-AA (16K)	M8417-BA (32K)
W2	X	X
W3	X	X
W4	X	X
W5	X	X
W10	X	
W11	X	X

ALL JUMPERS ARE MACHINE INSERTABLE.

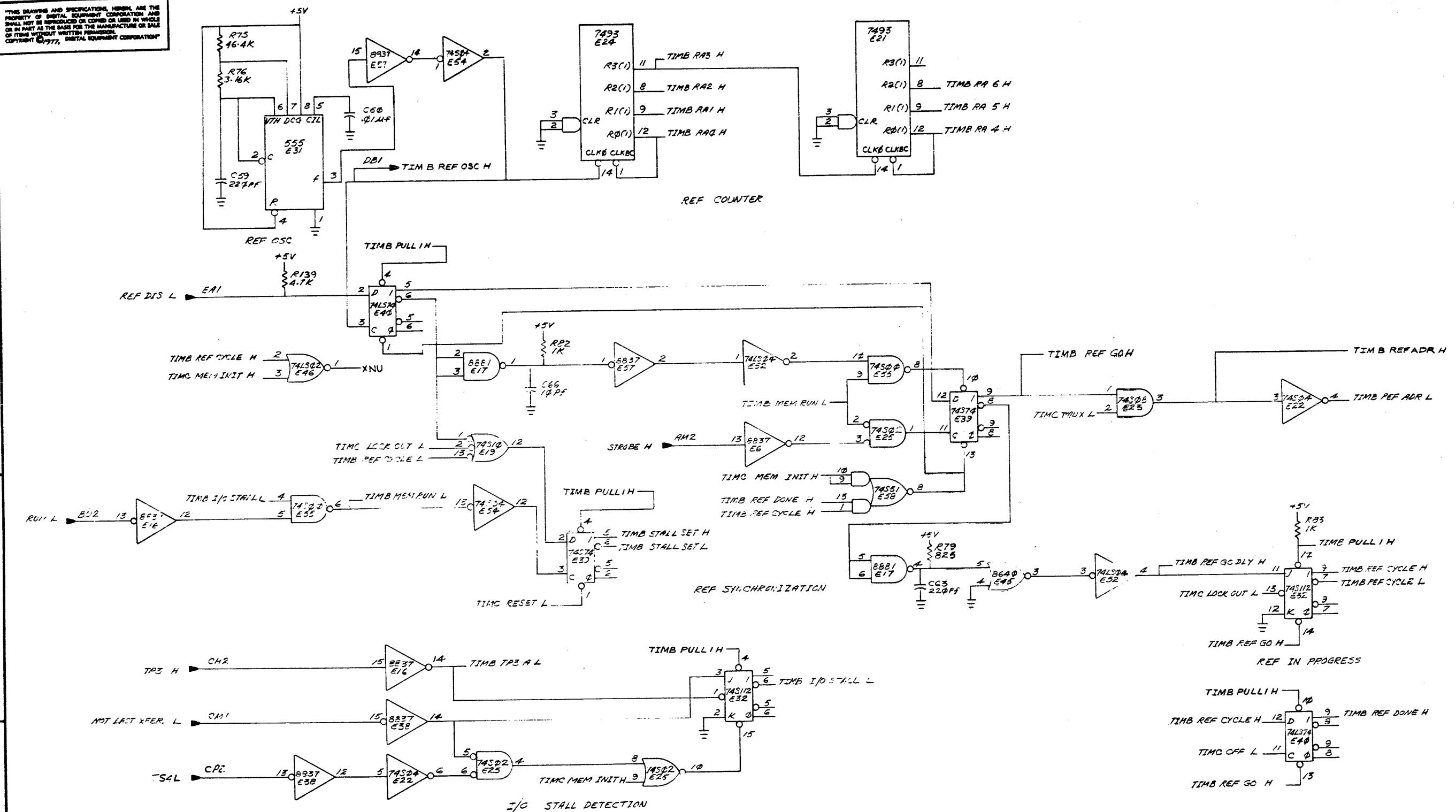
THESE JUMPERS ARE REMOVED:
W1, W5, W6, W7, W8, W12, W13, W14,
W15, W16

X = JUMPER INSTALLATION

IC PIN LOCATIONS					
IC TYPE	GND	+5	-5	+12	
555		1	5		
7493	10	5			
74LS75	12	5			
8640	1	8			
75107B	7	14	13		
MK4027	16	9	1	8	
OTHER 16 PIN IC's	8	16			
OTHER 14 PIN IC's	7	14			
75451	4	8			

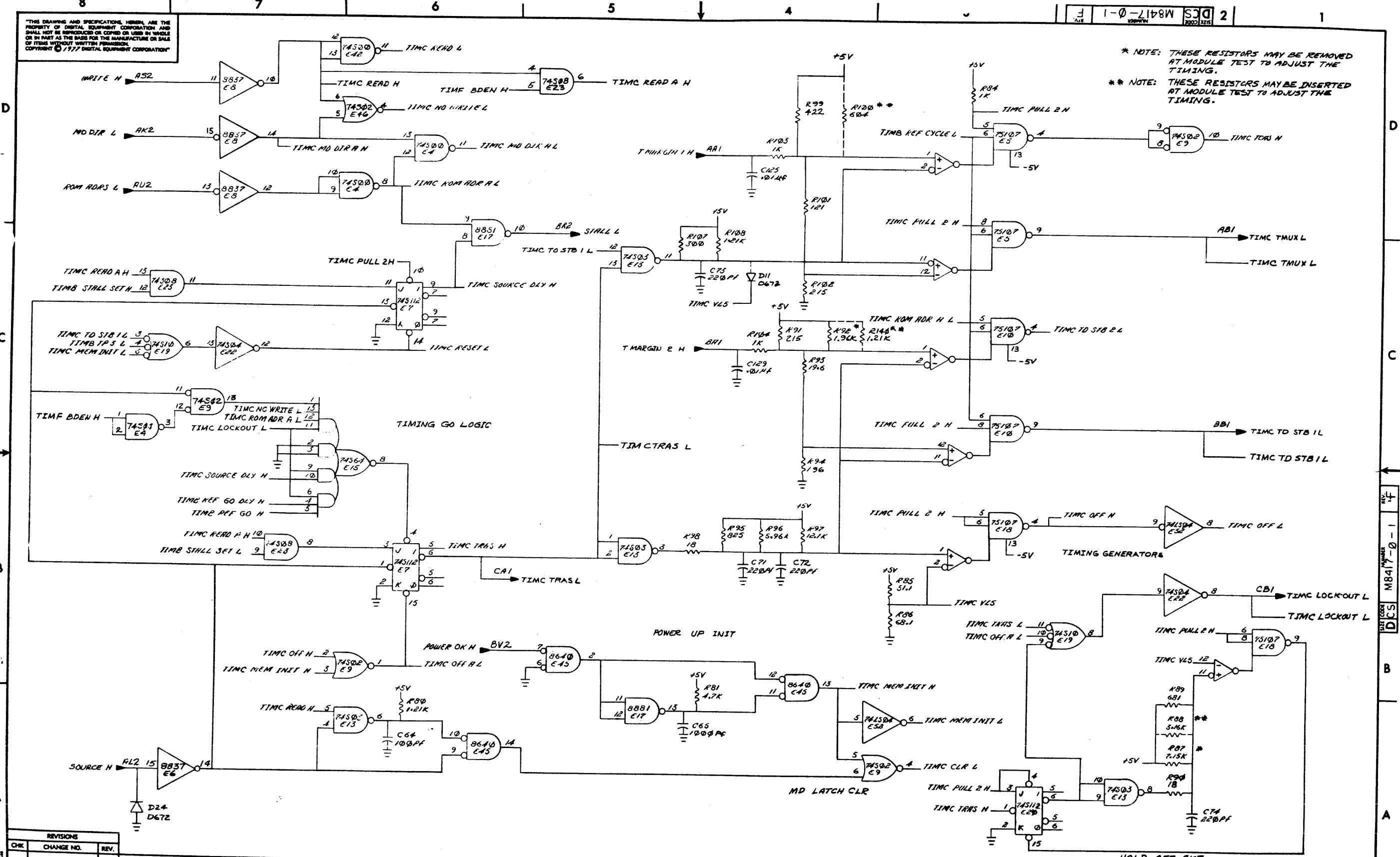
REV	CHANGING NO.	REV	DATE
2.0	M8417-00001	B	1-22-78
2.1	J. STEGEMAN	C	2-22-78
2.2	M8417-00002	D	3-22-78
2.3	J. STEGEMAN	E	4-22-78
2.4	M8417-00003	F	5-22-78
2.5	J. STEGEMAN	G	6-22-78
2.6	M8417-00004	H	7-22-78
2.7	J. STEGEMAN	I	8-22-78
2.8	M8417-00005	J	9-22-78
2.9	J. STEGEMAN	K	10-22-78
3.0	M8417-00006	L	11-22-78
3.1	J. STEGEMAN	M	12-22-78

DRN. ANGEL COLOR	4/25/77	FIRST USED ON	MS8-C	digital	
CHK'D	7/7/77	TITLE	PDP8 MOS MEMORY		
ENG.	1-1-1	PROJ. ENG.	1-1-1	PROD. ENG.	1-1-1
NEXT HIGHER ASSY.		B-DD-M8417-0			SIZE
		SCALE NCNE			CODE
		NUMBER M8417-0-1			REV.
		SHEET	1 OF 14	DIST.	F



REVISIONS		
CHK	CHANGE NO.	REV.

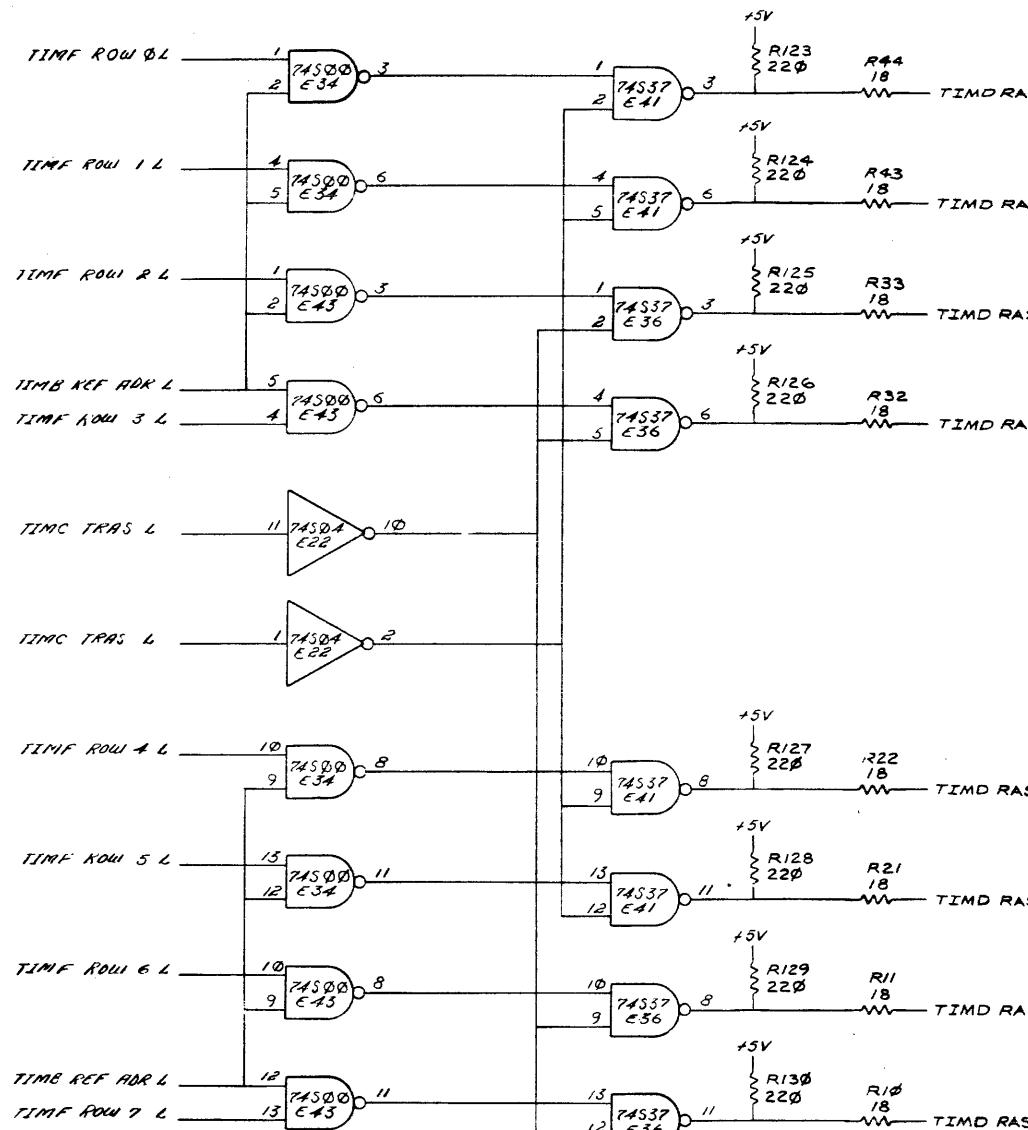
TITLE: PDP8 MOS MEMORY (TIM B)
 SIZE CODE: DCS M8417-0-1
 REV. F
 SCALE: N/N E
 SHEET 2 OF 14 DIST.
 1



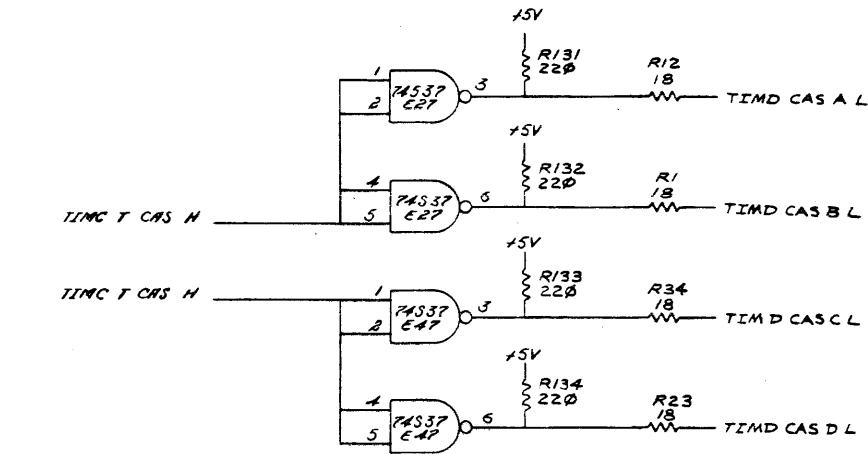
REV.	DATE	FILE

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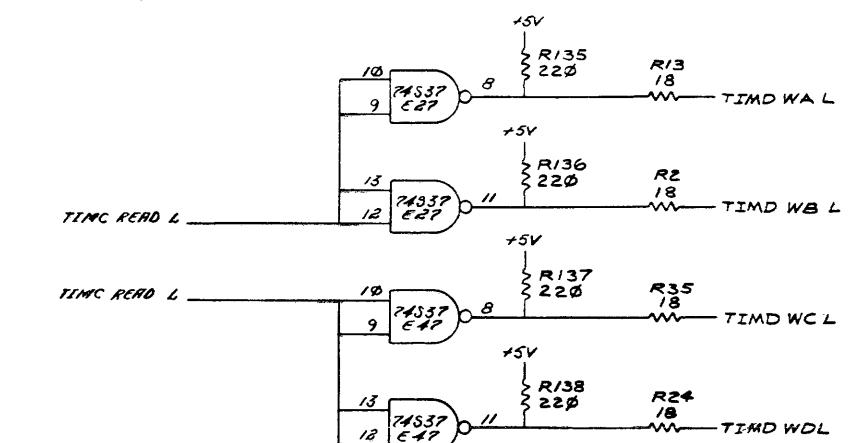
D



RAS DRIVERS & SELECT



CAS DRIVERS

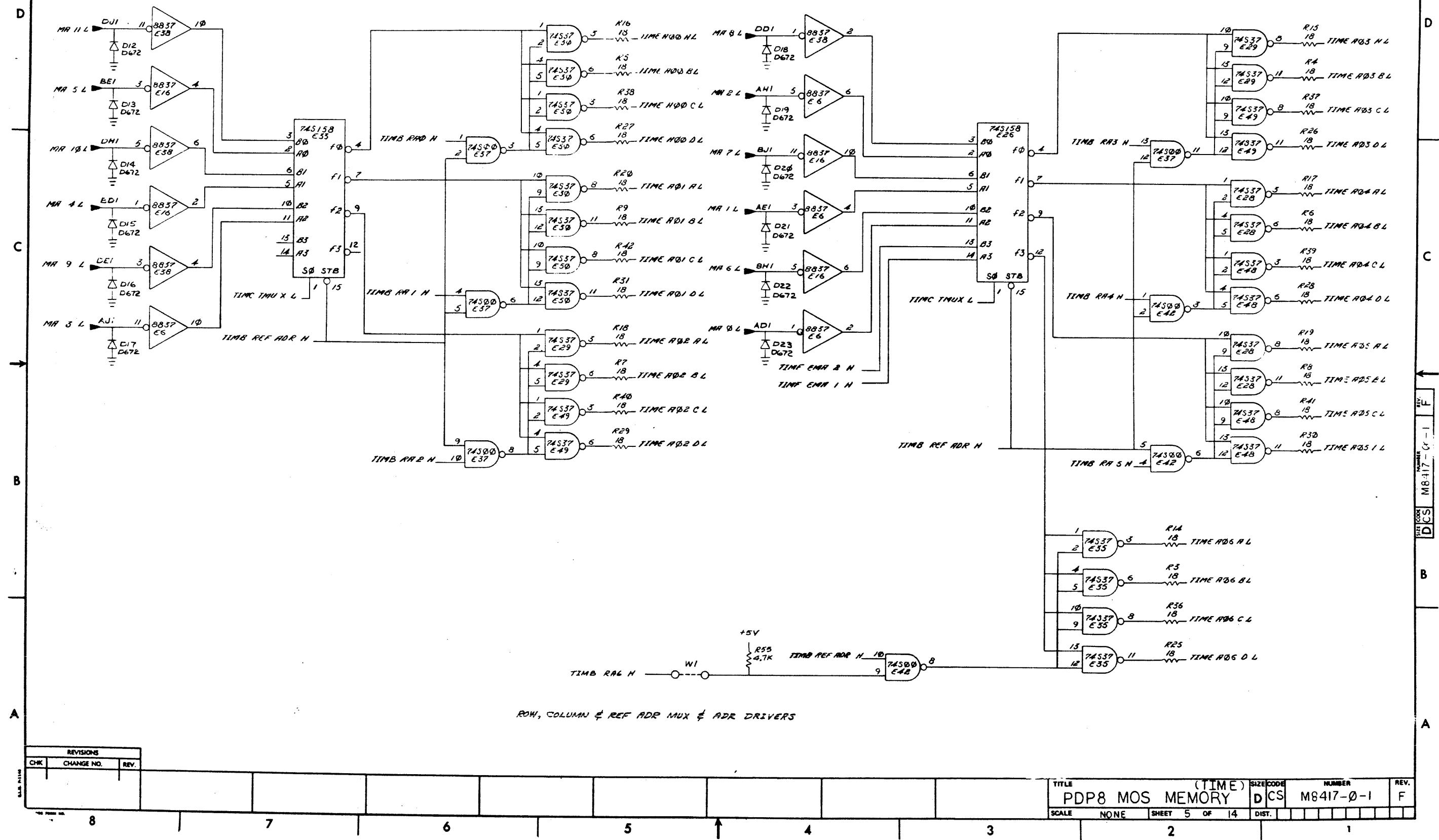


WRITE ENABLE DRIVERS

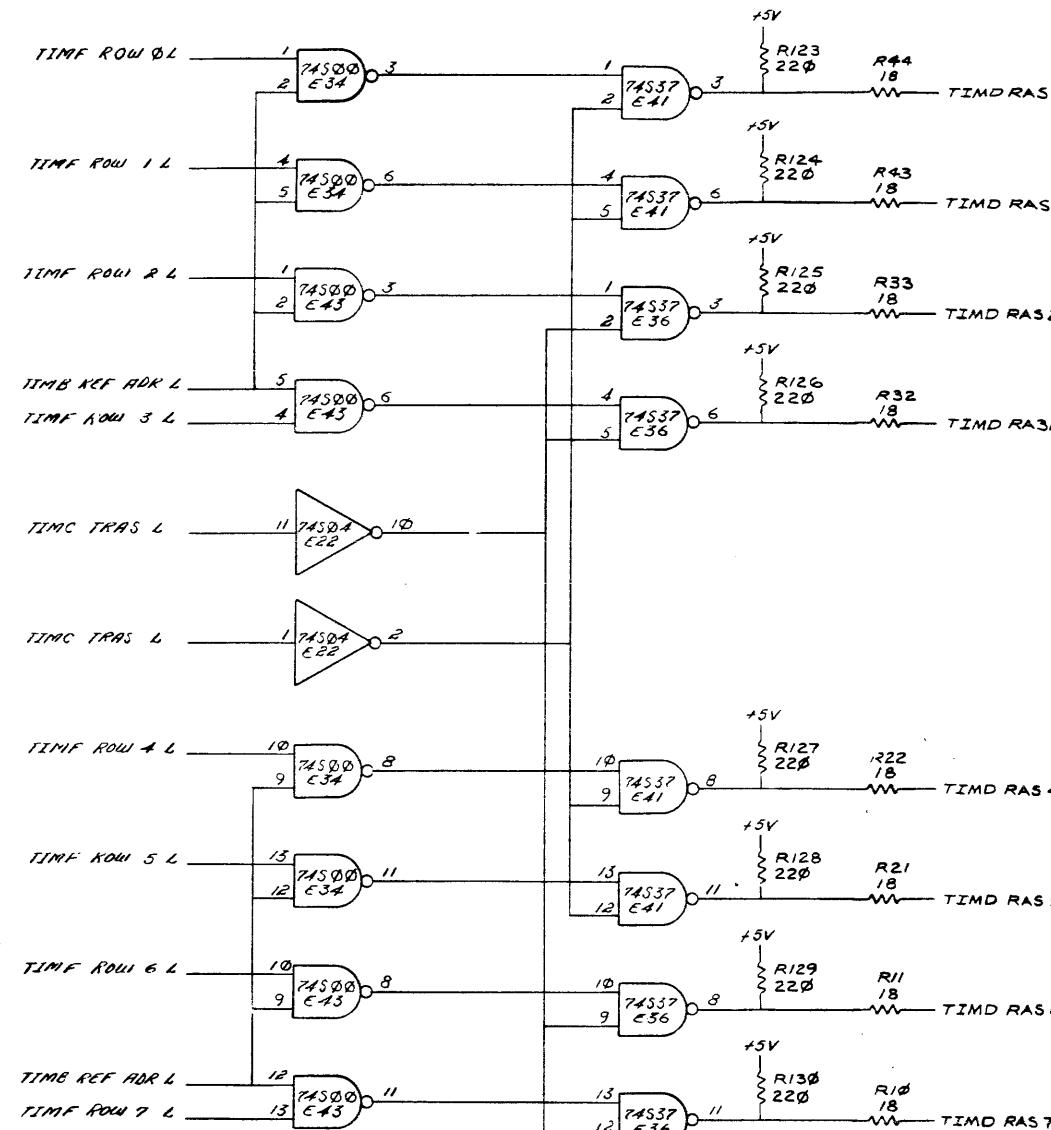
REVISIONS		
CHK	CHANGE NO.	REV.

TITLE			(TIM D)	SIZE	NUMBER	REV.
SCALE	CODE	REV.	PDP8 MOS MEMORY	D	M8417-0-1	E
NONE						

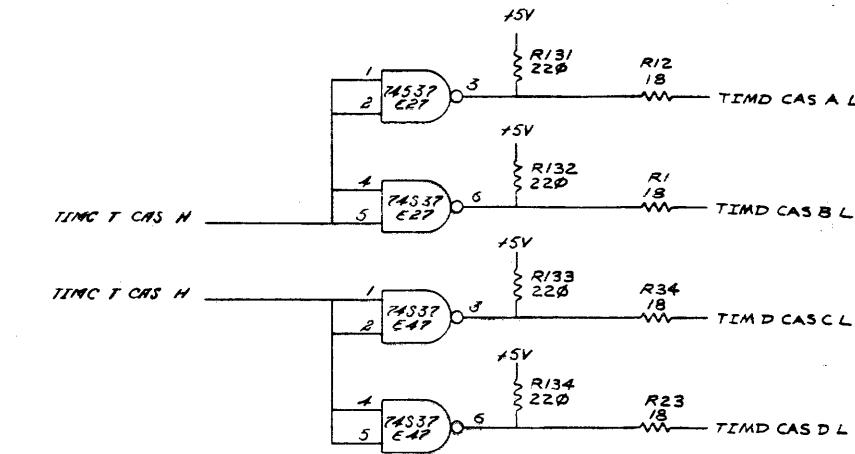
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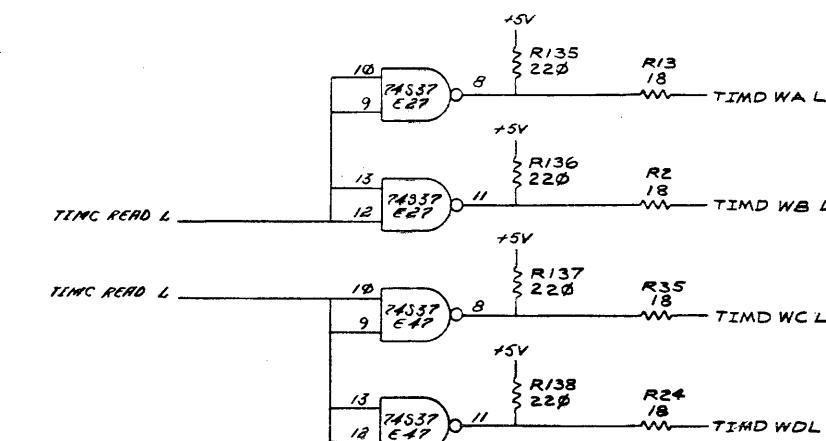
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RAS DRIVERS & SELECT



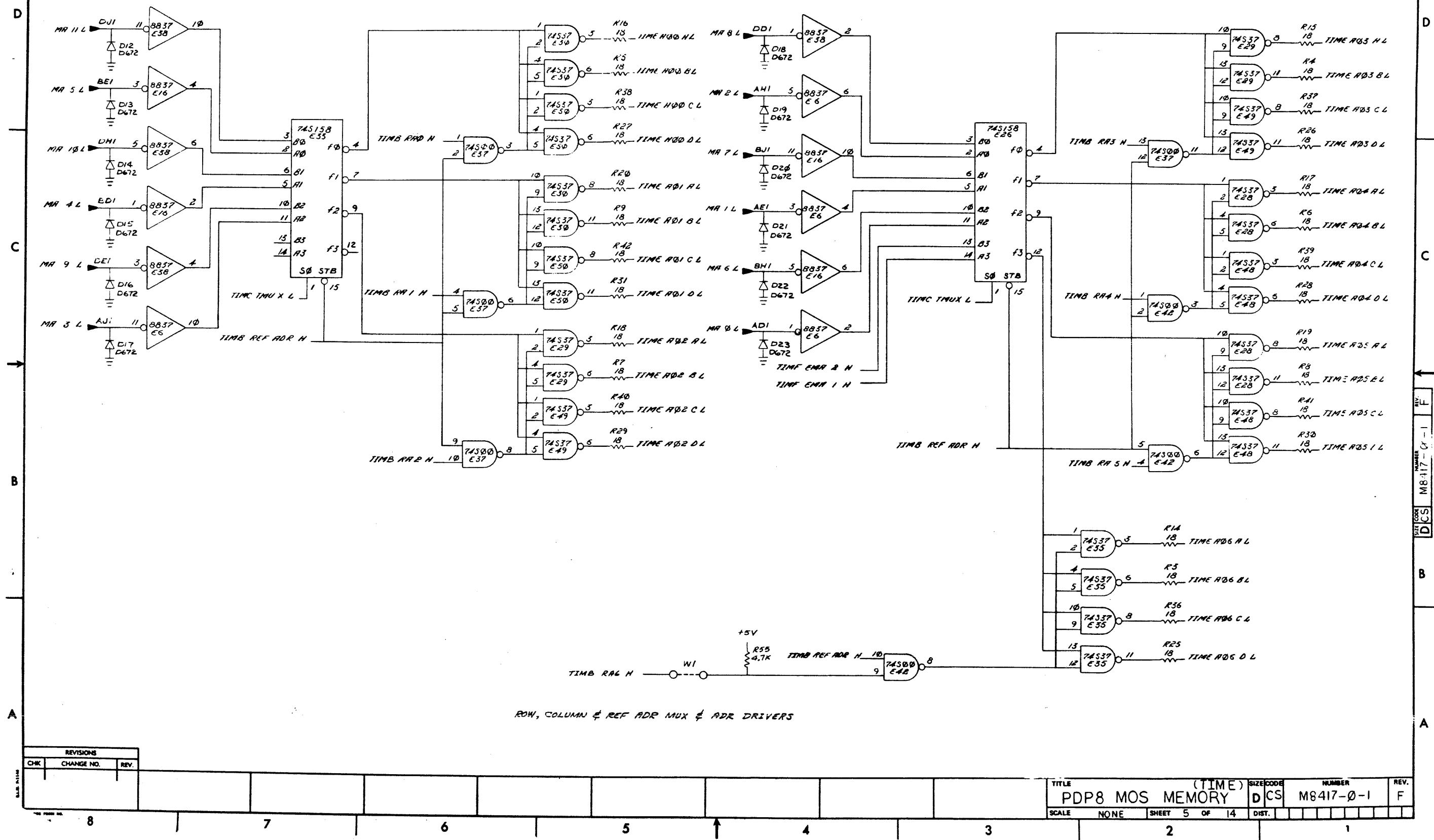
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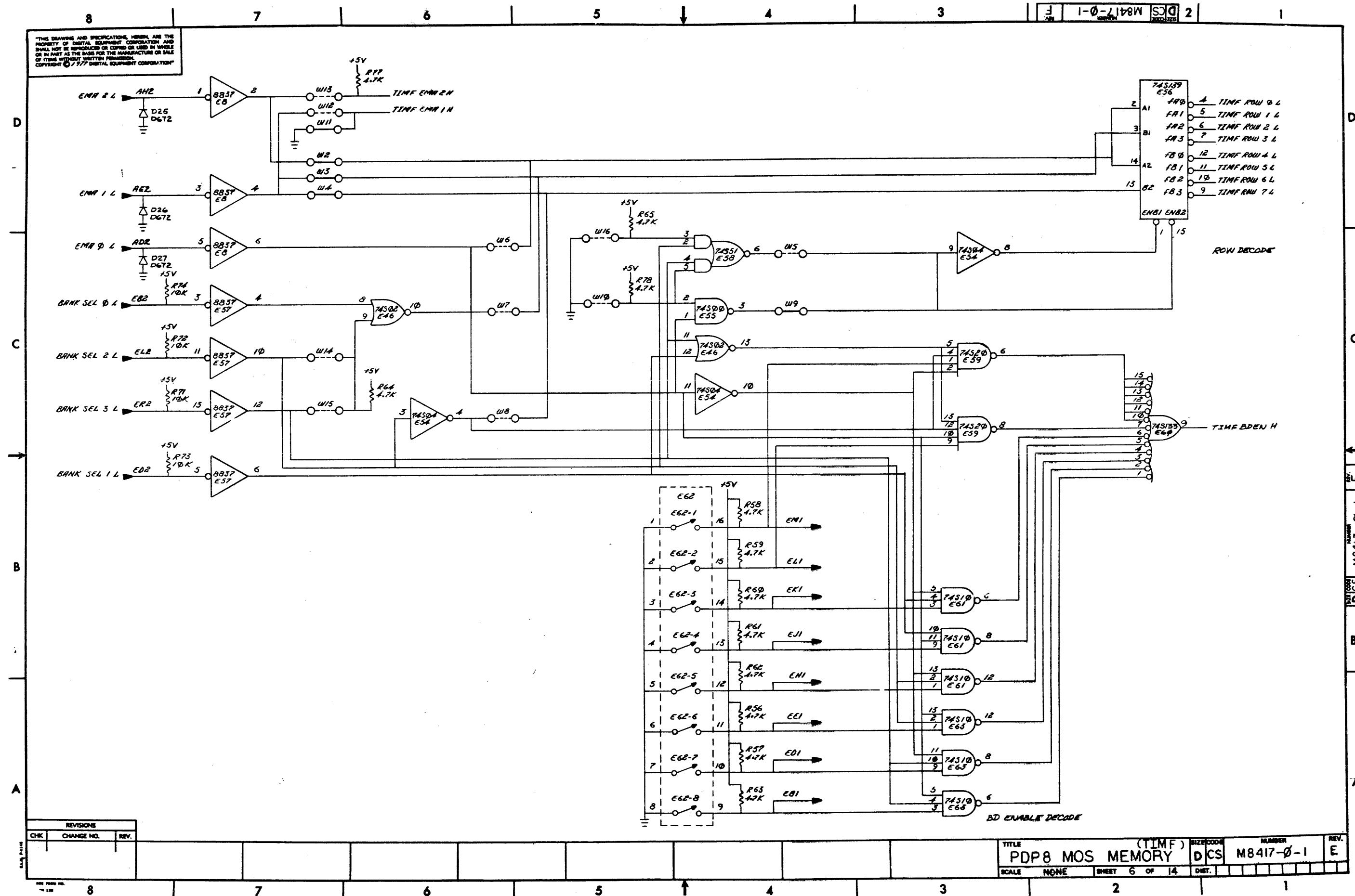


WRITE ENABLE DRIVERS

REVISIONS		
CHK	CHANGE NO.	RE

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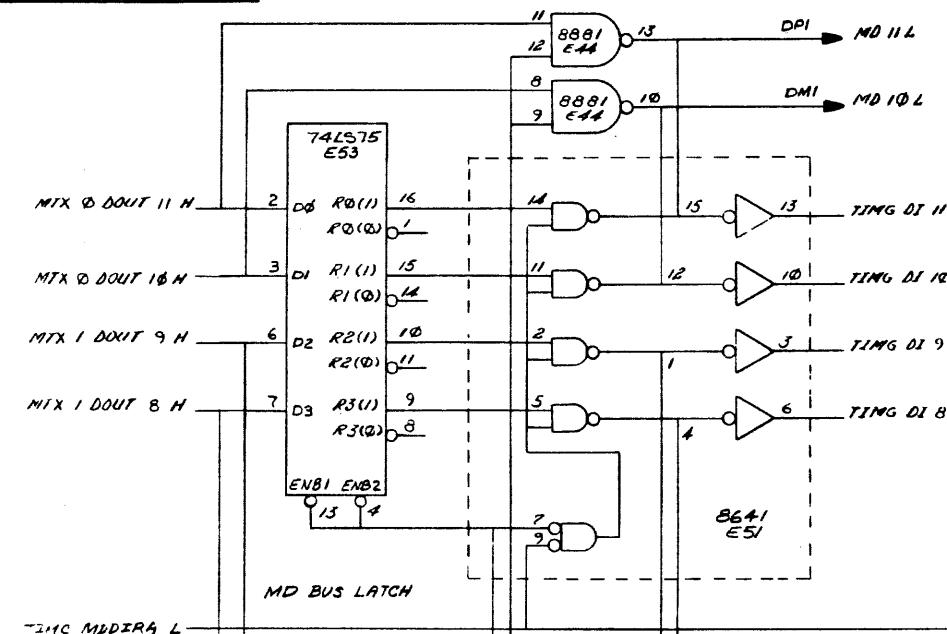
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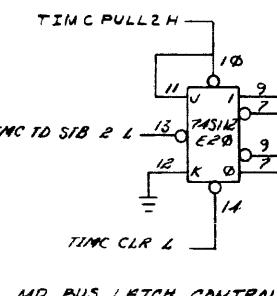
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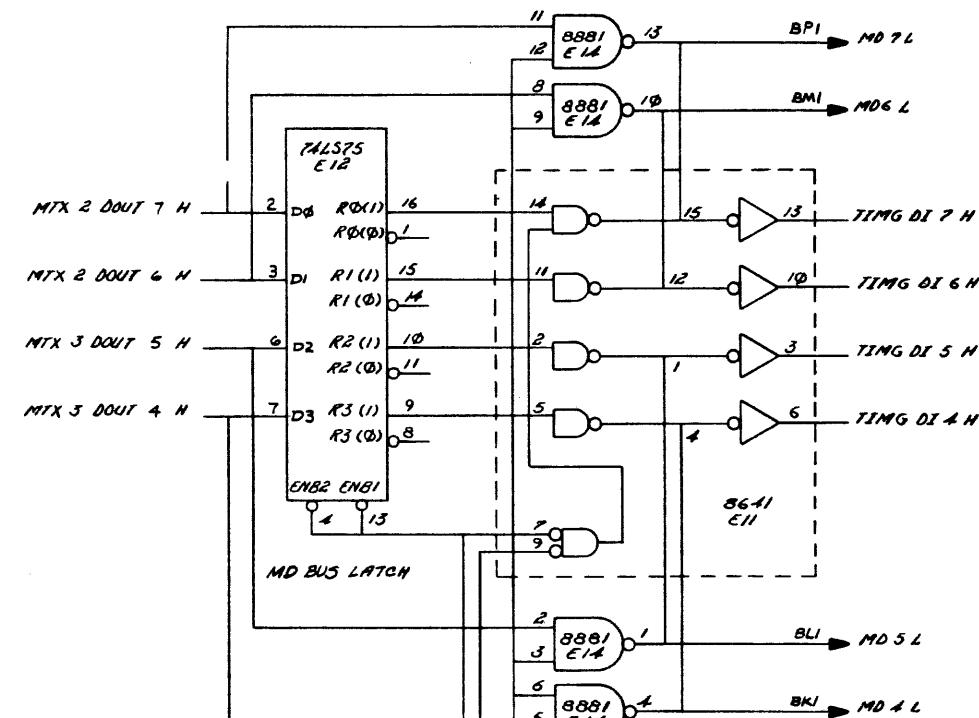
MD BUS TRANSCEIVERS



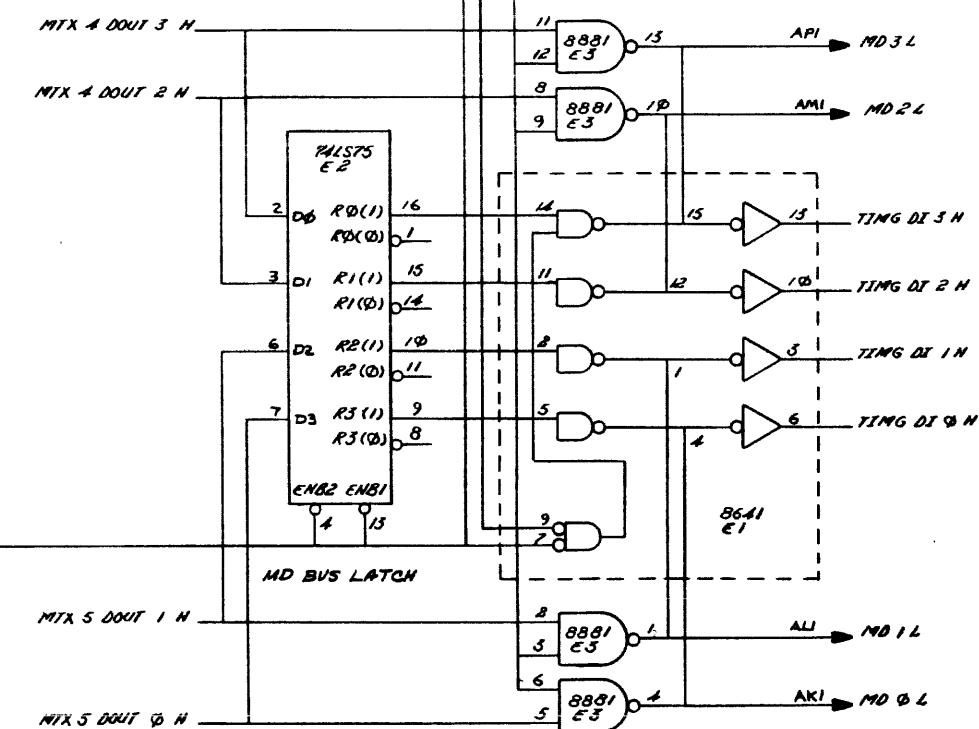
MD BUS LATCH



MD BUS LATCH CONTROL



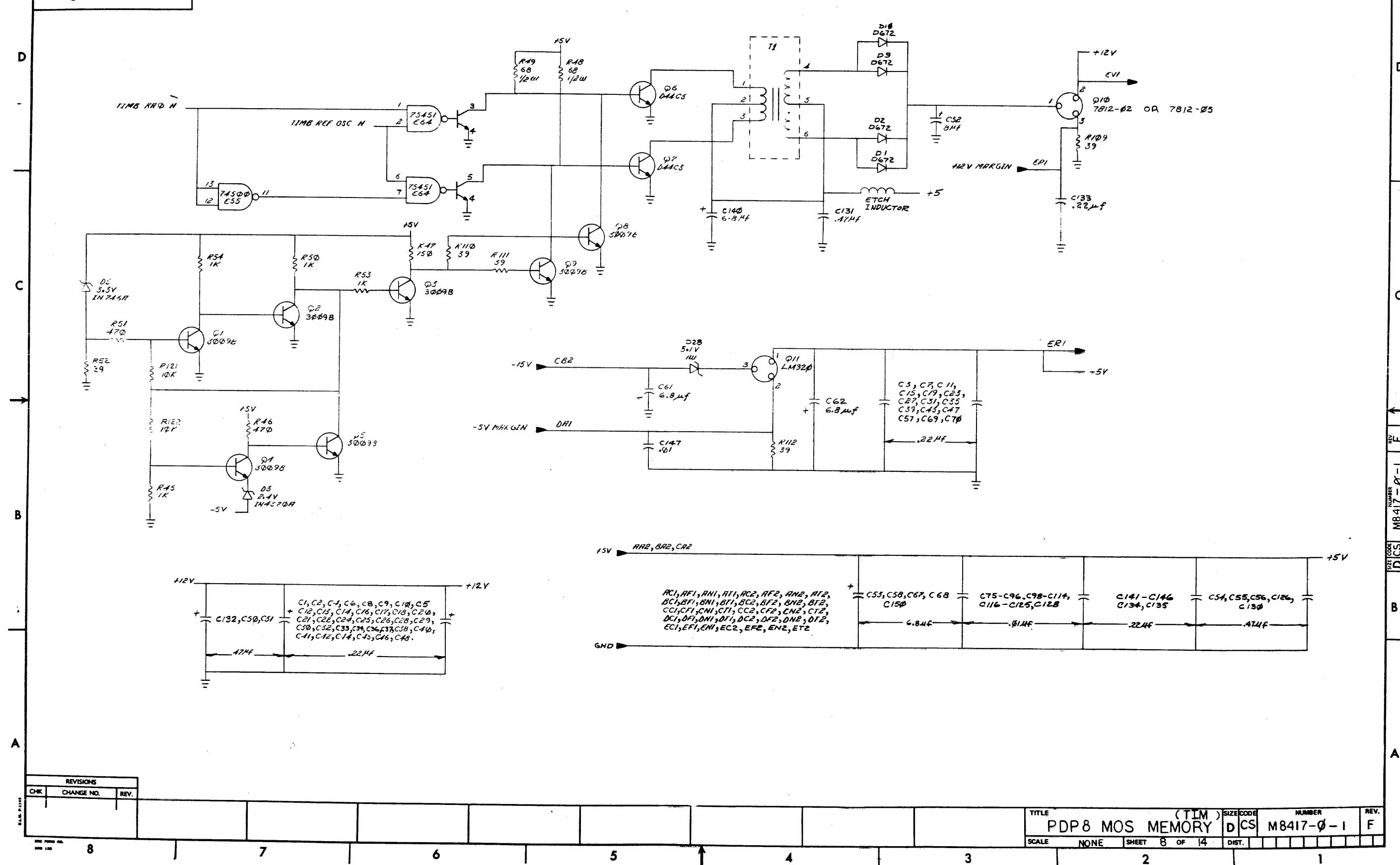
MD BUS TRANSCEIVERS



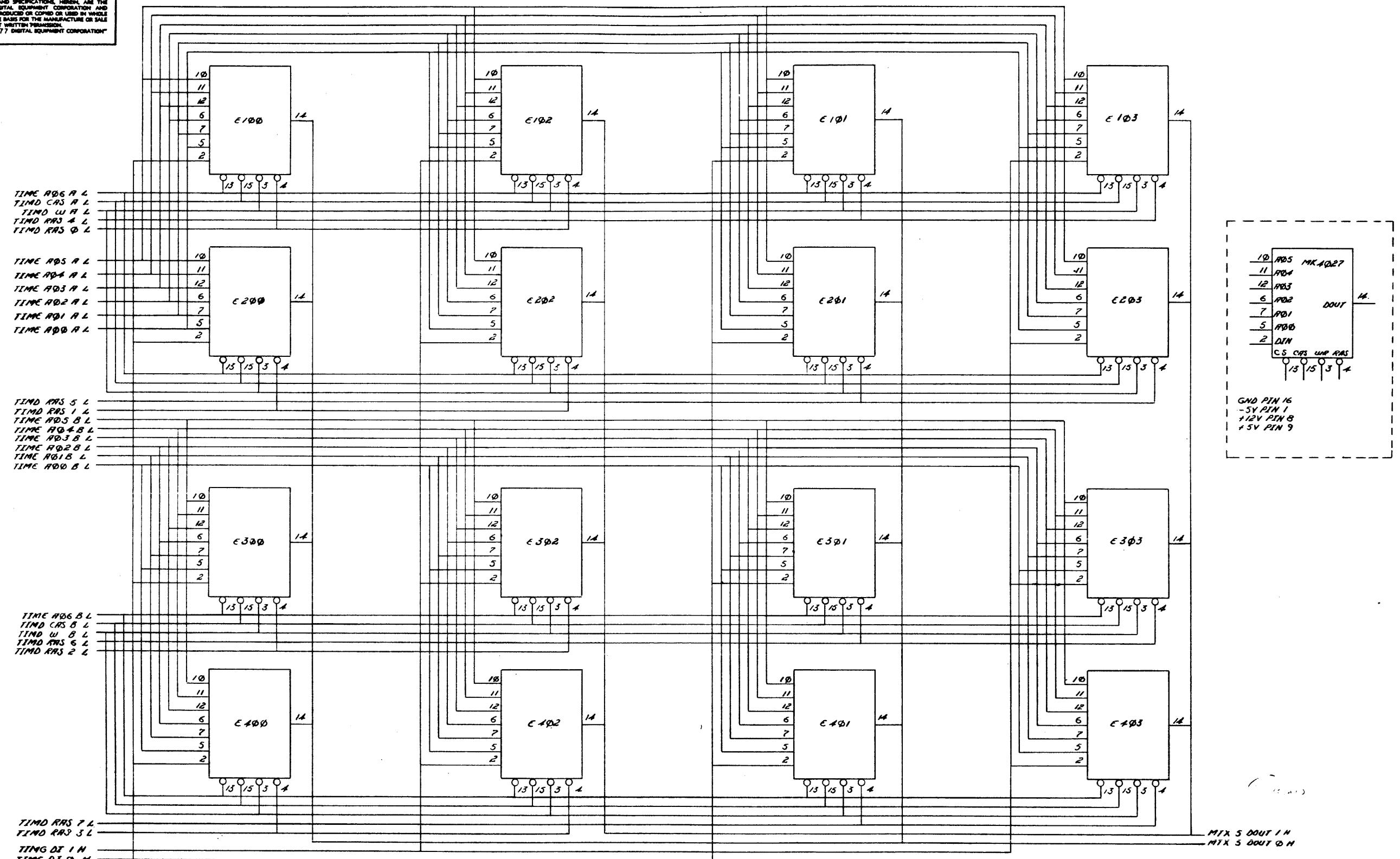
MD BUS LATCH

REVISIONS			TITLE			(TIMG)			SIZE CODE			NUMBER			REV.		
CHK	CHANGE NO.	REV.	PDP8 MOS MEMORY			DCS			M8417-0-1			F.					
			SCALE	NONE		SHEET	7	OF	14	DIST.							

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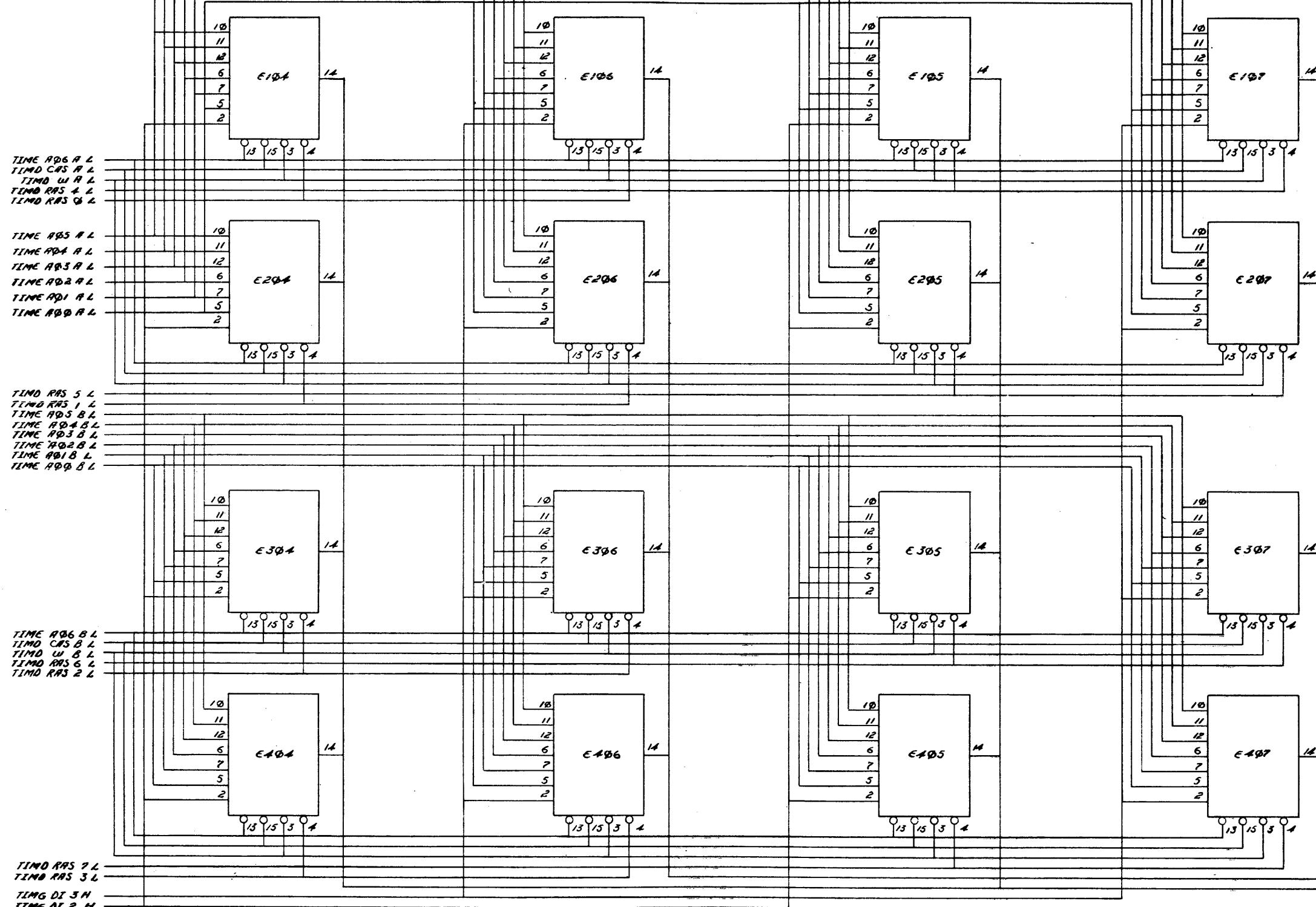
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REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		SIZE CODE	NUMBER	REV.
PDP8 MOS MEMORY	D CS	M8417-0-1	F	
SCALE NONE	SHEET 9 OF 14	DIST.		

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REVISIONS		
CHK	CHANGE NO.	REV.

REV. A

M8417-0-1

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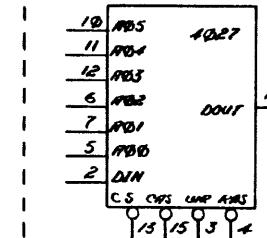
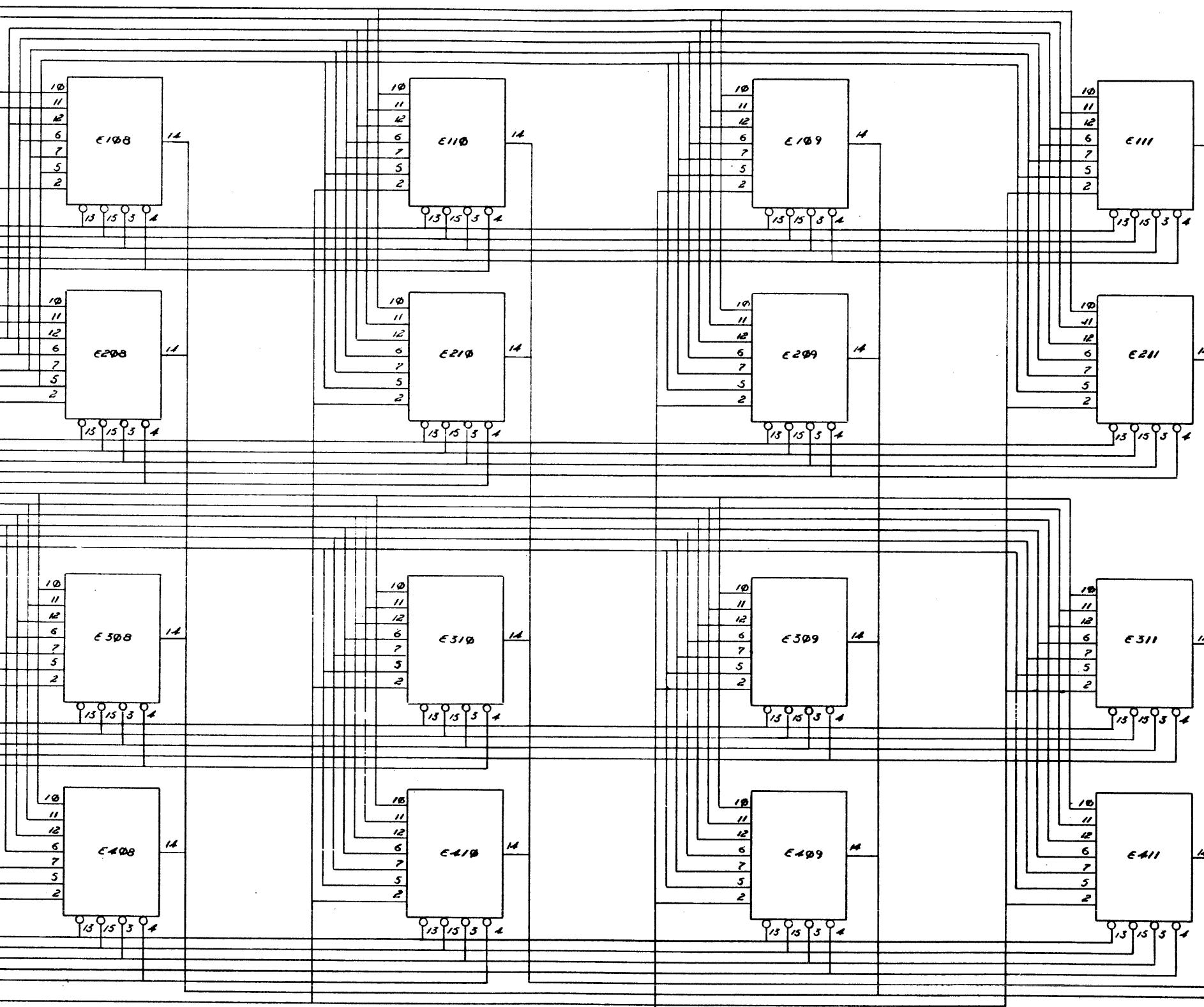
TIME RQ6 AL
TIME CAS AL
TIME W AL
TIME RAS 4 L
TIME RAS 0 L

TIME RQ5 AL
TIME RQ4 AL
TIME RQ3 AL
TIME RQ2 AL
TIME RQ1 AL
TIME RQ0 AL

TIME RAS 5 L
TIME RAS 1 L
TIME RQ5 B L
TIME RQ4 B L
TIME RQ3 B L
TIME RQ1 B L
TIME RQ0 B L

TIME RQ6 B L
TIME CAS B L
TIME W B L
TIME RAS 6 L
TIME RAS 2 L

TIME RAS 7 L
TIME RAS 3 L
TIME DI 5 N
TIME DI 4 N



GND PIN 16
-5V PIN 1
+12V PIN 8
+5V PIN 5

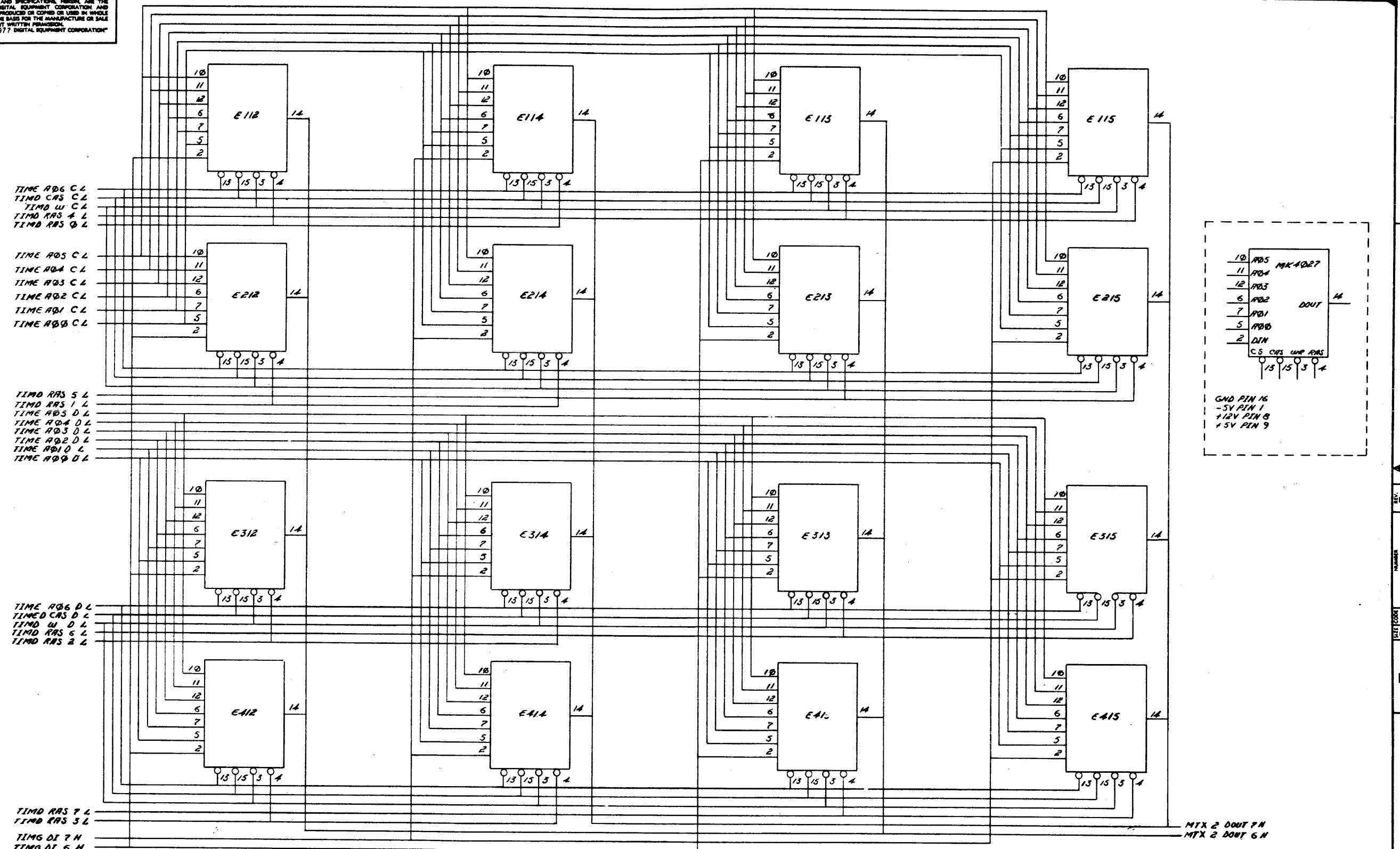
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MTX 5

MTX 5
MTX 5

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE PDP-8 MOS MEMORY
SCALE NONE SHEET 11 OF 14 DRAFT. DIST. 1
SIZE CODE DCS NUMBER M8417-0-1 REV. F

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(ARRAY) DATA BITS 6,7
MTX 2

REVISIONS		
CHK	CHANGE NO.	R

TITLE	PDP8 MOS MEMORY		SIZE CODE	NUMBER	REV.
			D C S	M 8417-0-1	F
SCALE	NONE	SHEET 12 OF 14	DIST.		
		2		1	

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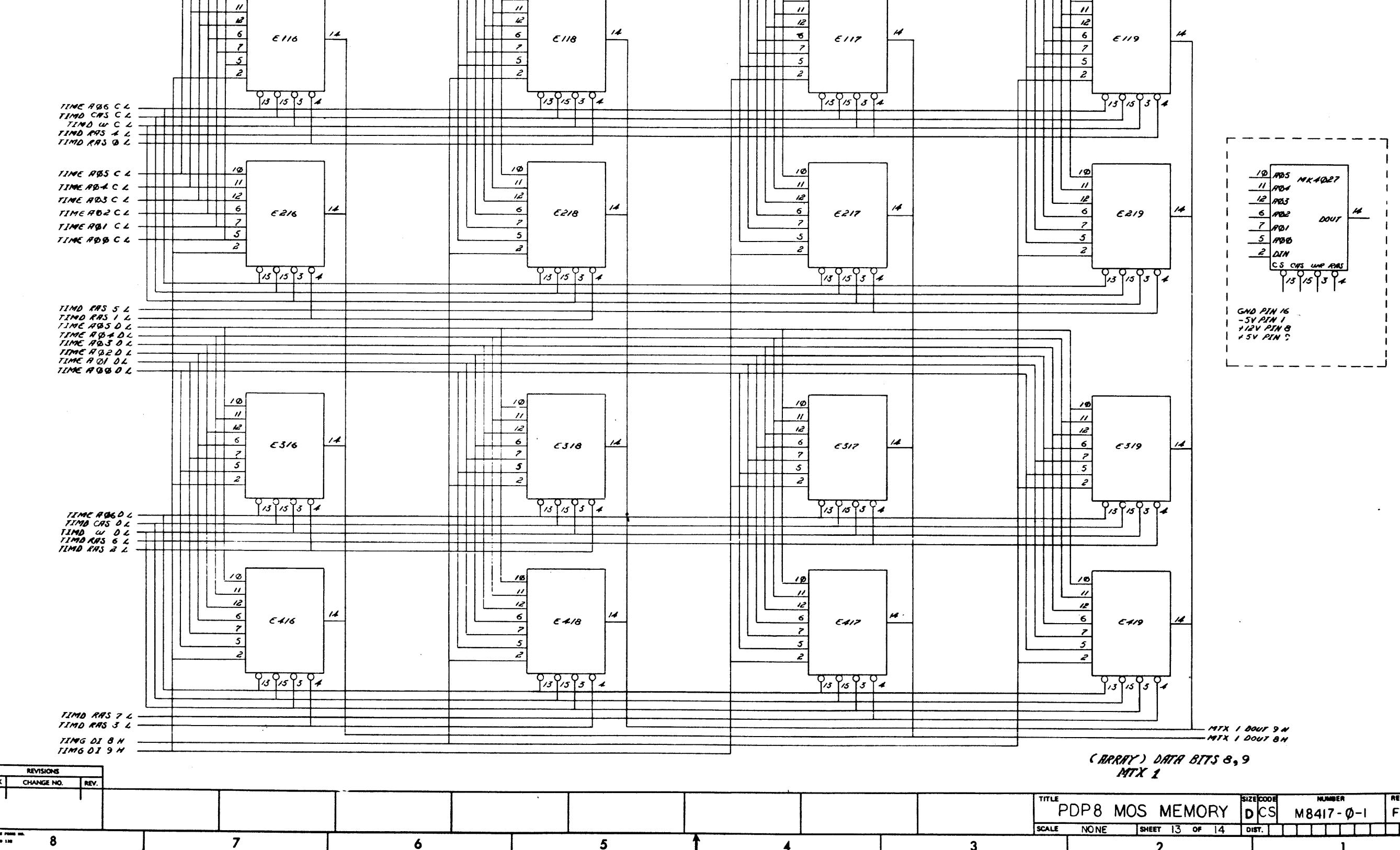
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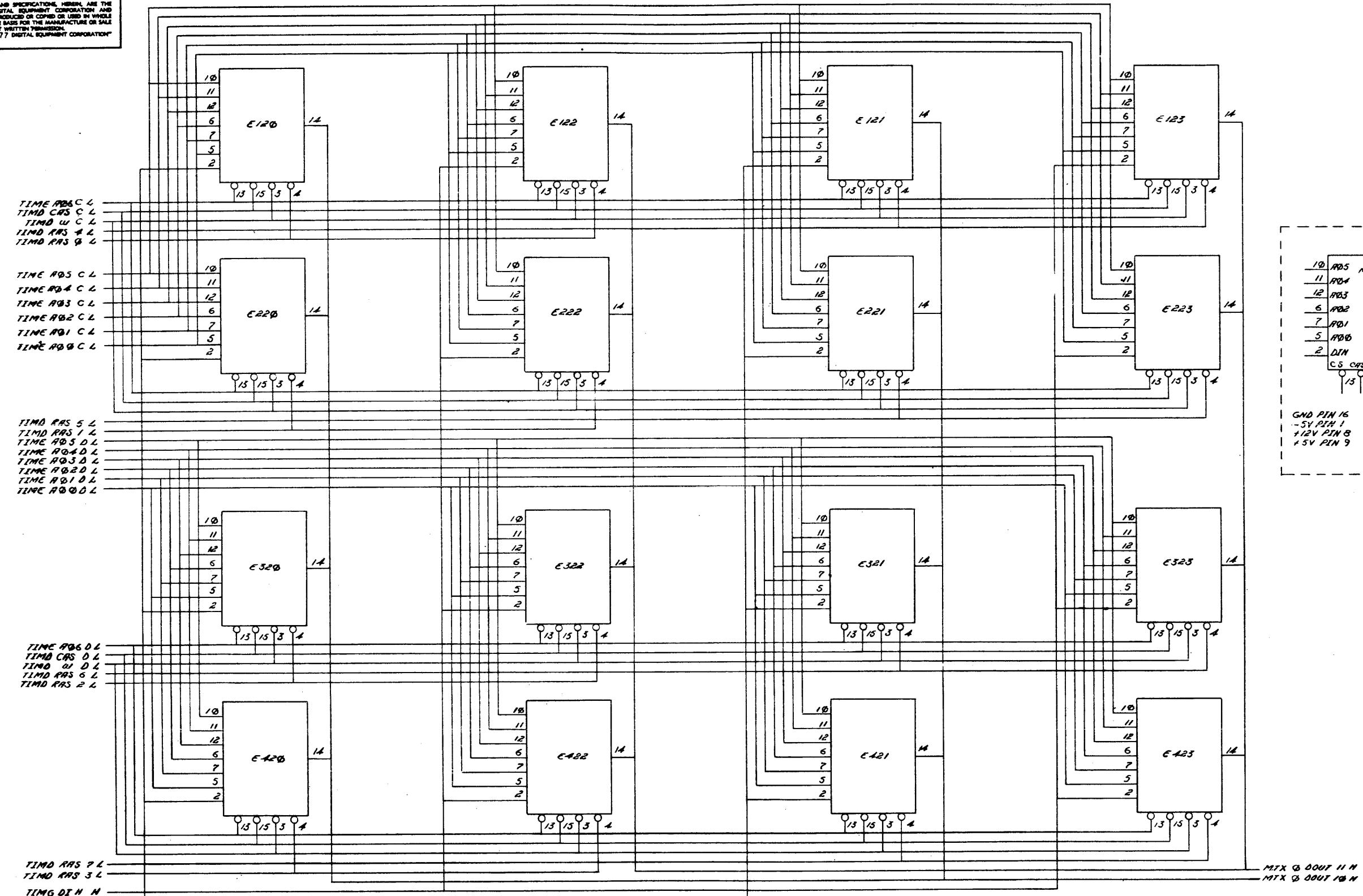
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(ARRAY) DATA BITS '0,,11
MTX 0

REVISIONS		
CHK	CHANGE NO.	REV.
NAME: R1140		

TITLE PDP8 MOS MEMORY

CODE	NUMBER	REV.
CS	M8417-0-1	F
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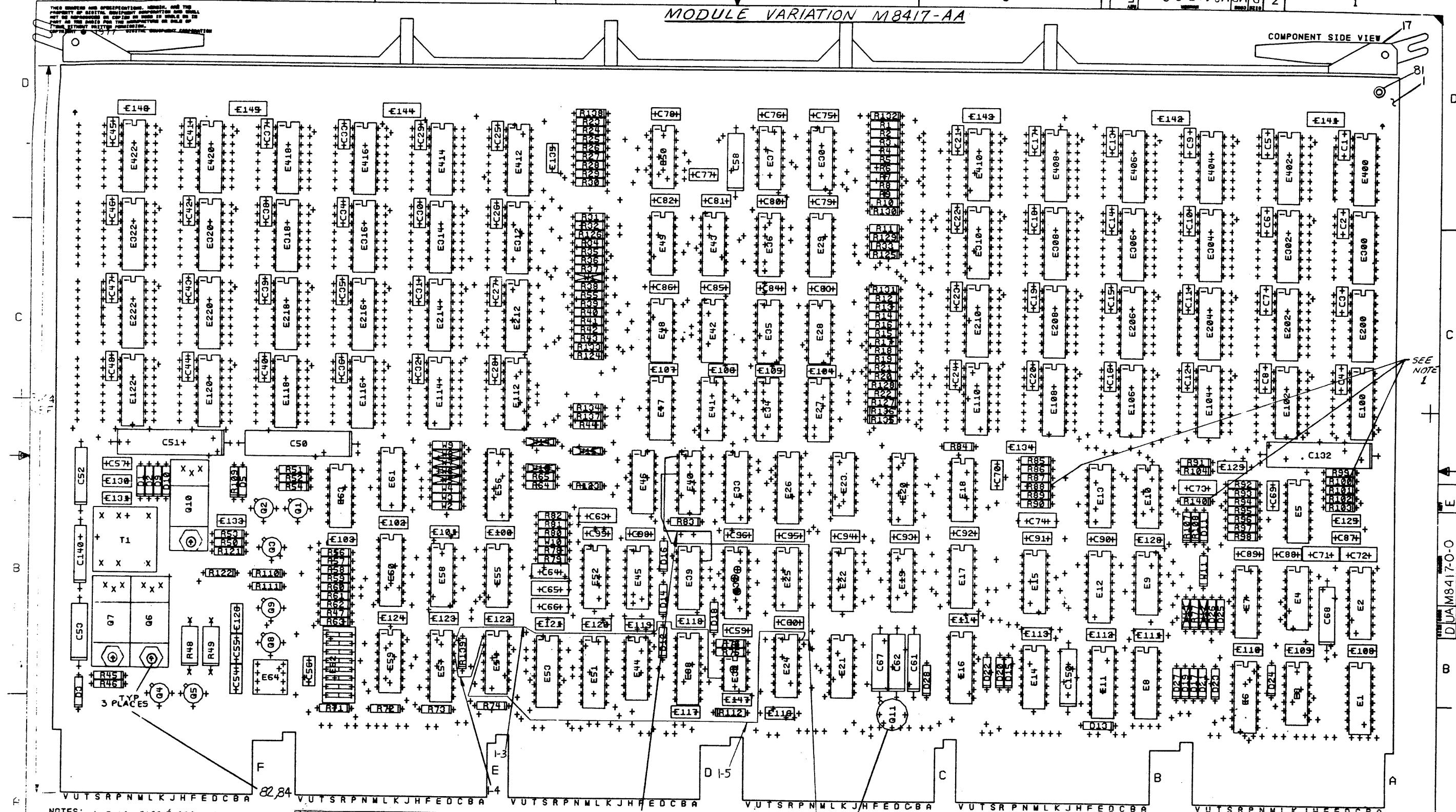
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MODULE VARIATION M8417-A



NOTES: 1. R100, R140 & R88 WILL NOT BE
INSTALLED AT ASSEMBLY, BUT AT
MEMORY TEST, IF NEEDED.
2. M8417-AB = M8417-AB, AC ETC.
(16KX12BIT MEMORY).

CHG	CHANGE NO	REV
G74	M8417-00001	B
G75	M8417-00002	C
G76	M8417-00003	D
G77	M8417-00004	E

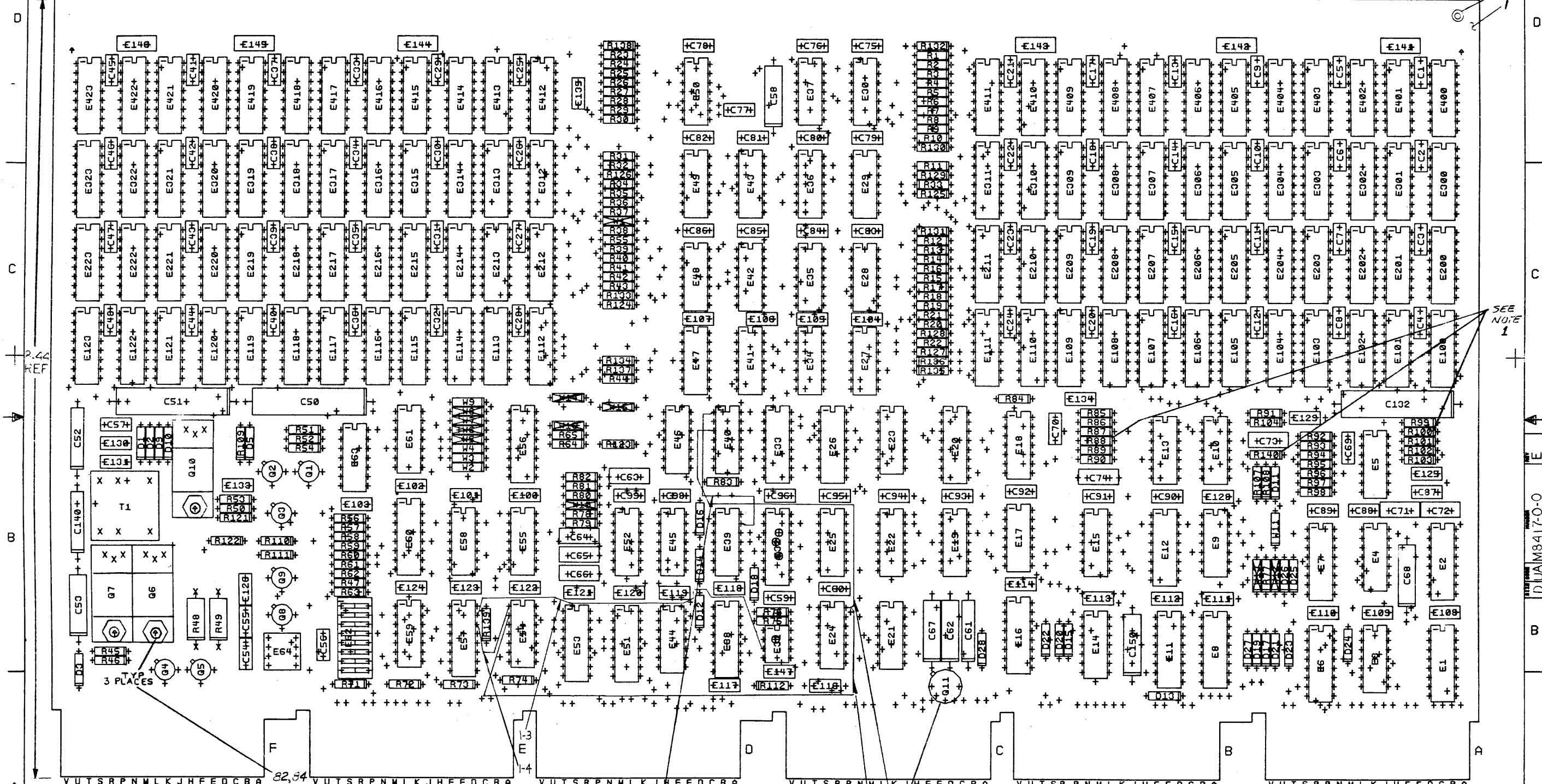
FOR PARTS LIST,
SEE B-PL-M8417-Ø-

		SIGNATURES	DATE	digital		
		DRN. <i>B. Sullian</i>	5-19-71			
		CHK'D. <i>J. J. Sorenson</i>	5-19-71			
		ENG. <i>J. Stoy</i>	7-8-71	TITLE	PDP 8	.
		PROJ. ENG. <i>J. Stoy</i>	7-8-71			MOS MEMORY
		PROD. <i>C. Palmer</i>	7-8-71			
SCALE 211		SHEET 1 OF 8	SIZE CODE	NUMBER		REV
			D	UA	M8417-0-0	E
NEXT HIGHER ASSY. B-DD-M8417-0						

MODULE VARIATION M8417-BA

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COMPONENT SIDE VIEW



NOTES: 1. R100, R140 & R88 WILL NOT BE
INSTALLED AT ASSEMBLY, BUT AT
MEMORY TEST, IF NEEDED.
2. M8417-BA = M8417-5B, PC E
(32KX12 BIT MEMORY).

REV	
CHG	

3-2
FOR PARTS LIST
SEE B-PL-M8417-0-0

H6	
83	
ETCH REV. B	
P.C. DESIGN DATA BASE REV. B1	

SIGNATURES	DATE	digital			
DRN. <i>Dr. Suydam</i>	5-19-71				
CHK'D. <i>James Becker</i>	5-19-71				
ENG.	7-8-71				
PROJ. ENG. <i>J. A. Z.</i>	2-2-71				
PROD. <i>James</i>	7-8-71				
SCALE 2:1		SIZE	CODE	NUMBER	REV
SHT. 2 OF 8		D	UA	M8417-0-0	E
NEXT HIGHER RISY. B-D M-3417-Q					

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L1

LAYER 1
M8417
5012701B

• CS*ABCDEFGHIJKLMNPRS
E423 E422E421 E420 E419 E418 E417 E416 E415 E414 E413, E412

E60

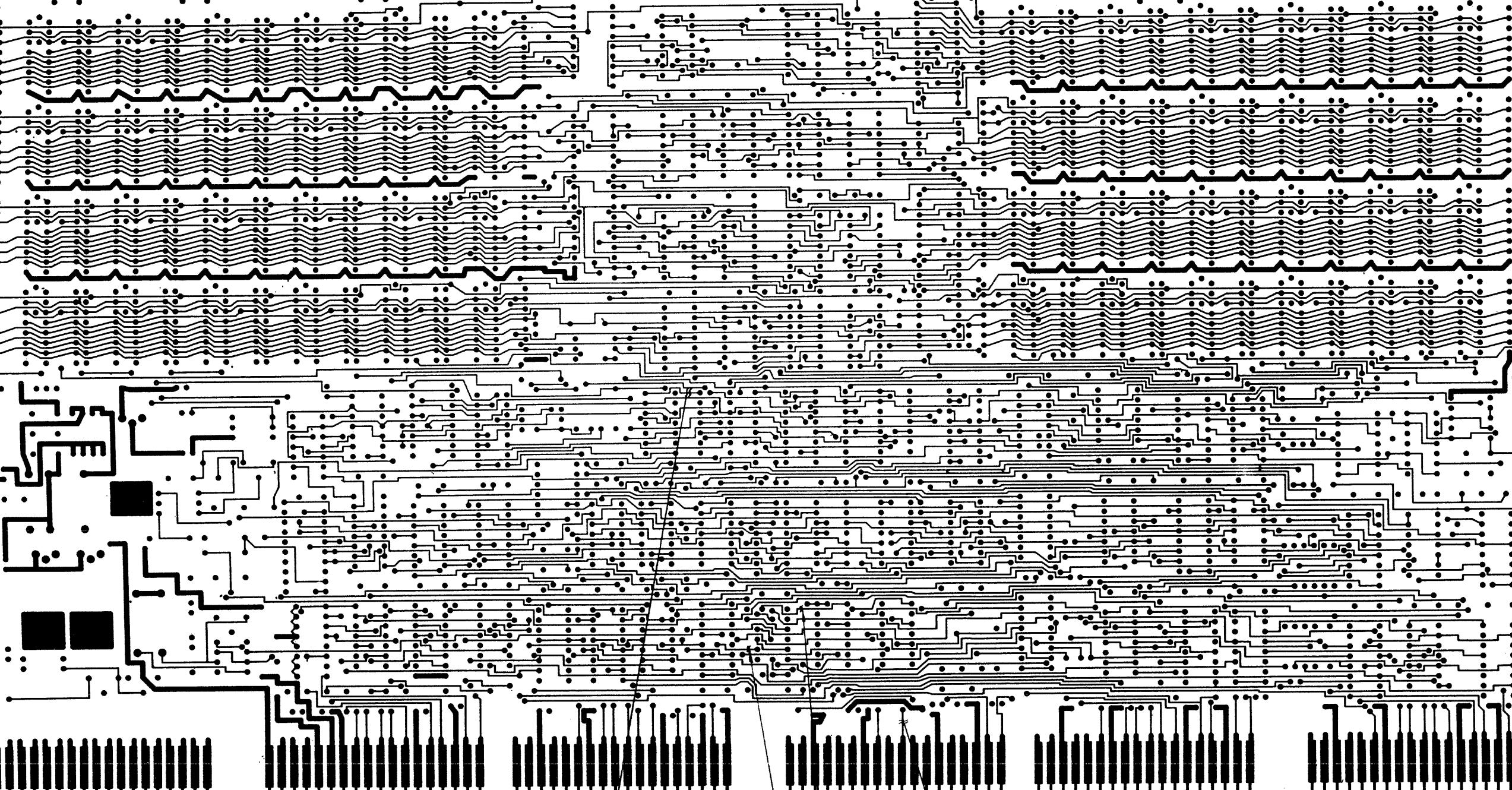
E37

E30

SIDE 1

digital

E411 E410 E409 E408 E407 E406 E405 E404 E403 E402 E401 E400



REVISIONS		
CHK	CHANGE NO.	REV.
8		

3-H 1-H 1-Z 2-I

TITLE PDP8
MOS MEMORY
SCALE 2/1 SHEET 3 OF 8 DIST. 1
SIZE CODE D UA NUMBER M8417-0-0 REV. E

8

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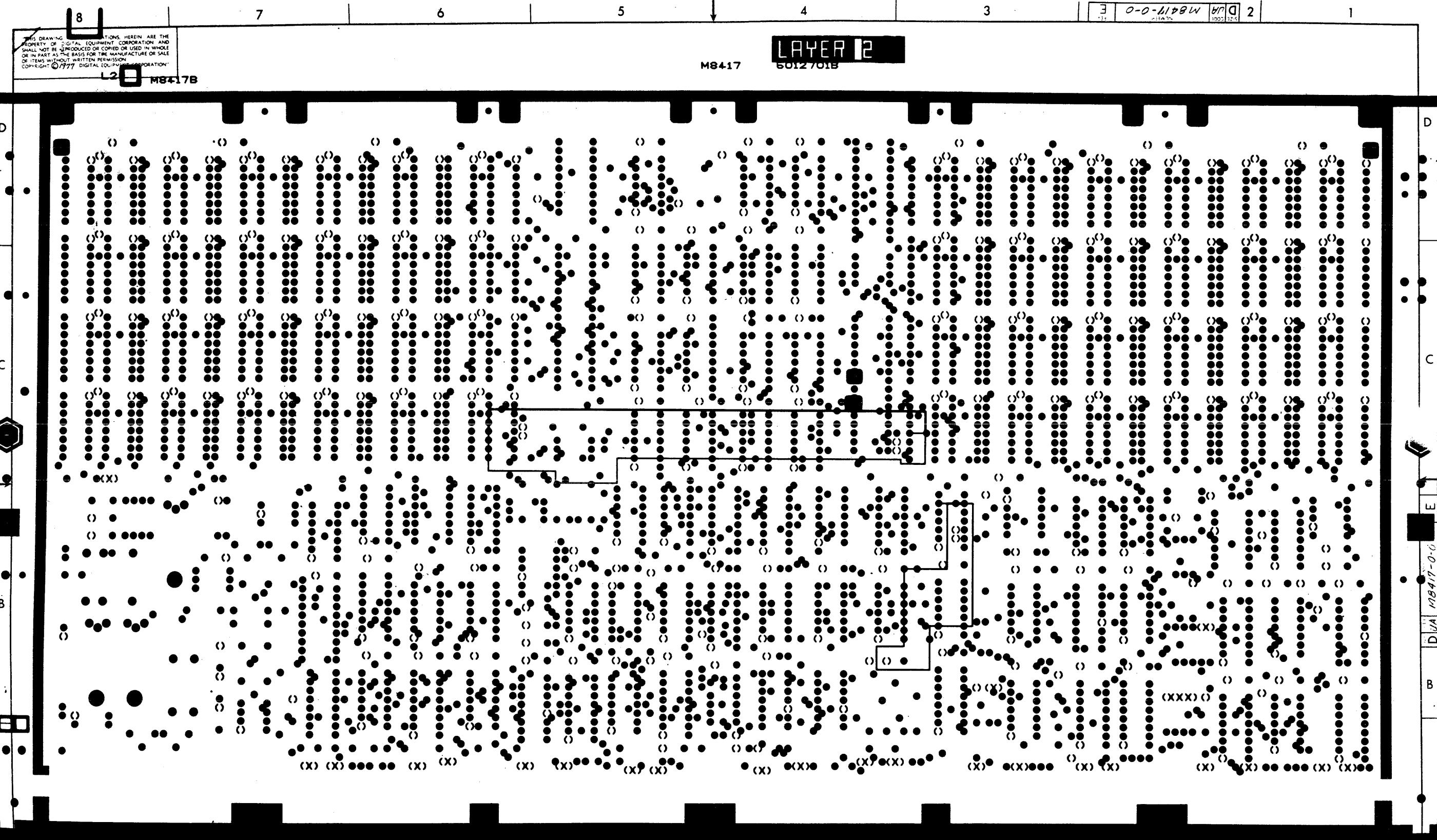
5

4

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1



REVISIONS			TITLE							SIZE CODE		NUMBER		REV.
CHK	CHANGE NO	REV	PDP-8 MOS MEMORY							D	UA	M8417-0-0		E
8			SCALE	2/1	SHEET	4	OF	8	DIST.			1		

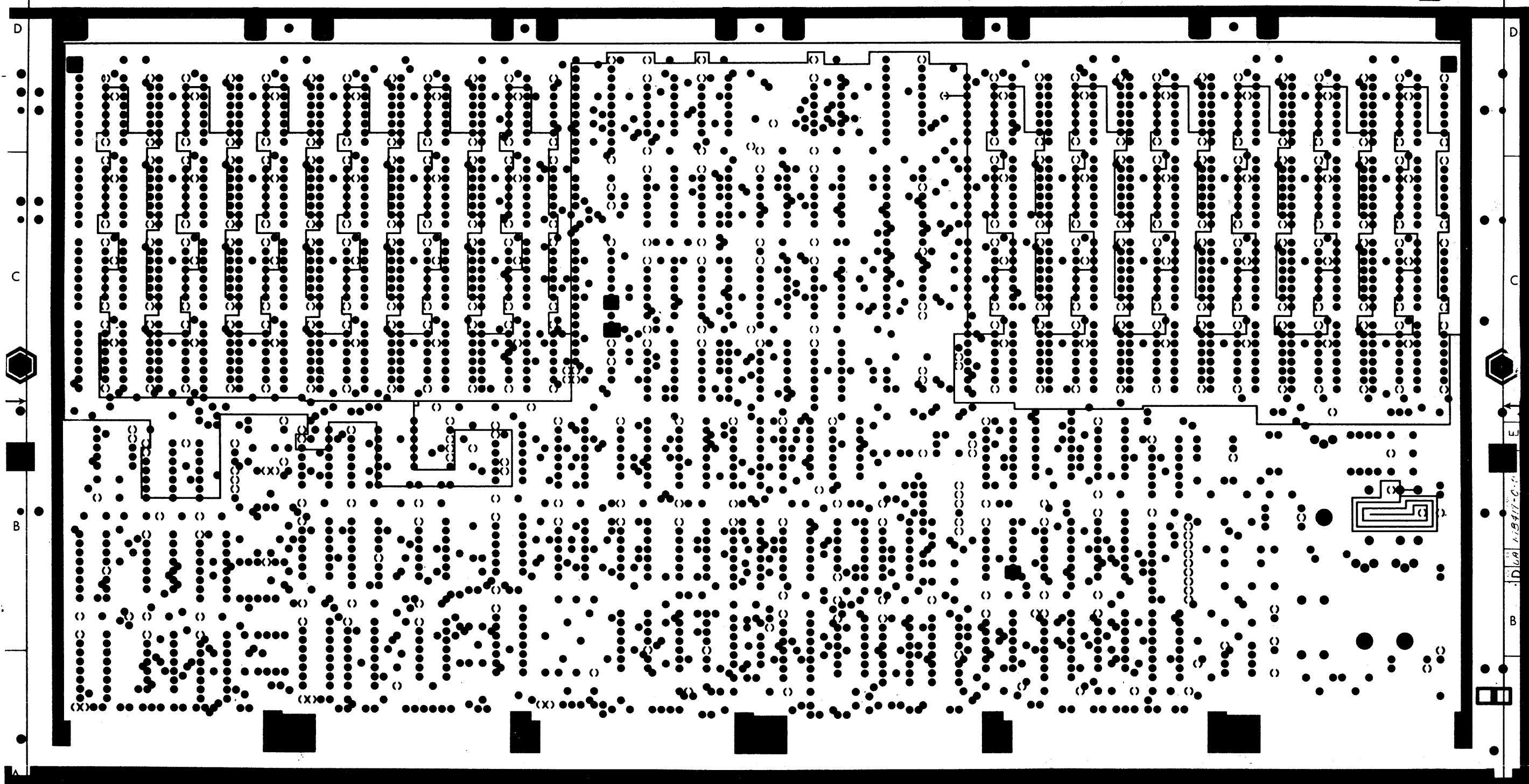
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8 7 6 5 4 3 2 1

E A R E Y A L

M8417B

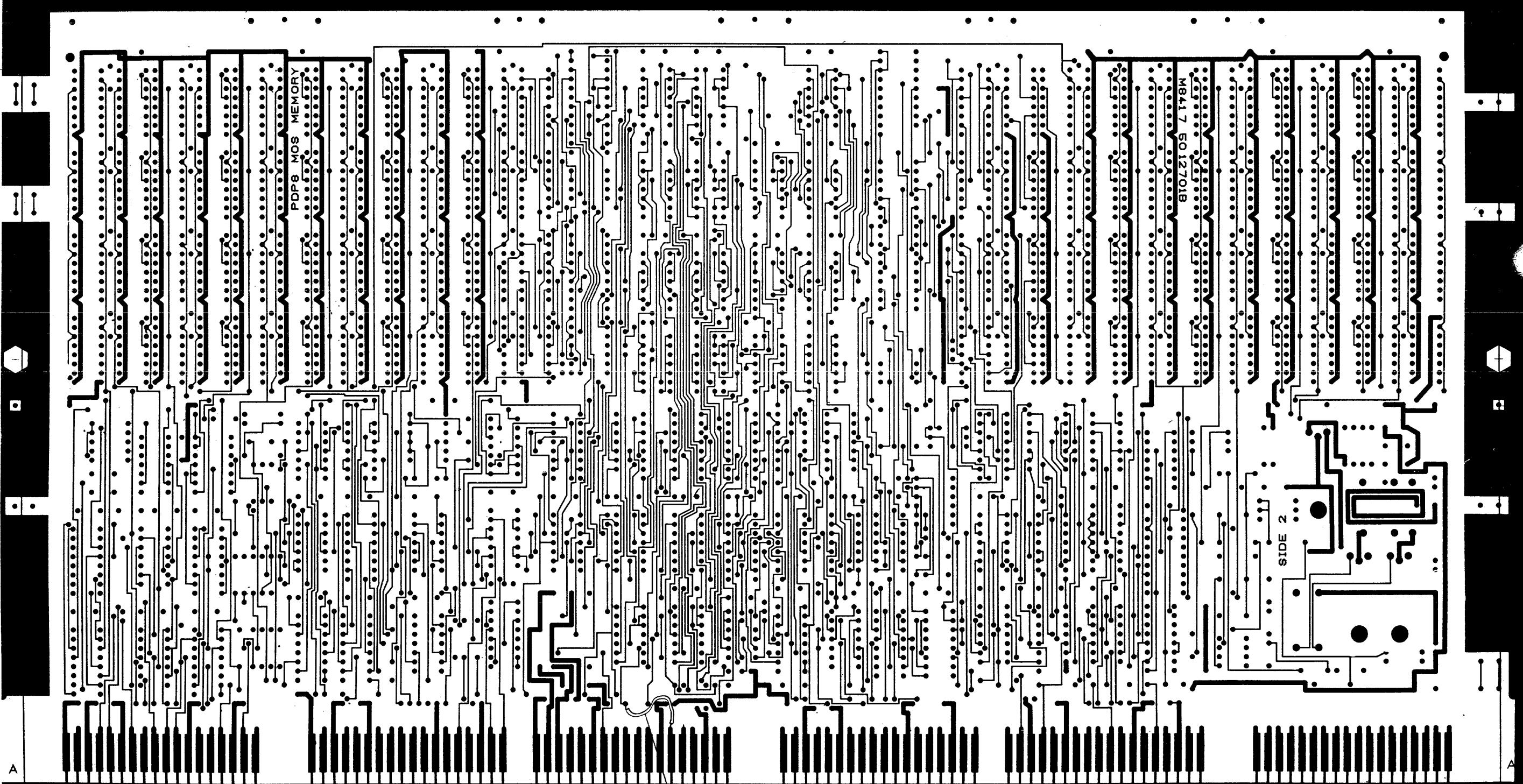
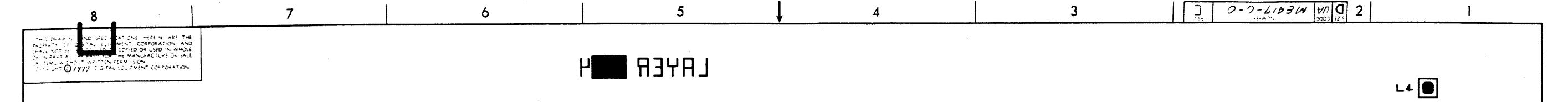
L3



REVISIONS		
CHK	CHANGE NO.	REV

TITLE PDP8
MOS MEMORY
SCALE 2/1 SHEET 5 OF 8 DIST. _____
SIZE CODE D U A M8417-0-0 REV. E

8 7 6 5 4 3 2 1



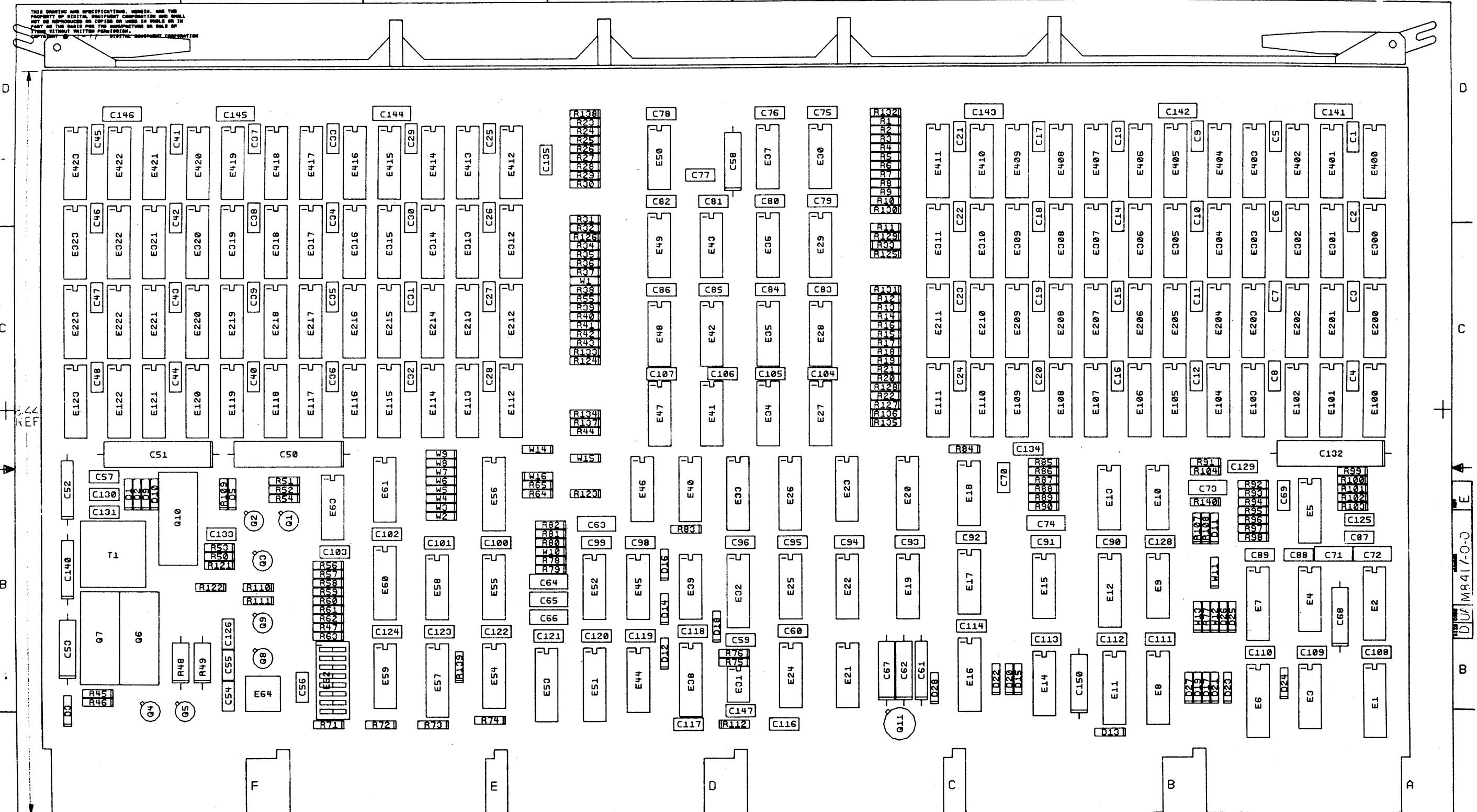
2-2

REVISIONS			TITLE							SIZE CODE		NUMBER		REV.
CHK	CHANGE NO	REV	PDP8 MOS MEMORY							D	UA	M8417-0-0		E
			SCALE	211	SHEET	6	OF	8						
			mL						DIST.					

8 7 6 5 ↓ 4 3 2 1

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DITITAL EQUIPMENT CORPORATION

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NOTES

CHG/CHANGE NO/REV	
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DWG - REF ONLY

SIGNATURES	DATE	digital	
DRN. <i>J. Cullinan</i>	5-18-77		
CHK'D. <i>A. Berger</i>	5-19-77		
ENG. <i>John</i>	7/8/77	TITLE	POP 8
PROJ. ENG. <i>John</i>	7/8/77		MOS MEMORY
PROD. <i>C. Lewis</i>	7-8-77		
SCALE 2:1	SIZE CODE	NUMBER	REV
SHT. 7 OF 8	D UA	M8417-0-0	E
NEXT HIGHER ASSY. 6-11-184	7-0		

8

2

1 MS# 104716

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REWORK INSTRUCTIONS

ECO #1

ETCH CUTS SIDE 1:

1-1 CUT ETCH TO FREE E31-3

1-2 CUT ETCH TO FREE E31-3

WIRE ADDS SIDE 1:

1-3 WIRE E31-3 TO E57-15

1-4 WIRE E57-14 TO E54-1

1-5 WIRE E54-2 TO E24-14

1-6 WIRE E24-14 TO E40-3

ECO #2

ETCH CUTS SIDE 1:

2-1 CUT ETCH BETWEEN PIN CK1 & FEED THRU.

WIRE ADDS SIDE 2:

2-2 WIRE PIN CP2 TO FEEDTHRU ABOVE

CK2 PER PROCEDURE IN MODULE REWORK

SPECIFICATION A-SP-7665265-0-0

ECO #3

ETCH CUTS SIDE 1:

3-1 CUT ETCH TO FREE E40-1

WIRE ADDS SIDE 1:

3-2 FROM E40-1 TO E39-13.

D

D

C

C

→

→

B

B

A

A

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	PDP 8 MOS MEMORY	SIZE CODE	NUMBER	REV.
SCALE	8 OF 8	SHEET	DUA	M8417-0-0
				E

8

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REV.

DUA M8417-0-0

AUTOMATED BY PRTLST.2D(16)

PARTS LIST

SHEET A1 OF A4

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION				REFERENCE DESIGNATOR
				AB	AD	AC	AE	
1	1	D-UA-M8417-0-0	UNIT ASSY	REF	REF	REF	REF	
2	2	D-UA-M8417-0-0	ETCH BOARD (M8418)	1	1	1	1	C66
3	3	5012701-00	UNIT ASSY					
4	4	1000006-00	10.0 MMF 100V 5%200PPM MICA	1	1	1	1	C64
5	5	1000016-00	100.0 MMF 100V 5%200PPM MICA	1	1	1	1	C65
6	6	1000042-00	1000.0 MMF 100V 5%200PPM MICA	1	1	1	1	C60,C75-C96,C98-C114,C116-C125, CONT C128,C129,C147
7	7	1001610-01	.01 MF050/100V +80-20% DISC	53	53	53	53	C53,C58,C67,C61,C62,C68,C140, CONT C150
8	8	1005306-00	6.8MF D 35V 10% S.TANT	8	8	8	8	C1-C48,C57,C69,C70,C133-C135, CONT C141-C146
9	9	1010274-00	.22 MFD 50V +80-20% ZSU CER	60	60	60	60	C54-C56,C126,C130,C131
10	10	1010279-00	.47 MFD 25V 20% CER	6	6	6	6	C59,C63,C71-C74
11	11	1012084-01	8 MFD 25V +75-10% AL EL	1	1	1	1	C52
12	12	1012121-00	220.0 MMF 100V 1%200PPM MICA	6	6	6	6	C50,C51,C132
13	13	1012219-00	47 MFD 30V +75-10% AL EL	3	3	3	3	D3
14	14	1101938-00	IN 4370A VZ= 2.4 5% .40W	1	1	1	1	D5
15	15	1104860-00	IN 746A VZ= 3.3 5%					
16	16	1105275-00	D 672 TR= 15NS PIV= 60V SI	21	21	21	21	D1,D2,D9-D27
17	17	1109943-00	IN 4733A VZ= 5.1 5% 1W Y.	1	1	1	1	D28
18	18	1211164-04	SW DIP 1P 1A 8POS	1	1	1	1	E62
19	19	1210711-02	/REPLACED BY 12-16988-02	1	1	1	1	
20	20	1300250-00	150.0 .25 W 5.0 % CC	1	1	1	1	R47
21	21	1300271-00	220.0 .25 W 5.0 % CC	16	16	16	16	R123-R138
22	22	1300316-00	470.0 .25 W 5.0 % CC	2	2	2	2	R46,R51
23	23	1300365-00	1.0 K .25 W 5.0 % CC	9	9	9	9	R45,R50,R53,R54,R82-R84,R103, CONT R104
24	24	1300447-00	4.70 K .25 W 5.0 % CC	15	15	15	15	R55-R65,R77,R78,R81,R139
25	25	1300479-00	10.0 K .25 W 5.0 % CC	6	6	6	6	R71-R74,R121,R122
26	26	1302124-00	18.0 .25 W 5.0 % CC	46	46	46	46	R1-R44,R90,R98

REVISION HISTORY		BASIC PART NO: M8417	DRN:	L. METZGER	DATE:	30-MAY-78	D	I	G	I	T	A	L
ENG	ECO NUMBER	REV	SECTION A OF C										
E.R	CJ003	D	SECTION VARIATION INDEX	CHK'D:	P.BOSMAN	DATE:	30-MAY-78						
J.S	M8417-ML004	E	[A] AB,AD,AC,AE										
JS	M8417-ML005	F	[B] BB,BC,BD,BE										
JS	M8417-ML006	H	[C] AF,BF	DES.ENG:	J.STEGEMAN	DATE:	30-MAY-78						
			[D]										
			[E]										
			[F]										
			[G]										
			[H]										
			[I]										
			[K]										
			[L]										
			[M]										
			[N]										
ASSEMBLY NUMBER: D-UA-M8417-0-0													
TOP DOCUMENT NUMBER: MS8-C													
FILE NAME: 20189H.PLS													
DOCUMENT NUMBER: H													
SIZE: REV: 6													

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PARTS LIST

SHEET A2 OF A4

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION	AB	AD	AC	AE	REFERENCE DESIGNATOR
-----------	-----------------	-------------	-------------	-------------------	----	----	----	----	----------------------

27	27		1302377-00	39.0	.25	W	5.0	%	CC	5	5	5	5	R52,R109-R112
28	28		1302859-00	5.76 K	.25	W	1.0	%	RN55D-F10	1	1	1	1	R96
29	29		1302871-00	1.21 K	.25	W	1.0	%	RN55D-F10	2	2	2	2	R80,R108
30	30		1302872-00	681.0	.25	W	1.0	%	RN55D-F1	1	1	1	1	R89
31	31		1302956-00	196.0	.25	W	1.0	%	RN55D-F10	1	1	1	1	R94
32	32		1302957-00	121.0	.25	W	1.0	%	RN55D-F10	1	1	1	1	R101
33	33		1303045-00	3.16 K	.25	W	1.0	%	RN55D-F10	1	1	1	1	R76
34	34		1303067-00	422.0	.25	W	1.0	%	RN55D-F10	1	1	1	1	R99
35	35		1303110-00	19.60	.25	W	1.0	%	RN55D-F10	1	1	1	1	R93
36	36		1303226-00	68.10	.25	W	1.0	%	RN55D-F10	1	1	1	1	R86
37	37		1303311-00	46.40 K	.25	W	1.0	%	RN55D-F10	1	1	1	1	R75
38	38		1303313-00	12.10 K	.25	W	1.0	%	RN55D-F10	1	1	1	1	R97
39	39	SEE NOTE 90	1304725-00	300.0	.25	W	1.0	%	RN55D-F10	1	1	1	1	R107
40	40	SEE NOTE 90	1304833-00	1.96 K	.25	W	1.0	%	RN55D-F10	1	1	1	1	R92
41	41		1305122-00	51.10	.25	W	1.0	%	RN55D-F10	1	1	1	1	R85
42	42		1305123-00	215.0	.25	W	1.0	%	RN55D-F10	2	2	2	2	R91,R102
43	43		1305253-00	825.0	.25	W	1.0	%	RN55D-F10	2	2	2	2	R95,R79
44	44	SEE NOTE 90	1309405-00	7.15 K	.25	W	1.0	%	RN55D-F10	1	1	1	1	R87
45	45		1503100-00	68.0	.50	W	5.0	%	CC	2	2	2	2	R48,R49
46	46		DEC3009B	NPN 200MW SI	20	25				7	7	7	7	Q1-Q5,Q8,Q9
47	47		1510171-00	D 44C3	NPN 30WT SI	30	20	Y		2	2	2	2	Q6,Q7
48	48		1614234-00	XFMR, CONVERTER, RATIO 1:3	300UH					1	1	1	1	T1
49	49		1909054-00	7493	COUNTER, ASYNCH UP, BI					2	2	2	2	E21,E24
50	50		1909705-00	DEC 8881	NAND GATE-QUAD 2IN	0				4	4	4	4	E3,E14,E17,E44
51	51		1910268-01	DEC 75107B-01	RECEIVER, LINE, DUAL					3	3	3	3	E5,E10,E18
52	52		1910406-00	75451	DRIVER, PERIPH, DUAL,					6	6	6	6	E64
53	53		1910532-00	74500	NAND GATE-QUAD 2IN	0				1	1	1	1	E4,E34,E37,E42,E43,E55
54	54		1910533-00	74503	NAND GATE-QUAD 2IN	0				1	1	1	1	E13
55	55		1910534-00	74504	INVERTER GATE-HEX	1I				2	2	2	2	E22,E54
56	56		1910536-00	74510	NAND GATE-TRIPLE	3IN				3	3	3	3	E19,E61,E63
57	57		1910539-00	74520	NAND GATE-DUAL	4INPU				1	1	1	1	E59
58	58		1910542-00	74564	A-O-I GATE	4-2-3-2				1	1	1	1	E15
59	59		1910544-01	74574-60GG-D	DUAL, EDGE TRIG					1	1	1	1	E39
60	60		1910545-00	745112	FF-JK DUAL, EDGE TRIG					3	3	3	3	E7,E20,E32
61	61		1910549-00	745158	MUX 1 OF 2 (QUAD)					2	2	2	2	E26,E33
62	62		1911116-00	DEC 8837	RECEIVER, BUS, HEX, UN					5	5	5	5	E6,E8,E16,E38,E57
63	63		1911469-00	DEC 8640	RECEIVER, BUS, QUAD, U					1	1	1	1	E45
64	64		1911579-00	8641	TRANSCEIVER, BUS, QUAD					3	3	3	3	E1,E11,E51
65	65		1911676-00	745139	DECODER-DUAL TWO-INP					1	1	1	1	E56
66	66		1911712-00	74551	AND-OR GATE-INVERT	D				1	1	1	1	E58
67	67		1911944-00	555CN	TIMER, FUNCT. BLOCK					1	1	1	1	E31
68	68		1911983-00	745133	NAND GATE-POSITIVE	1				1	1	1	1	E60
69	69	SEE NOTE 93	1912048-06	DEC 7812	VOLT REG, FIX	+12V				1	1	1	1	Q10
70	70		1912388-00	74502	NOR GATE-QUAD	2IN, PO				3	3	3	3	E9,E25,E46
71	71		1912389-00	74508	AND GATE-QUAD	2IN, PO				1	1	1	1	E23
72	72		1912541-00	79M05	VOLT REG, FIX	-5V				1	1	1	1	Q11
73	73		1912649-00	LS75	LATCH 4BIT, BISTABLE					3	3	3	3	E2,E12,E53
74	74		1912746-00	DEC 74S37	NAND GATE-QUAD	2IN				11	11	11	11	E27-E30,E35,E36,E41,E47-E50

D	I	G	I	T	A	L	TITLE	SECTION A OF C	SIZE	CODE	DOCUMENT NUMBER	REV	
							PDP8 MOS MEMORY			K	PL	M8417-0-DBP	H

AUTOMATED BY PRTLST.2D(16)

PARTS LIST

SHEET A3 OF A4

LINE ITEM DOCUMENT NUMBER PART NUMBER DESCRIPTION QTY PER VARIATION AB AD AC AE REFERENCE DESIGNATOR

75	75		1912803-00	74LS04 INVERTER GATE, HEX	1	1	1	1	E52
76	76		1912824-00	L574 FF-D DUAL EDGE TRIGG	1	1	1	1	E40
77	77		2113735-01	4K MOS RAM 200NS 1	48	-	-	-	E100, E102, E104, E106, E108, E110, CONT E112, E114, E116, E118, E120, E122, CONT E200, E202, E204, E206, E208, E210, CONT E212, E214, E216, E218, E220, E222, CONT E300, E302, E304, E306, E308, E310, CONT E312, E314, E316, E318, E320, E322, CONT E400, E402, E404, E406, E408, E410, CONT E412, E414, E416, E418, E420, E422
78	78		2114114-01	4K MOS RAM 200NS 1	-	-	48	-	E100, E102, E104, E106, E108, E110, CONT E112, E114, E116, E118, E120, E122, CONT E200, E202, E204, E206, E208, E210, CONT E212, E214, E216, E218, E220, E222, CONT E300, E302, E304, E306, E308, E310, CONT E312, E314, E316, E318, E320, E322, CONT E400, E402, E404, E406, E408, E410, CONT E412, E414, E416, E418, E420, E422
79	79		2113914-01	4K MOS RAM 200NS 1	-	48	-	-	E100, E102, E104, E106, E108, E110, CONT E112, E114, E116, E118, E200, E120, CONT E202, E204, E206, E208, E210, CONT E212, E214, E216, E218, E220, E222, CONT E300, E302, E304, E306, E308, E310, CONT E312, E314, E316, E318, E320, E322, CONT E400, E402, E404, E406, E408, E410, CONT E412, E414, E416, E418, E420, E422
80	80	SEE NOTE 91	2114475-01	4K MOS RAM 200NS 1	-	-	-	48	E100, E102, E104, E106, E108, E110, CONT E112, E114, E116, E118, E120, E122, CONT E200, E202, E204, E206, E208, E210, CONT E212, E214, E216, E218, E220, E222, CONT E300, E302, E304, E306, E308, E310, CONT E312, E314, E316, E318, E320, E322, CONT E400, E402, E404, E406, E408, E410, CONT E412, E414, E416, E418, E420, E422
81	81		9000024-01	EYELET, ROLL FLANGE .1210DX .192	12	12	12	12	
82	82	USE WITH Q6, Q7, Q10	9006557-00	NUT, KEP 4-40X 1/4 AF	3	3	3	3	
83	83	USE WITH Q11	9007254-00	TRANSIPADS #10146	1	1	1	1	
84	84	USE WITH Q6, Q7, Q10	9008301-01	SCREW, PAN, PHIL 4-40X 1/4 SS	3	3	3	3	
85	85		9009185-00	JUMPER, WIRE, INSULATED, BLACK B	6	6	6	-	W2, W3, W4, W9, W10, W11
86	86		9105740-55	WIRE(WRAP)30AWG	UL1423	A/R	A/R	A/R	W2, W3, W4, W9, W11
87	87		2113789-01	*** THIS ITEM IS NOT USED ***		-	-	-	

88 NOTE: M8417-AA=M8417-AB,AC,AD,AE,AF,(16KX12)

D	I	G	I	T	A	L	TITLE	SECTION A OF C	SIZE!CODE!	DOCUMENT NUMBER	REV
							PDP8 MOS MEMORY		K	PL M8417-0-DBP	H

AUTOMATED BY PRTLST.20(16)

P A R T S L I S T

SHEET A4 OF A4

LINE ITEM DOCUMENT NUMBER

PART NUMBER

DESCRIPTION

QTY PER VARIATION

AB AD AC AE REFERENCE DESIGNATOR

89 NOTE: M8417-BA=M8417-BB,BC,BD,BE,BF(32KX12)

90 NOTE: USED ON:OPTION/MODEL MS8-CA,MS8-CB

91 NOTE: R87,R92 MAY BE REMOVED AT MODULE TEST IF NEEDED.

92 NOTE: MIXING OF MOS RAMS IS NOT PERMITTED.

93 NOTE: REF ITEM #69 19-12048-05 ACCEPTABLE SUBSTITUTE FOR 19-12048-06.

94 NOTE:

D I G I T A L TITLE PDP8 MOS MEMORY

SECTION A OF C

SIZE CODE: DOCUMENT NUMBER REV
K PL M8417-0-DBP H

AUTOMATED BY PRTLST.2D(16)

PARTS LIST

SHEET B1 OF B3

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION				REFERENCE DESIGNATOR
				BB	BC	BD	BE	

1	1	D-UA-M8417-0-0	UNIT ASSY	REF	REF	REF	REF	
2	2	D-UA-M8417-0-0	*** THIS ITEM IS NOT USED ***	-	-	-	-	
3	3	5012701-00	ETCH BOARD (M8418)	1	1	1	1	C66
4	4	1000006-00	10.0 MMF 100V 5%200PPM MICA	1	1	1	1	C64
5	5	1000016-00	100.0 MMF 100V 5%200PPM MICA	1	1	1	1	C65
6	6	1000042-00	1000.0 MMF 100V 5%200PPM MICA	1	1	1	1	C60,C75-C96,C98-C114,C116-C125,
7	7	1001610-01	.01 MFD50/100V +80-20% DISC	53	53	53	53	CONT C128,C129,C147
8	8	1005306-00	6.8MFD 35V 10% S.TANT	8	8	8	8	C53,C58,C67,C61,C62,C68,C140,
9	9	1010274-00	.22 MFD 50V +80-20% ZSU CER	60	60	60	60	CONT C150 C1-C48,C57,C69,C70,C133-C135,
10	10	1010279-00	.47 MFD 25V 20%	CER	6	6	6	C54-C56,C126,C130,C131
11	11	1012084-01	.8 MFD 25V +75-10% AL EL	1	1	1	1	C52
12	12	1012121-00	220.0 MMF 100V 1%200PPM MICA	6	6	6	6	C59,C63,C71-C74
13	13	1012219-00	.47 MFD 30V +75-10% AL EL	3	3	3	3	C50,C51,C132
14	14	1101938-00	1N 4370A VZ= 2.4 5% .1OW	1	1	1	1	D3
15	15	1104860-00	1N 746A VZ= 3.3 5%	1	1	1	1	D5
16	16	1105275-00	D 672 TR= 15NS PIV= 60V SI	21	21	21	21	D1,D2,D9-D27
17	17	1109943-00	1N 4733A VZ= 5.1 5% 1W Y	1	1	1	1	D28
18	18	1211164-04	SW,DIP 1P 1A 8POS	1	1	1	1	E62
19	19	1210711-02	/REPLACED BY 12-16988-02	1	1	1	1	
20	20	1300250-00	150.0 .25 W 5.0 %	CC	1	1	1	R47
21	21	1300271-00	220.0 .25 W 5.0 %	CC	16	16	16	R123-R138
22	22	1300316-00	470.0 .25 W 5.0 %	CC	2	2	2	R46,R51
23	23	1300365-00	1.0 K .25 W 5.0 %	CC	9	9	9	R45,R50,R53,R54,R82-R84,R103,
								CONT R104
24	24	1300447-00	4.70 K .25 W 5.0 %	CC	15	15	15	R55-R65,R77,R78,R81,R139
25	25	1300479-00	10.0 K .25 W 5.0 %	CC	6	6	6	R71-R74,R121,R122
26	26	1302124-00	18.0 .25 W 5.0 %	CC	46	46	46	R1-R44,R90,R98

REVISION HISTORY	BASIC PART NO: M8417	DRN: L. METZGER	DATE: 30-MAY-78	D	I	G	I	T	A	L
ENG! ECO NUMBER	REV SECTION B OF C	CHK'D: P.BOSSEMAN	DATE: 30-MAY-78	TITLE PARTS LIST						
E.R C003	D SECTION VARIATION INDEX	DES.ENG: J. STEGEMAN	DATE: 30-MAY-78	PDP8 MOS MEMORY						
J.S M8417-ML004	E [A] AB,AD,AC,AE	RESP.ENG.: J. STEGEMAN	DATE: 30-MAY-78	DOCUMENT NUMBER						
JS M8417-ML005	F [B] BB,BC,BD,BE	MFG.ENG.: C.TANNER	DATE: 30-MAY-78	K	PL	M8417-0-DBP	H			
JS M8417-ML006	H [C] AF,BF	[L]	[M]	ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER:	FILE NAME:	EDIT #			
	[D]	[E]	[F]	D-UA-M8417-0-0	MS8-C	20189H.PLS	6			
	[G]	[H]	[J]							
	[K]	[L]	[M]							
	[N]									

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AUTOMATED BY PRTLST.2D(16)

PARTS LIST

SHEET B2 OF B3

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION				REFERENCE DESIGNATOR
				BB	BC	BD	BE	
27	27	1302377-00	39.0	.25	W 5.0 %	CC	5	R52,R109-R112
28	28	1302859-00	5.76 K	.25	W 1.0 %	RN55D-F10	1	R96
29	29	1302871-00	1.21 K	.25	W 1.0 %	RN55D-F10	2	R80,R108
30	30	1302872-00	681.0	.25	W 1.0 %	RN55D-F1	1	R89
31	31	1302956-00	196.0	.25	W 1.0 %	RN55D-F10	1	R94
32	32	1302957-00	121.0	.25	W 1.0 %	RN55D-F10	1	R101
33	33	1303045-00	3.16 K	.25	W 1.0 %	RN55D-F10	1	R76
34	34	1303067-00	422.0	.25	W 1.0 %	RN55D-F10	1	R99
35	35	1303110-00	19.60	.25	W 1.0 %	RN55D-F10	1	R93
36	36	1303226-00	68.10	.25	W 1.0 %	RN55D-F10	1	R86
37	37	1303311-00	46.40 K	.25	W 1.0 %	RN55D-F10	1	R75
38	38	1303313-00	12.10 K	.25	W 1.0 %	RN55D-F10	1	R97
39	39	1304725-00	300.0	.25	W 1.0 %	RN55D-F10	1	R107
40	40	SEE NOTE 90	1.96 K	.25	W 1.0 %	RN55D-F10	1	R92
41	41	1305122-00	51.10	.25	W 1.0 %	RN55D-F10	1	R85
42	42	1305123-00	215.0	.25	W 1.0 %	RN55D-F10	2	R91,R102
43	43	1305143-00	825.0	.25	W 1.0 %	RN55D-F10	2	R95,R79
44	44	SEE NOTE 90	7.15 K	.25	W 1.0 %	RN55D-F10	1	R87
45	45	1305253-00	68.0	.50	W 5.0 %	CC	2	R48,R49
46	46	1503100-00	DEC30098	NPN 200MW SI 20 25	7	7	7	Q1-Q5,Q8,Q9
47	47	1510171-00	D 44C3	NPN 30WT SI 30 20 Y	2	2	2	Q6,Q7
48	48	1614234-00	XFMR, CONVERTER, RATIO 1:3	300UH	1	1	1	T1
49	49	1909054-00	7493	COUNTER, ASYNCH UP, BI	2	2	2	E21,E24
50	50	1909705-00	DEC 8881	NAND GATE-QUAD 2IN 0	4	4	4	E3,E14,E17,E44
51	51	1910268-01	DEC 75107B-01	RECEIVER, LINE, DUA	3	3	3	E5,E10,E18
52	52	1910406-00	75451	DRIVER, PERIPH, DUAL,	1	1	1	E64
53	53	1910532-00	74S00	NAND GATE-QUAD 2IN	6	6	6	E4,E34,E37,E42,E43,E55
54	54	1910533-00	74S03	NAND GATE-QUAD 2IN, 0	1	1	1	E13
55	55	1910534-00	74S04	INVERTER GATE-HEX II	2	2	2	E22,E54
56	56	1910536-00	74S10	NAND GATE-TRIPLE 3IN	3	3	3	E19,E51,E63
57	57	1910539-00	74S20	NAND GATE-DUAL 4INPU	1	1	1	E59
58	58	1910542-00	74S64	A-O-I GATE 4-2-3-2	1	1	1	E15
59	59	1910544-01	74S74-60GG-D	DUAL, EDGE TRIG	1	1	1	E39
60	60	1910545-00	74S112	FF-JK DUAL, EDGE TRIG	3	3	3	E7,E20,E32
61	61	1910549-00	74S158	MUX 1 OF 2 (QUAD)	2	2	2	E26,E33
62	62	1911116-00	DEC 8837	RECEIVER, BUS, HEX, UN	5	5	5	E6,E8,E16,E38,E57
63	63	1911469-00	DEC 8640	RECEIVER, BUS, QUAD, U	1	1	1	E45
64	64	1911579-00	8641	TRANSCEIVER, BUS, QUA	3	3	3	E1,E11,E51
65	65	1911676-00	74S139	DECODER-DUAL TWO-INP	1	1	1	E56
66	66	1911712-00	74S551	AND-OR GATE-INVERT D	1	1	1	E58
67	67	1911944-00	555CN	TIMER, FUNCT. BLOCK	1	1	1	E31
68	68	1911983-00	74S133	NAND GATE-POSITIVE 1	1	1	1	E60
69	69	SEE NOTE 93	DEC 7812	VOLT REG, FIX +12V	1	1	1	Q10
70	70	1912048-06	74S02	NOR GATE-QUAD 2IN, PO	3	3	3	E9,E25,E46
71	71	1912388-00	74S08	AND GATE-QUAD 2IN, PO	1	1	1	E23
72	72	1912541-00	79M05	VOLT REG, FIX -5V	1	1	1	Q11
73	73	1912649-00	LS75	LATCH 4BIT, BISTABLE	3	3	3	E2,E12,E53
74	74	1912746-00	DEC 74S37	NAND GATE-QUAD 2IN	11	11	11	E27-E30,E35,E36,E41,E47-E50

D	I	G	I	T	A	L	TITLE	PDP8 MOS MEMORY	SIZE	CODE	DOCUMENT NUMBER	REV
							SECTION B OF C		K	PL	M8417-0-DBP	H

AUTOMATED BY PRTLST.2D(16)

PARTS LIST

SHEET B3 OF B3

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION				REFERENCE DESIGNATOR
				BB	BC	BD	BE	
75	75	1912803-00	74LS04 INVERTER GATE,HEX	1	1	1	1	E52
76	76	1912824-00	LS74 FF-D DUAL,EDGE TRIGG	1	1	1	1	E40
77	77	2113735-01	4K MOS RAM 200NS 1	96	-	-	-	E100-E123,E200-E223,E300-E323, CONT E400-E423
78	78	2114114-01	4K MOS RAM 200NS 1	-	96	-	-	E100-E123,E200-E223,E300-E323, CONT E400-E423
79	79	2113914-01	4K MOS RAM 200NS 1	-	-	96	-	E100-E123,E200-E223,E300-E323, CONT E400-E423
80	80	SEE NOTE 91	2114475-01	4K MOS RAM 200NS 1	-	-	96	E100-E123,E200-E223,E300-E323, CONT E400-E423
81	81	9000024-01	EYELET,ROLL FLANGE .1210DX .192	12	12	12	12	
82	82	USE WITH Q6,Q7,Q10	9006557-00 NUT,KEP 4-40X 1/4 AF	3	3	3	3	
83	83	USE WITH Q11	9007254-00 TRANSIPADS #10146	1	1	1	1	
84	84	USE WITH Q6,Q7,Q10	9008301-01 SCREW,PAN,PHIL 4-40X 1/4 SS	3	3	3	3	
85	85	9009185-00	JUMPER WIRE, INSULATED, BLACK B	5	5	5	5	
86	86	9105740-55	WIRE(WRAP)30AWG UL1423 A/R A/R A/R A/R					W2,W3,W4,W9,W11
87	87	2113789-01	*** THIS ITEM IS NOT USED ***	-	-	-	-	

88 NOTE: M8417-AA=M8417-AB,AC,AD,AE,AF (16KX12)

89 NOTE: M8417-BA=M8417-BB,BC,BD,BE,BF (32KX12)

90 NOTE: USED ON:OPTION/MODEL MS8-CA,MS8-CB

91 NOTE: R87,R92 MAY BE REMOVED AT MODULE TEST IF NEEDED.

92 NOTE: MIXING OF MOS RAMS IS NOT PERMITTED.

93 NOTE: REF ITEM #69 19-12048-05 ACCEPTABLE SUBSTITUTE FOR 19-12048-06.

94 NOTE: -----

D	I	G	I	T	A	L	TITLE	PDP8 MOS MEMORY	SECTION B OF C	SIZE	CODE	DOCUMENT NUMBER	REV
										K	PL	M8417-0-DBP	H

AUTOMATED BY PRTLST.2D(16)

PARTS LIST

SHEET C1 OF C3

LINE ITEM DOCUMENT NUMBER

PART NUMBER

DESCRIPTION

QTY PER VARIATION
AF BF

REFERENCE DESIGNATOR

1	1	D-UA-M8417-0-0		UNIT ASSY	REF	REF			
2	2	D-UA-M8417-0-0	5012701-00	UNIT ASSY	REF	REF			
3	3		1000006-00	ETCH BOARD (M8418)	1	1			
4	4		10.0 MMF 100V 5%200PPM	MICA	1	1	C66		
5	5		100.0 MMF 100V 5%200PPM	MICA	1	1	C64		
6	6		1000.0 MMF 100V 5%200PPM	MICA	1	1	C65		
7	7		.01 MFD50/100V +80-20%	DISC	53	53			
8	8		1005306-00	6.8MFD 35V 10% S.TANT	8	8	CONT		
9	9		1010274-00	.22 MFD 50V +80-20% ZSU CER	60	60	CONT		
10	10		1010279-00	.47 MFD 25V 20% CER	6	6	CONT		
11	11		1012084-01	.8 MFD 25V +75-10% AL EL	1	1	C52		
12	12		1012121-00	220.0 MMF 100V 1%200PPM	MICA	5	5	C59, C63, C71-C74	
13	13		1012219-00	.47 MFD 30V +75-10% AL EL	3	3	C50, C51, C132		
14	14		1101938-00	1N 4370A VZ= 2.4 5% .40W	1	1	D3		
15	15		1104860-00	1N 746A VZ= 3.3 5%	1	1	D5		
16	16		1105275-00	D 672 TR= 15NS PIV= 60V SI	21	21	D1, D2, D9-D27		
17	17		1109943-00	1N 4733A VZ= 5.1 5% 1W Y	1	1	D28		
18	18		1211164-04	SW, DIP 1P 1A 8POS	1	1	E62		
19	19		1210711-02	/REPLACED BY 12-16988-02	1	1			
20	20		1300250-00	150.0 .25 W 5.0 % CC	1	1	R47		
21	21		1300271-00	220.0 .25 W 5.0 % CC	16	16	R123-R138		
22	22		1300316-00	470.0 .25 W 5.0 % CC	2	2	R46, R51		
23	23		1300365-00	1.0 K .25 W 5.0 % CC	9	9	R45, R50, R53, R54, R82-R84, R103, R104		
24	24		1300447-00	4.70 K .25 W 5.0 % CC	15	15	CONT R55-R65, R77, R78, R81, R139		
25	25		1300479-00	10.0 K .25 W 5.0 % CC	6	6	R71-R74, R121, R122		
26	26		1302124-00	18.0 .25 W 5.0 % CC	46	46	R1-R44, R90, R98		

REVISION HISTORY		BASIC PART NO: M8417		DRN:	L. METZGER	DATE: 30-MAY-78	D I G I T A L
ENG:	ECO NUMBER	REV	SECTION C OF C				TITLE PARTS LIST
E.R.	00003	D	SECTION.VARIATION INDEX	CHK'D:	P. BOSSMAN	DATE: 30-MAY-78	PDP8 MOS MEMORY
J.S.	M8417-ML004	E	[A] AB, AD, AC, AE				
JS	M8417-ML005	F	[B] BB, BC, BD, BE				
JS	M8417-ML006	H	[C] AF, BF	DES. ENG:	J. STEGEMAN	DATE: 30-MAY-78	
		[D]					
		[E]					
		[F]		RESP. ENG.:	J. STEGEMAN	DATE: 30-MAY-78	
		[H]					
		[J]					
		[K]		MFG. ENG.:	C. TANNER	DATE: 30-MAY-78	K PL M8417-0-DBP H
		[L]					
		[M]		ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER:		FILE NAME: EDIT #
		[N]		D-UA-M8417-0-0	MS8-C		Z0189H.PLS 6

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PARTS LIST

SHEET C2 OF C3

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
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27	27	1302377-00	39.0	.25	W 5.0 % CC	5	5	R52,R109-R112
28	28	1302859-00	5.76 K	.25	W 1.0 % RN55D-F10	1	1	R96
29	29	1302871-00	1.21 K	.25	W 1.0 % RN55D-F10	2	2	R80,R108
30	30	1302872-00	681.0	.25	W 1.0 % RN55D-F10	1	1	R89
31	31	1302956-00	196.0	.25	W 1.0 % RN55D-F10	1	1	R94
32	32	1302957-00	121.0	.25	W 1.0 % RN55D-F10	1	1	R101
33	33	1303045-00	3.16 K	.25	W 1.0 % RN55D-F10	1	1	R76
34	34	1303067-00	422.0	.25	W 1.0 % RN55D-F10	1	1	R99
35	35	1303110-00	19.60	.25	W 1.0 % RN55D-F10	1	1	R93
36	36	1303226-00	68.10	.25	W 1.0 % RN55D-F10	1	1	R86
37	37	1303311-00	46.40 K	.25	W 1.0 % RN55D-F10	1	1	R75
38	38	1303313-00	12.10 K	.25	W 1.0 % RN55D-F10	1	1	R97
39	39	1304725-00	300.0	.25	W 1.0 % RN55D-F10	1	1	R107
40	40	SEE NOTE 90	1.96 K	.25	W 1.0 % RN55D-F10	1	1	R92
41	41	1305122-00	51.10	.25	W 1.0 % RN55D-F10	1	1	R85
42	42	1305123-00	215.0	.25	W 1.0 % RN55D-F10	2	2	R91,R102
43	43	1305143-00	825.0	.25	W 1.0 % RN55D-F10	2	2	R95,R79
44	44	SEE NOTE 90	7.15 K	.25	W 1.0 % RN55D-F10	1	1	R87
45	45	1309405-00	68.0	.50	W 5.0 % CC	2	2	R48,R49
46	46	1503100-00	DEC3009B	NPN 200MW SI	20 25	7	7	Q1-Q5,Q8,Q9
47	47	1510171-00	D 44C3	NPN 30WT SI	30 20 Y	2	2	Q6,Q7
48	48	1614234-00	XFMR,	CONVERTER, RATIO 1:3	300UH	1	1	T1
49	49	1909054-00	7493	COUNTER ASYNCH UP BI	2	2	E21,E24	
50	50	1909705-00	DEC 8881	NAND GATE-QUAD 2IN	0	4	E3,E14,E17,E44	
51	51	1910268-01	DEC 751078-01	RECEIVER, LINE, DUA	3	3	E5,E10,E18	
52	52	1910406-00	75451	DRIVER PERIPH, DUAL,	1	1	E64	
53	53	1910532-00	74500	NAND GATE-QUAD 2IN	6	6	E4,E34,E37,E42,E43,E55	
54	54	1910533-00	74503	NAND GATE-QUAD 2IN, 0	1	1	E13	
55	55	1910534-00	74504	INVERTER GATE-HEX 1I	2	2	E22,E54	
56	56	1910536-00	74510	NAND GATE-TRIPLE 3IN	3	3	E19,E61,E63	
57	57	1910539-00	74520	NAND GATE-DUAL 4INPU	1	1	E59	
58	58	1910542-00	74564	A-O-I GATE 4-2-3-2	1	1	E15	
59	59	1910544-01	74574-60GG-D	CUAL, EDGE TRIG	1	1	E39	
60	60	1910545-00	74S112	FF-JK DUAL, EDGE TRIG	3	3	E7,E20,E32	
61	61	1910549-00	74S158	MUX 1 OF 2 (QUAD)	2	2	E26,E33	
62	62	1911116-00	DEC 8837	RECEIVER, BUS, HEX, UN	5	5	E6,E8,E16,E38,E57	
63	63	1911469-00	DEC 8640	RECEIVER BUS QUAD U	1	1	E45	
64	64	1911579-00	8641	TRANSCEIVER, BUS, QUA	3	3	E1,E11,E51	
65	65	1911676-00	74S139	DECODER-DUAL TWO-INP	1	1	E56	
66	66	1911712-00	74S551	AND-OR GATE-INVERT D	1	1	E58	
67	67	1911944-00	555CN	TIMER, FUNCT. BLOCK	1	1	E31	
68	68	1911983-00	74S133	NAND GATE-POSITIVE 1	1	1	E60	
69	69	SEE NOTE 93	1912048-06	DEC 7812	VOLT REG, FIX +12V	1	1	Q10
70	70	1912388-00	74S02	NOR GATE-QUAD 2IN, PO	3	3	E9,E25,E46	
71	71	1912389-00	74S08	AND GATE-QUAD 2IN, PO	1	1	E23	
72	72	1912541-00	79M05	VOLT REG, FIX -5V	1	1	Q11	
73	73	1912649-00	LS75	LATCH 4BIT, BISTABLE	3	3	E2,E12,E53	
74	74	1912746-00	DEC 74537	NAND GATE-QUAD 2IN	11	11	E27-E30,E35,E36,E41,E47-E50	

TITLE		SECTION C OF C	SIZE CODE	DOCUMENT NUMBER	REV						
D	I	G	I	T	A	L	POPB MOS MEMORY	K	PL	M8417-0-DBP	H

AUTOMATED BY PRTLST.2D(16)

PARTS LIST

SHEET C3 OF C3

LINE ITEM DOCUMENT NUMBER

PART NUMBER

DESCRIPTION

QTY PER VARIATION
AF BF

REFERENCE DESIGNATOR

75	75		1912803-00	74LS04 INVERTER GATE HEX	1	1	E52
76	76		1912824-00	LS74 FF-D DUAL,EDGE TRIGG	1	1	E40
77	77		2113735-01	*** THIS ITEM IS NOT USED ***	-	-	
78	78		2114114-01	*** THIS ITEM IS NOT USED ***	-	-	
79	79		2113914-01	*** THIS ITEM IS NOT USED ***	-	-	
80	80	SEE NOTE 91	2114475-01	*** THIS ITEM IS NOT USED ***	-	-	
81	81		9000024-01	EYELET, ROLL FLANGE .1210DX .192	12	12	
82	82	USE WITH Q6,Q7,Q10	9006557-00	NUT, KEP 4-40X 1/4 AF	3	3	
83	83	USE WITH Q11	9007254-00	TRANSIPADS #10146	1	1	
84	84	USE WITH Q6,Q7,Q10	9008301-01	SCREW, PAN PHIL 4-40X 1/4 SS	3	3	
85	85		9009185-00	JUMPER, WIRE, INSULATED, BLACK B	6	6	
86	86		9105740-55	WIRE(WRAP)30AWG UL1423	A/R	A/R	
87	87		2113789-01	4K MOS RAM 200NS 1	48	-	
CONT							
CONT E100,E102,E104,E106,E108,E110, CONT E112,E114,E116,E118,E120,E122, CONT E200,E202,E204,E206,E208,E210, CONT E212,E214,E216,E218,E220,E222, CONT E300,E302,E304,E306,E308,E310, CONT E312,E314,E316,E318,E320,E322, CONT E400,E402,E404,E406,E408,E410, CONT E412,E414,E416,E418,E420,E422, CONT E100-E123,E200-E223,E300-E323, CONT E400-E423							
- 96							

- 88 NOTE: M8417-AA=M8417-AB,AC,AD,AE,AF,(16KX12)
 89 NOTE: M8417-BA=M8417-BB,BC,BD,BE,BF(32KX12)
 90 NOTE: USED ON:OPTION/MODEL MS8-CA MS8-CB
 91 NOTE: R87,R92 MAY BE REMOVED AT MODULE TEST IF NEEDED.
 92 NOTE: MIXING OF MOS RAMS IS NOT PERMITTED.
 93 NOTE: REF ITEM #69 19-12048-05 ACCEPTABLE SUBSTITUTE FOR 19-12048-06.
 94 NOTE: -----

TITLE				SIZE	CODE	DOCUMENT NUMBER	REV					
D	I	G	I	T	A	L	PDP8 MOS MEMORY	SECTION C OF C	K	PL	M8417-0-DBP	H

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

ENGINEERING SPECIFICATION

DATE 10/3/73

TITLE MR8-F FIELD INSTALLATION AND ACCEPTANCE PROCEDURE

REVISONS

REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE
A	ECO CHANGE	MR8F-00001	ADAMS	1-74	David J Adams	10/3/73
B	UPDATE TO ADD PDP8A	MR8F-00004	REGAN	12-74	R. Regan	10/30/74

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ENG
Richard Morris

APPD
Richard J Adams 10/11/73

SIZE

A

CODE

SP

NUMBER
MR8-F-2

REV

B

DEC FORM NO.
DRA 107

SHEET 1 OF 3

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE MR8-F FIELD INSTALLATION AND ACCEPTANCE PROCEDURE

1. Shipping Hardware

1.1 See A-PL-MR8-F-5 (Shipping List)

2. Shipping Software

2.1 See A-PL-MR8-F-6 (Software List)

NOTE: Prom Diagnostics are not used in this Acceptance Procedure. These Maindecs are used to diagnose the MR8-FB after the MR8-FB has been programmed to the customers specifications.

3. Equipment required for acceptance

3.1 PDP8E, 8M, or 8A with a programmers console. If 4K of read/write memory is present, the system must have a KM8-E (M837), or KM8-A (M8317), extended memory control. All these options must be customer supplied.

4. Unpacking and Installation

4.1 Unpack and inspect the modules for physical damage.

4.2 Make sure all four top edge connectors on the M8349 are fitted correctly.

4.3 Turn power off in the PDP8E, 8M, 8F, or 8A.

4.4 If the MR8-FB is a PDP8E, 8F, or 8M, add-on remove M8330 and insert the new M8330-YB in the same slot. The MR8-FB requires an M8330-YB inorder to operate.

4.5 Insert the M8349 in the OMNIBUS behind the RFI shield (M849) in the PDP8E, 8F, or 8M, and in the lowest available Omnibus slot in the PDP8A.

4.6 Remove or disable all other options in PDP8E, 8M, 8F, or 8A that use the "SW" or "BOOT" switch option.

Sheet 2 of 3

SIZE	CODE	NUMBER	REV
A	SP	MR8-F-2	B

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE**MR8-F FIELD INSTALLATION AND ACCEPTANCE PROCEDURE****5. Acceptance**

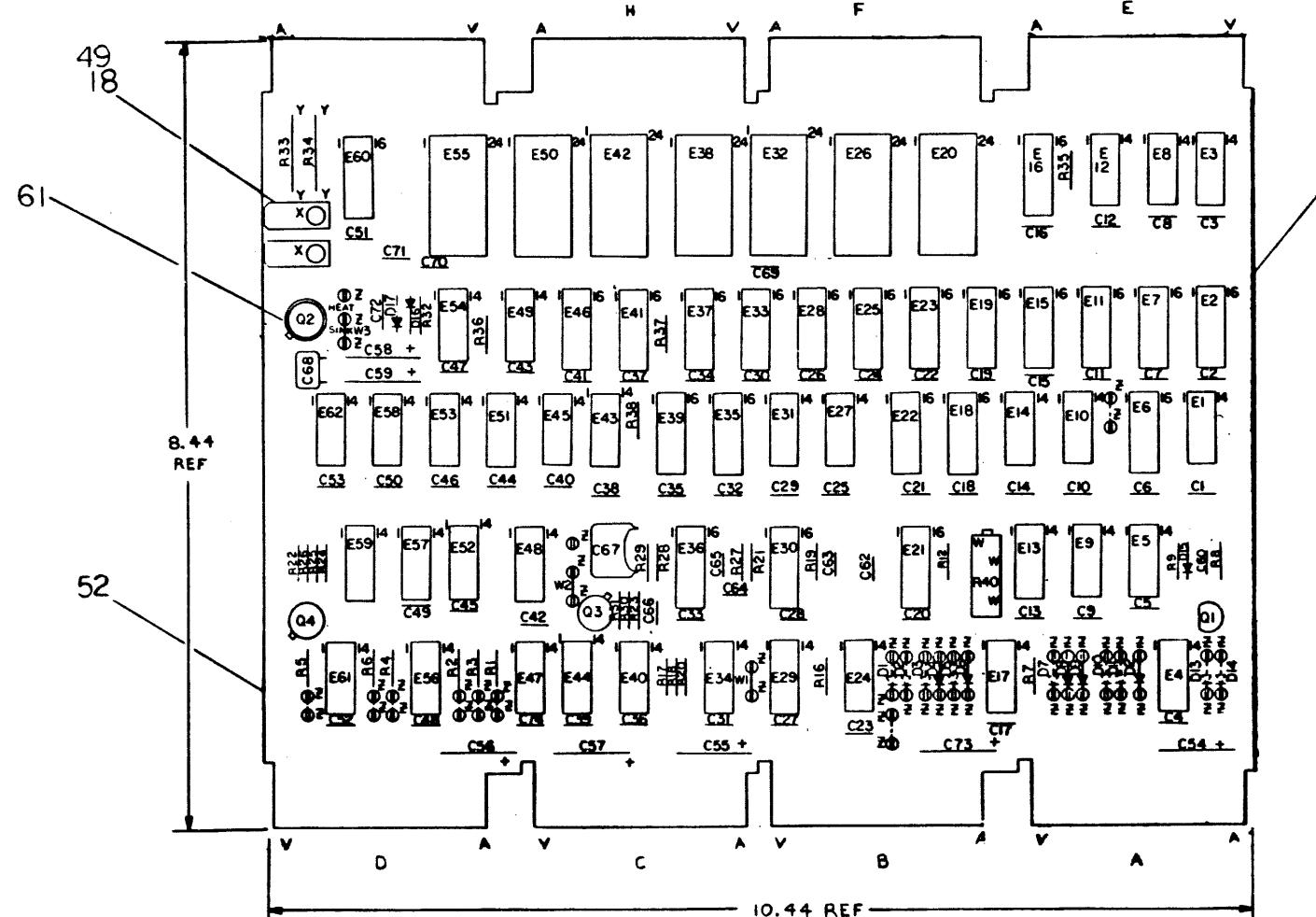
- 5.1 The MR8-FB is shipped with Prom Internal test Maindec-08-DHMRE programmed in the Prom chips.
- 5.2 Turn PDP8E, 8M, 8F, or 8A, power on.
- 5.3 If the MR8-FB is an add-on and an M8330-YB was installed, run all basic 8E diagnostics and EAE diagnostics if applicable.
- 5.4 Toggle "SW" or "BOOT" switch. The Prom Internal Test should be running. Refer to MAINDEC-08-DHMRE writeup if there are any errors. With the switch register = 0000 the test will halt in approx. 3 min. Repeat the test 4 times.
- 5.5 If no errors have occurred the MR8-FB is ready to be erased and reprogrammed by the customer.

DIGITAL EQUIPMENT CORPORATION

NOTES

- 5:

 1. JUMPERS IN FOR S OUT FOR 1
 2. DIODES ON OUTPUT OF INVERTERS ARE IN FOR 1
DIODES ON INPUT OF INVERTERS ARE IN FOR S
SOME DIODES ARE INSTALLED IN PRODUCTION TO
FACILITATE CHECKOUT. A CUSTOMER WILL RE-ARRANGE
DIODES TO HIS REQUIREMENTS
 3. DELAY OUTPUT IS ADJUSTED TO 2.2 S + 50 MS.
 4. DIODES AND JUMPERS SHOWN IN DOTTED LINES ARE NOT PUT ON THE BOARD DURING MANUFACTURE. THEY ARE ADDED DURING CHECKOUT AS REQUIRED. SOLID LINE JUMPERS ARE PUT IN WHEN BOARD IS ASSEMBLED.
 5. UNLESS OTHERWISE NOTED
RESISTANCE IS IN OHMS 1/4W 5%.
 6. YAI JUMPER (ROM ADDRESS) IS ONLY INSTALLED IF
THE PROM ADDRESSES OVERLAY CORE MEMORY ADDRESSES.
 7. UNLESS OTHERWISE SPECIFIED ALL DELAY TIMES ARE + 20%.



AC2, AF1, AF2, AT1, AT2
BC1, BC2, BF1, BF2, BN1, BN2, BT1, BT2
CC1, CC2, CF1, CF2, CN1, CN2, CT2,
DC1, DC2, DF1, DF2, DM1, DM2, DT1, DT2

DEC 1702A	-	12
DEC 5380	1	8
DEC 7384	1	8
DEC 74151	8	16
DEC 74123	8	16
DEC 74157	8	16
DEC 74174	8	16
DEC 74200	8	16
IC TYPE	GND	4 5V
GND AND 5V ARE USUALLY PIN 7 AND 14 RESPECTIVELY. EXCEPTIONS ARE STATED ABOVE.		
IC PIN LOCATIONS		

GND AND 5V ARE USUALLY PIN 7 AND 14
RESPECTIVELY. EXCEPTIONS ARE STATED ABOVE.

IC PIN LOCATION

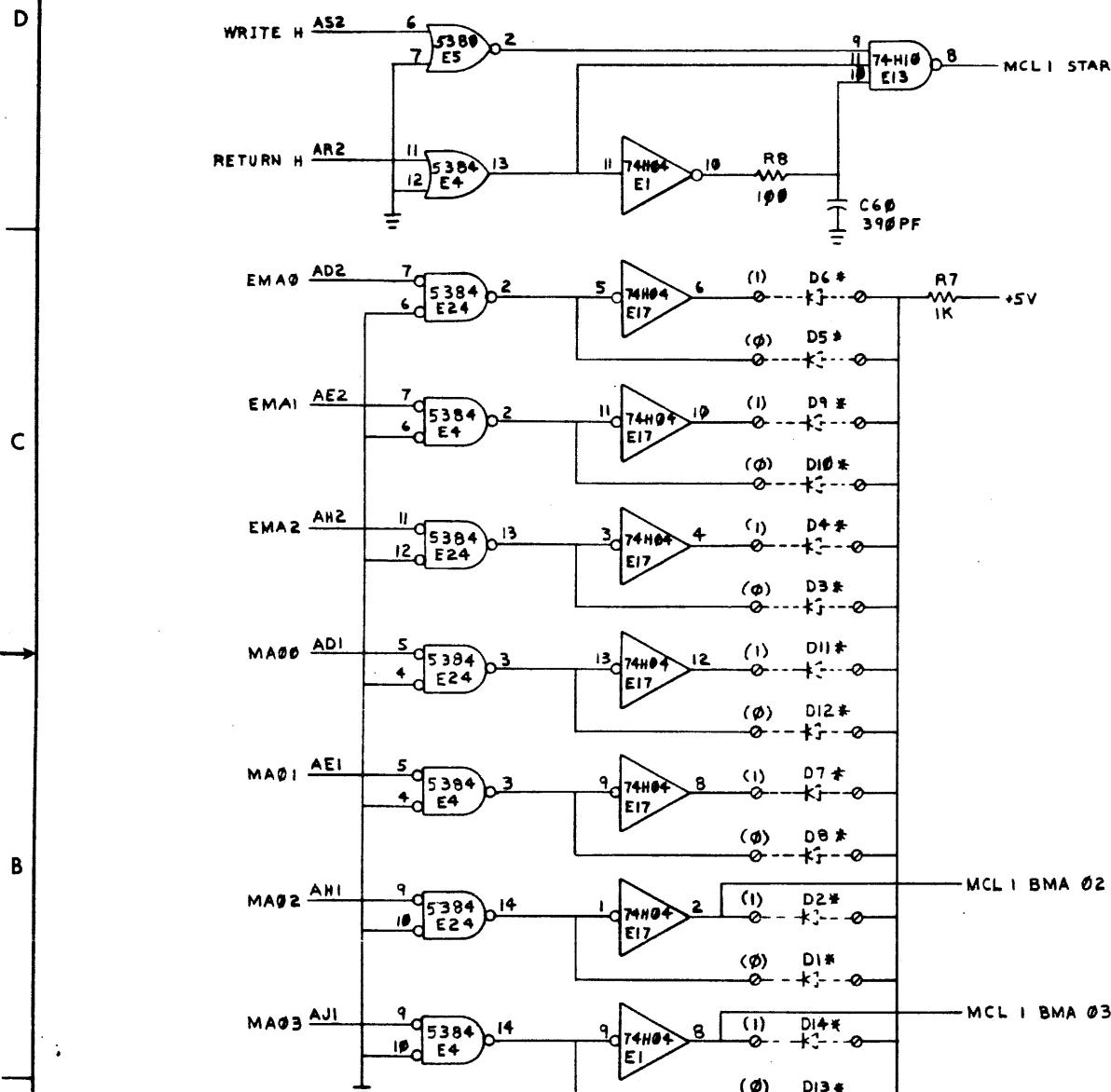
ITEM NO.	QTY	REF DESIGNATION	DESCRIPTION	PARTS LIST		REV.
				NO.		
USED ON OPTION MODEL						
MR8-F		ETCH BOARD REV C				
		DRAWN G. Pudell	DATE 3-17-73			
		COPYED G. Pudell	DATE 1-17-73			
	3762	NONE	END D. D. Head	DATE 1-17-73		
	6531B	NONE	PRINT ENG. R. L. Head	DATE 1-17-73		
	IN 757A	NONE	PROOF R. L. Head	DATE 1-17-73		
	D664	IN 3606	NEXT HIGHER ASSY			
	D662	IN 645	C-UA-MR8-F-0		SIZE CODE DCS	NUMBER M8349-0-1
0.	EIA NO.	DEC NO.	EIA NO.	SCALE NONE	DIST.	REV. F
SEMICONDUCTOR CONVERSION CHART						

PROM_IK

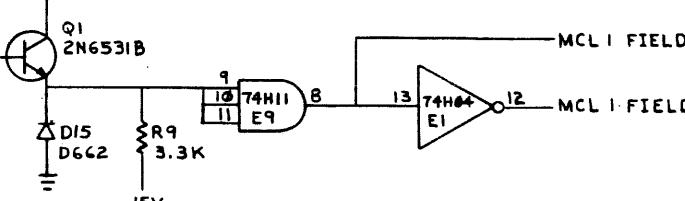
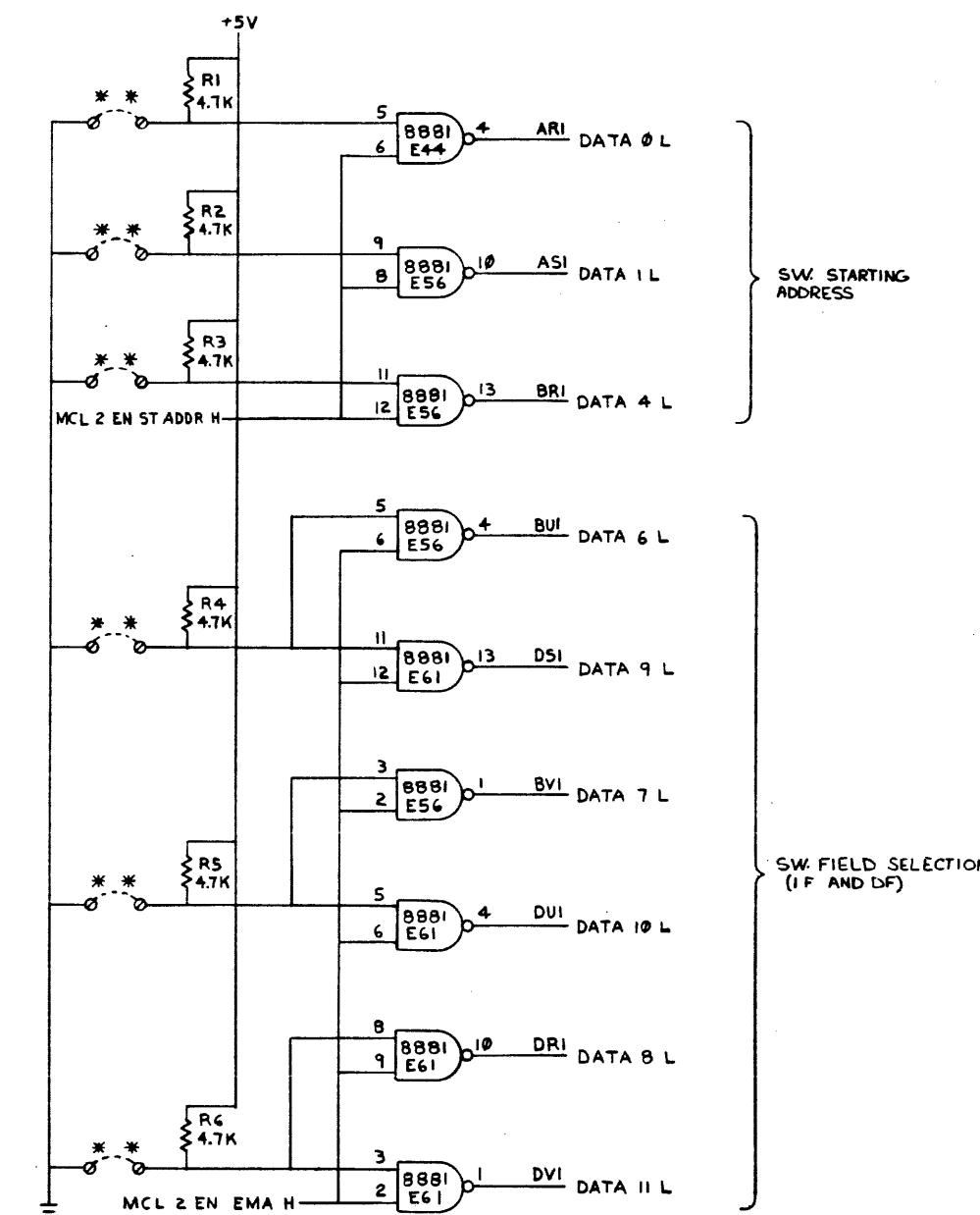
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8 7 6 5 4 3 2 1 DCSM8349-0-1 2



* SEE NOTES 2 AND 4 AND Y VARIATION CHART.
** SEE NOTES 1 AND 4.



REVISIONS
CHK CHANGE NO. REV.

1 SEC FORM NO. 8

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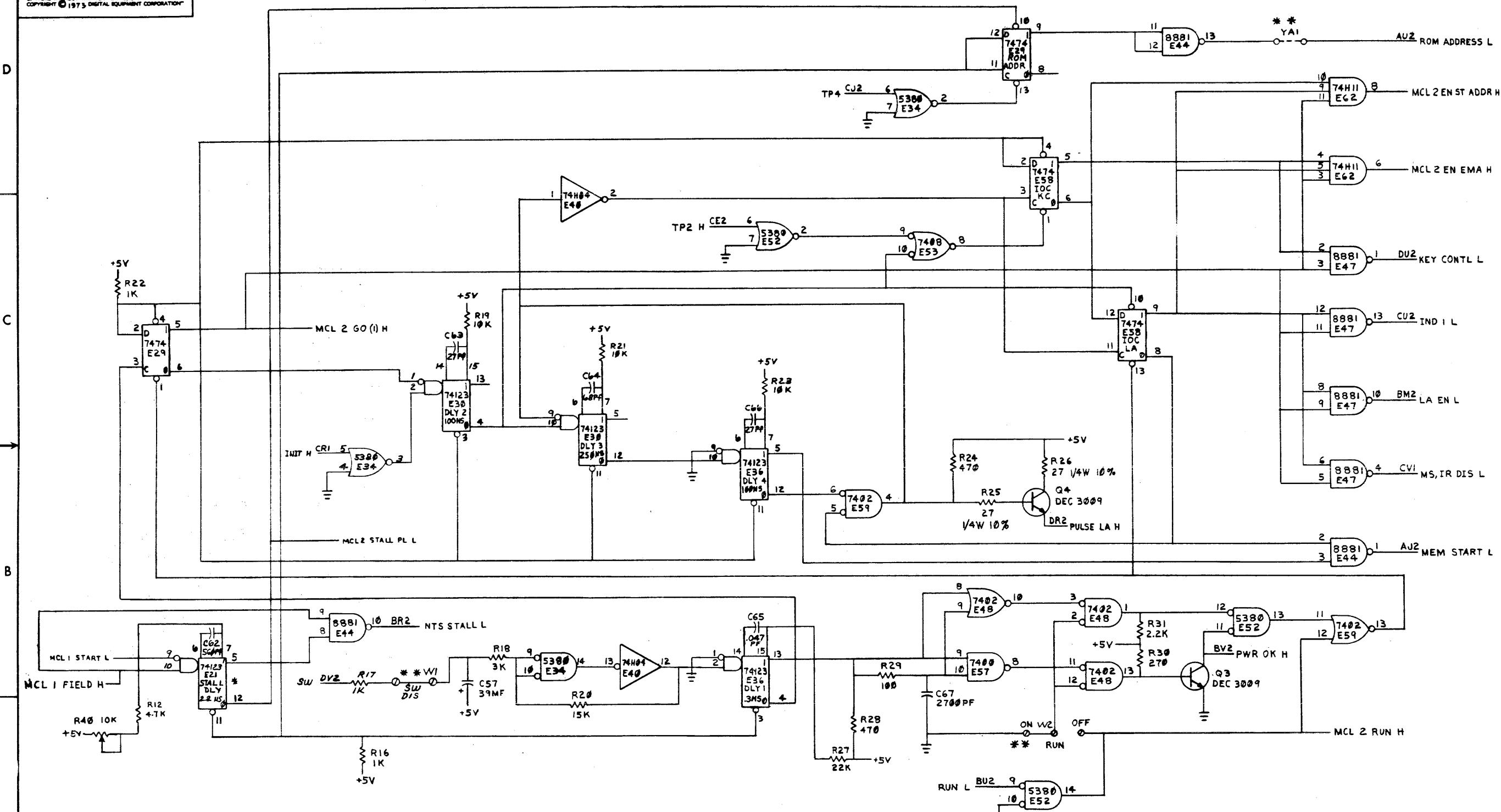
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TITLE PROM IK (MCLI) SIZECODE DCS M8349-0-1 REV. F

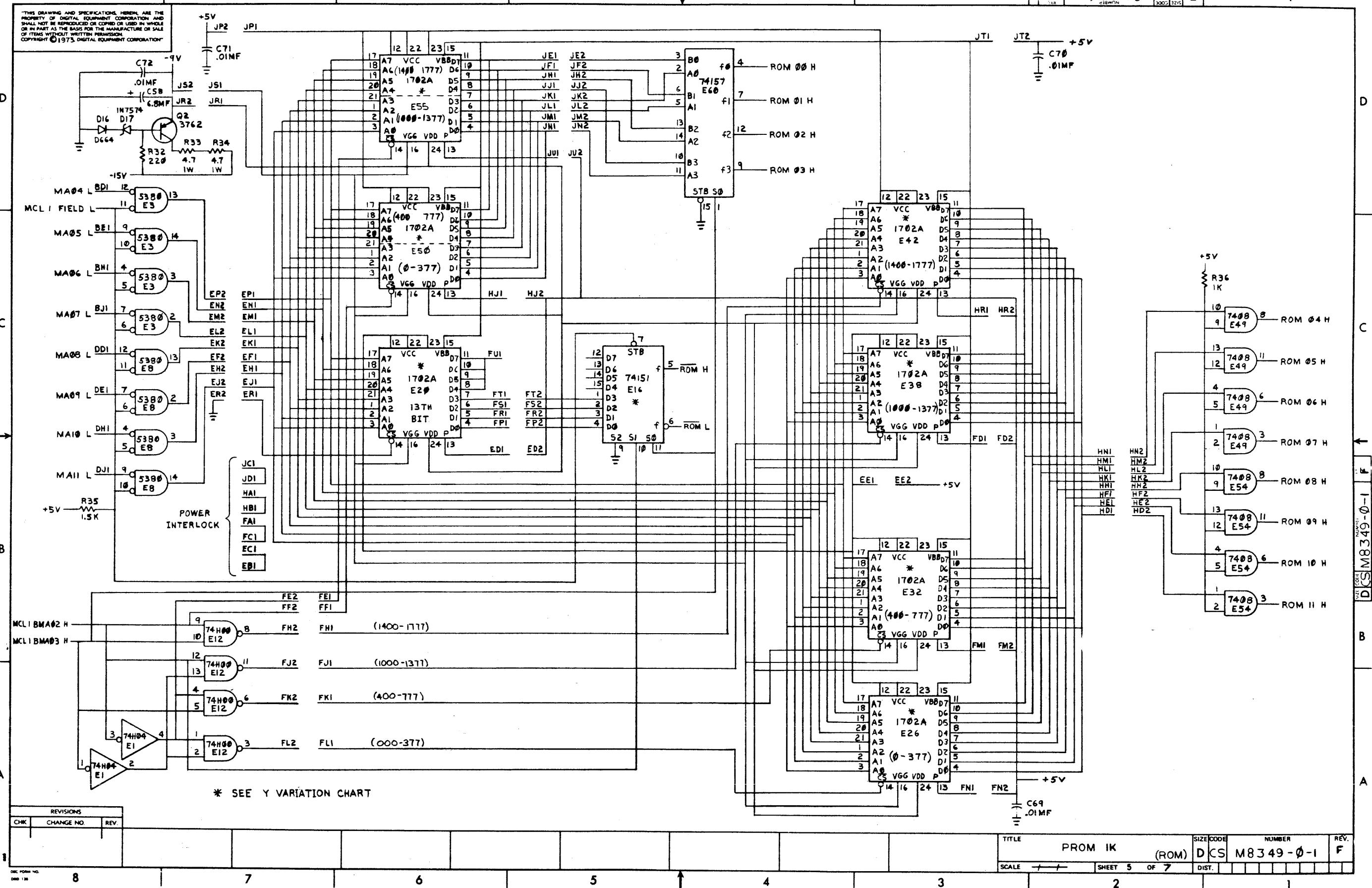
SCALE - - - SHEET 3 OF 7 DIST. _____

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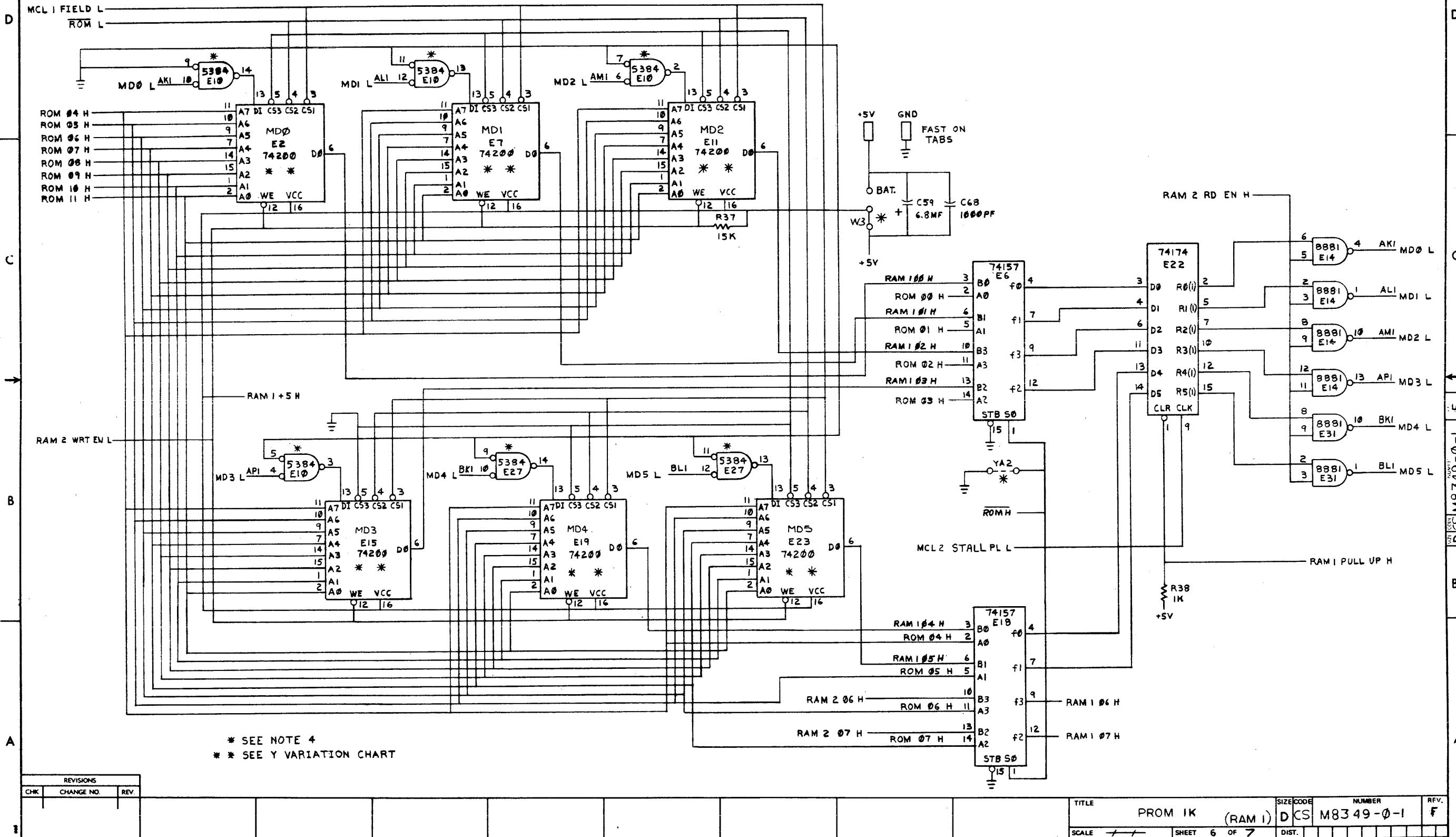


* SEE NOTE 3
** SEE NOTE 4

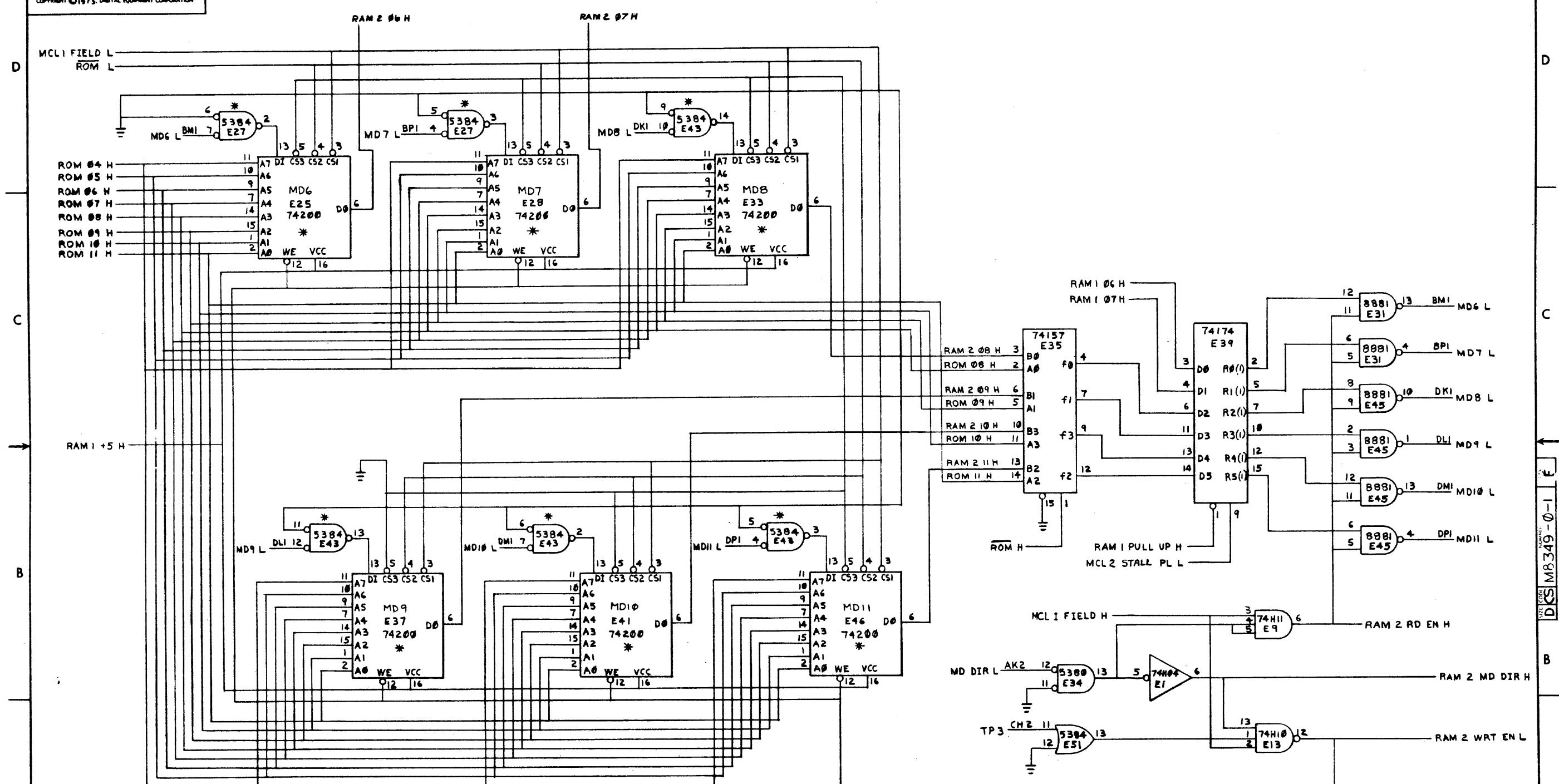
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* SEE Y VARIATION CHART

REVISIONS

CHK CHANGE NO. REV.

			TITLE		PROM 1K (RAM 2)	SIZE CODE	NUMBER	REV.
SCALE	/ /	SHEET 7 OF 7	DCS	M8349-0-1				E

DIGITAL EQUIPMENT CORPORATION					
MAYNARD, MASSACHUSETTS					
ENGINEERING SPECIFICATION					
DATE 11/19/74					
TITLE MS8-A FIELD INSTALLATION & ACCEPTANCE PROCEDURE					
REVISIONS					
REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY

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DECO FORM NO DEC 16-1022-N370
DRA 107

ENGINEERING SPECIFICATION					
CONTINUATION SHEET					
TITLE MS8-A FIELD INSTALLATION & ACCEPTANCE PROCEDURE					
DATE 11/19/74					
I GENERAL					
This procedure defines the performance standards required of an MS8A RAM memory.					
NOTE: If MS8A was shipped as part of a PDP-8A system, proceed to installation procedure.					
MS8AA (M8311YA) 1K Semiconductor Random Access Memory MS8AB (M8311YB) 2K Semiconductor Random Access Memory MS8AD (M8311YD) 4K Semiconductor Random Access Memory					
II INSPECTION					
After removing the MS8A from packing material, inspect the module for the following:					
1. Check for loose or broken component. 2. Inventory software against software list, if ordered. 3. Inventory prints against shipping list, if ordered. 4. Inventory hardware against shipping list.					
III INSTALLATION PROCEDURE					
Install the equipment using the following procedure:					
1. Set up switches as indicated below for the particular variation being accepted.					
MS811YA 1K MS811YB 2K MS811YD 4K					
MS8311 YA MS8311 YB MS8311 YD					
S1-1 ON ON * field 0 S1-2 ON ON * field 0 S1-3 ON ON * field 0 S1-4 ON ON ST Add S1-5 ON ON ST Add S1-6 OFF OFF On for 4K S1-7 OFF OFF Off S1-8 OFF ON On for 2K S1-9 ON OFF Off S1-10 ON ON Normally On					
NOTE: Reference Operator's Handbook for complete description of switch settings.					
DEC FORM NO DEC 16-1022-N370 DRA 108					
SHEET 2 OF 3					

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE MS8-A FIELD INSTALLATION & ACCEPTANCE PROCEDURE

III INSTALLATION PROCEDURE (continued)

2. Insure that the PDP-8A power is removed from the Omibus TM.
3. Insert the MS8A into the last slot vacant in the Omibus TM.
4. Turn power on.

IV ACCEPTANCE PROCEDURE

Perform the Acceptance Test as indicated in Table B. If problems are encountered, refer to the diagnostic listing for type of error. Reference Operator's Manual and Diagnostic Write-up for instructions on loading diagnostic.

Equipment required:

1. PDP-8A with MS8A R/W Memory
2. Programmer's Console
3. Paper Tape Input Device
4. Diagnostic and Listings

NOTE: If the Programmer's console and paper tape input device are not available as part of the system being used, they must be supplied in good working order by the customer.

Table B

Acceptance of MS8A: YA or YB

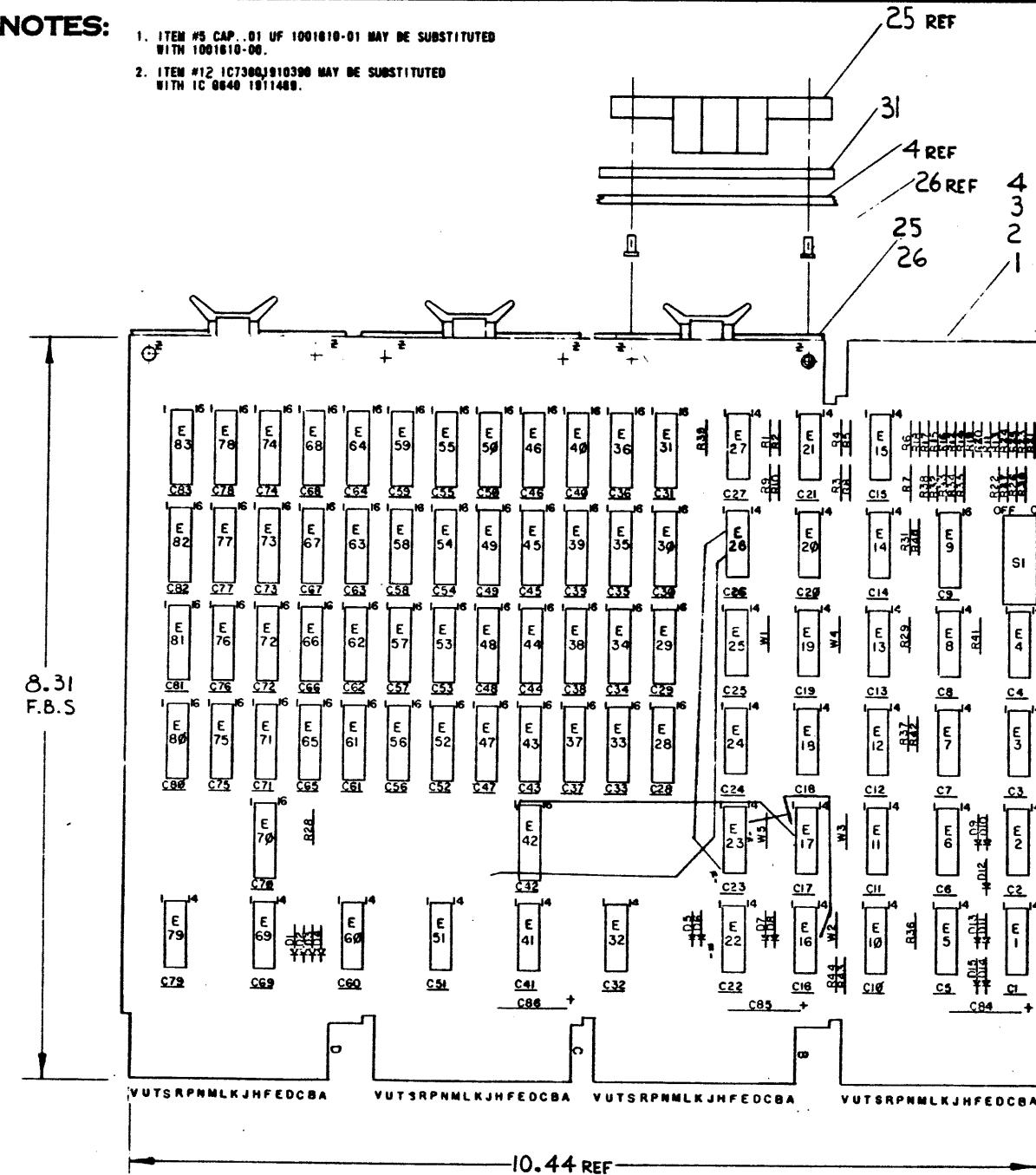
Program Name	Maindec #	Accept Time	Restrictions
1-4K MOS Memory Test (RIM)	08-DJMSA-PM	30 min	1K or 2K MS8A R/W Memory
Acceptance of MS8A: YD			
1-4K MOS Memory Test	08-DJMSA-PM	15 min.	4K MS8A R/W Memory
4-32K Memory Test	08-DJMMA-PB	15 min.	4K MS8A R/W Memory

SHEET 3 OF 3

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NOTES

- 1. ITEM #5 CAP..01 UF 1001610-01 MAY BE SUBSTITUTED WITH 1001610-00.
 - 2. ITEM #12 1C7300J910300 MAY BE SUBSTITUTED WITH 1C 0840 1811400



8223	8	16
2102	9	18
74174	8	16
7384	1	8
7380	1	8
IC TYPE	GND	+5V

**GND AND +5V ARE USUALLY PIN 7 AND 14
RESPECTIVELY. EXCEPTIONS ARE STATED ABOVE**

IC PIN LOCATIONS

GND AND BY ARE USUALLY PIN 7 AND 34
RESPECTIVELY. EXCEPTIONS ARE STATED ABOVE.

16 NEW LOCATIONS

IC PIN LOCATIONS

DRAFTS										2		1	
REVISIONS													
ITEM NO.													
REF#	REF#	REF#	REF#	REF#	REF#	REF#	REF#	REF#	REF#	X-Y COORDINATE HOLE LOCATION	ASSY/DRILLING HOLE LAYOUT	MODULE ECO HISTORY	ETCHED CIRCUIT BOARD
REF#	REF#	REF#	REF#	REF#	REF#	REF#	REF#	REF#	REF#	REF#	REF#	REF#	REF#
M8311-XA	M8311-XB	M8311-XC	M8311-XD	M8311-XE	M8311-XF	M8311-XG	M8311-XH	M8311-XI	M8311-XJ	N-CO-M8311-0-4	O-AH-M8311-0-5	P-MM-M8311-0-6	5010586
- - - 71 - - -	- - - 71 - - -	C1-C27, C28-C32, C34-C36, C38; C42, C44-C46, C48-C51, C53-C55, C57-C60, C62-C64, C66-C70, C72, C74, C76-C79, C81-C83	C1-C27, C28-C32, C34-C36, C38, C40, C41, C42, C45, C48, C49, C50, C51, C54, C55, C58, C59, C60, C63, C64, C67-C70, C73, C74, C77, C78, C79, C82, C83	C1-C27, C31, C32, C36, C40-C42, C46, C50, C51, C55, C58, C60, C64, C68-C78, C74, C78, C79, C83	CAP .01 UF 100V 20% (SEE NOTE #1)	1001010-01							
47 - - - 47 - - -	83	83	CI - C83										
15 15 15 15 15 15 15 15 15 15 D1-D15										DIODE D064			1100114
11 11 11 11 11 11 11 11 11 R1-R10, R36										RES. 180 1/4W 5%			1301322
5 5 5 5 5 5 5 5 5 R28-R43										RES. 220 1/4W 5%			1300271
13 13 13 13 13 13 13 13 13 R11-R20, R36, R31, R37										RES. 2.2K 1/4W 5%			1300417
1 1 1 1 1 1 1 1 1 S1										SWITCH 10 POS			1211164-06
1 1 1 1 1 1 1 1 1 E7										IC 7402			1900084
6 6 6 6 6 6 6 6 6 E22, E23, E41, E68, E78, E1										IC 7380 (SEE NOTE #2)			1910398
4 4 4 4 4 4 4 4 4 E5, E32, E51, E68										IC 8801			1909705
3 3 3 3 3 3 3 3 3 E4, E8, E17										IC 7408			1905575
1 1 1 1 1 1 1 1 1 E19										IC 7418			1905576
1 1 1 1 1 1 1 1 1 E2										IC 8242			1908712
1 1 1 1 1 1 1 1 1 E13										IC 74184			1910041
1 1 1 1 1 1 1 1 1 E10										IC 7488			1910155
5 5 5 5 5 5 5 5 5 E8, E18, E15, E21, E27										IC 7437			1910081
4 4 4 4 4 4 4 4 4 E3, E14, E28, E28										IC 7384			1910393
4 4 4 4 4 4 4 4 4 E11, E12, E16, E24										IC 7474			1905547
2 2 2 2 2 2 2 2 2 E42, E78										IC 74174			1910652
- - - 48 - - -										E28-E31, E33-E40, E43-E50, E52-E59, E61-E68, E71-E78, E80-E83			
- - - 36 - - -										E29-E31, E34-E36, E38-E40, E44-E46, E48-E50, E53-E55, E57-E59, E62-E64, E66-E68, E72-E74, E76-E78, E81-E83			
- 24 - - - -										E30, E31, E35, E36, E38, E40, E45, E48, E49, E50, E54, E55, E56, E59, E63, E64, E67, E68, E73, E74, E77, E78, E82, E83			
12 - - - - -										E31, E36, E40, E46, E50, E55, E59, E64, E68, E74, E78, E83,			
-- - - - - 48										E29-E31, E33-E40, E43-E50, E52-E59, E61-E68, E71-E78, E80-E83			
- - - - - 36										E29-E31, E34-E36, E38-E40, E44-E48, E48-E50, E53-E55, E57-E59, E62-E64, E66-E68, E72-E74, E76-E78, E81-E83			
ITEM NO.										DESCRIPTION			PART NO.
FIRST USED ON OPTION MODEL										PARTS LIST			
PDP8A										PARTS LIST			
ETCH BOARD REV. E										PARTS LIST			
DATE George Driscoll 5/21/79										digital			
CNC DATE 6/1/79										TITLE 4K X 12			
PROL. DATE 6/1/79										MOS. MEMORY			
NEXT HIGHER ASSY D-DD-PDP8A-0										SIZE CODE DCS M8311-0-1			
SCALE NONE										NUMBER			
DIST. F.													
SEMICONDUCTOR CONVERSION CHART													

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8 7 6 5 4 3 2 1

M631-YA
M631-YB
M631-YC
M631-YD
M631-YE
M631-YF
M631-YH
M631-YJ

							E30, E31, E35, E36, E39, E40 E45, E46, E49, E50, E54, E55 E58, E59, E63, E64, E67, E68, E73, E74, E77, E78, E82, E83.	IC DEC 2102-1	2111318-0-1	24
							E31, E36, E40, E46, E50, E55, E58, E64, E68, E74, E78, E83.			
3	3	3	3	3	3	3	HANDLE FLIP CHIP MAGENTA	9008337-08	25	
8	8	8	8	8	8	8	EYELET GS4-7	9008750	26	
1	1	1	1	1	1	1	IC DEC 8223 OR EQUIVALENT	2308341	27	
5	5	5	5	5	5	5	W1-W5	9008185	28	
3	3	3	3	3	3	3	C64, C65, C66	KC05366	29	
14	14	14	14	14	14	14	R21-R29, R32-R35, R38	1300439	30	
3	3	3	3	3	3	3	RES. 3.3K 1/4W 5%	1300300	31	
1	1	1	1	1	1	1	R44	1300300	32	
1	1	1	1	1	1	1	E25	1811660-01	33	
QTY	QTY	QTY	QTY	QTY	QTY	REF DESIGNATION	DESCRIPTION	PART NO	ITEM NO	

D

D

C

C

SWITCH DEFINITIONS

SWI-1	EMA2	FIELD SELECTION 'ON' IS Ø
SWI-2	EMA1	
SWI-3	EMAØ	
SWI-4	SELØ	STARTING ADDRESS SELECT 'ON' IS Ø
SWI-5	SEL1	
SWI-6	4K	
SWI-7	3K	
SWI-8	2K	
SWI-9	IK	MEMORY SIZE SELECT CORRECT SIZE - 'ON' OTHERS - 'OFF'
SWI-10		USED FOR TEST ONLY, ALWAYS 'ON'

DCCSM8311-0-1

B

B

A

A

JUMPER CONFIGURATION

YA, YB, YC AND YD — W1, W2, W3 ARE IN	W4 AND W5 ARE OUT
YE, YF, YH AND YJ — W4 AND W5 ARE IN	W1, W2 AND W3 ARE OUT

REVISIONS		
CHK	CHANGE NO	REV
1		

8 7 6 5 4 3 2 1

TITLE 4K X 12
MOS MEMORY

SIZE CODE DCS M8311-0-1

SCALE NONE SHEET 2 OF 6 DIST.

8

7

6

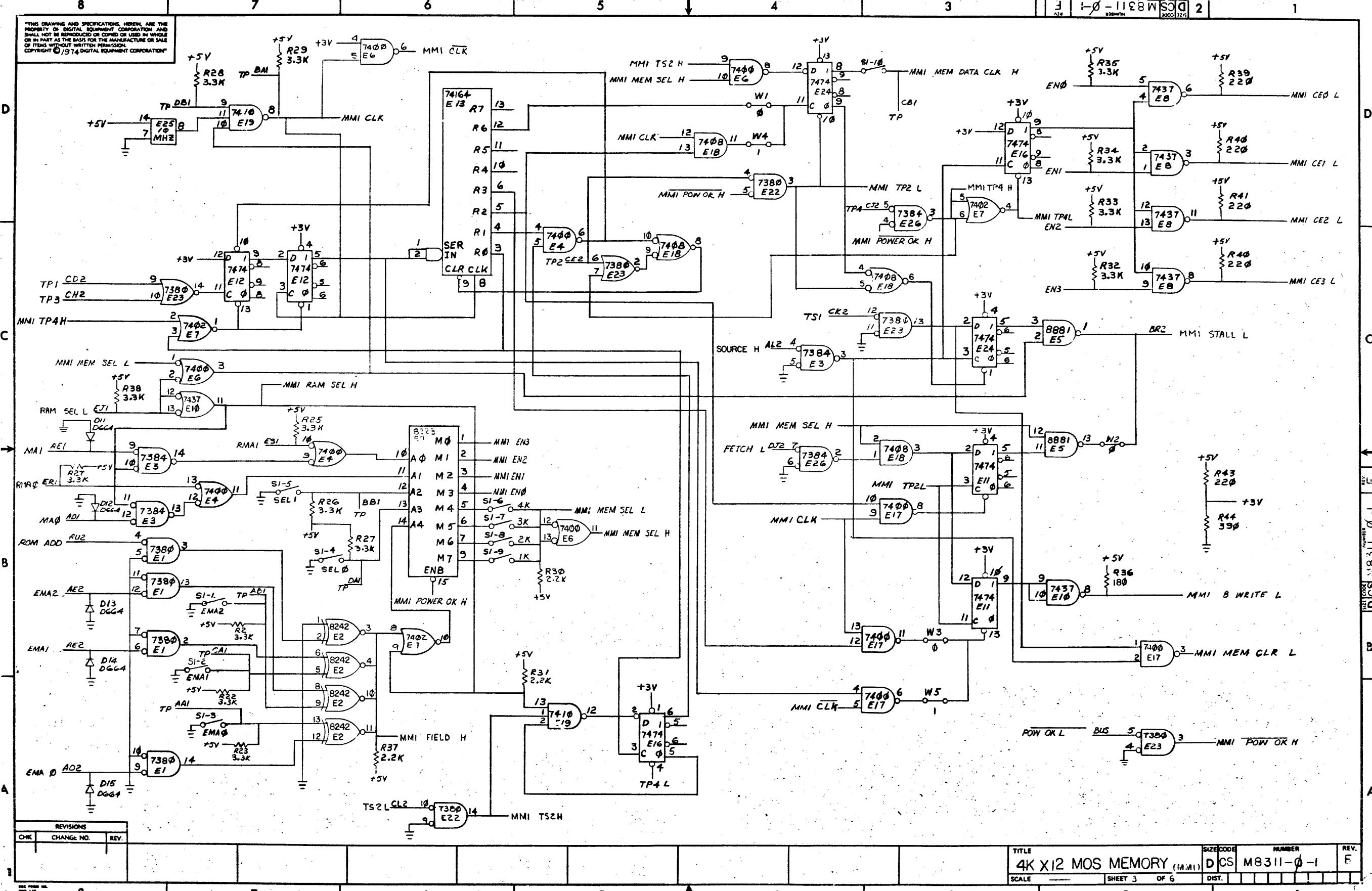
5

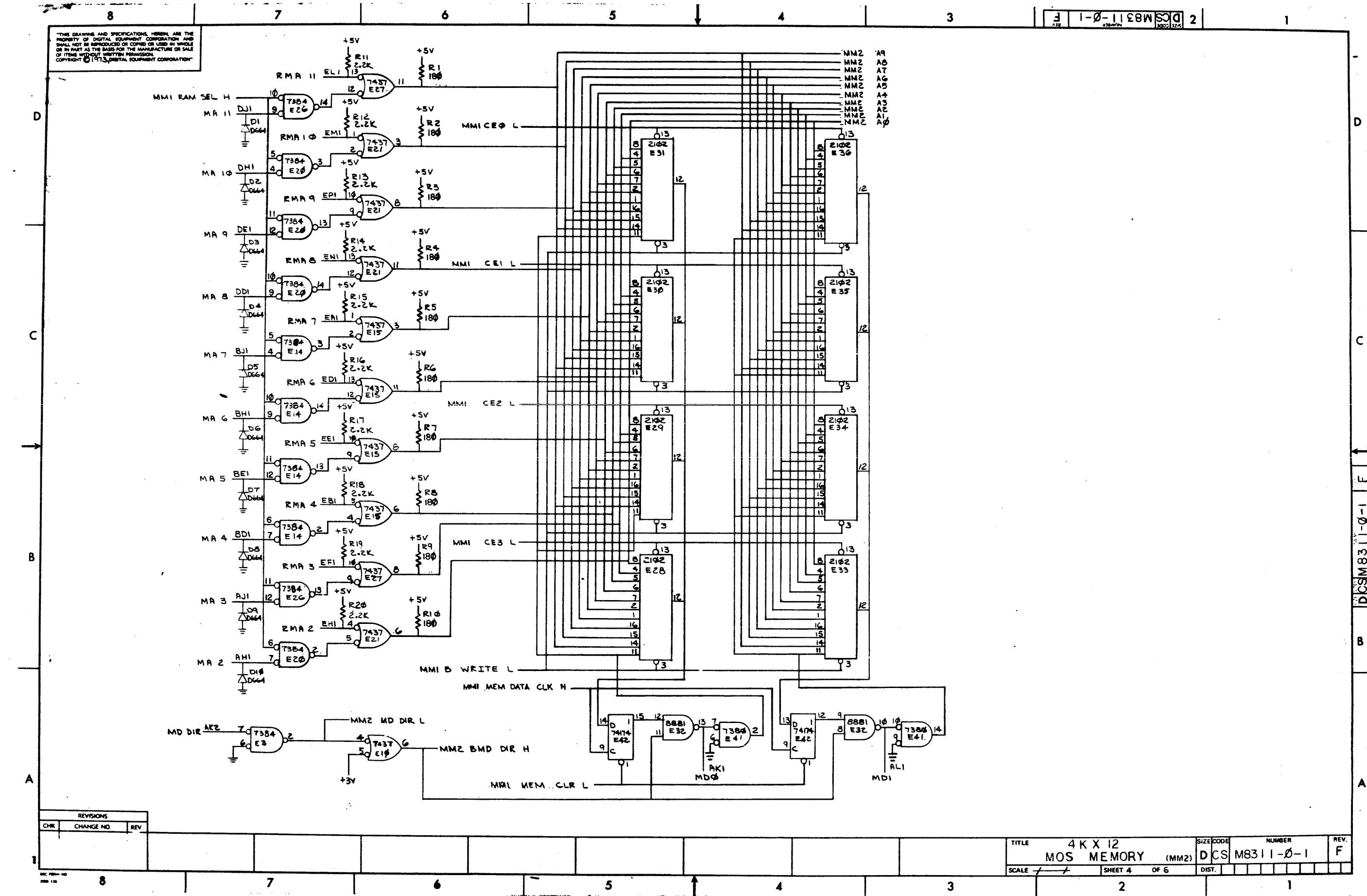
4

3

2

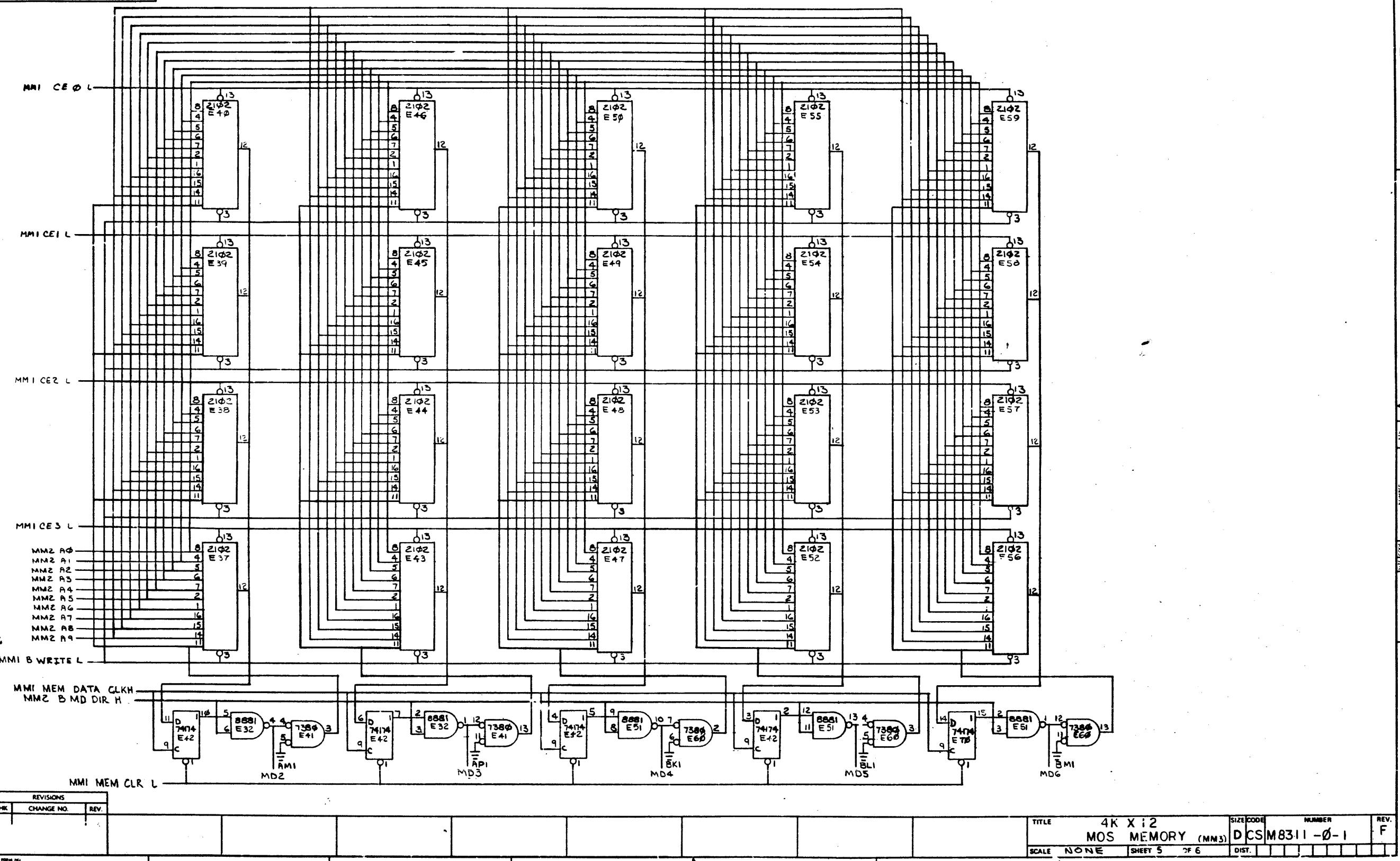
1





8
7
6
5
4
3
2
1

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