CONTACT

Y VENKATA SIVA

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OBJECTIVE

Secure a responsible career opportunity to fully utilize my training and skills, while making a significant contribution to the success of the company.

EDUCATION

- o 10th Sri Chaitanya high school 88 2014
- o Intermediate Narayana jr college 90.2 2016
- Btech pvkk institute of technology 65 2020

SKILLS

Full stack java c language HTML

PROJECTS

Automatic drain cleaning system

The project is about replacing the manual work in drainage cleaning by automated system

 Design of an efficient multilayer arithametic logical unit of quatum-dot cellular autometa

Quantum-dot cellular Autometa(QCA) is a new technology that due to making significant improvements in the design of electronic circuits can be considered as an appropriate alternative to CMOS technology. The Arithmetic Logic Unit(ALU) is a fundamental component of the Central Processing Unit(CPU) to carry out the arithmetic and logical operations that multiplexer and full adder play on important role on its operations. Based on the extracted features of the arithmetic operations of the ALU, we propose a new special low-complexity QCA 4:1 multiplexer, which is application-specific to the proposed ALU. Moreover, a new QCAfull adder is proposed based on cell interaction

ACHIEVEMENTS & AWARDS

 1.Completed PLC certification in JNTUA. 2.In arts and crafts got first prize held in our college.

LANGUAGE

English - Telugu - Hindi

PERSONAL DETAILS

• Date of Birth : 21/08/1998

Marital Status : SingleNationality : Indian

• Father's name: Y Ramachandra

Adress
13/349 near Hutch tower vempalli, kadapa(D), Andhra Pradesh