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Netlist\_Schematic1\_2023-12-15.tel contains the following texts

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$PACKAGES
'BGA-121_L10.0-W10.0-R11-C11-P0.80-TL' ! 'CYUSB3014-BZXI_C57294' !
; U1
'CAP-TH_BD10.0-P5.00-D1.0-FD' ! 400BXC10MEFC10X16 ! ; C40 C50
'CON_TP_TH_V_RED_KEC_5000' ! 'Test Point' ! ; TP1 TP2 TP3 TP4 TP5
TP6 TP7 TP8
'CONN-TH_3P-P5.00_WJ500V-5.08-3P' ! 'WJ500V-5.08-3P' ! ; CON1 CON2
CON3
'HC-49S_L11.4-W4.8-LS12.3' ! '9C-19.200MAAJ-T' ! ; Y1
'IND-SMD_L3.0-W3.0_LVS303015H-2R2M-N' ! LQH3NPN100MJRL ! ; L1 L2
'IND-SMD_L3.2-W2.5_82103C' ! LQH32CN100K23L ! ; L4
'IND-SMD_L4.0-W4.0' ! SWPA4030S100MT ! ; L3
'KEY-SMD_L6.0-W3.5-LS9.0' ! 'PTS635SL43SMTR LFS' ! ; SW1
'LED0805-R-RD_GREEN' ! SZYY0805YG ! ; D1 D2 D3
'LED1206-R-RD_GREEN' ! 'HSMG-C150' ! ; D7 D9 D11 D13
'SMA_L4.4-W2.6-LS5.0-RD' ! '3SMAJ5929B-TP' ! ; D4 D5
'SMB_L4.3-W3.6-LS5.4-RD' ! 'SZP6SMB6.8AT3G' ! ; D6 D8 D10
'SMB_L4.6-W3.6-LS5.3-RD' ! 'SMBJ3V3-E3/52' ! ; D12
'SOIC-8_L4.9-W3.9-P1.27-LS6.0-BL' ! 'M24M01-RMN6TP' ! ; U3
'SOT-9X3-3_L1.0-W0.8-P0.35-LS1.0-BR' ! TPD2EUSB30DRTR ! ; U5 U6 U7
'TEST-TH_BD2.54-P1.39' ! 5002 ! ; TP9 TP10 TP11 TP12 TP13 TP14
TP15 TP16 TP17 ,
        TP18 TP19 TP20 TP21
'VQFN-20_L3.5-W3.5-P0.50-BL-EP2.0' ! TPS7A8300RGRR ! ; U8 U11 U16
692221030100 ! '692221030100_' ! ; J1
AXK5S80047YG ! 'Connector_to TRX_Board' ! ; P1 P2
C0201 ! 'TMK063ABJ104KP-F' ! ; C41 C51
C0402 ! 'EMK105B7104KV-F' ! ; C1 C3 C6 C8 C10 C12 C14 C16 C17 C22
C23 C24 C25 ,
        C26 C28 C30 C32 C33 C34 C35 C36 C37 C38 C39 C43 C47 C54 C58
C60 C64 ,
        C65 C69 C72 C76 C88 C98 C99 C100 C101 C102 C103
C0402 ! 0402CG330J500NT ! ; C55 C68
C0402 ! C1005X7R1E103KT000F ! ; C81 C82 C83 C86 C92 C93
C0402 ! CL05B103K05NNNC ! ; C2 C9 C11 C13 C18 C19 C20 C21 C27
C0402 ! GRM1555C1H100JA01D ! ; C4 C5
C0402 ! GRM155R60J225ME15D ! ; C7
C0603 ! '0603YD105KAT2A_C597247' ! ; C59 C63 C70
C0603 ! GRM1885C1H151JA01D ! ; C52
C0603 ! GRM188C81C106MA73D ! ; C42 C44 C45 C46 C48 C49 C53 C56 C57
C61 C62 ,
        C66 C67 C71 C73 C74 C75 C77 C78 C79 C80 C84 C85 C87 C89 C90
C91 C94 ,
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FB13
L0805 ! MPZ2012S601AT000 ! ; FB1 FB2 FB3 FB4
R0402 ! 'AC0402FR-0714KL' ! ; R39 R45
R0402 ! 0402WGF2000TCE ! ; R15
R0402 ! ARG02BTC1002 ! ; R50 R51 R56
R0402 ! CRCW04020000Z0EDHP ! ; R10 R13 R34 R35 R42 R43 R48 R49 R53
R54 R55 ,
       R57
R0402 ! CRCW0402100KFKED ! ; R33 R38
R0402 ! CRCW040210K2FKED ! ; R1 R2 R3 R5 R6
R0402 ! CRCW04021K00FKEDC ! ; R41 R46 R47 R52
R0402 ! CRCW040230K1FKED ! ; R40 R44
R0402 ! CRCW04023K00FKED ! ; R37
R0402 ! CRCW04024K70FKED ! ; R4 R7 R8 R9 R11 R14 R16 R17 R18 R21
R22 R23 R24,
       R25 R26 R27 R28 R29 R30 R31 R32
R0402 ! CRCW04025K10FKED ! ; R36
R0402 ! CRCW04026K04FKED ! ; R12
R0402 ! ERA2AEB222X ! ; R19 R20
$A_PROPERTIES
$NETS
'$1I1617\$1N153386' ; R12.1 U1.C8
'$1I1617\$1N159795'; C35.1 FB5.2 J1.S1
'$1I1617\$1N160066' ; C4.2 U1.C6 Y1.2
'$1I1617\$1N160179'; C5.2 U1.C7 Y1.1
'$1I1617\$1N162130'; C3.1 U1.E10
'$1I1617\$1N162315' ; C39.2 J1.6 U6.1
'$1I1617\$1N162434' ; C37.1 J1.S2
'$1I1617\$1N164346' ; C6.1 C7.2 FB1.1 U1.A7
'$1I1617\$1N164644' ; C29.2 C30.1 C32.1 FB4.2 U1.A2
'$1I1617\$1N164773'; C27.2 C28.1 FB2.1
'$1I1617\$1N166183'; R15.1 U1.B3
'$1I1617\$1N23833' ; FB6.2 J1.1
'$1I1617\$1N27718' ; C38.2 J1.5 U6.2
'$1I1617\$1N6044' ; D1.1 R4.2
'$1I1617\$1N6223' ; D2.1 R8.2
'$1I1617\$1N6458' ; R10.2 R11.1
'$1I1617\$1N6675'; R13.2 R14.1
'$1I1617\$1N69121' ; R10.1 U1.D3
'$1I1617\$1N69182' ; R13.1 U1.C1
'$1I1617\$1N7120' ; D3.1 R17.2
'$1I1617\$1N7209' ; R7.2 U1.D7
'$1I1617\$1N7311'; R9.2 U1.D6
'$1I1617\$1N8038'; R16.2 U1.C9
'$1I1617\$1N8459'; R1.2 U1.E7
'$1I1617\$1N8480'; R6.2 U1.B11
'$1I1617\$1N8620'; R2.2 U1.C10
'$1I1617\$1N9003'; C31.2 FB3.2 U1.B5
'$1I1617\$1N93780'; R3.2 U1.F6
'$1I1617\$1N93784'; R5.2 U1.E8
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2/6

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'$1I1617\FX3_I2C_SCL' ; R19.1 R20.2 U1.D9 U3.6
'$1I1617\FX3_I2C_SDA' ; R20.1 U1.D10 U3.5
'$1I1617\FX3_PMODE0'; R23.2 R24.1 U1.G4
'$1I1617\FX3_PMODE1' ; R27.2 R28.1 U1.H4
'$1I1617\FX3_PMODE2' ; R31.2 R32.1 U1.L4
'$1I1617\FX3_RST' ; C33.2 R18.1 SW1.1 U1.C5
'$1I1617\SSRXM' ; J1.8 U1.A3 U7.2
\verb|'$1I1617\SSRXP' ; J1.9 U1.A4 U7.1|
'$1I1617\SSTXM' ; C38.1 U1.A6
'$1I1617\SSTXP'; C39.1 U1.A5
'$1I1617\U1_J11' ; C25.1 C26.1 U1.J11 U1.L7
'$1I1617\USB_5V' ; C34.1 FB6.1 U1.E11
'$1I1617\VCC_FX3_CVDDQ' ; R18.2 R22.2 R26.2 R30.2
'$1I2635\$1N115118'; FB8.1 R35.2
'$1I2635\$1N117214' ; C67.2 FB9.1 FB11.1 R43.2
'$1I2635\$1N122518' ; C55.2 C66.2 R39.2 R40.2 R43.1 U14.5
'$1I2635\$1N122534' ; C55.1 U14.4
'$1I2635\$1N122663' ; C61.2 C68.2 FB10.1 R42.2 R45.2
'$1I2635\$1N122666'; C68.1 R44.2 R45.1 U15.5
'$1I2635\$1N123006' ; C83.1 U16.13
'$1I2635\$1N15572' ; C56.2 R42.1 U15.3 U15.4
'$1I2635\$1N16990' ; CON3.1 L3.1
'$1I2635\$1N17064' ; CON3.3 L4.1
'$1I2635\$1N20248'; D9.2 R46.1
'$1I2635\$1N20412'; D7.1 R41.1
'$1I2635\$1N24825' ; C81.1 R50.2 U16.3
'$1I2635\$1N24920' ; C73.2 C81.2 R48.2 U16.1 U16.2 U16.19 U16.20
'$1I2635\$1N2542' ; C48.2 C52.2 R35.1 R36.2 U13.3 U13.4
'$1I2635\$1N27656' ; R50.1 U16.4
'$1I2635\$1N31885' ; C82.1 U11.3
'$1I2635\$1N33908' ; R51.1 U11.4
'$1I2635\$1N3467' ; C52.1 R36.1 R37.2 U13.5
'$1I2635\$1N35280'; D11.1 R47.1
'$1I2635\$1N4177'; CON1.1 L1.1
'$1I2635\$1N42079'; D13.1 R52.1
'$1I2635\$1N4302'; CON1.3 L2.1
'$1I2635\$1N49466' ; C74.2 C82.2 R49.2 R51.2 U11.1 U11.2 U11.19
U11.20
'$1I2635\$1N50632' ; C86.1 U11.13
'$1I2635\$1N51011' ; C93.1 U8.13
'$1I2635\$1N52931' ; C89.2 FB12.1 R54.2
'$1I2635\$1N54216' ; C92.1 U8.3
'$1I2635\$1N54358'; C92.2 FB12.2 FB13.2 R56.2 U8.1 U8.2 U8.19
U8.20
'$1I2635\$1N54501'; R56.1 U8.4
'$1I2635\$1N56466'; C95.2 FB13.1 R57.2
'$1I2635\$1N793'; FB7.1 R34.2
'$1I2635\$1N802'; C45.2 R34.1 U12.3 U12.4 U12.5
'$1I2635\$1N9090'; R39.1 U14.3
'$1I2635\RX_5V_INT' ; C42.2 C43.2 C44.2 C47.2 C53.2 C54.2 C57.2
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3/6

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'1.2V_DVDD' ; C46.2 FB7.2 P2.19 P2.21 TP18.1
'1.8DVDD'; P2.47 P2.49
'1.8V_AVDD_VCA' ; C69.2 C70.2 FB11.2 P1.59 P1.60 P1.61 P1.62 TP15.1
'1.8V_AVDD' ; C58.2 C59.2 FB9.2 P1.51 P1.52 P1.53 P1.54 P1.55 P1.56
P2.39 ,
        TP16.1
'1.8V DVDD FX3' ; C1.2 C2.1 C8.2 C9.1 C10.2 C11.1 C12.1 C13.2 C14.2
C15.2 ,
        C16.1 C17.1 C18.2 C19.2 C20.2 C21.2 C22.1 C23.1 C24.1 C36.1
C90.1 ,
        C91.1 FB2.2 R19.2 R24.2 R28.2 R32.2 R54.1 TP10.1 U1.B1
U1.B6 U1.B10 ,
        U1.C3 U1.C11 U1.E3 U1.E9 U1.F1 U1.F11 U1.H1 U1.H11 U1.L5
U1.L9 U3.8
'3.3V_AVDD' ; C49.2 FB8.2 P1.65 P1.66 P1.67 P1.68 TP13.1
'AVDD_IO' ; C63.2 C64.2 FB10.2 P2.68 P2.70 TP14.1
'CLK_DC1'; P2.43 TP19.1
'CLK_DC2'; P2.44 TP20.1
'CLK_DC3'; P2.40 TP21.1
'CLKIN_FX3'; P2.30 R7.1
'FPGA_2P5V' ; C84.1 C85.1 C87.1 C88.2 C94.1 CON2.3 D12.1 R52.2
TP4.1 U8.12 ,
       U8.14 U8.15 U8.16 U8.17 U11.12 U11.14 U11.15 U11.16 U11.17
U16.12 ,
       U16.14 U16.15 U16.16 U16.17
'FX3_A0' ; P2.7 U1.H5
'FX3_A1' ; P2.8 U1.J5
'FX3_CLK' ; P1.46 U1.J6
'FX3_CTL' ; P2.6 U1.L8
'FX3_FLAGA' ; P2.3 U1.G7
'FX3_FLAGB' ; P2.4 U1.G6
'FX3_GP00' ; P2.11 U1.K6
'FX3_GP01'; P2.13 U1.G5
'FX3_GPO2'; P2.12 U1.H6
'FX3_GPO3'; P2.14 U1.K5
'FX3_INITB'; P2.28 R11.2
'FX3_PKTEND' ; P2.15 U1.H8
'FX3_PRGB'; P2.26 R14.2
'FX3_SLCS' ; P2.2 U1.K8
'FX3_SLOE' ; P2.1 U1.J7
'FX3_SLRD' ; P2.5 U1.H7
'FX3_SLWR'; P2.16 U1.K7
'RX_5V'; C75.1 C76.2 CON2.1 D10.1 R47.2 R53.1 TP7.1
'TX -5V'; C71.1 C72.2 D8.2 L4.2 P2.67 P2.69 R46.2 TP6.1
'TX_5V' ; C62.1 C65.2 D6.1 L3.2 P2.57 P2.58 P2.59 P2.60 P2.61 P2.62
R41.2 ,
        R55.1 TP5.1
'UART_RX' ; P2.29 U1.D5
'UART_TX' ; P2.25 U1.C2
'VCC AUX 1V8' ; C96.1 C97.1 P2.20 P2.22 R57.1 TP17.1
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P1.18 P1.23 ,
        P1.24 P1.29 P1.30 P1.44 P1.47 P1.48 P2.9 P2.10 P2.17 P2.18
P2.23 P2.24 ,
        P2.27 P2.31 P2.32 P2.37 P2.38 P2.41 P2.42 P2.45 P2.46 P2.51
P2.52,
        P2.53 P2.54
D0 ; P1.1 U1.F9
D1 ; P1.2 U1.K11
D10 ; P1.19 U1.G9
D11 ; P1.20 U1.J8
D12 ; P1.21 U1.J9
D13 ; P1.22 U1.F7
D14 ; P1.25 U1.H9
D15 ; P1.26 U1.E5
D16 ; P1.27 U1.G8
D17 ; P1.28 U1.E4
D18 ; P1.31 U1.F5
D19 ; P1.32 U1.F3
D2 ; P1.5 U1.F10
D20 ; P1.33 U1.F4
D21 ; P1.34 U1.J4
D22 ; P1.35 U1.J3
D23 ; P1.36 U1.K2
D24 ; P1.37 U1.H3
D25 ; P1.38 U1.K1
D26 ; P1.39 U1.G3
D27 ; P1.40 U1.J2
D28 ; P1.41 U1.H2
D29 ; P1.42 U1.J1
D3 ; P1.6 U1.J10
D30 ; P1.43 U1.G2
D31 ; P1.45 U1.E1
D4 ; P1.9 U1.G10
D5 ; P1.10 U1.K10
D6 ; P1.13 U1.H10
D7 ; P1.14 U1.L10
D8 ; P1.15 U1.F8
D9 ; P1.16 U1.K9
GND ; C1.1 C2.2 C3.2 C4.1 C5.1 C6.2 C7.1 C8.1 C9.2 C10.1 C11.2
C12.2 C13.1 ,
        C14.1 C15.1 C16.2 C17.2 C18.1 C19.1 C20.1 C21.1 C22.2 C23.2
C24.2 ,
        C25.2 C26.2 C27.1 C28.2 C29.1 C30.2 C31.1 C32.2 C33.1 C34.2
C35.2 ,
        C36.2 C37.2 C40.1 C41.2 C42.1 C43.1 C44.1 C45.1 C46.1 C47.1
C48.1 ,
        C49.1 C50.1 C51.1 C53.1 C54.1 C56.1 C57.1 C58.1 C59.1 C60.1
C61.1 ,
        C62.2 C63.1 C64.1 C65.1 C66.1 C67.1 C69.1 C70.1 C71.2 C72.1
C73.1 ,
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D2.2 D3.2 ,
        D4.2 D5.1 D6.2 D7.2 D8.1 D9.1 D10.2 D11.2 D12.2 D13.2 FB5.1
FB14.1 ,
        FB15.1 FB16.1 FB17.1 FB18.1 FB19.1 J1.4 P1.49 P1.50 P1.57
P1.58 P1.63 ,
        P1.64 P1.69 P1.70 P1.71 P1.72 P1.73 P1.74 P2.55 P2.56 P2.63
P2.64,
        P2.65 P2.66 P2.71 P2.72 P2.73 P2.74 R1.1 R2.1 R3.1 R4.1
R5.1 R6.1 R8.1 ,
        R9.1 R12.2 R15.2 R16.1 R17.1 R21.1 R23.1 R25.1 R27.1 R29.1
R31.1 R33.1 ,
        R37.1 R38.1 R40.1 R44.1 SW1.2 TP3.1 TP8.1 U1.A1 U1.A8 U1.B7
U1.B8 ,
        U1.B9 U1.D1 U1.D8 U1.D11 U1.E2 U1.F2 U1.G1 U1.G11 U1.K3
U1.K4 U1.L1 ,
        U1.L2 U1.L3 U1.L6 U1.L11 U3.2 U3.3 U3.4 U3.7 U5.3 U6.3 U7.3
U8.7 U8.8 ,
        U8.10 U8.18 U8.21 U11.7 U11.8 U11.18 U11.21 U12.6 U12.9
U13.6 U13.9 ,
        U14.6 U14.9 U15.6 U15.9 U16.8 U16.9 U16.18 U16.21
HVM ; C50.2 C51.2 D5.2 L2.2 P2.79 P2.80 R38.2 TP2.1
HVP ; C40.2 C41.1 D4.1 L1.2 P1.79 P1.80 R33.2 TP1.1
$SCHEDULE
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