

## Circuit Analysis 1

Wire in a circuit allows any level of current to move without any voltage drop. Current is a flow rate of charge through an element. Think of it like water, charges being its molecules.

Voltage is an electric potential difference between two points. Imagine gravitational potential differences of an object, the object here being the charge.

3 basic component of a circuit is resistor, capacitor and inductor. Often called as RLC.

Resistor is an element whose voltage is proportional to current, also known as the ohm's law.

Capacitor is an element that stores electrical energy in an electrical field. It also opposes sudden change in voltage.

Inductor is an element that stores energy into a magnetic field, this allows the current to oppose sudden changes in current.

$$\text{ohm's law} \quad V = IR \quad I = C \frac{dV}{dt} \quad V = L \frac{di}{dt}$$

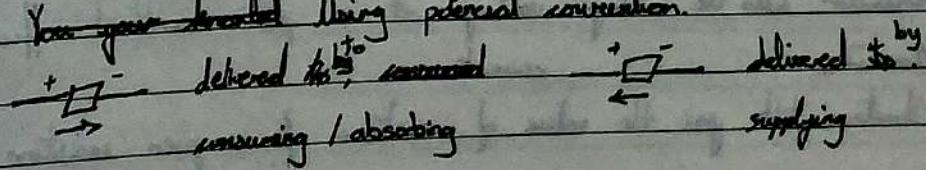
$$Q = CV$$

As with any other objects, the elements consumes or produce energy. The unit J, represents the energy delivered to 1C charge by a field with 1V voltage.

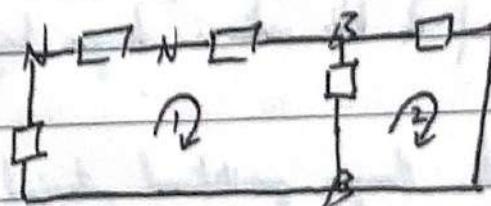
Power is the energy per time & thus the power of an element in circuit can be derived.

$$P = IV \quad \text{How do we know if power is consumed or produced?}$$

You can detect using potential convention.



Geometry of circuit consists of:



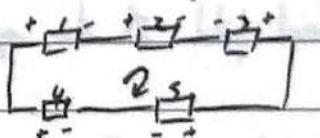
Node, point of connection.

Branch, a place connecting two nodes.

Loop, node  $\Rightarrow$  branches  $\Rightarrow$  original node.

Two main laws governing circuit analysis are:

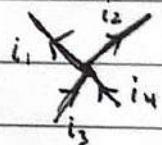
Kirchhoff's voltage law, where sum of voltage in a given loop is zero.



$$-V_1 - V_2 + V_3 - V_4 + V_5 = 0 \quad \text{note your polarity.}$$

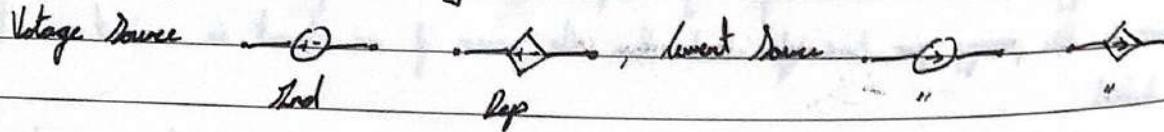
use potential convention if possible

Kirchhoff's current law, sum of current in & out is zero, in a given node / branch.



$$-i_1 - i_2 + i_3 + i_4 = 0 \quad \text{* in} \rightarrow \text{pos out} \rightarrow \text{neg.}$$

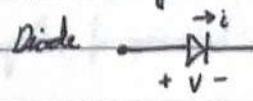
Three elements of the circuit, mainly sources:



Voltmeter tells the voltage voltage at a place without affecting its current.  
Ammeter current

Ohm's law tells you the value of resistance of a given resistor.

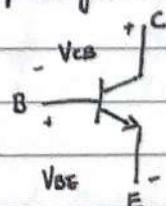
Simple overview of electronics:



is a device that allows the current to flow only in one direction. When applied small  $+V$ , current is allowed to pass, however, when  $-V$  is applied, no current is allowed to pass.  $V \approx 0 \quad i > 0$

$$V < 0 \quad i = 0$$

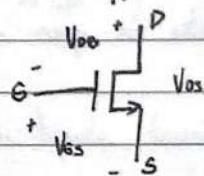
Bipolar Junction Transistor is a device that in which current through collector ( $C$ ) & emitter ( $E$ ) is controlled by the base ( $B$ ).



$$i_C = \beta i_B \quad i_E = i_C + i_B$$

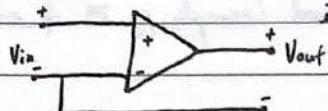
$\hookrightarrow$  current gain

Field-Effect Transistor is a device in which the current flowing in Source ( $S$ ) & Drain ( $D$ ) is controlled by the voltage applied in Gate ( $G$ )



$$i_D \rightarrow \text{depends on } V_{GS} \quad i_S = i_G + i_D$$

Operational Amplifier is a device that amplifies the difference in voltage between its two terminals.



## Introduction to 1<sup>st</sup>-order circuit:

1<sup>st</sup> order time dependant circuit is a circuit in which the behavior of voltage and current is described by 1<sup>st</sup> order linear ODE.

As a systematic approach to 1<sup>st</sup> order circuit

1. KVL KCL element, before switching. Get desired variable.
2. Determine how the variable of interest behaves  $\Rightarrow$  with switching action.
3. KVL KCL element, after switching. (a new circuit)
4. Eliminate variable to get 1<sup>st</sup>-order ODE
5. Use the info. on step 2 to solve the ODE.

Behaviors of C & L

$\rightarrow$   $i = \frac{dV}{dt}$  During switching, capacitor will resist abrupt changes in voltage.

In DC-ss, capacitor will act like an open circuit.

$\rightarrow$   $V = L \frac{di}{dt}$  During switching, inductor will resist abrupt change in current.

In DC-ss, inductor  $\Rightarrow$  will act like a short circuit, like on a wire.

Growth & decay of both components can be derived (example pg 25 of e-note).

Plan

$\tau$  is a time constant which the exponentially decaying function dropped to  $\approx 0.36$  to its original value.

$\forall t > 0$  the general equation  $V(t) = (V_{\text{cap}} - V_{\text{final}})e^{-t/\tau} + V_{\text{final}}$  is valid.

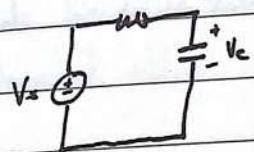
General solution for  $V_c(t) = Ae^{-t/\tau} + B$  where  $\tau = RC$ .

$$V_c(t) = Ae^{-t/(RC)} + B \text{ where } \tau = \frac{R}{C}$$

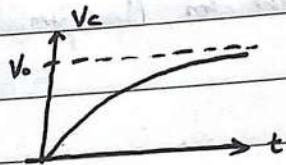
$\therefore V(t) = Ae^{-t/\tau} + B$  is the general solution for all first order circuit components.

Unit Response

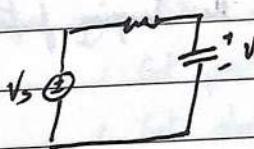
Step response is the response of a circuit to a step function.



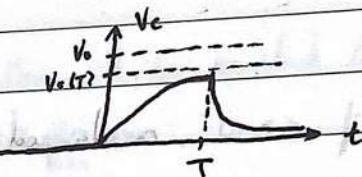
$$V_s = V_o u(t)$$



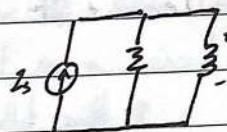
Pulse response is the response of a circuit to a pulse function.



$$V_s = V_o [u(t) - u(t-T)]$$



Numerical Approach to TD circuit (finite difference time domain)



$$I_s = I_o u(t)$$

$$V_L = V_R = R_i i = R_o o$$

$$V'_L = -\frac{1}{L/R} V_L$$

$$V'_L + \frac{1}{L/R} V_L = 0$$

using derivation

$$V'_L \approx \frac{\Delta V_L}{\Delta t} = V_L(t + \Delta t) - V_L(t) = -\frac{1}{L/R} V_L(t)$$

$$V_L(t + \Delta t) = V_L(t) - \frac{\Delta t}{L/R} V_L(t)$$

Introduction to 2nd Order circuit:

come about when

2nd order time dependent circuit, unlike the first, consists of both component P.C & L. The combination, then, provides a oscillating factor that exhibits a damping property.

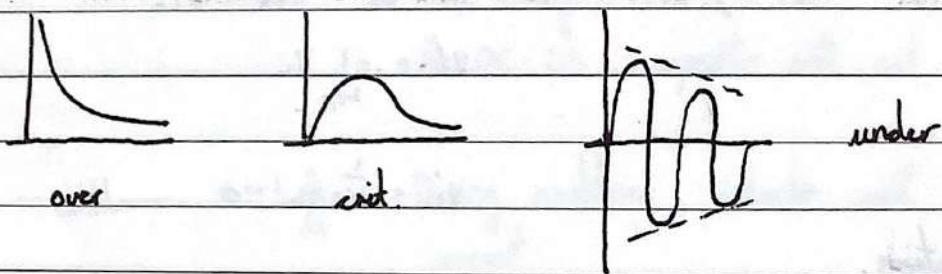
Damping

For the equation  $\frac{d^2x}{dt^2} + 2\alpha \frac{dx}{dt} + \omega^2 x = f$  where  $\omega$  is the natural frequency  
 $\alpha$  is the damping factor

if  $\alpha > \omega$ , over damped  $x(t) = m_1 e^{s_1 t} + m_2 e^{s_2 t} + \frac{f}{\omega^2}$   
 $s_{1,2} = -\alpha \pm \sqrt{\alpha^2 - \omega^2}$

if  $\alpha = \omega$ , critically damped  $x(t) = (m_1 t + m_2) e^{-\alpha t} + \frac{f}{\omega^2}$

if  $\alpha < \omega$ , under damped  $x(t) = (m_1 \cos(\omega_d t) + m_2 \sin(\omega_d t)) e^{-\alpha t} + \frac{f}{\omega^2}$



Linear Circuit:

Linear circuit is a circuit where all the components hold linear relationships.

All voltage and current will have linear dependence on all independent sources.

$$V_j = d_{1j}S_1 + d_{2j}S_2 \dots + d_{nj}S_n$$

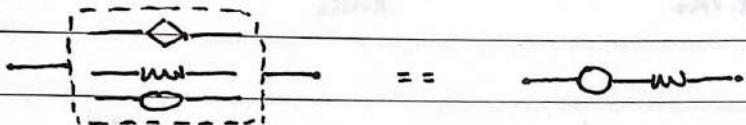
Superposition:

Superposition in a circuit describes that in a linear circuit you are able compute the sum of all independent source by computing individual parts and adding them.

Remember setting VS to zero  $\rightarrow$  short, Using this we are able to compute individual CS to zero  $\rightarrow$  open sources V & I.

### Thevenin Equivalent :

This theorem says that if we have a circuit comprising of  $R$ , independent & dependent sources, in a linear relationship, we can represent any two point in a circuit by an independent voltage source and in series with  $R$ .

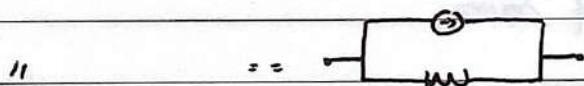


Steps you take

1. Get your open circuit voltage ( $V_{TH}$ ). This will be equivalent to  $V_{TH}$ .
2. Get short circuit current ( $I_{SC}$ ). Note  $R_{TH} = \frac{V_{TH}}{I_{SC}}$ .

### Norton Equivalent :

Under the same assumption & with Thevenin, this theorem suggests that any two point in a circuit can be represented by independent current source in series parallel with  $R$ .



Steps also are identical.

No inter-change between the two representation, use source transformation.

$$V_{TH} = R_N I_{IN} \rightarrow R_N = R_{TH} \quad \therefore V_{TH} = R_{TH} I_{SC}$$

$$\frac{V_{TH}}{R_{TH}} = I_{IN} \quad = R_N I_{IN}$$

Equivalent resistance is similar to Thevenin & Norton equivalent.  $R_{TH}$ .

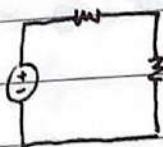
When ~~there's~~ <sup>not</sup> no independent source, apply a voltage or current and compute the rest, divide the two to get  $R$ .

$$R_S = R_1 + R_2 \dots + R_n \quad (\text{same current})$$

$$R_{II} = \left( \frac{1}{R_1} + \frac{1}{R_2} \dots + \frac{1}{R_n} \right)^{-1} \quad (\text{same voltage})$$

Circuit Division :

Voltage division allow a analysis of two R's in series.

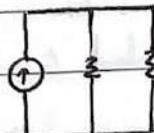


$$i = \frac{V}{R_1 + R_2}$$

$$V_1 = R_1 i = \frac{R_1}{R_1 + R_2} V$$

$$V_2 = R_2 i = \frac{R_2}{R_1 + R_2} V$$

Current division allow a analysis of two R's in parallel.



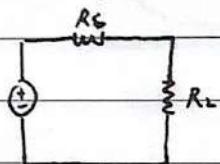
$$V = \frac{I}{\frac{1}{R_1} + \frac{1}{R_2}}$$

$$i_1 = \frac{V}{R_1} = \frac{R_2}{R_1 + R_2} I$$

$$i_2 = \frac{V}{R_2} = \frac{R_1}{R_1 + R_2} I$$

Maximum Power :

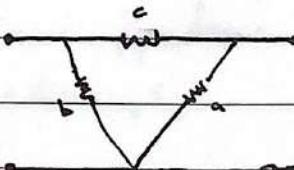
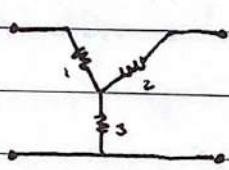
Maximum power theorem states that to draw a maximum power, resistance of load must equal resistance of source.



$$P_{max} \rightarrow R_s = R_L$$

$$P_{max} = \frac{V_{TH}^2}{4R_{TH}}$$

Wye - Delta Transformation :



$$R_1 = \frac{R_a R_c}{R_a + R_b + R_c}$$

$$R_a = \frac{R_1 R_2 + R_2 R_3 + R_3 R_1}{R_1}$$

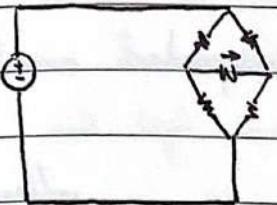
$$R_2 = \frac{R_a R_b}{R_a + R_b + R_c}$$

$$R_b = \frac{R_1 R_2 + R_2 R_3 + R_3 R_1}{R_2}$$

$$R_3 = \frac{R_a R_b}{R_a + R_b + R_c}$$

$$R_c = \frac{R_1 R_2 + R_2 R_3 + R_3 R_1}{R_3}$$

## The Wheatstone Bridge :

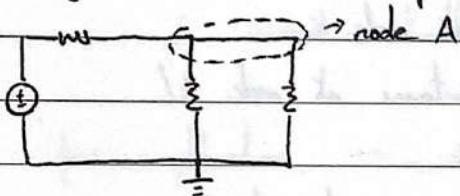


current through the middle is 0.

## Nodal Analysis :

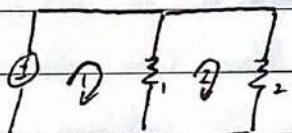
Node voltage is the voltage difference between a given node and a fixed reference node.

Referring any one of the nodes as a reference, with nodal voltages, we can compute any information in a given circuit.



## Mesh Analysis :

Use the understanding that in a given loop, <sup>Voltage</sup> current must add up. And the current within two loops through a element ~~must~~ co-exists in both must equal zero.



$$i_1 = I_1 - I_2$$

$$i_2 = I_2$$

$$N_1: -V + V_1 = 0$$

$$N_2: -V_1 + V_2 = 0$$

$$V_1 = I_2 R_1$$

$$V_2 = I_2 R_2$$

## Super-position in larger circuits :

To find  $V_{oc}$ , solve  $V$  by ~~to~~ after killing independent voltage source and add the  $V$  you get killing the independent current source.

Also, utilize source transformations in getting the equivalent circuits.

Mesh Analysis Inspection:

Analysis Inspection:

Nodal inspection can be made when all sources in a circuit is independent current source.

Mesh inspection

"

voltage

source.

$G_{11} \dots G_{1N}$	$V_1$	$I_1$
$\vdots$	$\vdots$	$\vdots$
$G_{NN} \dots G_{NN}$	$V_N$	$I_N$

Nodal -  $G_{kk}$ , sum of conductance ( $\frac{1}{R}$ ) at node k.

$G_{kl}$ , neg. of sum of the conductance at node kl.

$V_k$ , unknown voltage at node k.

$I_k$ , sum of indep. current source at node k.

Mesh -  $R_{kk}$ , " resistance " mesh k

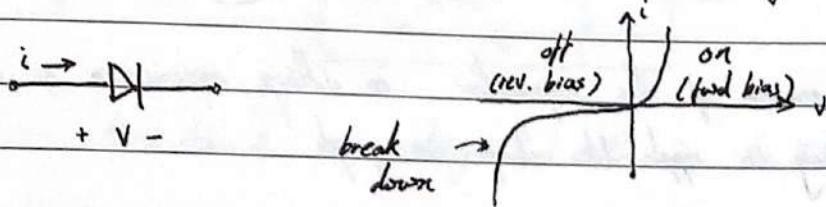
$G_{kl}$ , " resistance " mesh kl

$I_k$ , " current " mesh k

$V_k$ , " voltage " mesh k,

## Introduction to Electronics :

Diodes are elements in which at a positive input voltage, it behaves with a low resistance allowing the current to go through and at high ~~negative~~<sup>neg. voltage</sup>, it behaves with high resistive resistance, allowing no current to go through.



$$i = I_s (e^{\frac{V}{nV_{TH}}} - 1)$$

$n$ , ideality factor

~~$V_{TH}$~~ ,  ~~$T$~~   $V_{TH} = kT$ , the Boltzmann constant · Absolute temp.

$I_s$ , reverse saturation current.

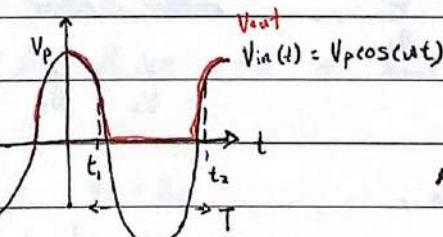
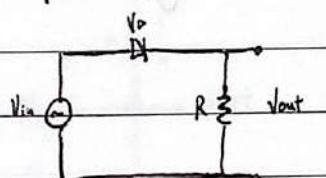
few simplifications,  $\rightarrow$   $\frac{V_0}{V_0 + i} \approx 1$

- assume reverse bias current is 0. And 0 until pos. voltage reaches  $\sim 0.7V$  of threshold value. Beyond that, a linear increase.
- neglect resistance at fwd bias.
- turn-on voltage is 0.

How do we know in a given circuit that a diode is fwd or rev?

We can't until we try every possibilities and find the right one with computing  $V$ 's on an assumptions.

Rectification,



\*  $V_{out}$  has pos. avg.

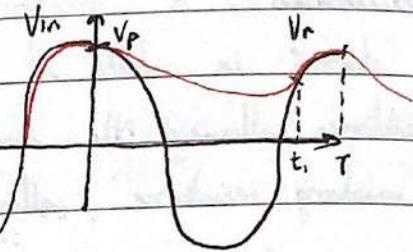
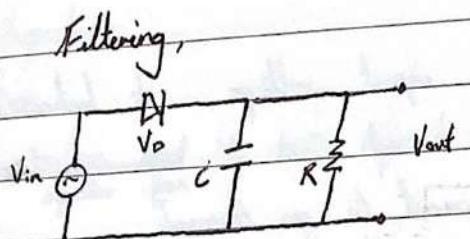
$$V_{in} = V_{out} + V_{Dout}$$

$$V_{in} > 0, V_D = 0$$

$$V_{in} < 0, V_{Dout} = 0$$

$$T = \frac{1}{f}$$

$$\omega = 2\pi f$$



Due to the presence of the capacitor, the voltage experiences an exponential decay, creating a ripple-like shape in graph.

when  $V_r \ll V_p$ ,

$$(T \gg RC) \quad V_r \approx V_p(1 - e^{-\frac{t}{RC}})$$

$T \ll RC$ ,

$$V_r \approx \frac{T}{RC} V_p$$

$$V_{avg} = V_p - \frac{T}{2RC} V_p \quad i_{avg} = \frac{V_{avg}}{R}$$

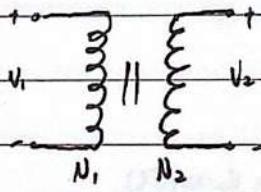
The conduction interval  $T - t_1 = at$ ?

$$V_p(\cos(\omega_0 t)) = V_p - V_r \quad \cos(\omega_0 t) \approx 1 \approx 1 - \frac{1}{2}(\omega_0 t)^2$$

$$V_p(1 - \frac{1}{2}(\omega_0 t)^2) \approx V_p - V_r$$

$$at = \frac{1}{2n} \left( \frac{2T V_p}{RC} \frac{1}{V_p} \right)^{\frac{1}{2}}$$

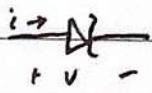
Transformer,



Device that transfers electric energy from one circuit to another, either increasing or decreasing the circuit voltage

$$\frac{V_1}{V_2} = \frac{N_1}{N_2}$$

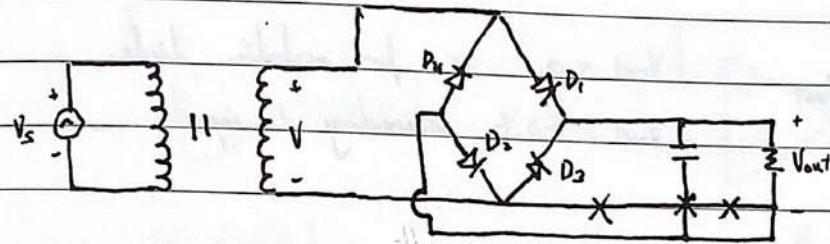
Zener Diode,



Allows forward flow in a similar manner with ideal diode but also permit reverse flow once above a value known as breakdown voltage, where the diode provide a constant output voltage regardless on the changes in the reverse input.

$$V_d = V_{D0} + r_D I_d \quad \text{At reverse } V_d = 8V \quad r_D = 20\Omega \quad \text{for } 1mA$$

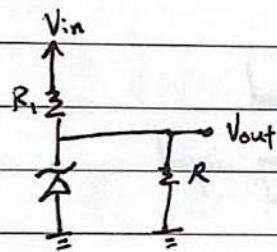
## Rect Rectification, Filtering & Transforming:



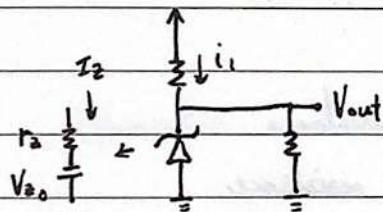
$V > 0$ ,  $D_1$  &  $D_2$  are on making  $V_{out} = V$  as  $V_{D1}$  &  $V_{D2} = 0$ .

$V < 0$ ,  $D_2$  &  $D_4$  are on making  $V_{out} = -V$  as  $V_{D3}$  &  $V_{D4} = 0$

### Regulation:



Want to deliver a certain voltage, with  $V_{in}$  being a sinusoidal input. This means we need to know what  $R_1$  should be.

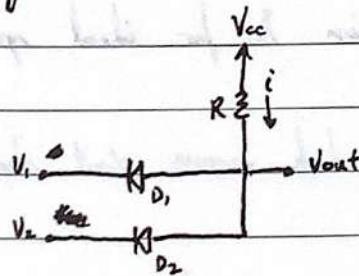


We must ensure  $I_{D2K} < I_{D2} < I_{D2max}$ .  
for  $I_{D2K} < I_{D2}$ ,

compute i, when  $V_{in}$  is at min and get  $R_{max}$ .  
for  $I_{D2} < I_{D2max}$ ,

compute i, when  $V_{in}$  is at max and get  $R_{min}$ .

### Logic Gates:



If  $V_1 = V_{cc}$   $V_2 = V_{cc}$ ,

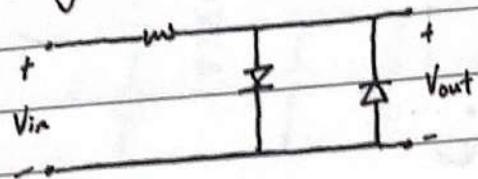
$D_1$  &  $D_2$  off  $\Rightarrow i = 0 \Rightarrow V_{out} = V_{cc}$

If  $V_1 = 0$   $V_2 = V_{cc}$

$D_1$  on  $\Rightarrow V_1 = V_{out} \Rightarrow V_{out} = 0$

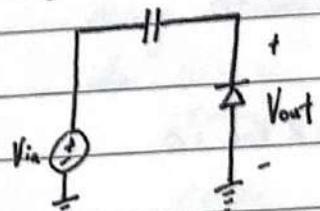
$\therefore$  AND gate.

Limiting:



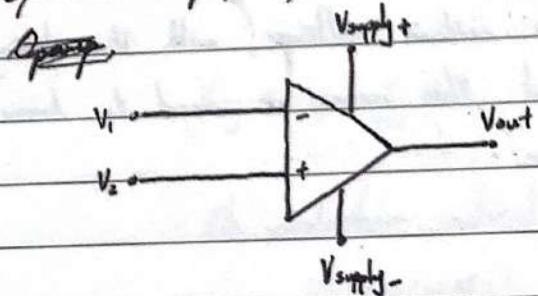
$V_{out} = 0$  or for realistic diode,  
 $V_{out} = \pm 0.7$  according to input.

Clamping:

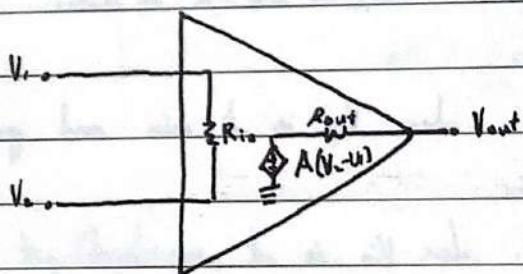


adding DC value to AC input  
(shifting waves in y-axis)

Operational amplifier,



$V_1$  - Inverting input  
 $V_2$  - Non-Inverting input



$R_i$  - Input resistance  
 $R_{out}$  - Output resistance  
 $A$  - Open loop gain

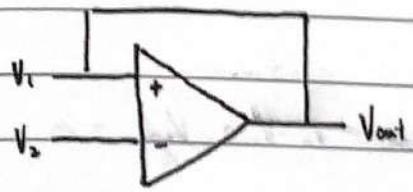
Operational amplifier amplifies voltages.

We want high  $R_i$  & low  $R_{out}$  for optimal amplification. So for ideal opamp,

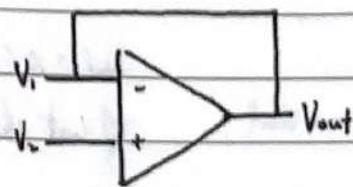
$$R_i = \infty, R_{out} = 0, A = \infty$$

We also want  $V_{out}$  stay within linear region, which means  $V_{out}$  should be bounded to  $V_{supply}$ 's.

positive & negative feedback :



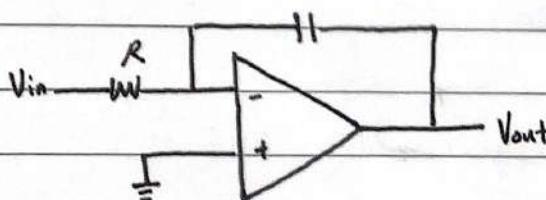
pos feedback



neg feedback ( $V_2 = V_-$ )

Other applications of opamps :

Integrator



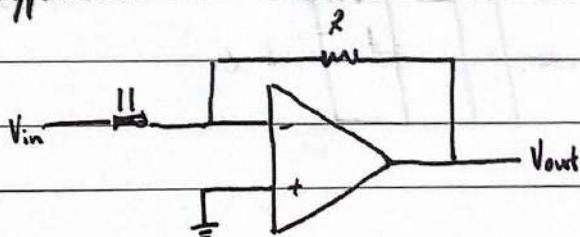
$$i_c = C \frac{dV_c}{dt} \quad i = \frac{V_{in}}{R}$$

$$V_c = \int_0^{V_c} dV_c = \frac{1}{C} \int_0^t i dt$$

↳ initially unchanged.

$$V_{out} = -\frac{1}{RC} \int_0^t V_{in} dt$$

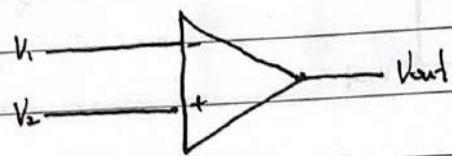
Differentiation :



$$i_c = C \frac{dV_c}{dt} = C \frac{dV_{in}}{dt}$$

$$V_{out} = -RC \frac{dV_{in}}{dt}$$

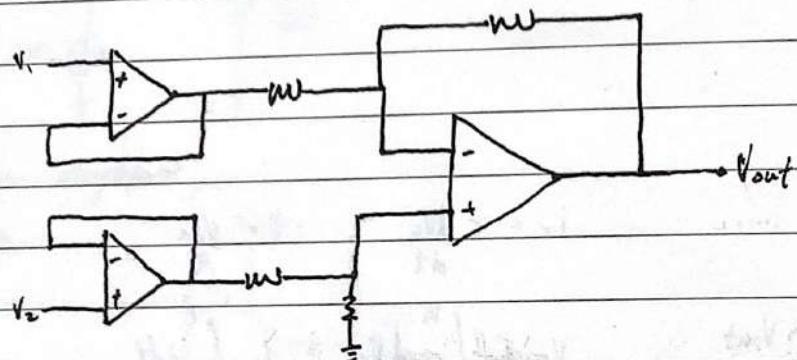
Ideal Op-Amp :



$$V_{out} = A(V_2 - V_1)$$

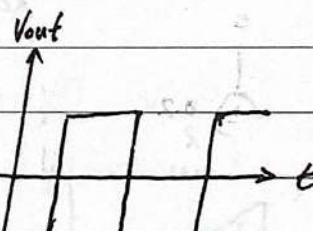
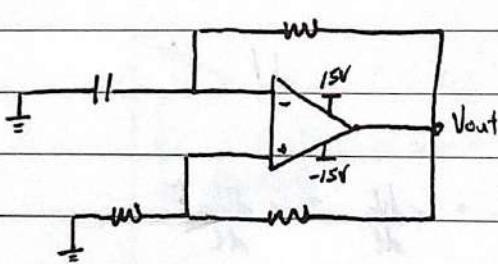
If at linear region  $V_2 - V_1 = \frac{V_{out}}{A} = 0$

Instrumental Amp :

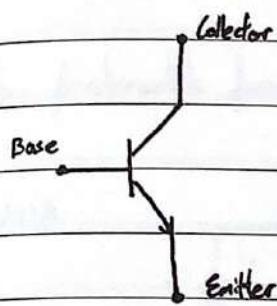


Cascading op-amp circuits.

Oscillator :



## Bipolar Junction Transistor



$$i_E = i_C + i_B$$

$$i_C = \beta i_B$$

$$= \alpha i_E$$

$$\alpha = \frac{\beta}{\beta + 1}$$

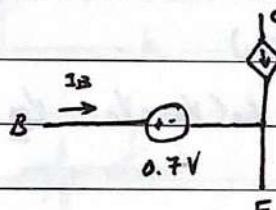
$$i_C \approx I_{SCE} \quad (\frac{V_{BE}}{V_T})$$

$\hookrightarrow$  saturation current

3 models to represent BJT depending on  $I_B$  &  $V_{CE}$ .

Steps:

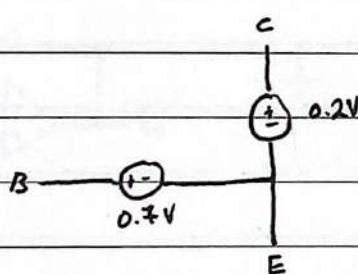
represent BJT as a linear model



if  $I_B < 0$ , cutoff model

if  $I_B > 0$

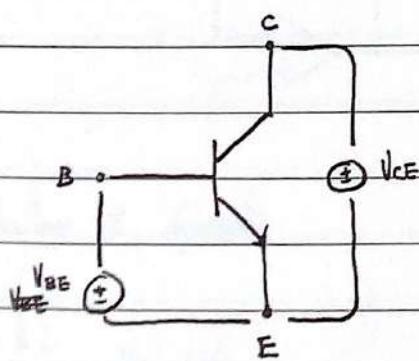
test for saturation



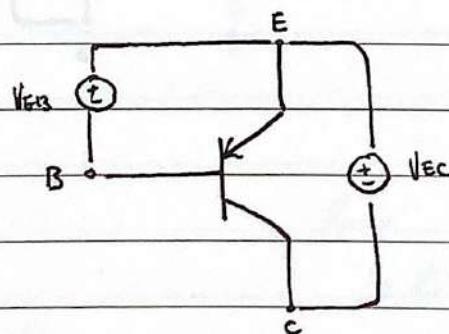
if  $V_{CE} \leq 0.2$ , saturation model

if  $V_{CE} > 0.2$ , linear model

PNP & NPN

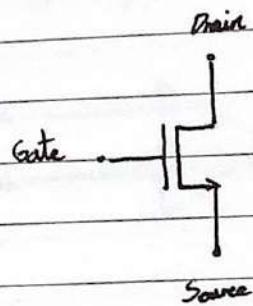


NPN



PNP

# Metal Oxide Semiconductor Field Effect Transistor



$$k_n = \frac{w}{l} \mu C_{ox}$$

width      oxide cap per unit area.  
channel length      mobility

n-channel

3 Ways to represent MOSFET  $V_{DS}$  &  $V_{GS}$

If  $V_{DS} < V_t$ , cut off so  $I_D = 0A$

If  $V_{DS} > V_t$ , compute  $V_{ov} = V_{DS} - V_t$

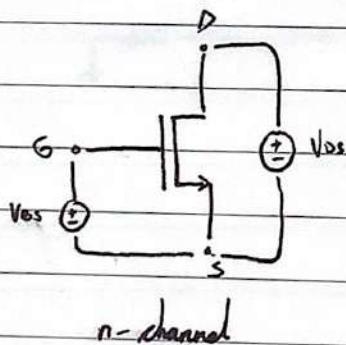
If  $V_{DS} > V_{ov}$ , triode (variable resistor)

$$g_{DS} = k_n \left( V_{DS} - V_t - \frac{V_{DS}}{2} \right), I_D = k_n \left( V_{DS} - V_t - \frac{V_{DS}}{2} \right) V_{DS}$$

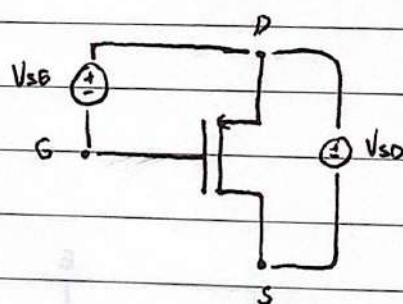
If  $V_{DS} \approx V_{ov}$ , saturation (linear)

$$I_D = \frac{1}{2} k_n (V_{DS} - V_t)^2$$

n & p channels



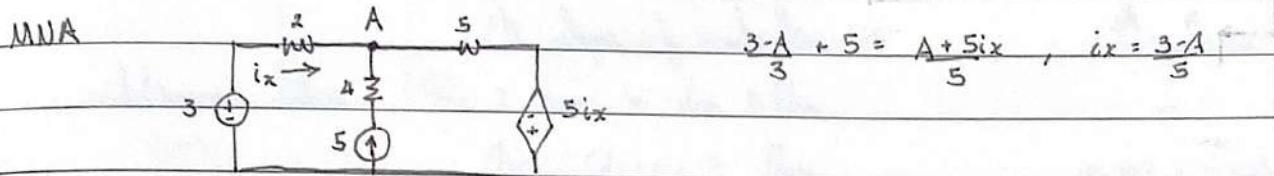
n-channel



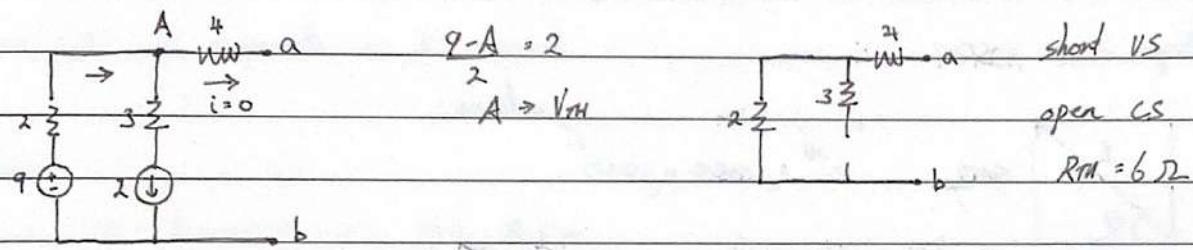
$V_{SS} - V_{tP}$  shift to this.

## Circuit Analysis 21

Few fundamentals from the previous :

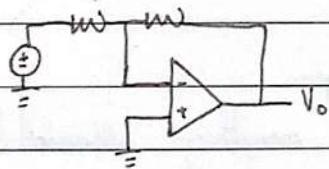


Therein

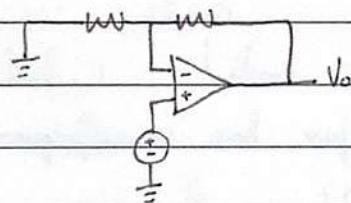


Op-amps (basic configuration)

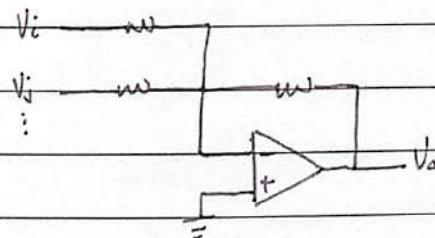
Inverting



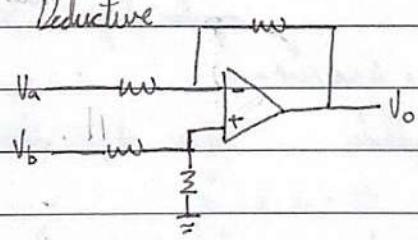
Non-inverting



Additive



Deductive



Inductors & Capacitors

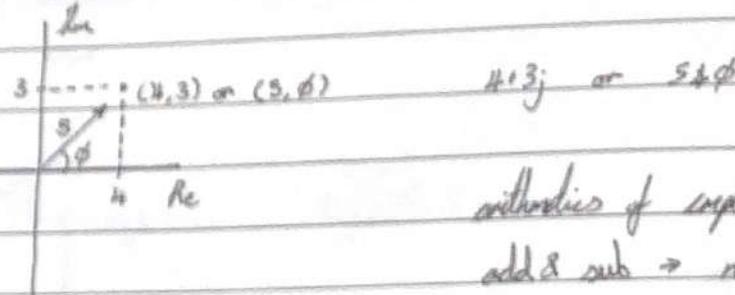
$$3L \quad V_L = L \frac{di}{dt}$$

$$\frac{1}{C} \quad I_C = C \frac{dv}{dt}$$

Power

$$P = IV$$

## Complex Numbers :



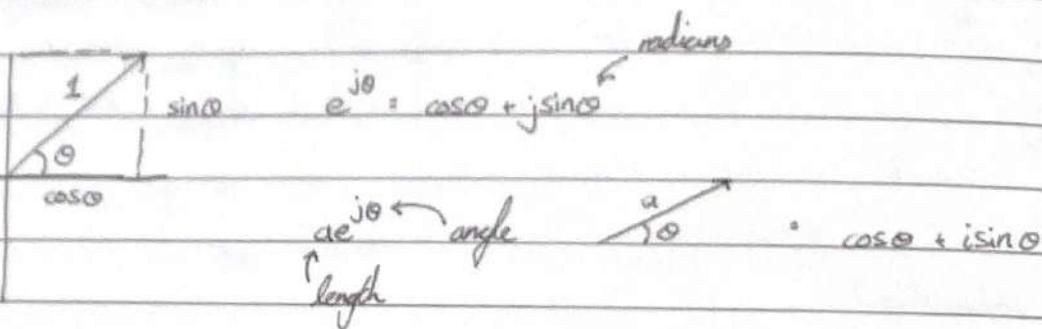
arithmetics of complex #'s

add & sub  $\rightarrow$  real & imaginary, vector representation

$$\text{mult.} \rightarrow (a+jb)(c+jd)$$

$$\text{div.} \rightarrow \frac{a+jb}{c+jd} = \frac{(a+jb)(c-jd)}{(c+jd)(c-jd)}$$

Albert's #



Steady State :

DC, long-time (5z)  $\rightarrow$  everything is constant  $\rightarrow$  1 short C open

AC, "  $\rightarrow$  sinusoidal func. have same frequency  $\rightarrow$  everything sinusoidal.  
LRC is represented by phasor (2)

The sine function,  $y = A \sin(\omega t + \phi)$   
 amplitude  $\leftarrow$  angular freq. (rad/s)  
 phase shift (radians)

Staircase idea (of add & sub sinusoidal functions)

$$\underbrace{A \cos(\omega t + \alpha) + B \sin(\omega t + \beta)}_{\sim} = C \cos(\omega t + \delta)$$

$$\text{Adding } \operatorname{Re}(Ae^{j(\omega t + \alpha)}) + \operatorname{Re}(Be^{j(\omega t + \beta)}) = \operatorname{Re}(Ce^{j(\omega t + \delta)})$$

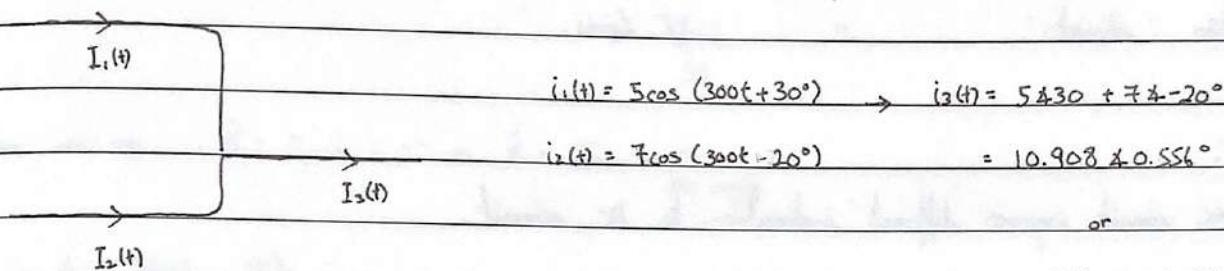
$$Ae^{j(\omega t + \alpha)} + Be^{j(\omega t + \beta)} = Ce^{j(\omega t + \delta)}$$

$$e^{j\omega t} (Ae^{j\alpha} + Be^{j\beta}) = e^{j\omega t} (Ce^{j\delta}) \leftarrow \text{complex # representation of sinusoidal Phasors.}$$

$$\text{or } A \cos \alpha + B \sin \beta = C \cos \delta$$

## KVL & KCL

Just like in DC circuit, we can use the circuit analysis skill like MNA.



Impedance :

Resistance ( $\Omega$ ) of components in AC circuit.

to compute instantaneous  $i_3$ .

Derivation

$$\text{say } i(t) = A \cos(\omega t + \alpha) \leftrightarrow A \angle \alpha$$

$$i'(t) = -\omega A \sin(\omega t + \alpha) \quad \int i(t) = \frac{A}{\omega} \cos(\omega t + \alpha - 90^\circ) \rightarrow \frac{A}{\omega} \angle -90^\circ$$

$$= \omega A \sin(\omega t + \alpha + 90^\circ) \rightarrow \omega A \angle 90^\circ$$

or

$$j\omega(A \angle \alpha)$$

$$\frac{A \angle \alpha}{j\omega}$$

Induction

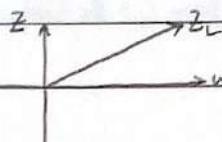
$$V = L \frac{di}{dt}$$

$$= L j\omega (A \angle \alpha)$$

$$\frac{\bar{V}_L}{V} = j\omega L \frac{\bar{I}}{A}$$

$$Z_L = R + jX_L = \omega L$$

impedance reactance



Capacitor

$$i_C = C \frac{dV}{dt}$$

$$= C j\omega (A \angle \alpha)$$

$$= j\omega C \bar{V}_C$$

$$\bar{V}_C = \frac{1}{j\omega C} i_C = -j \frac{1}{\omega C} i_C$$

$$Z_C = R + jX_C = \frac{1}{\omega C}$$

Resistor

$$\bar{V}_R = R \bar{I}_R$$

Ohm's Law

$$\bar{V} = \bar{Z} \bar{I}, \text{ where } Z_L = j\omega L \quad Z_C = -j \frac{1}{\omega C} \quad Z_R = R$$

Phase Implication of Ohm's Law

Inductor 'stretches' current phasor by  $\omega L$  times.

Capacitor 'shrink's' "  $\omega C$  times.

Power in AC :

Power for AC circuit requires different implication to DC circuit.

DC Review

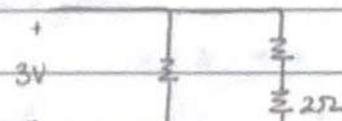
$$P = VI$$

Power in Resistor (only resistor!)

$$P_R = I^2 R = \frac{V^2}{R}$$



$$P_R = 2(3)^2 \text{ as } I_{2\Omega} = 3A$$



$$P_R = \frac{3^2}{2} \text{ as } V_{2\Omega} = 3V$$

Root Mean Square

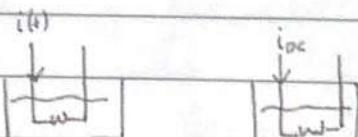
DC ① reads  $I_{avg}$   $\Rightarrow$  for  $I(t)_{avg} = 0$  as  $\sin \theta \cos \theta$ .

To make the reading work  $\rightarrow$  take square &  $\sqrt$

$\therefore$  at AC,  $I_{AC} = (\frac{1}{T} \int_0^T i^2(t) dt)^{1/2}$ , AC ②  $\rightarrow I_{rms}$  AC ③  $\rightarrow V_{rms}$

Effective Value

Analogy:



where heating =  $P_{avg}$  dissipated.

When  $i(t)$  &  $i_{AC}$  cause the water to be same temp at the same time, the value of  $i$  is denoted as  $I_{eff}$  (or  $I_{AC}$ ).

$$I_{AC} \rightarrow P = RI_{AC}^2$$

$$I_{AC} \rightarrow P = \frac{1}{T} \int_0^T R i^2(t) dt$$

## Effective Value for Sinusoid

$$i(t) = I_{\text{peak}} \sin(\omega t + \phi) \Rightarrow I_{\text{eff}} \text{ or } I_{\text{ac}} = \sqrt{\frac{1}{T} \int_0^T I_{\text{peak}}^2 \sin^2(\omega t + \phi) dt}$$

$= \frac{I_{\text{peak}}}{\sqrt{2}}$

From now on...  $\bar{I} = I_{\text{peak}} \angle 0^\circ \Rightarrow \bar{I} = I_{\text{rms}} \angle 0^\circ$   
rms angle

Power in AC Steady State :

Generalization of  $I_{\text{eff}}$

$i(t) = A \sin(\omega t + \phi)$  heat a resistor the same as  $I_{\text{ac}}$  with a value of  $\frac{A}{\sqrt{2}}$ .

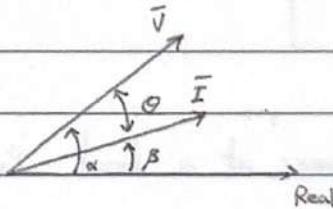
If we call 'M' effective value of  $i(t)$ ,  $M = \frac{A}{\sqrt{2}}$ . Then,  $i(t) = \sqrt{2} M \sin(\omega t + \phi)$

Active & Reactive & Apparent

Voltage in generic load is  $v(t) = \sqrt{2} V \sin(\omega t + \alpha)$  if  $i(t) = \sqrt{2} I \sin(\omega t + \beta)$

$$\bar{V} = V_{\text{rms}} \angle \alpha^\circ$$

$$\bar{I} = I_{\text{rms}} \angle \beta^\circ$$



$$P(t) = i(t)v(t) = 2VI \sin(\omega t + \phi) \sin(\omega t + \beta) \text{ simplifying...}$$

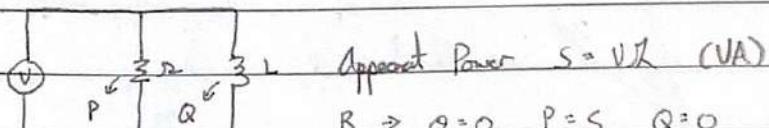
$$= VI \cos(\phi - \beta) + VI \cos(2\omega t + \phi + \beta)$$

$$P_{\text{avg}} = VI \cos(\phi - \beta) \quad \left. \begin{array}{l} \text{power factor (\%)} \\ \text{= } V_{\text{rms}} I_{\text{rms}} \cos(\phi - \beta) \end{array} \right\} \begin{array}{l} \text{inductive \& capacitive} \\ \text{(lagging) (leading)} \end{array}$$

} Active Power (W), positive source  $\Rightarrow$  load

$\phi$ , power factor angle

$$Q = VI \sin(\phi - \beta) \quad \left. \begin{array}{l} \text{Reactive Power (Var), load } \rightarrow \text{source (phenomenon in all non-linear)} \\ \text{power that goes back \& forth without doing meaningful work.} \end{array} \right.$$



$$L \Rightarrow \phi = 90^\circ \quad P = 0 \quad Q = S$$

$$C \Rightarrow \phi = -90^\circ \quad P = 0 \quad Q = -S$$

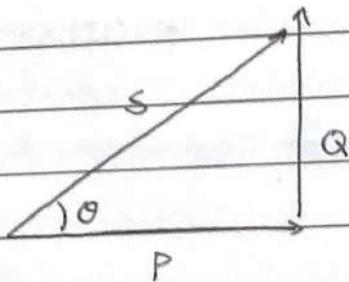
$$S = P + Qj$$

$$Q = (S^2 - P^2)^{1/2} \text{ for power triangle}$$

$$P = S \cos \theta = S \cdot \rho$$

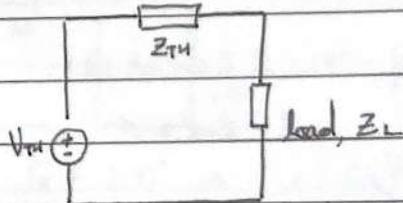
$$Q = S \sin \theta ; \text{ Powers can also be computed using}$$

$$\frac{ZI^2}{Z} \text{ & } \frac{VI^2}{Z}$$



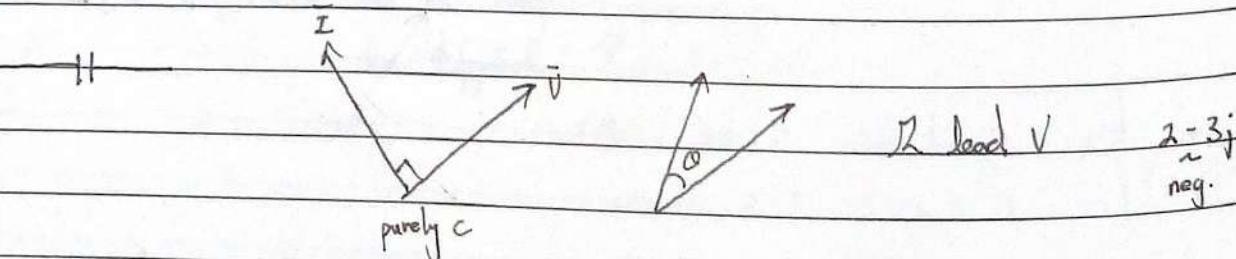
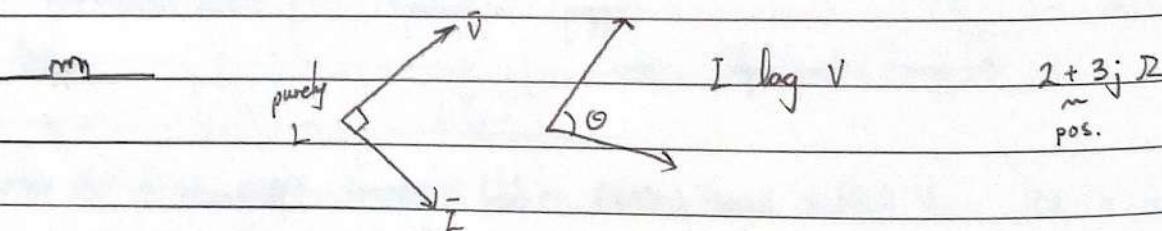
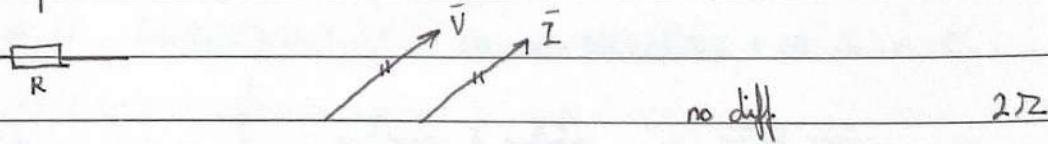
### Maximum Power

To obtain Power max,  $V_m \& Z_m$  must be established where  $Z_L = Z_{TH}^*$

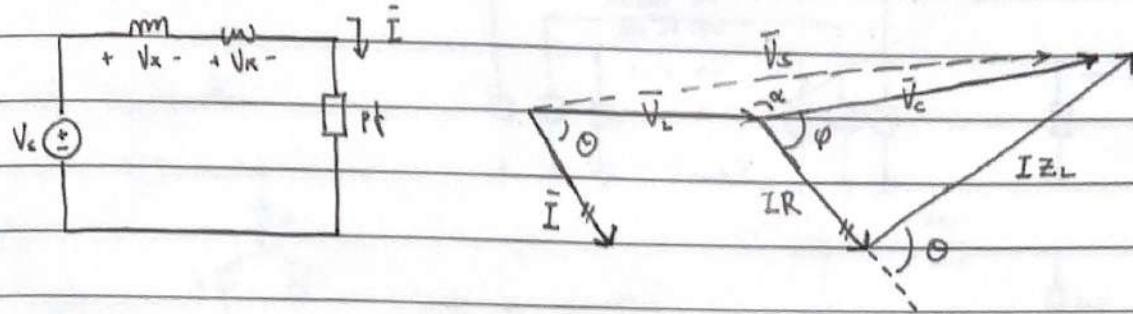


The maximum complex power at a load  $P_{max} = \frac{|V_m|^2}{8Z_m}$

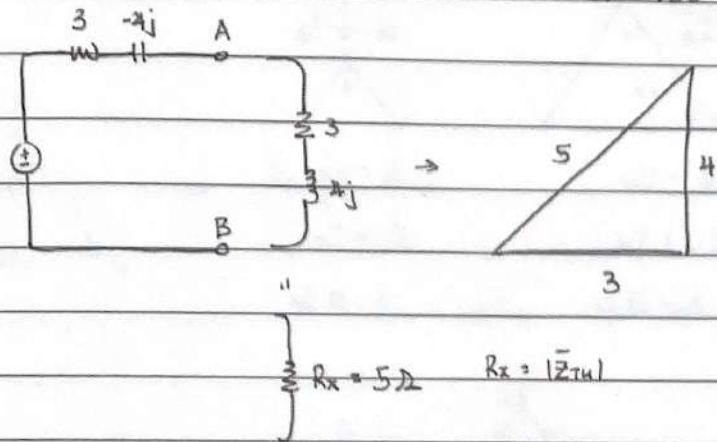
### Graphic Representation



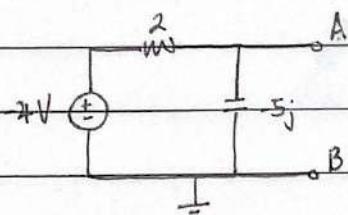
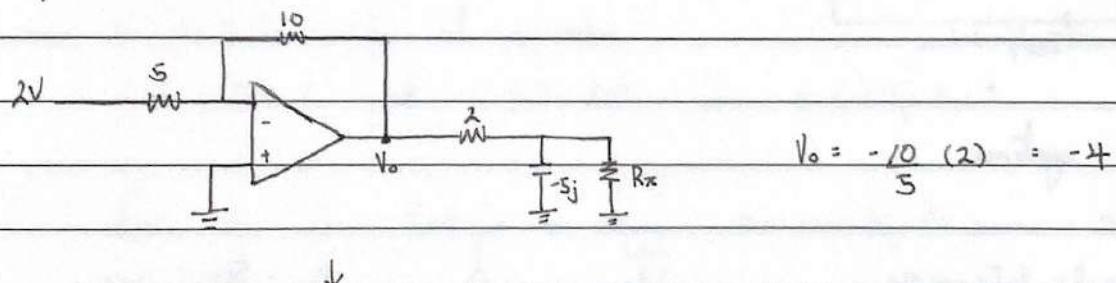
## One Lateral Circuit



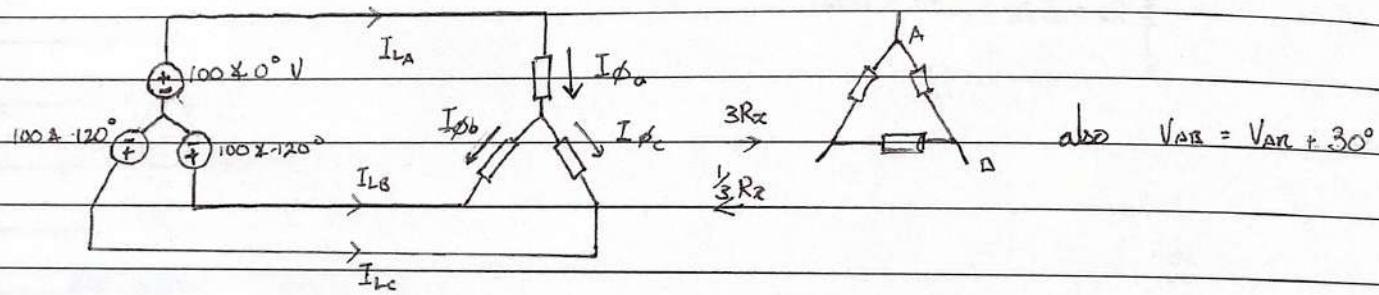
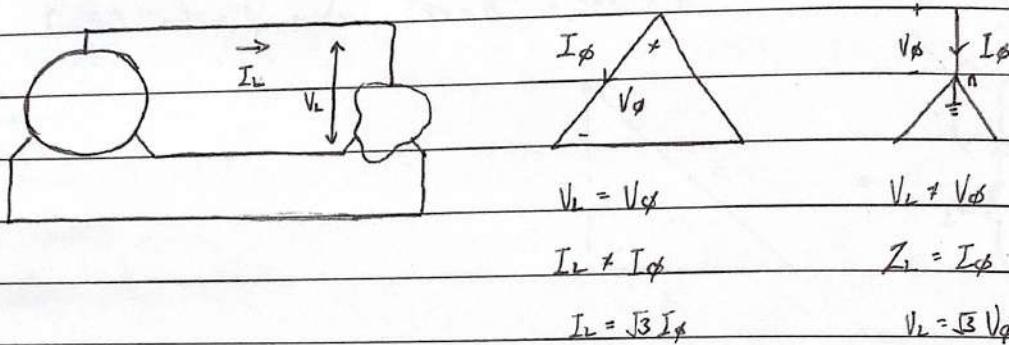
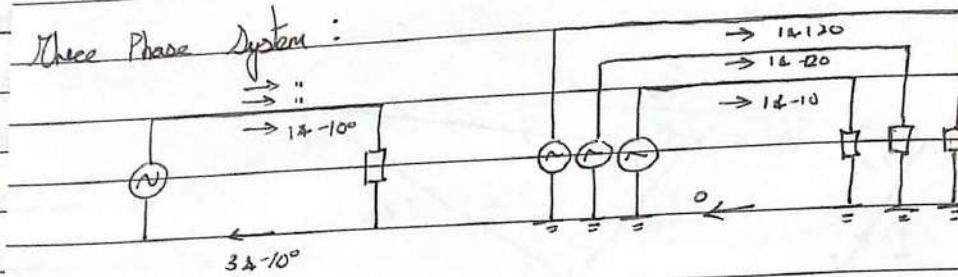
$$\alpha = 180 - (\phi - \theta) \quad \text{where } \phi = \tan^{-1} \left( \frac{Z_L}{R} \right)$$



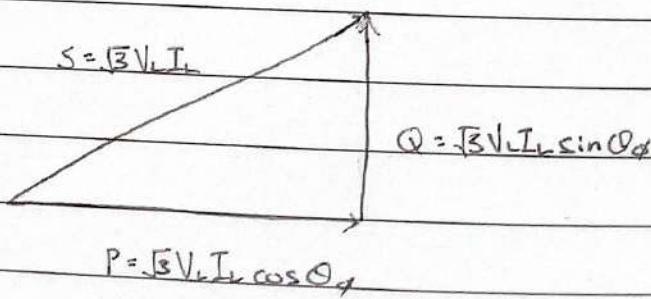
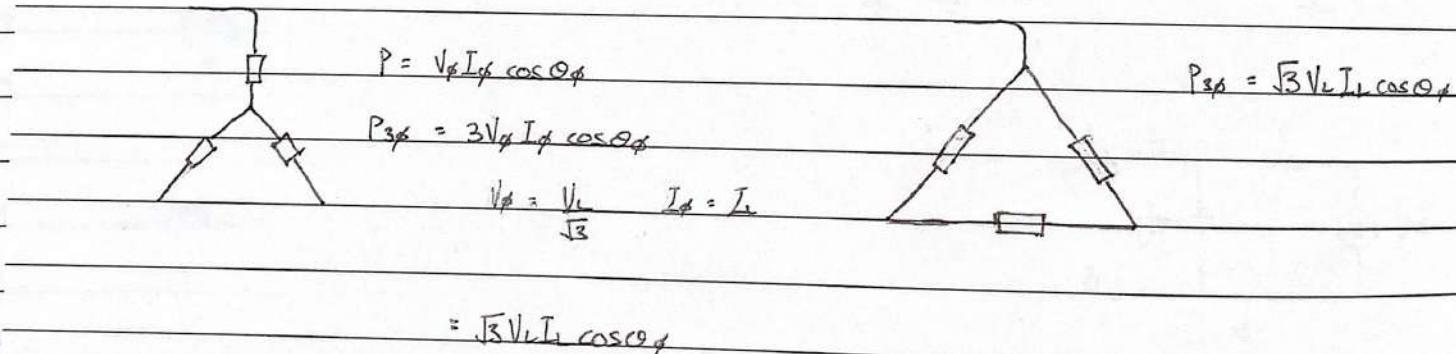
Op-amp.



Three Phase System :

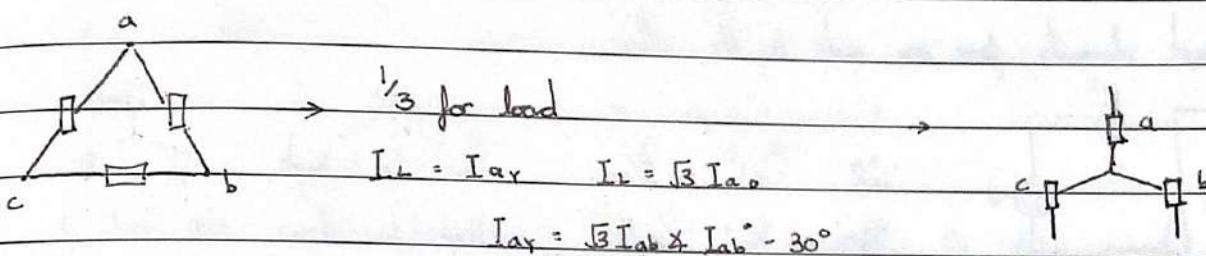
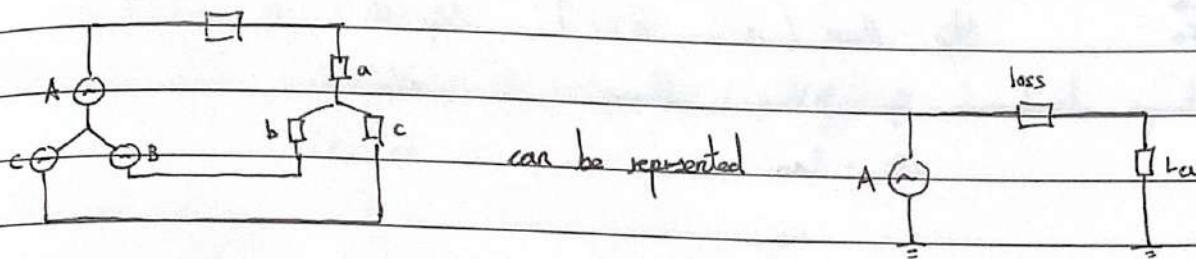


Power in 3 phase systems



$$\text{Power at the load} \Rightarrow \bar{S} = |\bar{I}|^2 \bar{Z}$$

Conversion and Simplification



$$V_L = \sqrt{3} V_{ay} \quad V_L = V_{ab}$$

$$V_{ay} = \frac{V_{ab}}{\sqrt{3}} \angle V_{ab} - 30^\circ$$

Power,  $S = \sqrt{3} V_L I_L$  is for real rms value.

but often we don't have this info IRL.

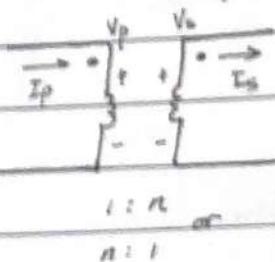
$$\therefore \bar{S} = \sqrt{3} V_L I_L \text{ with } \angle V_L - I_L \text{ or } S = 3 V_{ph} I_{ph}^*$$

also,  $S_L$  where load is in series,  $S_L$  seen by the source =  $3 S_L$

$S_L$       "      parallel      "       $= S_L$

$$S_L = |I_L|^2 Z_L \text{ or } V_L I_L, \quad \bar{S}_L = V_L I_L^* \dots$$

Transformers:  
a device that adjust the  $V \cdot Z \cdot P$  according to the device's coefficient (transformation ratio)



Step - Down [ $n < 1$ ,  $n > 1$ ]

$$V_p = nV_s$$

$$n > 1 \text{ or}$$

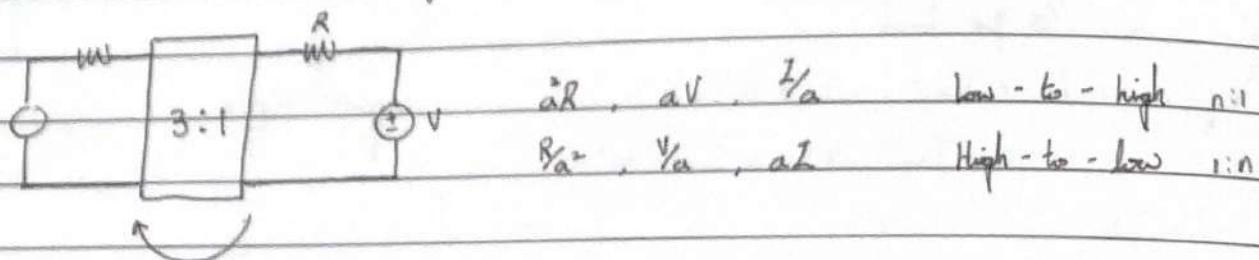
$$nI_p = I_s$$

Step - Up [ $n > 1$ ,  $n > 1$ ]

$$nV_p = V_s$$

$$I_p = nI_s$$

We can move circuit elements from one side to the other.



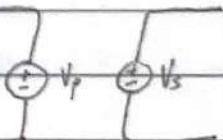
$$3R, \frac{V}{I}, \frac{I}{A}$$

$$R_L, \frac{V}{A}, \frac{A}{I}$$

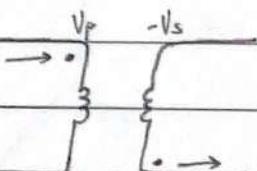
Low - to - high  $n:1$

High - to - low  $1:n$

Modeling each transformer winding as a voltage source, simplifies many analytical steps.



Dot - Connection :



current goes in at one side, must come out at other

Notice that power equation stays the same.  $S = V_{rms} I_{rms}^*$

\*  $I_p = \sum I_{s,k}$  Primary current is the combination of sum of all secondary currents.

2nd - Order circuit :

The order in a circuit represents # reactive elements in a circuit  
in circuit with 2 reactive current

During the circuit, we make use of  $p$ -operator ( $p = \frac{d}{dx}$ ) & characteristic equations for ODE.

Steps for solving :

- Find conditions  $t \geq 0$ , using usual analysis tools.
  - Find conditions  $t > 0^+$ , using  $i_L(0^+) = V_L(0^+)$        $V_L(0^+) = i_C(0^+)$  with reactive values from step 1 as  $V_C(0^+) = V_C(0^-)$  &  $i_L(0^+) = i_C(0^-)$ .  
 $V = \text{Src}$        $I = \text{Src}$
  - Find the equation for asked variable by converting the  $t \geq 0$  circuit to p-operable using  $R = R$      $L = L_p$      $C = \frac{1}{C_p}$ .
  - Take the characteristic equation from the denominator of the eq. at step 3, and get eigen-values. This allows us to know which solution to ODE is most fit, and the time-to-steady state =  $5\tau_{\text{larger}}$   
 $\Rightarrow \zeta_1 = -\frac{1}{b_1}, \quad \zeta_2 = -\frac{1}{b_2}$
  - Use ODE solution,  $b_1, b_2$ 
    - $b^2 > 4ac$      $b_1, b_2 \in \mathbb{R}$  (Over-damped) —  $y(t) = k_1 e^{b_1 t} + k_2 e^{b_2 t} + k_3$
    - $b^2 < 4ac$      $b_1, b_2 \notin \mathbb{R}$  (Under-damped) —  $y(t) = k_1 e^{\alpha t} \cos(\omega t + k_2) + k_3, \quad s = \alpha + \omega j$
    - $b^2 = 4ac$      $b_1 = b_2$       (Critically-damped) —  $y(t) = (k_1 t + k_2) e^{b_1 t} + k_3$

and the IC's,  $y(0)$  &  $y'(0)$ , to solve the coefficients  $k_1$ ,  $k_2$  &  $k_3$ .

Note

$$\cdot (0.1p^2 + 17p + 100)V_c = \cancel{p}^{\circ} + 600 \quad \& \quad k_3 = \frac{600}{\cancel{p}^{\circ}} = 6.$$

• For U-D case, one can also use

$$y(t) = e^{\alpha t} [k_1 \cos(\omega t) + k_2 \sin(\omega t)]$$

$$y_1(t) = e^{\alpha t} [(\omega k_2 - \alpha k_1) \cos(\omega t) - (\omega k_1 + \alpha k_2) \sin(\omega t)]$$

To Laplace:

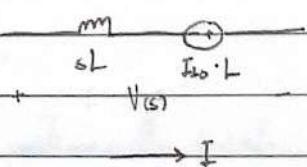
$$A \cos(\omega t + \phi) \rightarrow \boxed{\text{circuit}} \rightarrow k A \cos(\omega t + \phi + \theta)$$

$$k A \cos(\theta + \phi) = k \cancel{\phi} \cdot \cancel{k} \cos \theta$$

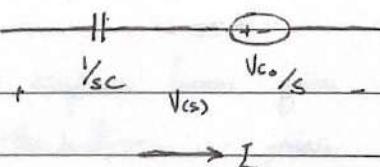
↳ effect of the circuit to signal.

With initial conditions; we can transform the circuit in Laplace domain  $t \geq 0$ .

Inductor



Capacitance



Solve the circuit using 'normal' & 'ilaplace' function in HP Prime by solving for the right equation.  
An equation for a given parameter can be solved by taking the inverse-Laplace.

Note that maximum of something can be solved using graph, or taking the 0's of derivative.

Transfer - Function :

$$Y(s) = H(s)X(s) \rightarrow \text{input function in } s\text{-domain}$$

↓  
output  $\hookrightarrow t_f$ , behavior of circuit  $H(s) = \frac{Y(s)}{X(s)}$  at energy 0 [ $V_{in} & T_{in} = 0$ ,  $V_{src}$  short ]  
Isoc open

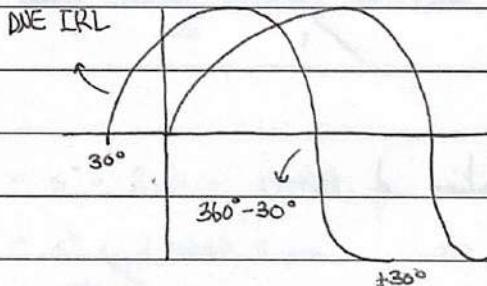
$$|H(s)| = k \quad H(s)^\circ = s \quad , \quad k(\text{dB}) = 20 \log_{10}(k) \quad s(\text{rad}) = \frac{s \cdot 180}{\pi}$$

Notice if  $X(s)$ , the input is dirac-delta  $\delta(x)$ ,  $H(s) = Y(s)$

Time delay of the signal can be computed using  $\delta$ ,  $(\delta(\text{rad}) - 2\pi) \cdot \frac{2\pi}{\omega}$  or  $(\delta(\text{deg}) - 360) \cdot \frac{2\pi}{\omega}$

\* if pole @ RHS  $\rightarrow$  unstable.

\* but  $s$  here must come before the output,  $\therefore -2\pi$  (if rad) or  $-360$  (if °)  
from the circuit signal  $H(s)$



## To Bode Plot:

We simulate the frequency response of a circuit using Bode plot (both amplitude & degrees).  
 (dB) (deg)

### Amplitude Plot

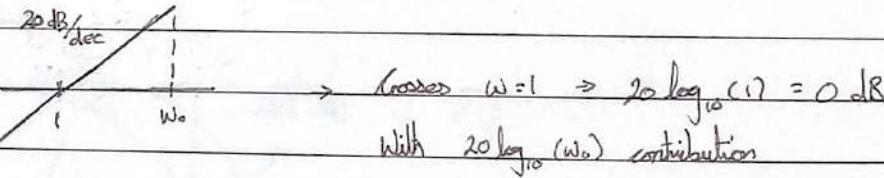
Decibels of any given frequency can be computed using  $20 \log_{10}(w)$  where  $w(\text{rad/s}) = 2\pi f$ .

$$\text{Given a } H(s) = k \cdot \frac{s(s+a)(s+b)^2}{(s+c)(s+d)}$$

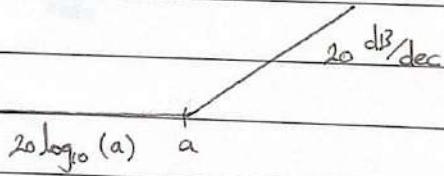
Contribution of a constant  $k$ ,

$$20 \log_{10} \left( \frac{k \cdot 1 \cdot a \cdot b^2}{c \cdot d} \right) \rightarrow \text{shifting the graph up / down.}$$

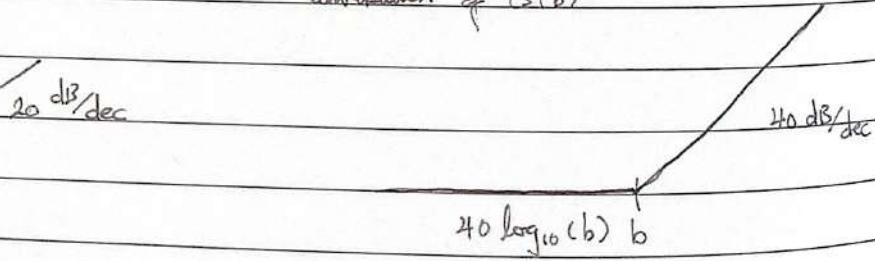
Contribution of  $s(0 @ 0)$



Contribution of  $(s+a)$



Contribution of  $(s+b)^2$



For denomination, just flip the graph upside-down.

For initial contribution (starting point), sum all the contributions from first  $w_1(w_0)$  that is not 1. For  $H(s)$  above  $\text{dB}(w_0) = 20 \log_{10} \left( \frac{k \cdot 1 \cdot a \cdot b^2}{c \cdot d} \right) + 20 \log_{10}(w_0)$

$$\text{Note, decade} = \log_{10} \left( \frac{w_2}{w_1} \right)$$

## Phasor Plot

$$\text{Given } H(s) = \frac{k \cdot s(s+a)(s+b)^2}{(s+c)(s+d)}$$

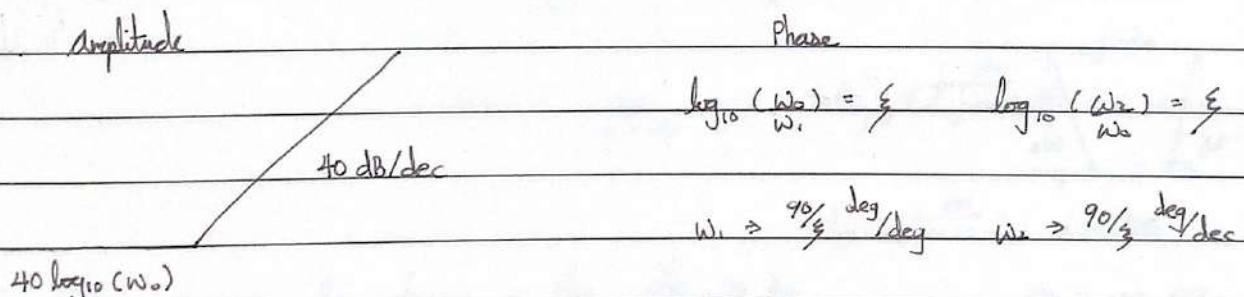
$$\begin{array}{lll}
 k \rightarrow 0^\circ & s \rightarrow 90^\circ/\text{dec} & 0.1a \rightarrow 45^\circ/\text{dec} \\
 s^2 \rightarrow 180^\circ/\text{dec} & 10a \rightarrow -45^\circ/\text{dec} & 0.1b \rightarrow 90^\circ/\text{dec} \\
 0.1c \rightarrow -45^\circ/\text{dec} & & \\
 10c \rightarrow 45^\circ/\text{dec} & &
 \end{array}$$

## Bode - Mantini Plot

When  $H(s)$  has a two complx conj., instead of using Bode approx, we make use of Mantini approx.

$$\begin{aligned}
 atbj &\& a-bj \rightarrow [s + (at+jb)][s + (a-jb)] = s^2 + 2as + a^2 + b^2 \\
 &= s^2 + 2\xi\omega_0 s + \omega_0^2, \quad a = \xi\omega_0
 \end{aligned}$$

$\xi$  (damping freq),  $\omega_0$  (undamped freq.)



\* watch for denom or numer. Above is numer example.

II Filters:

Resonance, is a freq where LRC cancels out (no reactive element) such that  $V_{out}$  only has resistive component. ( $Z_r$  is real only)

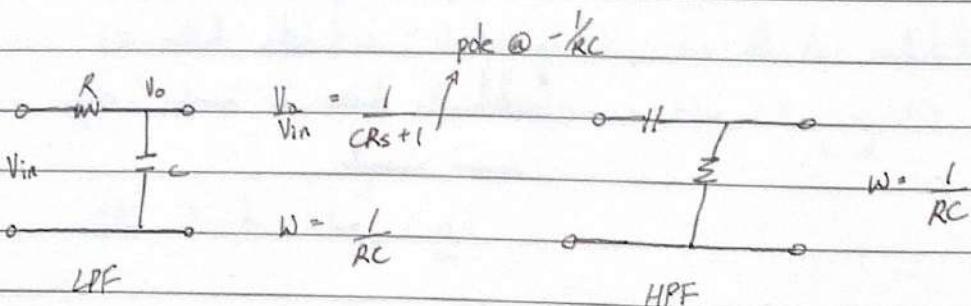
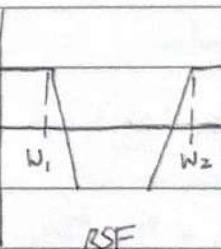
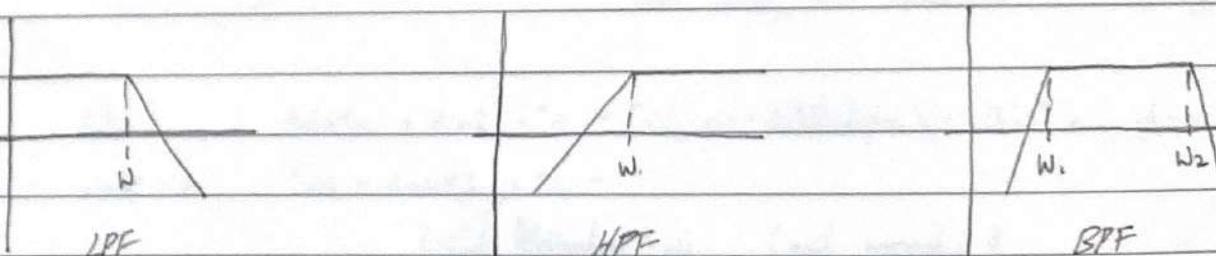
Quality Factor, number — that determines the selectivity of the circuit (filter)

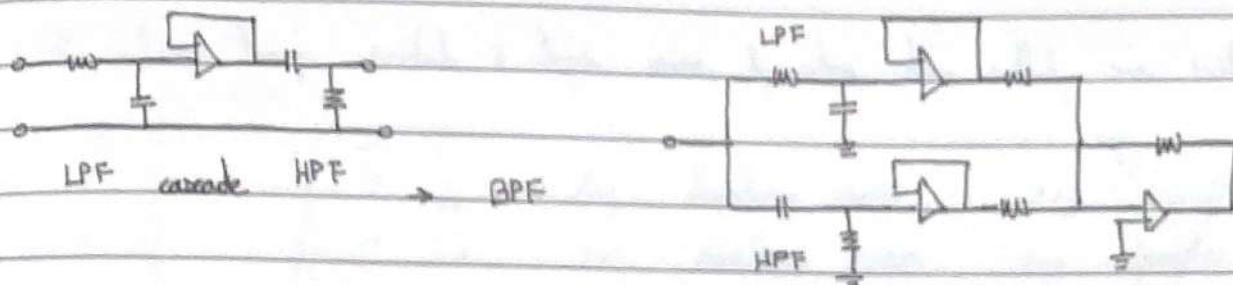
Half-Power Freq,  $\omega_0$  where the signal is 3dB off of filtered value.

$$\omega_0 = \frac{1}{\sqrt{LC}} \quad Q_s = \omega_0 L \text{ or } \frac{1}{\omega_0 RC} \quad \text{HPF} = \omega_0 \left[ \sqrt{1 + \left(\frac{1}{2Q}\right)^2} \pm \frac{1}{2Q} \right] \quad (\omega_1, \omega_2)$$

$$BW = \frac{\omega_0}{Q} \quad Q_r = \frac{R}{\omega_0 L} \text{ or } \omega_0 RC$$

$$Q \gg 10 \rightarrow \text{highly selective}, (\omega_1, \omega_2) = \frac{\omega_0 \pm BW}{2}$$



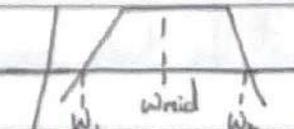


additive op-amp  $\rightarrow$  BSF

Order of filters are determined by the slope of its poles.

Mid-freq. is where filter has its peak among BPF.

$$\omega_{mid} = (\omega_1 \omega_2)^{1/2}$$



Amax & Amin

Amax is the max level of signal it rejects & Amin is the min level of signal it rejects.

example.

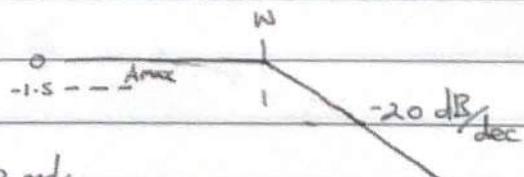
$$V_{in} \xrightarrow[1K]{T_{SOMF}} V_o \quad H(\omega) = \frac{1/SC}{R + 1/SC} = \frac{1/RC}{s + 1/RC} \quad \omega = \frac{1}{RC}$$

$$f = 1 \text{ Hz}$$

$$A_{min} = 1.5$$

$$A_{max} = 15$$

$$\omega = \frac{1}{RC} = 20 \text{ rad/s}$$



\*fsobe.

$$H(s) = \frac{20}{s+20} \rightarrow 20 \log_{10} \left( \frac{20}{j\omega + 20} \right) = 0 - 1.5$$

$$\omega_p = 12.85$$

$$20 \log_{10} \left( \frac{20}{j\omega_s + 20} \right) = 0 - 15$$

Sensitivity factor measures the changes a filter experience with a change of variable. Sensitivity =  $\frac{\omega_p}{\omega_s} < 1$

$$\omega_s = 110.672$$

Filter cookbook allows a scaling computation with a given filter.

$$R' = k_m R \quad R' = R$$

$$R' = k_m R$$

$$L' = k_m L \quad L' = \frac{1}{k_f} \quad \rightarrow \quad L' = L \frac{k_m}{k_f}$$

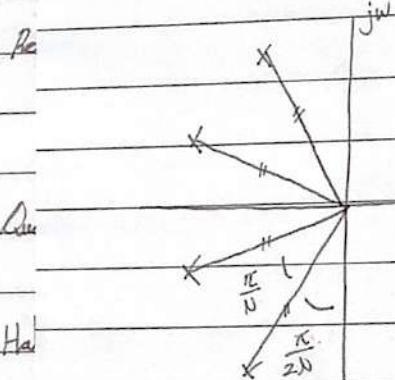
$$C' = C/k_m$$

$$C' = \frac{C}{k_f}$$

$$C' = C \frac{1}{k_m k_f}$$

Butterworth Filters are filters with poles of same angle & distance apart

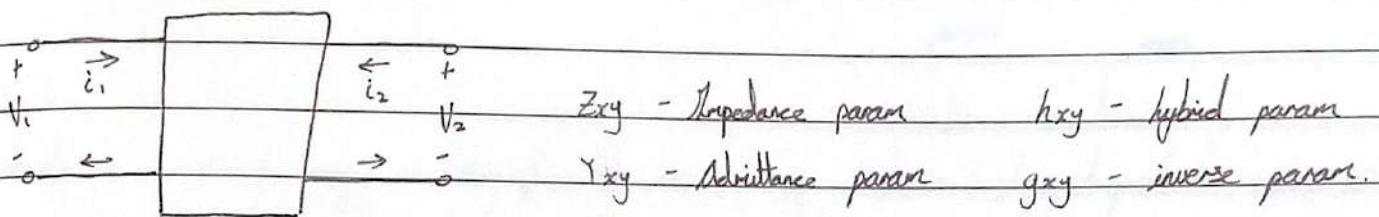
Th Filter



N - order

x's are point which are equidistant from the origin.

## Two-Port Network :



$$V_1 = Z_{11}i_1 + Z_{12}i_2 \quad i_1 = Y_{11}V_1 + Y_{12}V_2$$

$$V_2 = Z_{21}i_1 + Z_{22}i_2 \quad i_2 = Y_{21}V_1 + Y_{22}V_2$$

Use param. chart and convert params.

$$V_1 = h_{11}i_1 + h_{12}V_2 \quad V_1 = A V_2 + B(i_2)$$

$$i_2 = h_{21}i_1 + h_{22}V_2 \quad i_1 = C V_2 + D(i_2) \quad \{ABCD \Rightarrow \text{transmission params}\}$$

