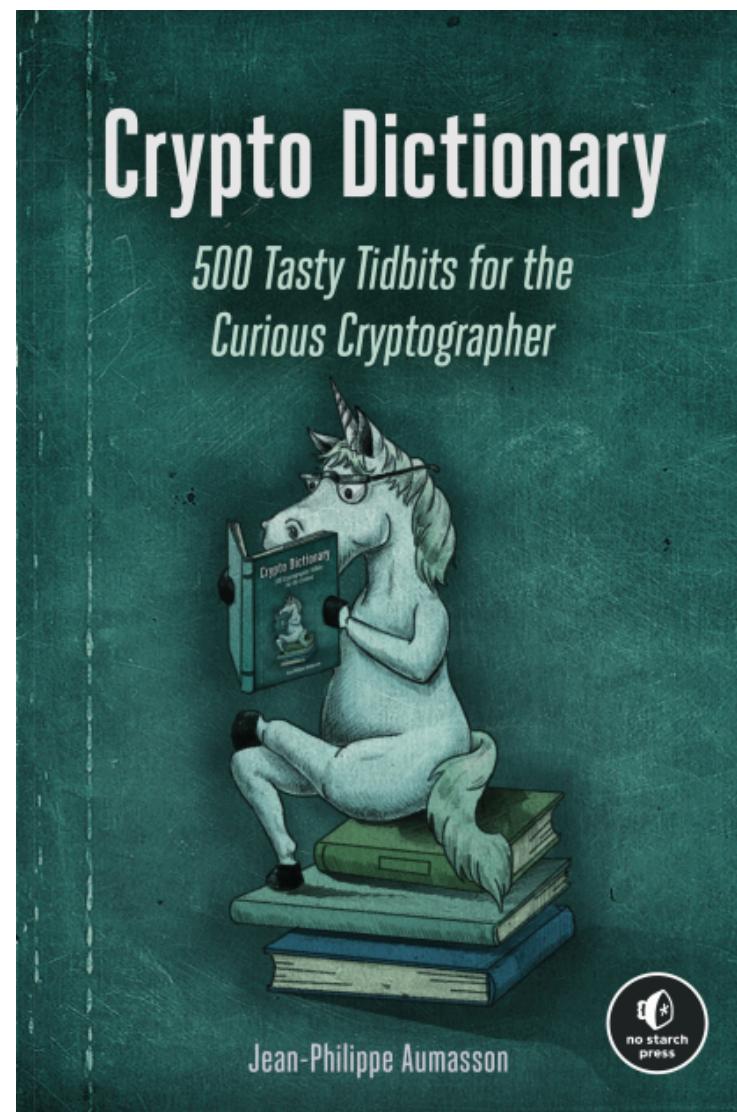
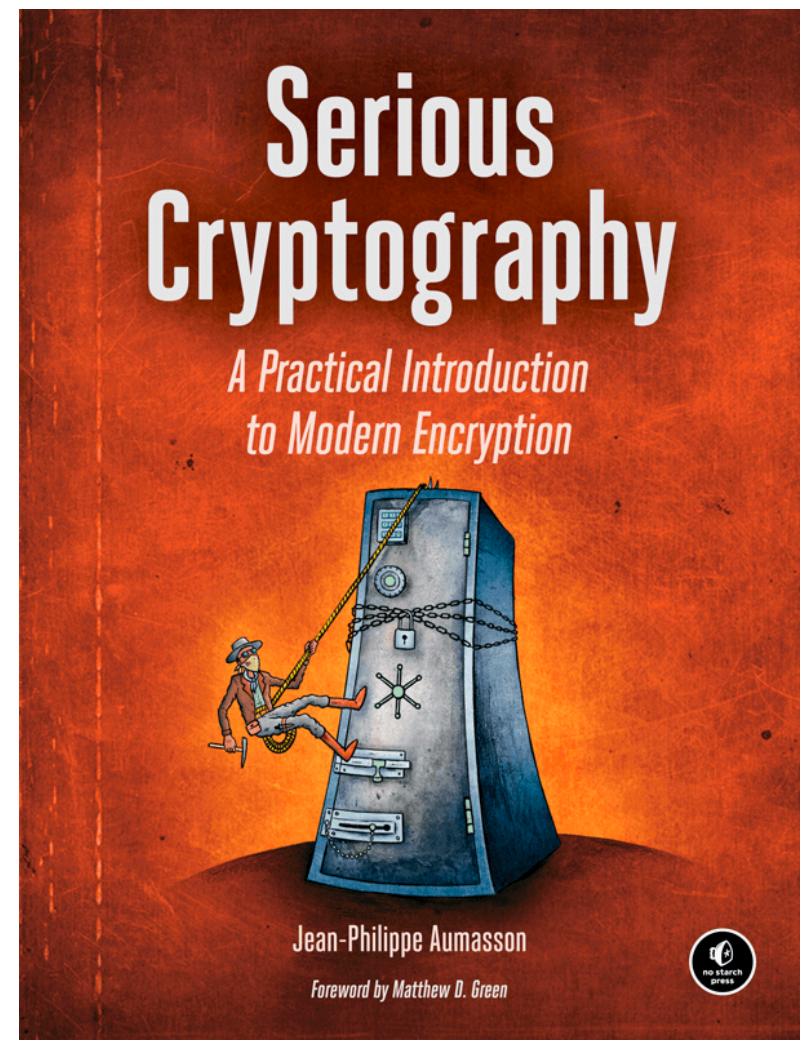


# Quantum Computing vs. Cryptography

JP Aumasson



# /me



**Cryptography** since 2006  
(PhD, industry, consulting, start-ups)  
Widely used algorithms (BLAKE2, SipHash)  
Author of reference books in the field  
<https://aumasson.jp> <https://twitter.com/veorq>

Co-founder (2018) & CSO of **Taurus**

Banking-grade digital asset infrastructure

- Cryptocurrency custody & management
- First regulated exchange platform (TDX)



# Fundamental Equations

Schrödinger equation:

$$i\hbar \frac{\partial \Psi}{\partial t} = H\Psi$$

Time independent Schrödinger equation:

$$H\psi = E\psi, \quad \Psi = \psi e^{-iEt/\hbar}$$

Standard Hamiltonian:

$$H = -\frac{\hbar^2}{2m} \nabla^2 + V$$

Time dependence of an expectation value:

$$\frac{d\langle Q \rangle}{dt} = \frac{i}{\hbar} \langle [H, Q] \rangle + \left\langle \frac{\partial Q}{\partial t} \right\rangle$$

Generalized uncertainty principle:

$$\sigma_A \sigma_B \geq \left| \frac{1}{2i} \langle [A, B] \rangle \right|^2$$

# **Simulating Physics with Computers**

**Richard P. Feynman**

*Department of Physics, California Institute of Technology, Pasadena, California 91107*

*Received May 7, 1981*

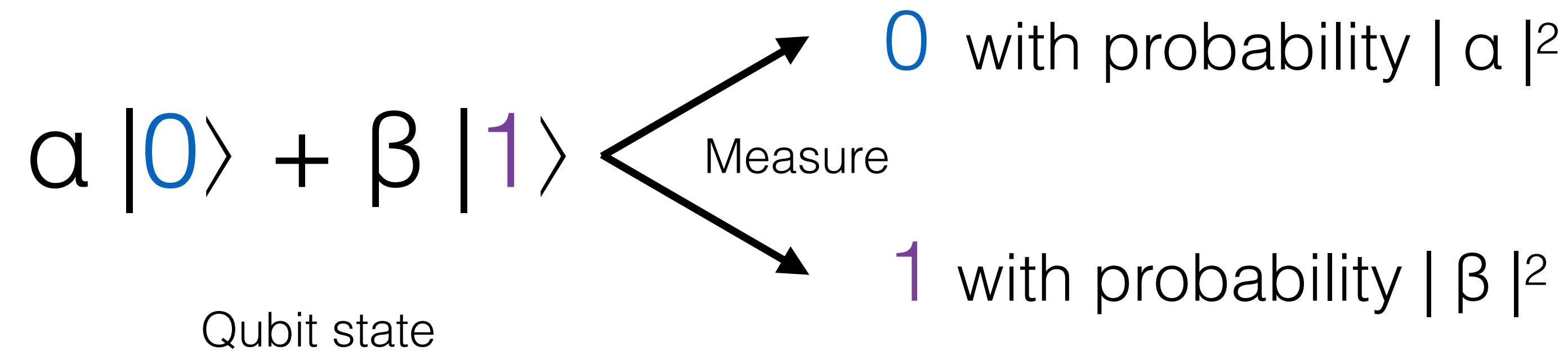
## **5. CAN QUANTUM SYSTEMS BE PROBABILISTICALLY SIMULATED BY A CLASSICAL COMPUTER?**

Now the next question that I would like to bring up is, of course, the interesting one, i.e., Can a quantum system be probabilistically simulated by a classical (probabilistic, I'd assume) universal computer? In other words, a computer which will give the same probabilities as the quantum system does. If you take the computer to be the classical kind I've described so far, (not the quantum kind described in the last section) and there're no changes in any laws, and there's no hocus-pocus, the answer is certainly, No! This is called the hidden-variable problem: it is impossible to represent the results of quantum mechanics with a classical universal device. To learn a little bit about it, I say let us try to put the quantum equations in a form as close as

## **4. QUANTUM COMPUTERS—UNIVERSAL QUANTUM SIMULATORS**

The first branch, one you might call a side-remark, is, Can you do it with a new kind of computer—a quantum computer? (I’ll come back to the other branch in a moment.) Now it turns out, as far as I can tell, that you can simulate this with a quantum system, with quantum computer elements. It’s not a Turing machine, but a machine of a different kind. If we disregard the continuity of space and make it discrete, and so on, as an approximation (the same way as we allowed ourselves in the classical case), it does seem to

# Qubits instead of bits



Stay 0 or 1 forever

Generalizes to more than 2 states: qutrits, qubbytes, etc.

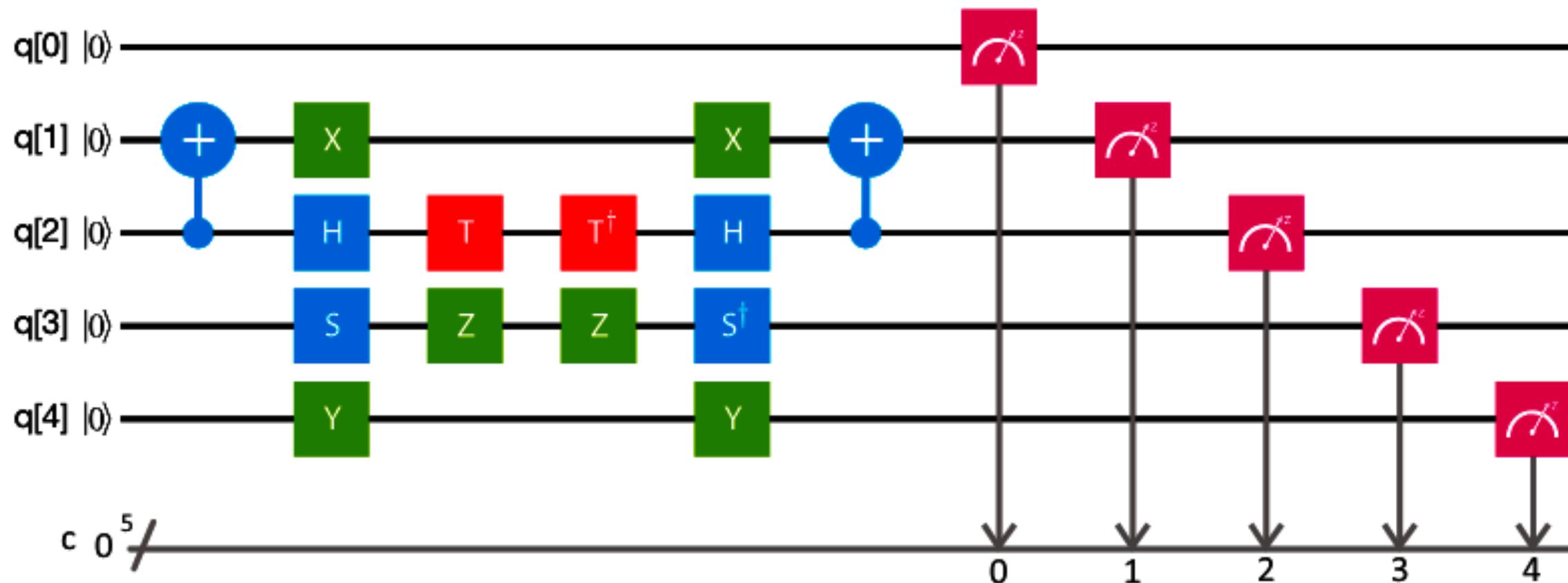
Complex, negative probabilities ("amplitudes"), **true randomness**

# Quantum computer

Can be **simulated** with just high-school linear algebra

- State = vector of  $2^N$  amplitudes for N qubits
- **Quantum gates** ~ matrix multiplications

Quantum circuits usually end with a **measurement**



**Simulated does NOT scale** (needs  $2^N$  storage/compute)

# Quantum speedup

When quantum computers can solve a problem faster than classical computers

Most interesting: **Superpolynomial** quantum speedup



List on the Quantum Zoo: <http://math.nist.gov/quantum/zoo/>

# Quantum parallelism

Quantum computers sort of encode all values simultaneously

But they do not “try every answer in parallel”

You can only observe one result, not all

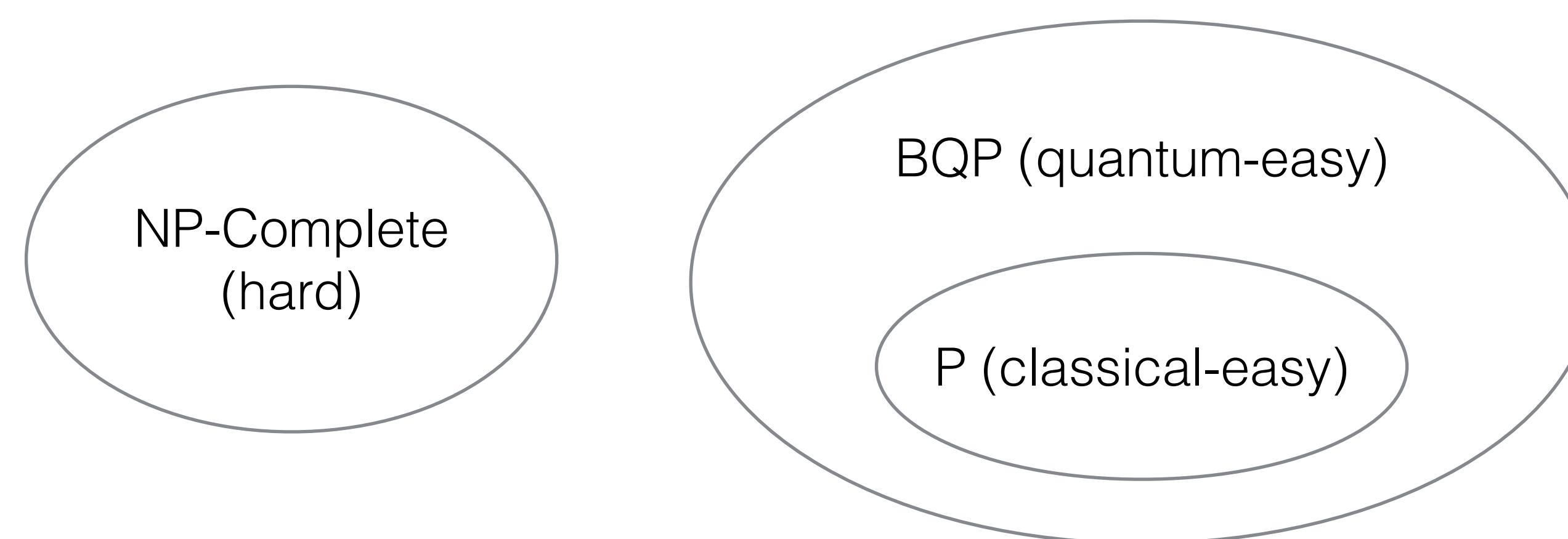


# NP-complete problems

- Solution hard to find, but easy to verify
- Constraint satisfaction problems (SAT, TSP, knapsacks, etc.)
- Sometimes used in crypto (e.g. lattice problems)

**Can't be solved faster** with quantum computers

*BQP = bounded-error quantum polynomial time*



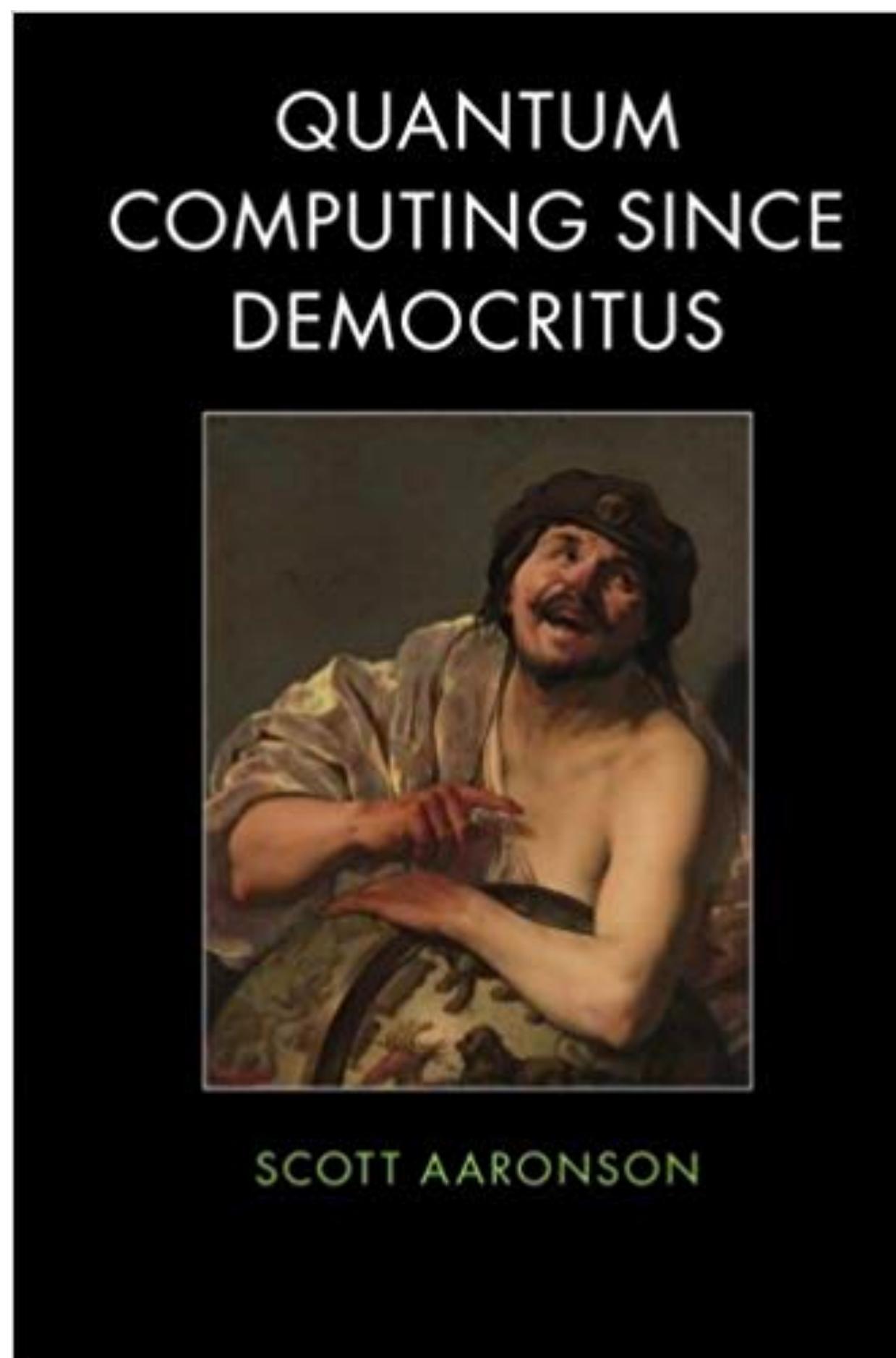
# Google thinks it's close to "quantum supremacy." Here's what that really means.

It's not the number of qubits; it's what you do with them that counts.

by Martin Giles and Will Knight   March 9, 2018

**S**eventy-two may not be a large number, but in quantum computing terms, it's massive. This week Google [unveiled](#) Bristlecone, a new quantum computing chip with 72 quantum bits, or qubits—the fundamental units of computation

# Recommended



How does it impact cryptography?

# Why I'm here today

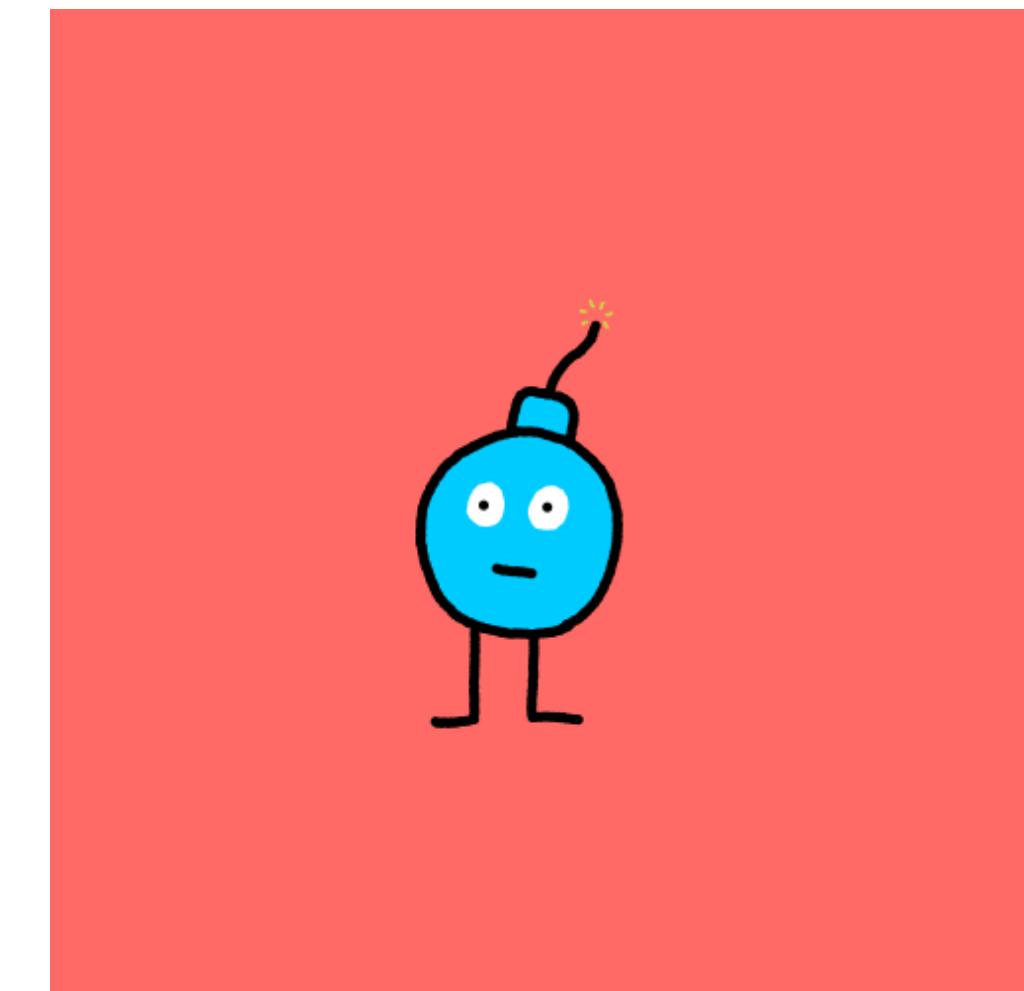
Shor's algorithm finds a structure in Abelian subgroups:

- Finds **p** given  $\mathbf{n} = \mathbf{pq}$   
= **Factoring** problem = breaking RSA
- Finds **d** given  $\mathbf{y} = \mathbf{x}^{\mathbf{d}} \bmod \mathbf{p}$   
= **Discrete logarithm** = breaking elliptic-curve crypto

**Fast** to solve on a quantum computer

**Practically impossible** on a classical one

“Exponential quantum speed-up”



# How bad is it?



**Cool: signatures** (ECDSA, Ed25519, etc.)

*Can be reissued with a post-quantum algorithm*

Applications: Bitcoin, application signing



**Bad: key agreement** (Diffie-Hellman, ECDH, etc.)

*Mitigated with secret states, or reseeding*

Applications: TLS, end-to-end messaging

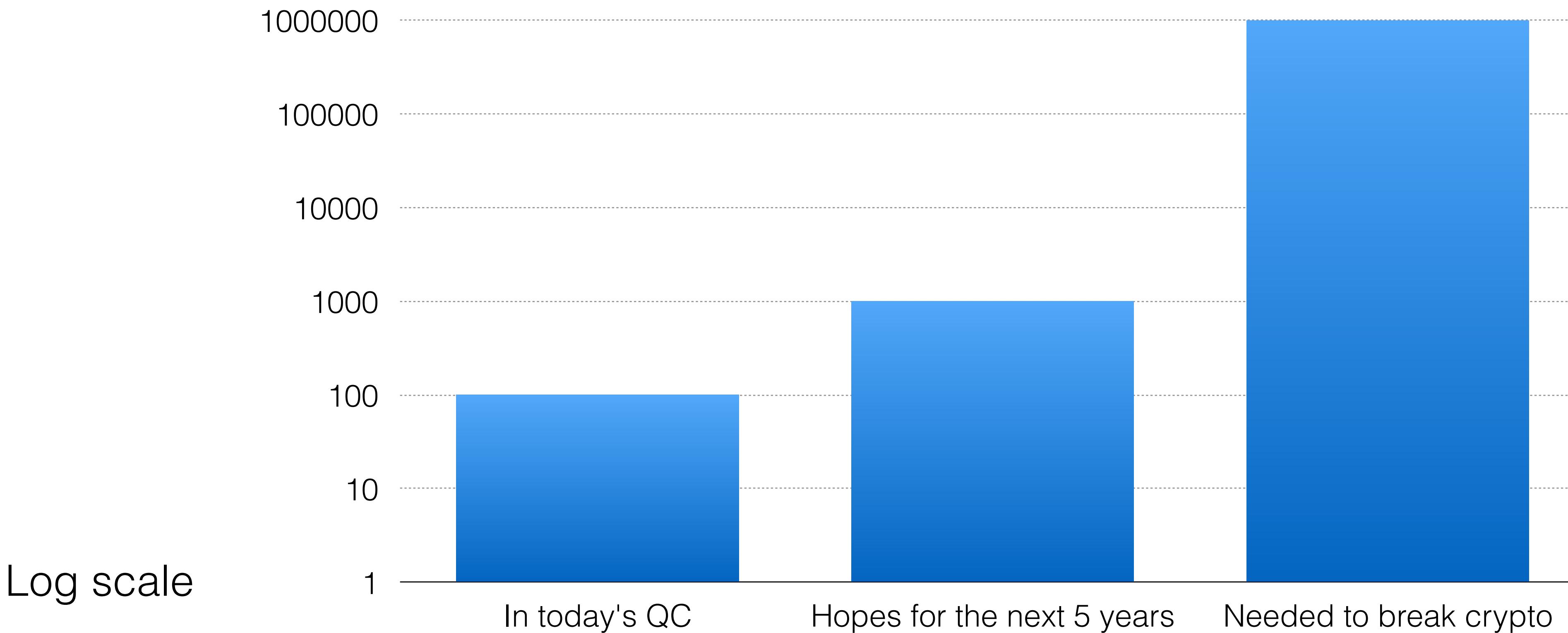


**Ugly: encryption** (RSA encryption, ECIES, etc.)

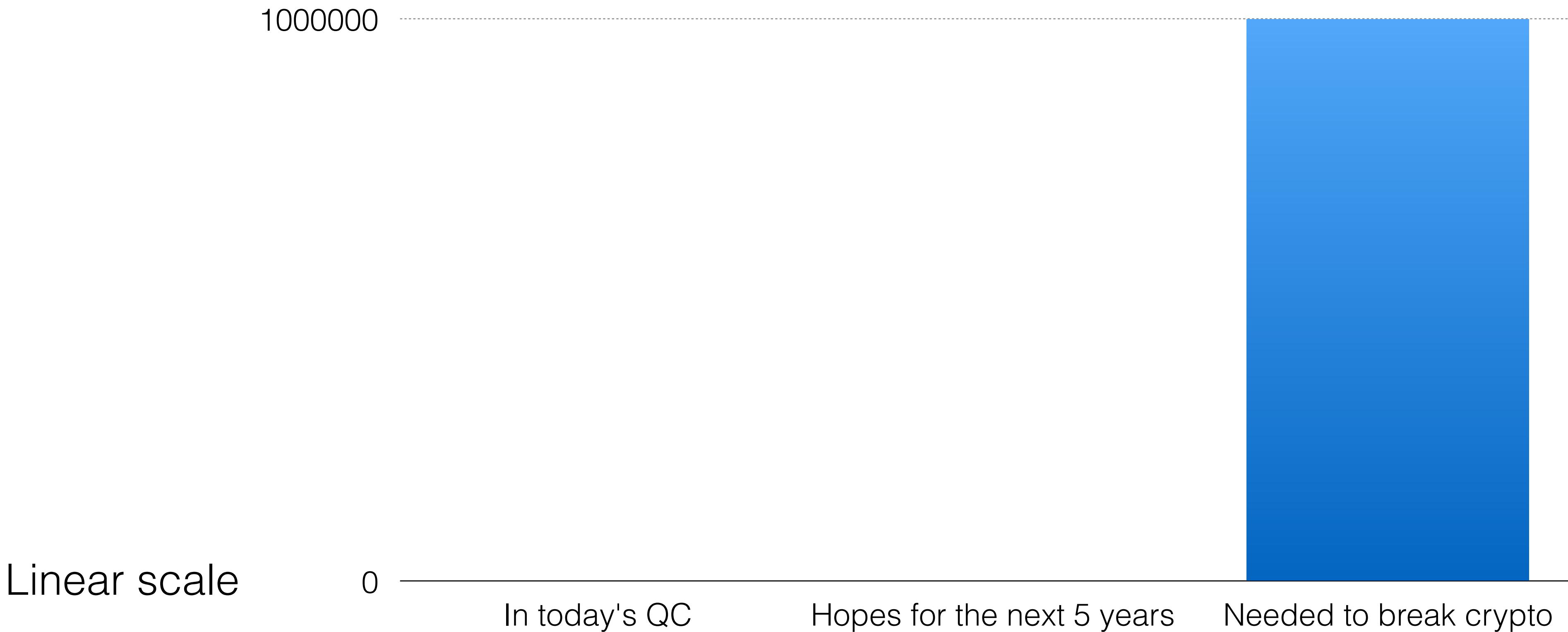
*Encrypted messages compromised forever*

Applications: Key encapsulation, secure enclaves

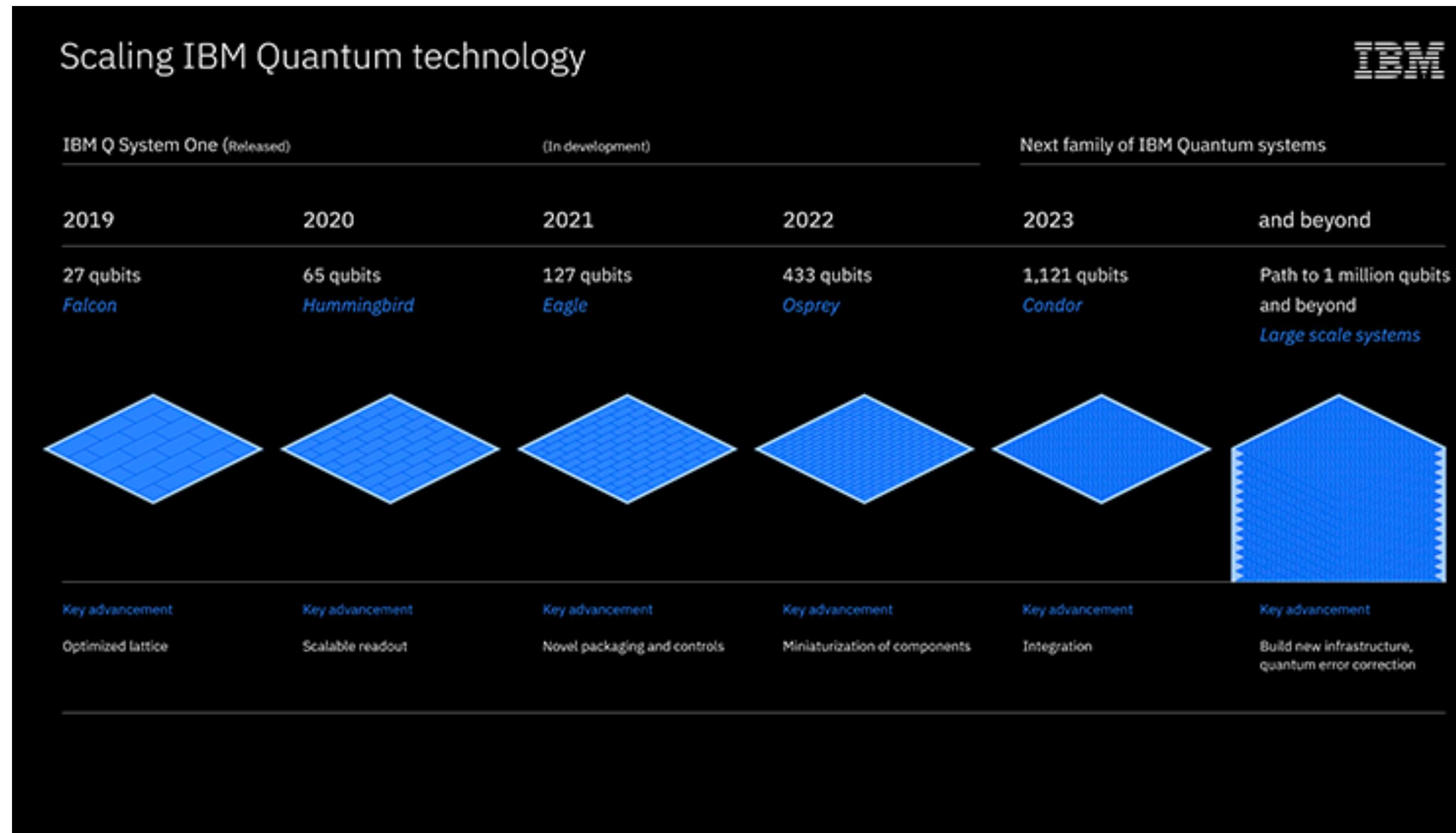
# How many quits in a quantum computer?



# How many quits in a quantum computer?



# Quantum computer today



Footnote: “and beyond” might be in a long time, if ever :)

# Is D-Wave a threat to crypto?

"The Quantum Computing Company"

**D-Wave's 5,000-qubit quantum computing platform handles 1 million variables**

Emil Protalinski @EPro September 29, 2020 7:45 AM f t in



# Is D-Wave a threat to crypto?

## No

D-Wave machines just do **quantum annealing**, not the real thing

- Quantum version of simulated annealing
- Dedicated hardware for specific optimization problems
- **Can't run Shor**, so can't break crypto, boring

Not about scalable, fault-tolerant, universal quantum computers

Quantum speed-up yet to be demonstrated

# Designing a Million-Qubit Quantum Computer Using Resource Performance Simulator

Muhammad Ahsan, Rodney Van Meter, Jungsang Kim

(Submitted on 2 Dec 2015)

The optimal design of a fault-tolerant quantum computer involves finding an appropriate balance between the burden of large-scale integration of noisy components and the load of improving the reliability of hardware technology. This balance can be evaluated by quantitatively modeling the execution of quantum logic operations on a realistic quantum hardware containing limited computational resources. In this work, we report a complete performance simulation software tool capable of (1) searching the hardware design space by varying resource architecture and technology parameters, (2) synthesizing and scheduling fault-tolerant quantum algorithm within the hardware constraints, (3) quantifying the performance metrics such as the execution time and the failure probability of the algorithm, and (4) analyzing the breakdown of these metrics to highlight the performance bottlenecks and visualizing resource utilization to evaluate the adequacy of the chosen design. Using this tool we investigate a vast design space for implementing key building blocks of Shor's algorithm to factor a 1,024-bit number with a baseline budget of 1.5 million qubits. We show that a trapped-ion quantum computer designed with twice as many qubits and one-tenth of the baseline infidelity of the communication channel can factor a 2,048-bit integer in less than five months.

# Factoring 2048 RSA integers in 177 days with 13 436 qubits and a multimode memory

Élie Gouzien\* and Nicolas Sangouard†

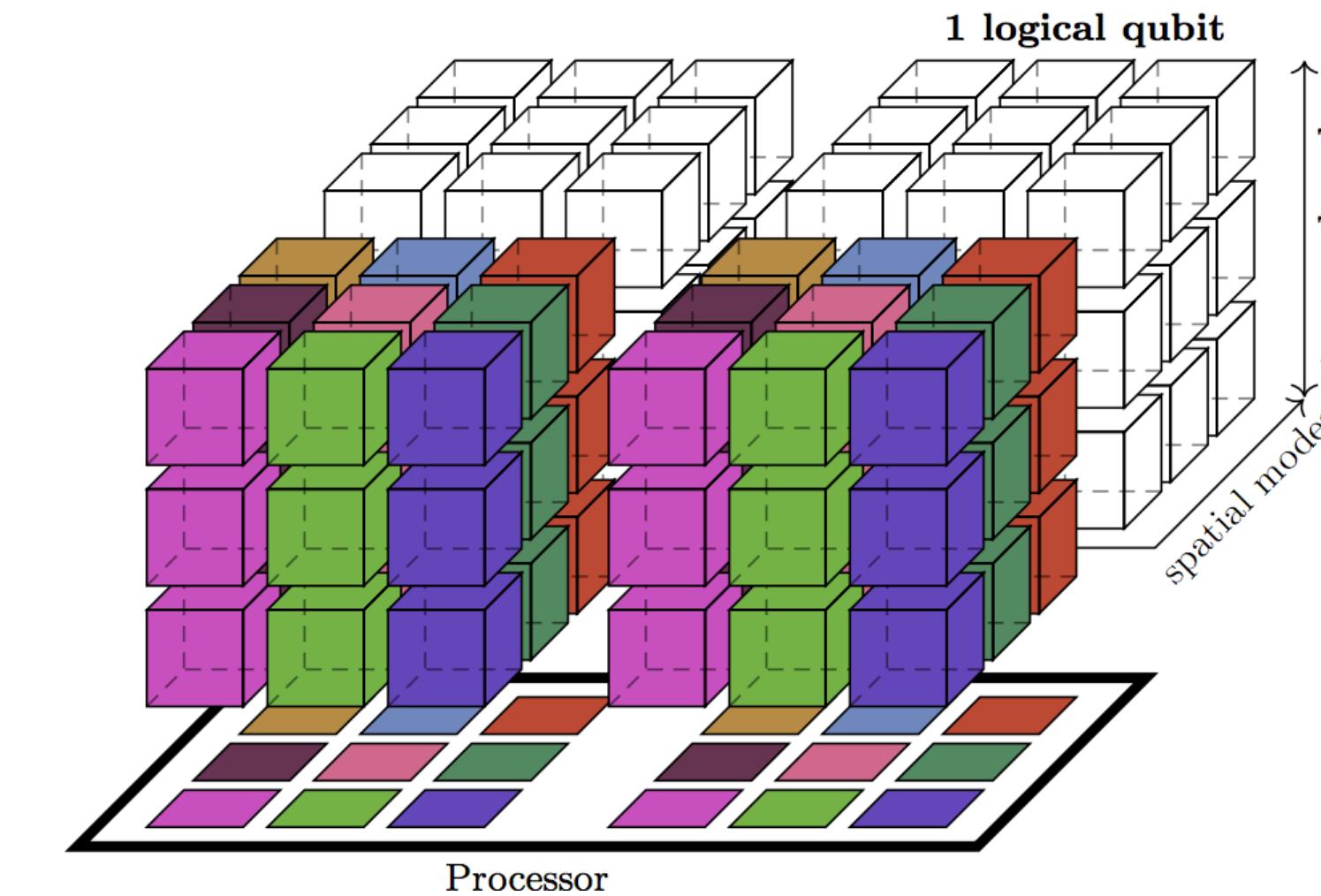
*Université Paris-Saclay, CEA, CNRS, Institut de physique théorique, 91 191 Gif-sur-Yvette, France*

(Dated: March 11, 2021)

We analyze the performance of a quantum computer architecture combining a small processor and a storage unit. By focusing on integer factorization, we show a reduction by several orders of magnitude of the number of processing qubits compared to a standard architecture using a planar grid of qubits with nearest-neighbor connectivity. This is achieved by taking benefit of a temporally and spatially multiplexed memory to store the qubit states between processing steps. Concretely, for a characteristic physical gate error rate of  $10^{-3}$ , a processor cycle time of 1 microsecond, factoring a 2048 bits RSA integer is shown possible in 177 days with a processor made with 13 436 physical qubits and a multimode memory with 2 hours storage time. By inserting additional error-correction steps, storage times of 1 second are shown to be sufficient at the cost of increasing the runtime by about 23 %. Shorter runtimes (and storage times) are achievable by increasing the number of qubits in the processing unit. We suggest realizing such an architecture using a microwave interface between a processor made with superconducting qubits and a multiplexed memory using the principle of photon echo in solids doped with rare-earth ions.

*Introduction* — Superconducting qubits form the building blocks of one of the most advanced platforms for realizing quantum computers [1]. The standard architecture consists in laying superconducting qubits in a 2D grid and making the computation using only neighboring interactions. Recent estimations showed however that fault-tolerant realizations of various quantum algorithms with this architecture would require millions physical qubits [2–4]. These performance analyses naturally raise the question of an architecture better exploiting the potential of superconducting qubits.

In developing a quantum computer architecture we have much to learn from classical computer architectures



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Sam Jaques  
@sejaques

Replying to @veorq

Very important caveat: it needs 430 million "memory qubits"

Craig Gidney @CraigGidney · Mar 15

Replies to @quantumVerd @KikeSolanoPhys and 4 others

The paper uses a cost model where quantum memory is comparatively cheap. I'd have included the mem qubit count in the title (at n=2048 there's 13K compute qubits and 430M mem qubits) but don't see anything wrong with considering a world where mem ends up cheaper than cpu.

5:22 PM · Mar 16, 2021 · Twitter for Android

AES vs. quantum search

# AES

NIST's “**Advanced Encryption Standard**”

- THE symmetric encryption standard
- Supports keys of 128, 192, or 256 bits
- **Everywhere**: TLS, SSH, IPsec, quantum links, etc.

# Quantum search

**Grover's** algorithm: searches in  $N$  items in  $\sqrt{N}$  queries!

=> AES broken in  $\sqrt{2^{128}} = 2^{64}$  operations

**Caveats** behind this simplistic view:

- It's actually  **$O(\sqrt{N})$** , constant factor in  $O()$ 's may be huge
- Doesn't easily parallelize as classical search does

# Quantum-searching AES keys

$k$	$T$	#gates Clifford	depth		#qubits
			$T$	overall	
128	$1.19 \cdot 2^{86}$	$1.55 \cdot 2^{86}$	$1.06 \cdot 2^{80}$	$1.16 \cdot 2^{81}$	2,953
192	$1.81 \cdot 2^{118}$	$1.17 \cdot 2^{119}$	$1.21 \cdot 2^{112}$	$1.33 \cdot 2^{113}$	4,449
256	$1.41 \cdot 2^{151}$	$1.83 \cdot 2^{151}$	$1.44 \cdot 2^{144}$	$1.57 \cdot 2^{145}$	6,681

**Table 5.** Quantum resource estimates for Grover's algorithm to attack AES- $k$ , where  $k \in \{128, 192, 256\}$ .

<https://arxiv.org/pdf/1512.04965v1.pdf>

If gates are the size of a hydrogen atom (12pm) this depth is the **diameter of the solar system** ( $\sim 10^{13}$ m)  
(Yet worth less than 5 grams of hydrogen)

No doubts more efficient circuits will be designed...

# Quantum-searching AES keys

From February 2020, better circuits found

## Implementing Grover oracles for quantum key search on AES and LowMC

Samuel Jaques<sup>1\*†</sup>, Michael Naehrig<sup>2</sup>, Martin Roetteler<sup>3</sup>, and Fernando Virdia<sup>4†‡</sup>

scheme	$r$	#Clifford	# $T$	# $M$	$T$ -depth	full depth	width	$G$ -cost	$DW$ -cost	$p_s$
AES-128	1	$1.13 \cdot 2^{82}$	$1.32 \cdot 2^{79}$	$1.32 \cdot 2^{77}$	$1.48 \cdot 2^{70}$	$1.08 \cdot 2^{75}$	1665	$1.33 \cdot 2^{82}$	$1.76 \cdot 2^{85}$	$1/e$
AES-128	2	$1.13 \cdot 2^{83}$	$1.32 \cdot 2^{80}$	$1.32 \cdot 2^{78}$	$1.48 \cdot 2^{70}$	$1.08 \cdot 2^{75}$	3329	$1.34 \cdot 2^{83}$	$1.75 \cdot 2^{86}$	1
AES-192	2	$1.27 \cdot 2^{115}$	$1.47 \cdot 2^{112}$	$1.47 \cdot 2^{110}$	$1.47 \cdot 2^{102}$	$1.14 \cdot 2^{107}$	3969	$1.50 \cdot 2^{115}$	$1.11 \cdot 2^{119}$	1
AES-256	2	$1.56 \cdot 2^{147}$	$1.81 \cdot 2^{144}$	$1.81 \cdot 2^{142}$	$1.55 \cdot 2^{134}$	$1.29 \cdot 2^{139}$	4609	$1.84 \cdot 2^{147}$	$1.45 \cdot 2^{151}$	$1/e$
AES-256	3	$1.17 \cdot 2^{148}$	$1.36 \cdot 2^{145}$	$1.36 \cdot 2^{143}$	$1.55 \cdot 2^{134}$	$1.28 \cdot 2^{139}$	6913	$1.38 \cdot 2^{148}$	$1.08 \cdot 2^{152}$	1

Grover is not a problem...

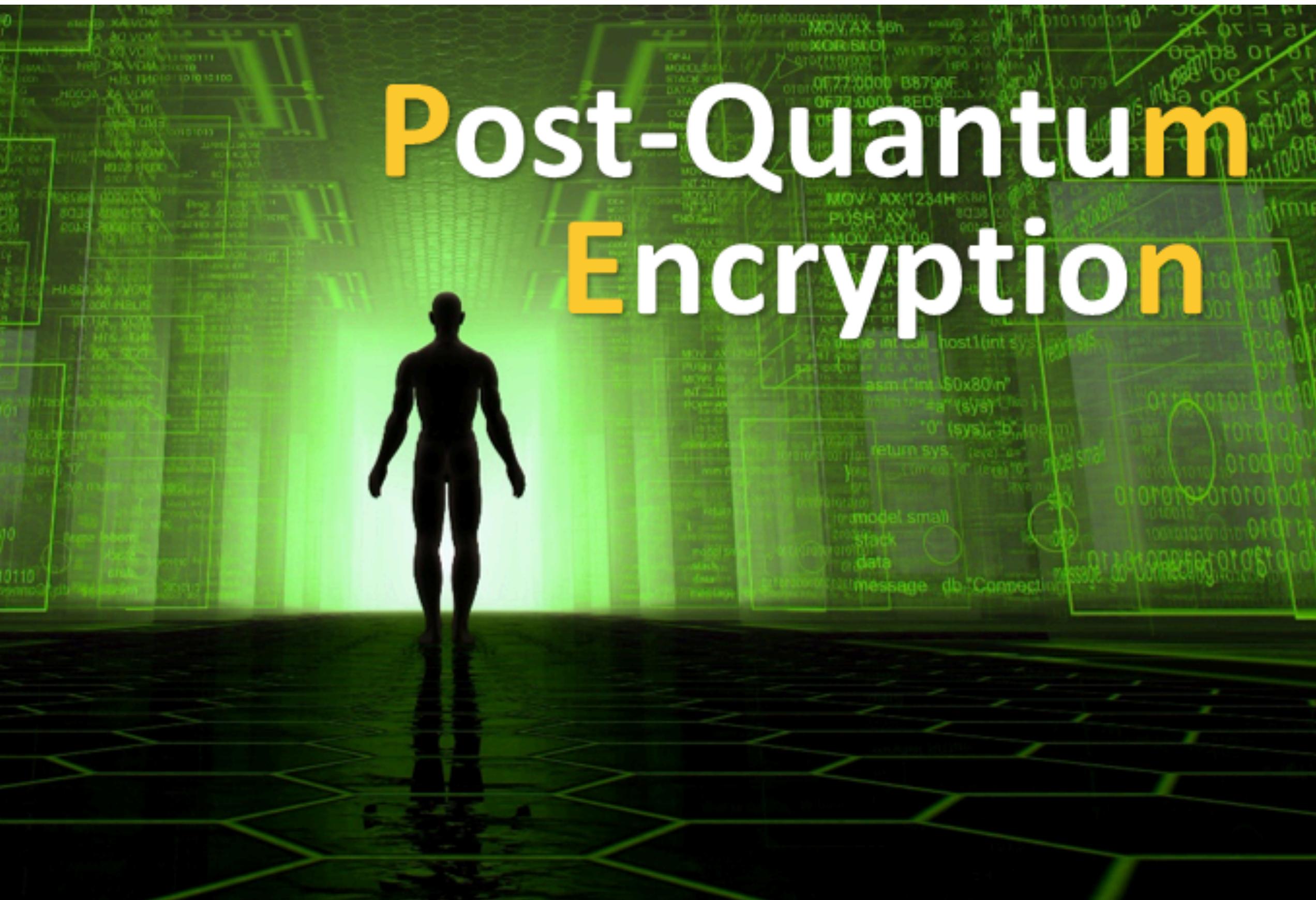
... just double key length

And that's it, problem solved!



Defeating quantum computing

# Post-Quantum Encryption



# Post-quantum crypto

A.k.a. “quantum-safe”, “quantum-resilient”

Algorithms not broken by a quantum computer...

- Must not rely on factoring or discrete log problems
- Must be well-understood with respect to quantum

Have sometimes been broken.. classically 

# Why care?

**Insurance** against QC threat:

- “QC has a probability  $p$  work in year 2YYY”
- “I’d like to eliminate this risk”

# Why care?

**NSA** recommendations for National Security Systems

"we anticipate a need to shift to quantum-resistant cryptography in the near future."

(In CNSS advisory 02-15)



# Why care?

[CSRC HOME](#) > [GROUPS](#) > [CT](#) > POST-QUANTUM CRYPTOGRAPHY PROJECT

## POST-QUANTUM CRYPTO PROJECT

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**NEWS -- August 2, 2016:** The National Institute of Standards and Technology (NIST) is requesting comments on a new process to solicit, evaluate, and standardize one or more quantum-resistant public-key cryptographic algorithms. Please see the Post-Quantum Cryptography Standardization menu at left.

Fall 2016	Formal Call for Proposals
Nov 2017	Deadline for submissions
Early 2018	Workshop - Submitter's Presentations
3-5 years	Analysis Phase - NIST will report findings <i>1-2 workshops during this phase</i>
2 years later	Draft Standards ready

# NIST's project today

## **“Round 3 Finalists”**

- 3 signature schemes
- 4 encryption and key establishment algorithms

Of these 7 algorithm, 5 are of the “lattice-based” type

# Lattice-based crypto

Based on problems such as **learning with errors** (LWE):

- **S** a secret vector of numbers modulo q
- Receive pairs for  $(\mathbf{A}, \mathbf{B} = \langle \mathbf{S}, \mathbf{A} \rangle + \mathbf{E})$ 
  - $\mathbf{A} = (\mathbf{A}_0, \dots, \mathbf{A}_{n-1})$ : **known**, *uniform-random*
  - $\langle \mathbf{S}, \mathbf{A} \rangle = (\mathbf{S}_0^* \mathbf{A}_0, \dots, \mathbf{S}_{n-1}^* \mathbf{A}_{n-1})$
  - $\mathbf{E} = (\mathbf{E}_0, \dots, \mathbf{E}_{n-1})$ : **unknown**, *normal-random*
  - $\mathbf{B} = (\mathbf{B}_i)_{i=0,\dots,n-1} = (\mathbf{S}_i^* \mathbf{A}_i + \mathbf{E}_i)_{i=0,\dots,n-1}$

Goal: find **S**, or just distinguish  $(\mathbf{A}, \mathbf{B})$  from uniform-random

# Lattice-based crypto

TECHNOLOGY

## Google Experimenting With ‘New Hope’ Post-Quantum Encryption To Safeguard Chrome

 Security Blog

The latest news and insights from Google on security and safety on the Internet

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Experimenting with Post-Quantum Cryptography  
July 7, 2016

# Lattice-based crypto

## Google's Post-Quantum Cryptography

News [has been bubbling](#) about an [announcement](#) by Google that it's starting to experiment with public-key cryptography that's resistant to cryptanalysis by a quantum computer. Specifically, it's experimenting with the [New Hope algorithm](#).

It's certainly interesting that Google is thinking about this, and probably okay that it's available in the Canary version of Chrome, but this algorithm is by no means ready for operational use. Secure public-key algorithms are *very* hard to create, and this one has not had nearly enough analysis to be trusted. Lattice-based public-key cryptosystems such as New Hope are particularly subtle -- and we cryptographers are still learning a lot about how they can be broken.

Targets are important in cryptography, and Google has turned New Hope into a good one. Consider this an opportunity to advance our cryptographic knowledge, not an offer of a more-secure encryption option. And this is the right time for this area of research, before quantum computers make discrete-logarithm and factoring algorithms obsolete.

# Challenges with lattices

- Estimate the **security level** for given parameters
- Protect against **side-channel** attacks (esp. sampling step)

# More post-quantumness

- Based on **coding theory** (McEliece, Niederreiter):
  - Solid foundations (late 1970s)
  - Large keys (dozen kB)
  - *Encryption only*
- Based on **multivariate polynomials** evaluation
  - Secure in theory, not always in practice
  - *Mostly for signatures*

# Hash functions to the rescue

# Hash functions



- Input of any size, output of 256 or 512 bits
- Can't invert, can't find collisions
- BLAKE3, SHA-3, SHA-256, ~~SHA-1, MD5...~~

# Hash-based signatures

**Unique** compared to other post-quantum schemes:

- No mathematical/structured hard problem
- As secure as underlying hash functions
- Good news: we have secure hash functions!

# Hash-based signatures

But there's a catch...

# Hash-based signatures

- Not fast (but not always a problem)
- Large signatures (dozen of kB<sub>s</sub>)
- Statefulness problem...

# One-time signatures

Lamport, 1979:

1. Generate a key pair
  - Pick random strings  $\mathbf{K}_0$  and  $\mathbf{K}_1$  (your **private key**)
  - The public key is the two values  $\mathbf{H}(\mathbf{K}_0)$ ,  $\mathbf{H}(\mathbf{K}_1)$
2. To sign the bit 0, show  $\mathbf{K}_0$ , to sign 1 show  $\mathbf{K}_1$

# One-time signatures



- Need as many keys as there are bits
- A key can only be used once

# Sign more than 0 and 1

Winternitz, **1979**:

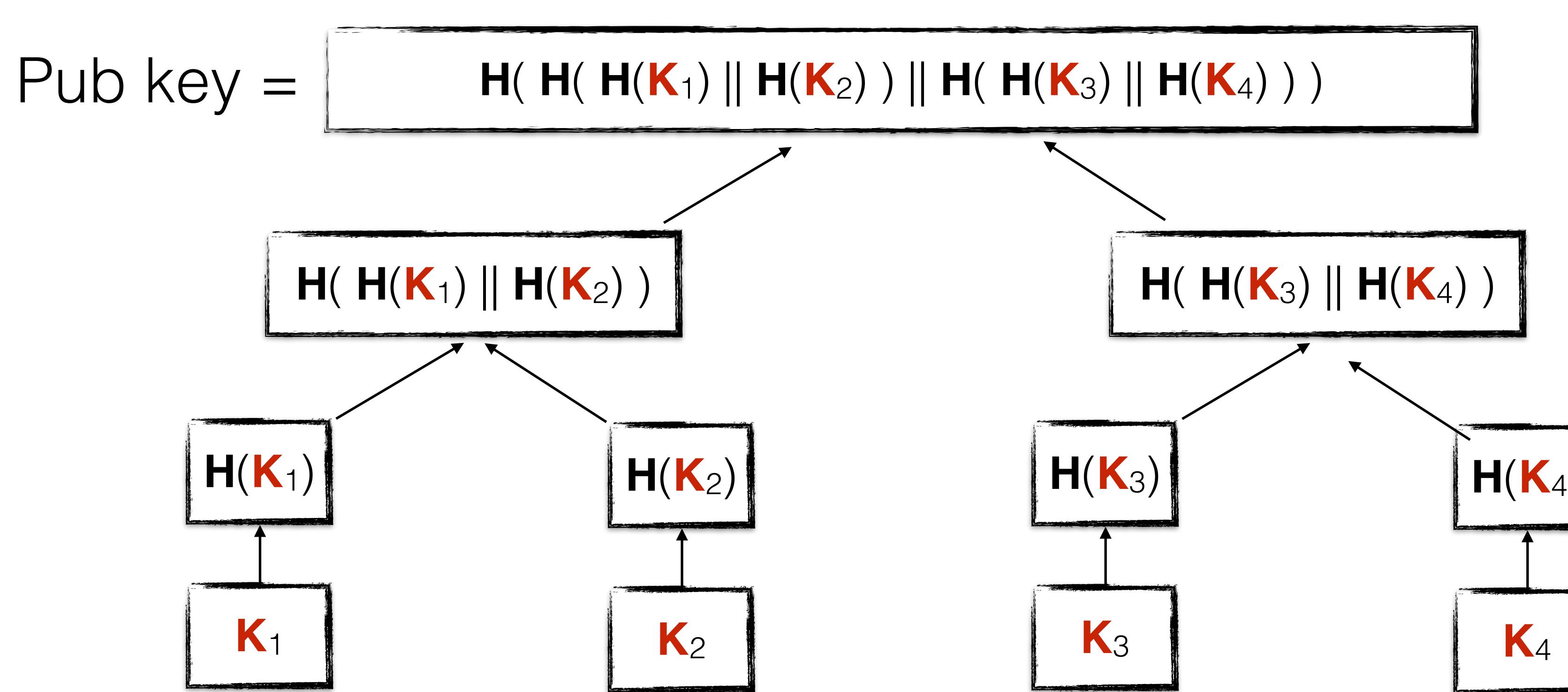
1. Public key is  $\mathbf{H}(\mathbf{H}(\mathbf{H}(\mathbf{H}(\dots (\mathbf{K})\dots))) = \mathbf{H}^w(\mathbf{K})$ . ( $w$  times)
2. To sign a number  $\mathbf{x}$  in  $[0; w - 1]$ , compute  $\mathbf{S}=\mathbf{H}^{\mathbf{x}}(\mathbf{K})$

Verification: check that  $\mathbf{H}^{w-x}(\mathbf{S}) = \text{public key}$

*A key must still be used only once*

# From one-time to many-time

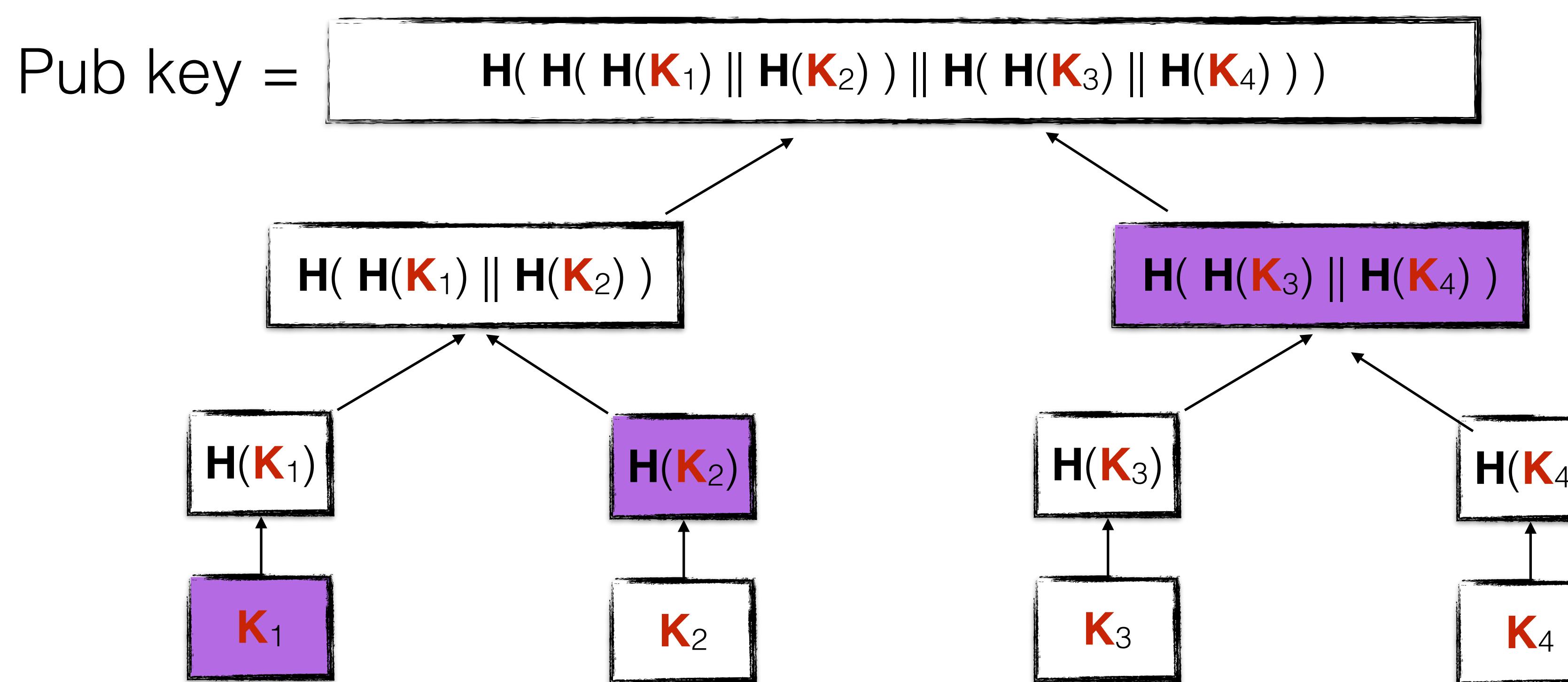
“Compress” a list of one-time keys using a **hash tree**



# From one-time to many-time

When a new **one-time public key  $\mathbf{K}_i$** , is used...

... give its **authentication path** to the root pub key



# Using PQC today

RFC 8391 (XMSS signatures), available in OpenSSH

Open quantum safe: fork of OpenSSL

The image displays four GitHub repository cards arranged in a 2x2 grid, each representing a different project in the field of post-quantum cryptography:

- open-quantum-safe / liboqs**: A C library for quantum-safe cryptography. It has 19 issues, 4 pull requests, and 0 actions. It is a fork of OpenSSL. The repository URL is <https://openquantumsafe.org/>. It uses the following tags: cryptography, key-exchange-algorithms, lattice-based-crypto, and post-quantum-cryptography.
- mupq / pqm4**: A Post-quantum crypto library for the ARM Cortex-M4. It has 3 issues, 0 pull requests, and 0 actions. It is a fork of OpenSSL. The repository URL is [pqm4](#). It uses the tag post-quantum-cryptography.
- PQClean / PQClean**: Clean, portable, tested implementations of post-quantum cryptography. It has 19 issues, 3 pull requests, and 0 actions. It is a fork of OpenSSL. The repository URL is [PQClean](#). It uses the tags post-quantum, cryptography, implementations, and c.
- pqshield.com**: A logo featuring the text "pqSHIELD" over a background of binary code and a shield shape.

# Conclusion

# When/if a scalable and quantum computer is built...

- Public keys could be broken after some effort...
- Symmetric-key security will be at most halved

# Post-quantum crypto..

- Would not be defeated by quantum computers
- Post-quantum crypto NIST competition
  - All submissions and their code soon public
  - Standardized algorithm available in ~2 years
  - Experimental solutions available today