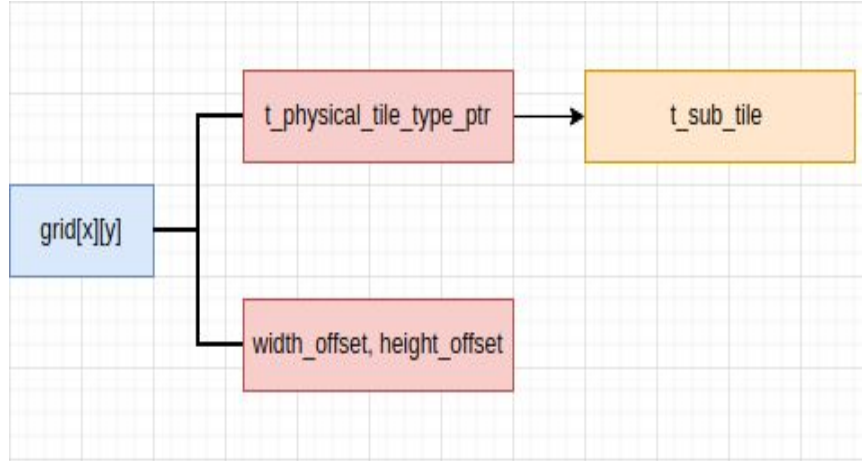


# Modeling multi-die stack FPGAs

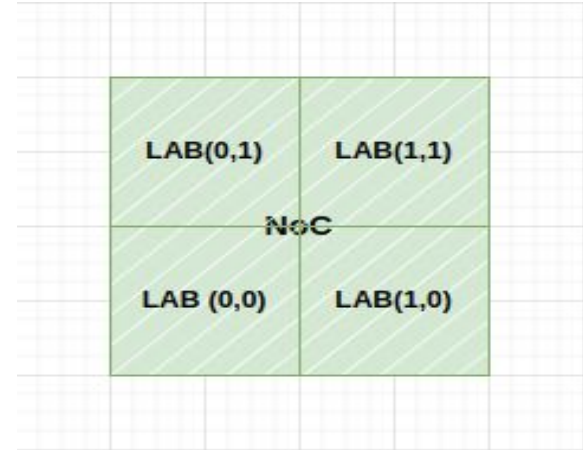
Sara Mahmoudi

# How VPR currently looks like

Current code data structures

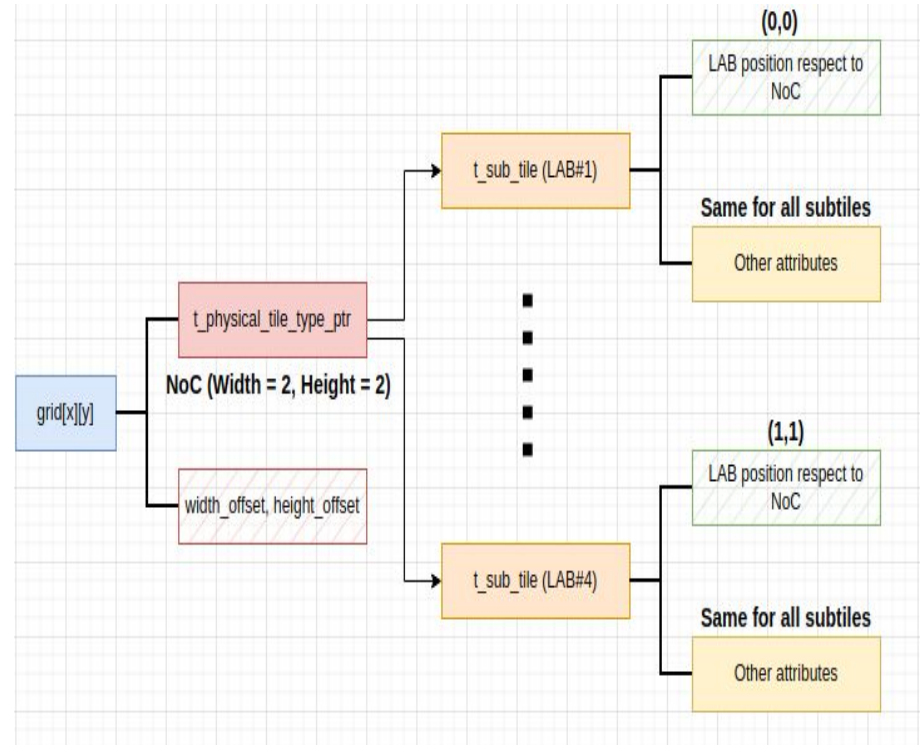


The physical blocks that we are trying to model



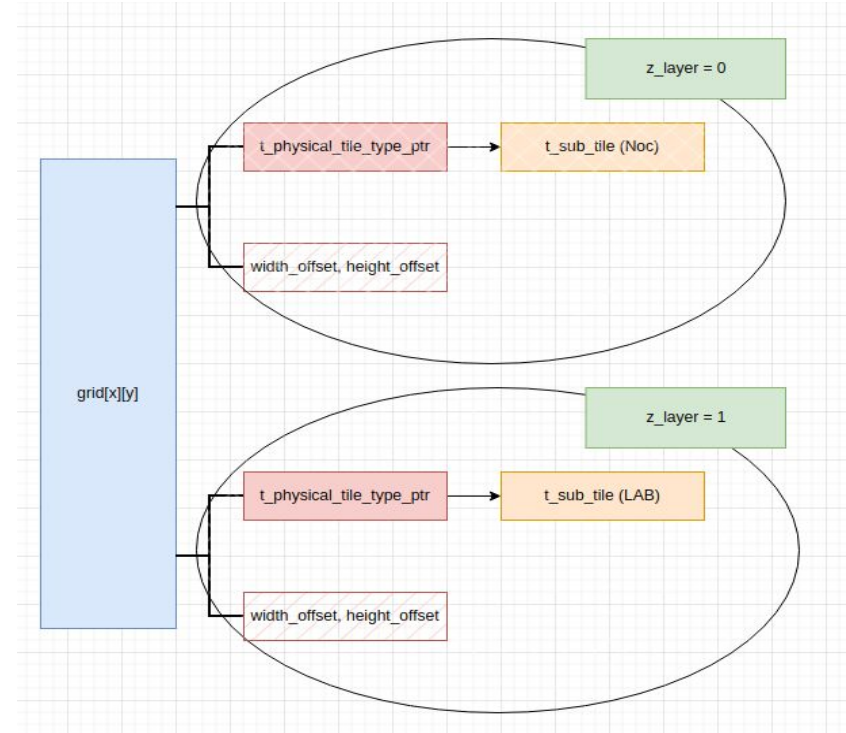
# First solution

- Keep the current sub\_tiles and tiles data structure (which can not currently model sub\_tiles with different size on top of each other)
- Add width\_offset, height\_offset to sub\_tile structure as well as the grid to specify anchor positions for LABs on top of a NoC block
- **Very straightforward approach and easy to implement.**
- **Have to store multiple sub\_tiles for LAB type since their anchor positions are different (which might be a bad solution if NoC size is large (10X10))**



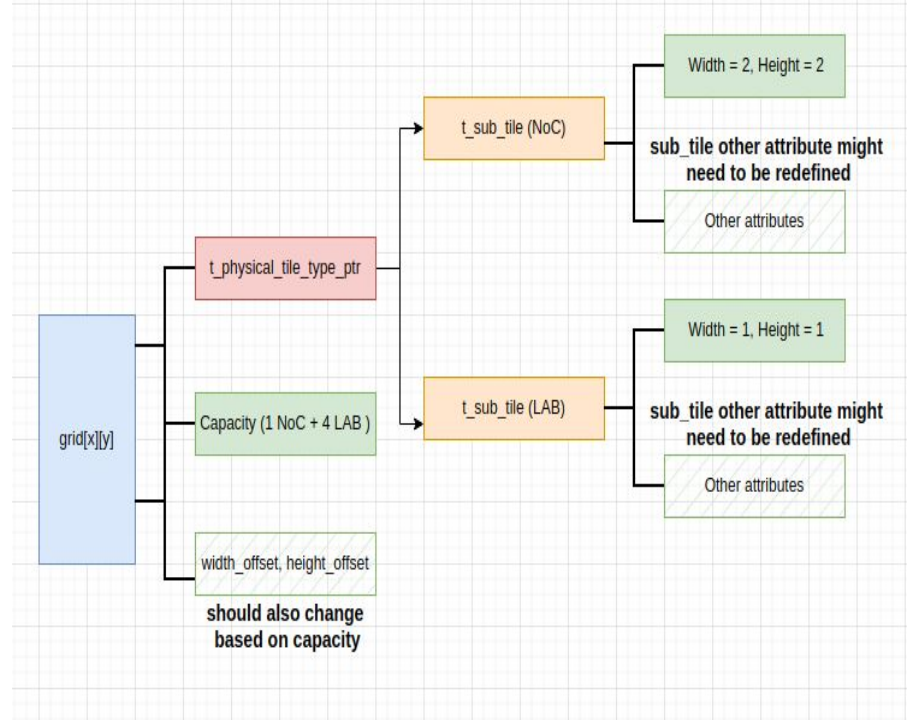
# Second solution

- Keep the current sub\_tiles and tiles data structure.
- Add an explicit z dimension to grid and a layer capacity that would specify how many physical tiles were stacked at that point.
- Access to `grid[x][y]` will be converted to `grid[x][y][z_layer]`.
- Existing code should work since we do not change the current sub\_tiles and tiles definitions and meaning.
- A lot of code changes since grid is widely used in different part of VPR (RR graph, placement, and etc)
- Might become slightly slower since code has to work with multiple layers.



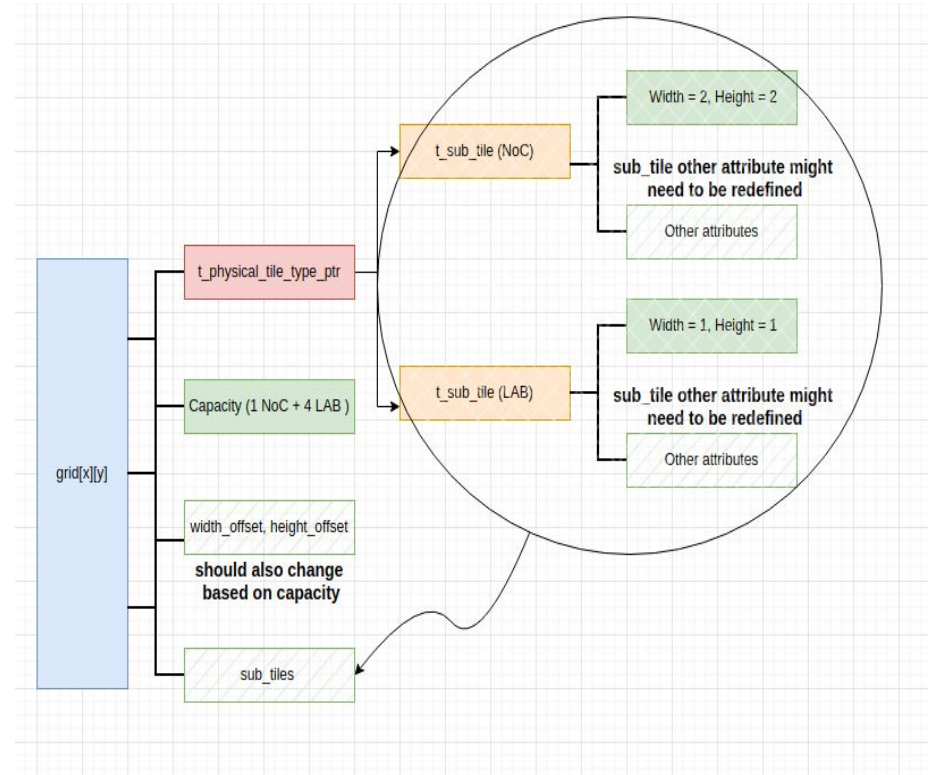
# Third solution

- Redefine sub\_tile and possibly tile data structure.
- Add a capacity attribute to grid data structure, and also adding some attribute (e.g. width and height) to sub\_tile itself.
- Keep multiple width and height offset for each sub\_tile (the exact number would be capacity)
- Less code changes.
- Will probably break the existing code while re-imagining sub\_tiles and tiles data structures (e.g. physical\_tile currently groups multiple sub\_tiles and allows their pins to be grouped and located around the composite physical\_tile).



# Fourth solution

- Move sub\_tile data structure from physical\_tile to grid.
- Add a capacity attribute to grid which shows how many sub\_tiles we have at the current location.
- Keeps multiple sub\_tiles with current attribute (plus some new attributes such as width and height), but no redefinition is required.
- Most of existing code should still be working since we do not change the sub\_tile definition with a few changes.
- More code changes (but less than second solution) since grid is widely used throughout the VPR.



# Conclusion

- Adding an explicit z dimension (second solution) seems to be the best proposal.
- Better code readability and maintenance.
- No need to change any existing architecture files.
- Will not break any existing code since main data structures remain untouched.