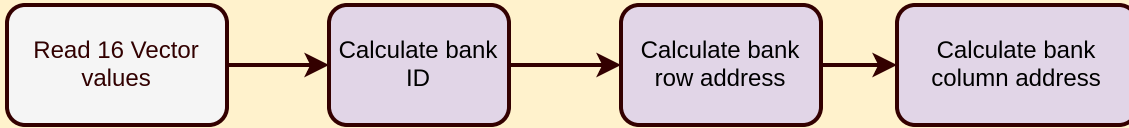


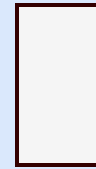
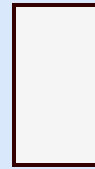
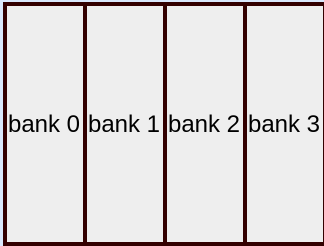
Vector Writer



Write 4 values per URAM

On-Chip Memory

18 bits

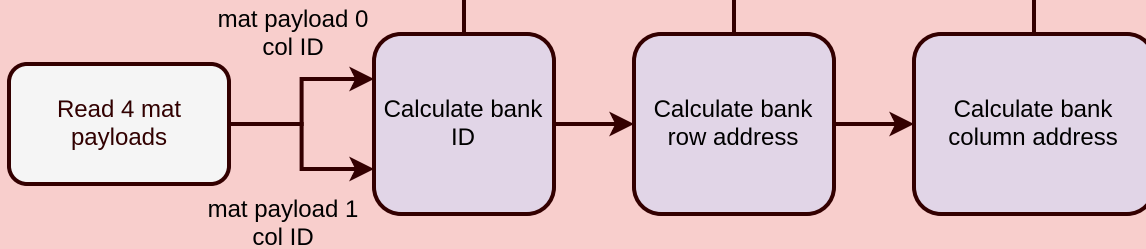


bank column address = ID % 4

bank ID = (ID / 4) % 4

bank row address = (ID / 16) % BANK_SIZE

Vector Reader



Read 2 vec values

Dense output packets

mat val 0	vec val 0	row id 0
mat val 1	vec val 1	row id 1
mat val 2	vec val 1	row id 2
mat val 3	vec val 1	row id 2

Sparse output packets

mat val 0	vec val 0	row id 0
mat val 1	vec val 1	row id 1
mat val 2	0	row id 2
mat val 3	0	row id 2