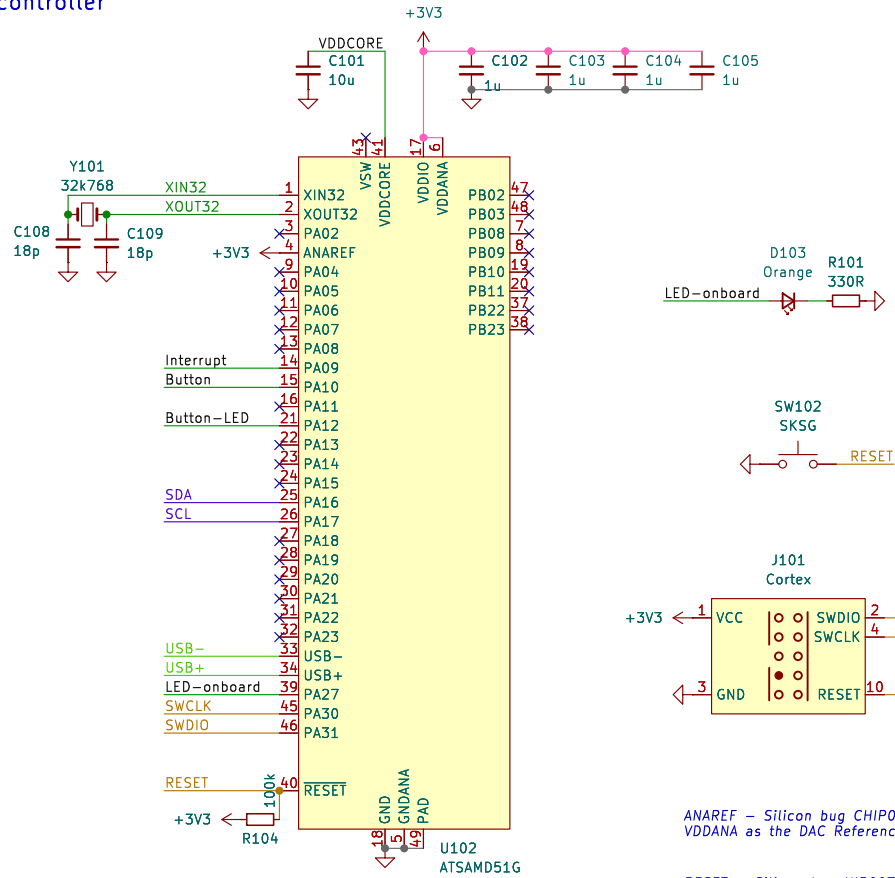


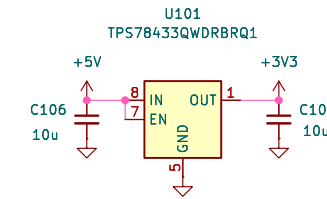
## Microcontroller



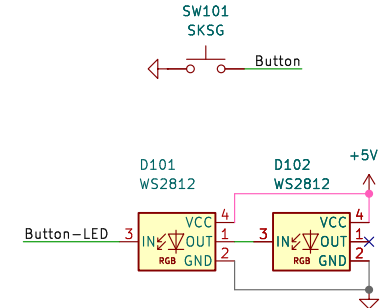
ANAREF – Silicon bug CHIP003–161  
VDDANA as the DAC Reference non-functional.

RESET – Silicon bug HIP003–159  
Internal pull-up non-functional.

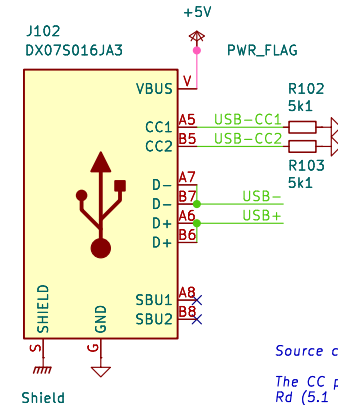
## Power



## Button

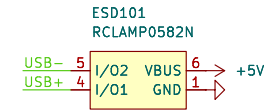


## USB



PWR\_FLAG

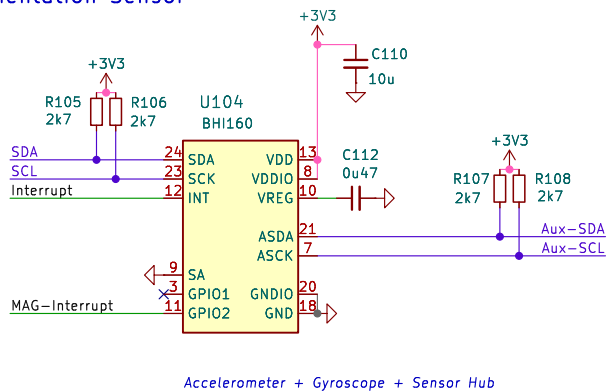
PWR\_FLAG



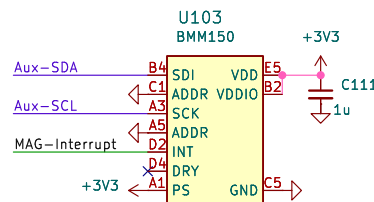
Source connected to a legacy device port.

The CC pins are pulled-down to GND  
Rd (5.1 kΩ ± 20%) to indicate that  
VBUS should be supplied by the host.

## Orientation Sensor



Accelerometer + Gyroscope + Sensor Hub



Magnetometer

## Mechanical / Production

**V2** ICON101

axis ICON102

Revision INFO102

1 2 3 4 INFO101

SWB101

Button-bottom

H101 2mm

H102 2mm

FID101

FID102

FID103

**V2 axis**

Versio Duo

Orientation Sensor

Revision: 2026-01-14

Id: 1/1

Sheet: Root

Updated: 2026-02-10