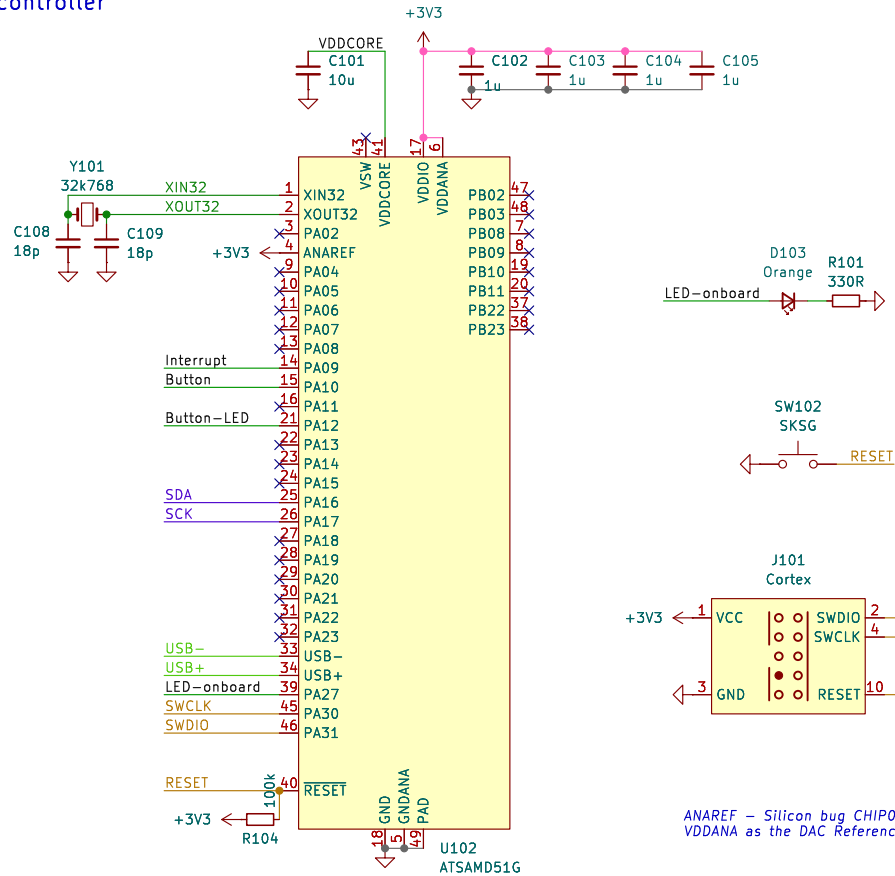


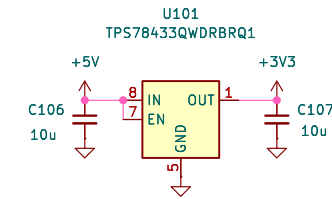
## Microcontroller



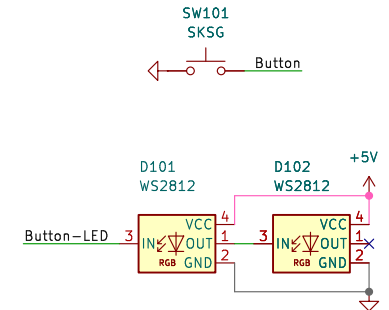
ANAREF – Silicon bug CHIP003–161  
VDDANA as the DAC Reference non-functional.

RESET – Silicon bug HIP003–159  
Internal pull-up non-functional.

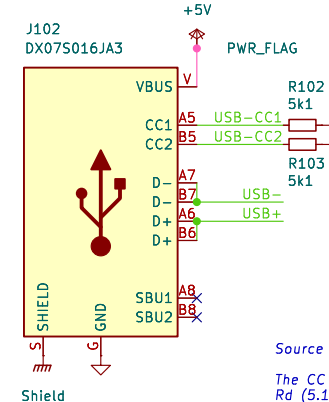
## Power



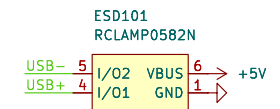
## Button



## USB

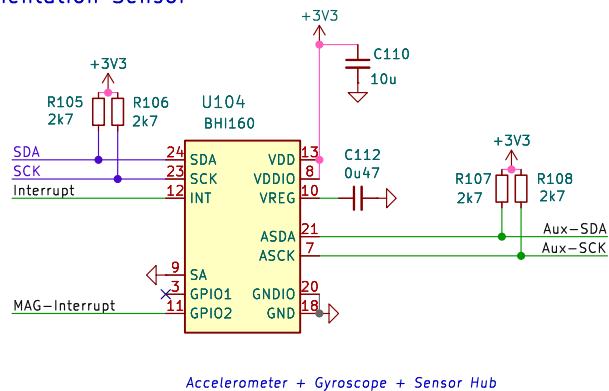


PWR\_FLAG  
PWR\_FLAG  
Shield

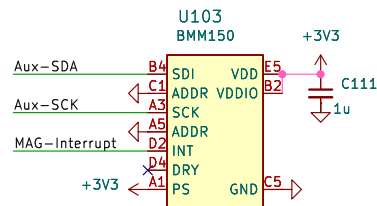


Source connected to a legacy device port.  
The CC pins are pulled-down to GND  
Rd (5.1 kΩ ± 20%) to indicate that  
VBUS should be supplied by the host.

## Orientation Sensor



Accelerometer + Gyroscope + Sensor Hub



Magnetometer

## Mechanical / Production

