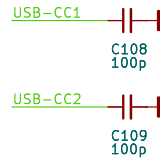
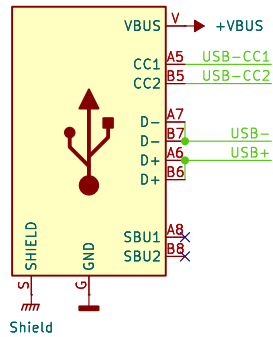


PWR_FLAG ◇ → +VBUS

PWR_FLAG ◇ Shield

PWR_FLAG ◇

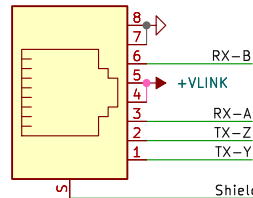
J103
DX07S016JA3



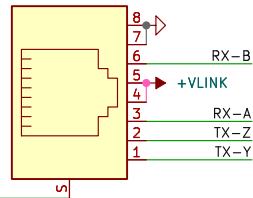
PWR_FLAG ◇ → +VLINK 9-20V, Minimum 250mA

PWR_FLAG ◇

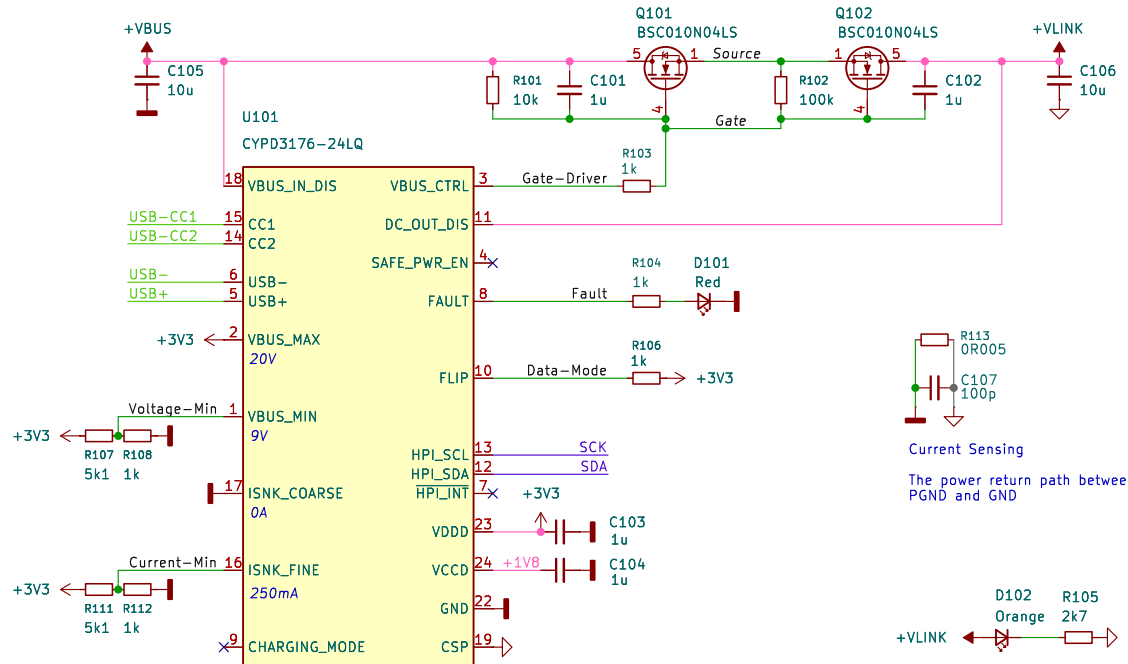
J101
NE8FAH



J102
NE8FAH



PoE Passive +24V
IEEE 802.3af Mode B



Current Sensing
The power return path between
PGND and GND

ICON101

V2

INFO101

ORDER-NUMBER

V2 Power Link USB
Versio Duo

Rev: 1
Id: 1/1
Sheet: Root
Date: 2026-01-19