

1 2 3 4 5 6

A

A

B

B

C

C

D

D

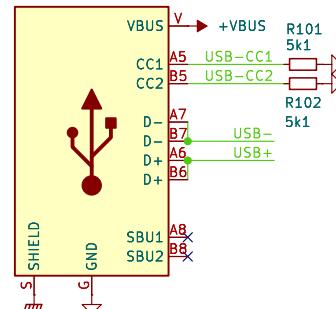
PWR\_FLAG ◊► +VBUS

PWR\_FLAG ◊►

PWR\_FLAG ◊► ─ Shield

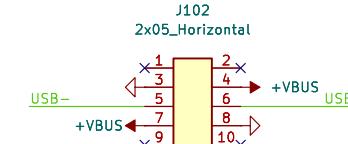
J101

DX07S016JA3



Shield

+VBUS  
C101  
0u1



Source connected to a legacy device port.

The CC pins are pulled-down to GND  
Rd ( $5.1\text{ k}\Omega \pm 20\%$ ) to indicate that  
VBUS should be supplied by the host.

ICON101  
V2



INFO101  
[1 2 3 4]

INFO101

INFO102

ORDER-NUMBER

PLUG101  
PCB\_Plug\_Horizontal



H101  
MountingHole\_2mm



H102  
MountingHole\_2mm

V2 usb  
Versio Duo  
USB Adapter  
Rev: 4  
Id: 1/1  
Sheet: Root  
Date: 2026-01-12

1 2 3 4 5 6