System → Verifier	Line Count			P/C Ratio	Time (s)		SMT (MB)
	trusted	proof	code		1 core	8 cores	
IronKV							
→ Verus	1613	4513	1533	2.9	30	10	15
→ Dafny	1205	8070	1923	4.2	424	187	352
NR							
→ Verus	469	6791	1005	6.8	16	9	20
→ L.Dafny	104	7828	730	10.7	1099	207	2063
Page table	1118	5330	400	13.3	64	30	38
Mimalloc	282	13703	3178	4.3	247	46	127
P. log	1301	2396	739	3.2	10	5	7
Verus total	4.8K	32.7K	6.9K	4.8			