

EC 5311 Digital IC Design: Assignment 8
Adder trees - Maximum operating frequency

1. Add a clock input and register the existing inputs/outputs at the posedge of clock for the three 16-bit tree adders in Assignment 7.
2. Use **OpenLane** to generate layouts and obtain post-layout STA report for the three adders.
3. Find the maximum operating frequency of the input clock for each tree adder.