

Logic level converter with controlled output enable used to ensure tri-state on MOSI pins when LoRa is turned off. DIR1 = LOW when IC202 enabled, hence direction is B1 to A1, A to B for rest IC203 LoRa nEnable G D IC202 NX2301P,215 DIR1 VCCA 15 LoRa_VDD DIR2 VCCB 14 LoRa_RX UART RX A1 В1 4 13 LoRa TX UART_TX D A2 B2 12 LoRa_PA06_ A3 В3 LoRa_PA06 D 6 11 A4 В4 10 GND OE DIR3 9 DIR4 SN74AVC4T774PWR LoRa_nEnable LoRa_nEnable D-

> MARK: LoRa module pins will draw power from nRF GPIO if it is not powered on. This lead to problems with I2C, SPI and UART comunication with other devices. Remove unneeded connections and/or insert LLC to ensure tri-state-inputs if LoRa module should be powered down completely.

REMOVED interface for LoRa:

I2C_SDA - PA16 I2C_SCL - PA17

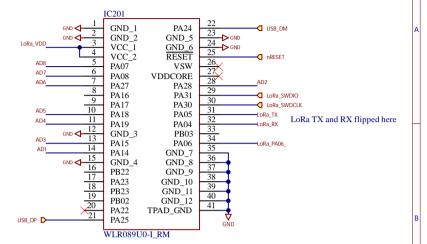
LoRa MOSI - PB22

LoRa MISO - PB02

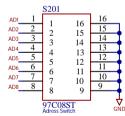
LoRa SCK - PB23

LoRa nCS - PA23

PB03 output removed, insert if LED-blue2 is wanted to be controlled by it



Adress Switch



Functionality must be added to the LoRa software to make use of Adress switches.

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| NTNU | | | |
| Sheet: / | | | |
| File: cSLIM-shield-LoRa.sch | | | |
| Title: cSLIM LoRa Module | | | |
| Size: A4 | Date: 2021-03-31 | | Rev: v1 |
| KiCad E.D.A. kicad (5.1.4)-1 | | ld: 2/11 | |

