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| **Layer** | **File** | **Notes** |
| Top | CrypTech.GTL | Must be plated to 35 um (shown as 19 um in Stackup.doc) |
| Internal 1 | CrypTech.GP1 | Negative Plane |
| Internal 2 | CrypTech.G1 |  |
| Internal 3 | CrypTech.GP2 | Negative Plane |
| Internal 4 | CrypTech.GP3 | Negative Plane |
| Internal 5 | CrypTech.G2 |  |
| Internal 6 | CrypTech.GP4 | Negative Plane |
| Bottom | CrypTech.GBL | Must be plated to 35 um (shown as 19 um in Stackup.doc) |
| Top Silk | CrypTech.GTO |  |
| Top Mask | CrypTech.GTS |  |
| Top Paste | CrypTech.GTP |  |
| Bottom Silk | CrypTech.GBO |  |
| Bottom Mask | CrypTech.GBS |  |
| Bottom Pastre | CrypTech.GBP |  |
| Outline | CrypTech.GM1 |  |

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| **Hole Type** | **File** | **Notes** |
| Plated Round Drills | CrypTech-RoundHoles-Plated.DRL | 0.25 and 0.5 mm vias must be plugged according to IPC4761 type VII |
| Unplated Round Drills | CrypTech-RoundHoles-NonPlated.DRL |  |
| Plated Slot Drills | CrypTech-SlotHoles-Plated.DRL |  |