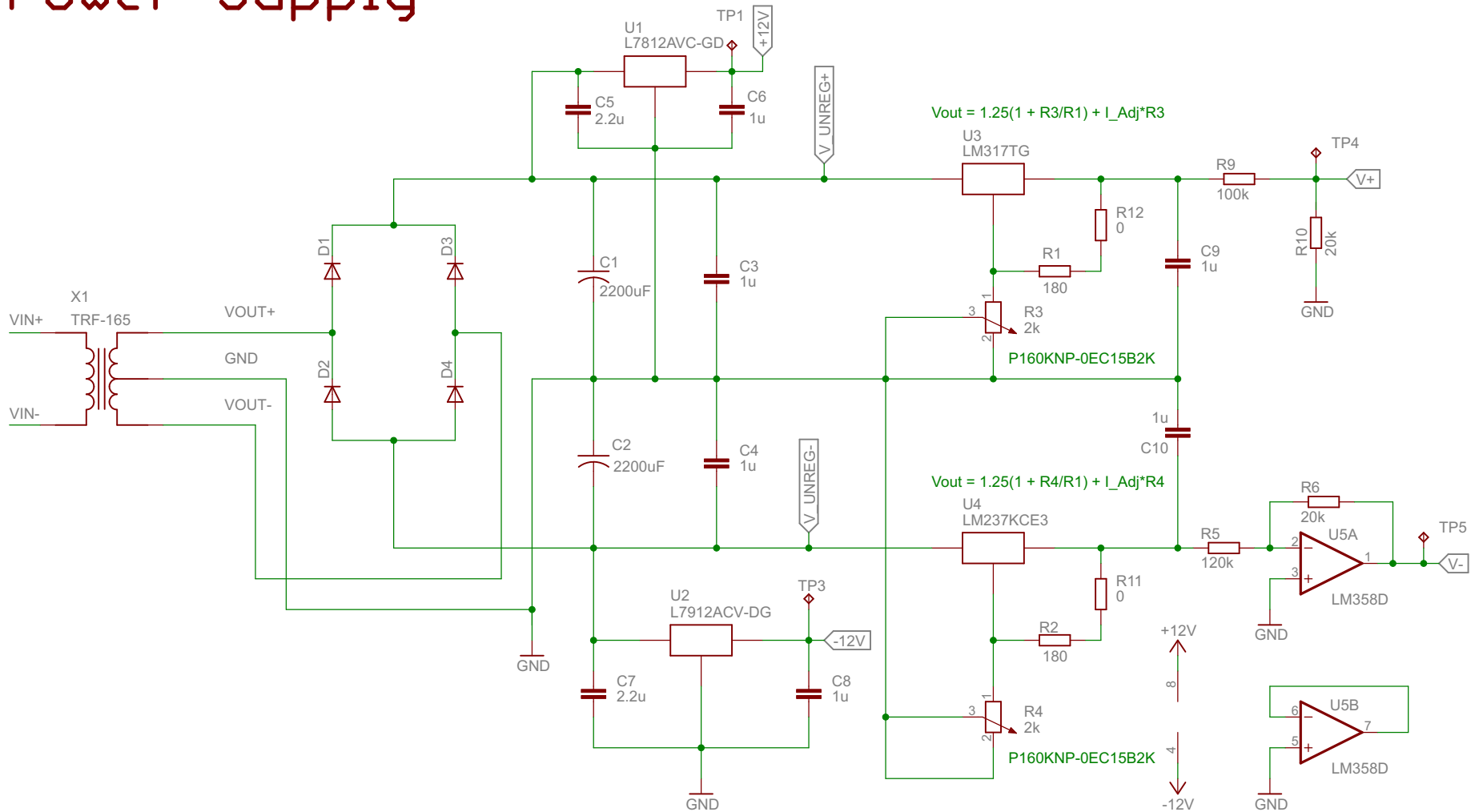


Power Supply



Power_Supply

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Sheet: 1/2

The diagram illustrates a microcontroller-based system with the following components and connections:

- Microcontroller (IC1):** ATMEGA8L-8AU.
 - Power and Reset:** VCC is connected to pins 7, 6, and 18. RESET is connected to pin 29. AVCC and AREF are connected to pins 18 and 20, respectively. AGND is connected to pin 21. GND is connected to pins 3, 5, and 17.
 - Crystal Oscillator:** PB6 (XTAL1/TOSC1) and PB7 (XTAL2/TOSC2) are connected to a crystal (C11) and a 100nF capacitor (C11).
 - ADC Configuration:** PC0 (ADC0) to PC5 (ADC5/SCL) are connected to pins 23-28. ADC6 and ADC7 are connected to pins 19 and 22, respectively. The ADC+ and ADC- inputs are connected to a voltage divider network (R36, R7, D6, D5) with V+, VCC, and V- inputs.
 - Port Configuration:** PD0 (RXD) to PD7 (AIN0) are connected to pins 30-37. PB0 (ICP1) to PB5 (SCK) are connected to pins 12-17.
- Display:** Two 16x2 LCD displays are connected to the microcontroller. The top display is connected to pins 14-16 (A, B, C), 13-15 (D, E, F), and 11-15 (G, H, I, J, K, L, M, N, O, P, Q, R, S, T, U, V, W, X, Y, Z). The bottom display is connected to pins 12-14 (A, B, C), 11-13 (D, E, F), and 9-13 (G, H, I, J, K, L, M, N, O, P, Q, R, S, T, U, V, W, X, Y, Z).
- Other Components:**
 - UNREG+:** A 10uF capacitor (C14) is connected to the UNREG+ pin.
 - Programmer ISP PIN HEADER:** VCC is connected to pin 1, RESET to pin 2, and GND to pin 3.
 - AVR ISP:** MISO is connected to pin 1, MOSI to pin 2, and SCK to pin 3.