

COMP 3050-201 Computer Architecture

Spring 2019 Class Schedule

Updated 3/18/19

(Subject to change – due dates on the assignments prevail)

January 28

ch 2, Integer Representation, Floating Point Representation, review on-line floating point slides (Floating_Point.pdf), read ch2.4 to end of ch 2, complete ch 2, read Mic1 handout

Assign #1 handed out (due 2/6)

February 4

Mic1 Machine Architecture, read Mic1 handout and examples, Mic1 Assembly Level Programming, Mic1 Microcode Instruction Architecture and examples

Assign #2 handed out (due 2/14)

February 11

Mic1 Microcode Instruction Implementation, Using the **mcc** Tool

Assign #3 handed out (due 2/21)

February 19 (Class meets on Tuesday due to Presidents' Day)

Mic1 Adding New Microcode Instructions, examples, Mic1 Implementing new machine level instructions and updating assembler support.

Assign #4 handed out (due ~~3/13~~ 3/20 3/24)

February 25

Mic1 Integrating all the pieces.

Review for exam #1, ch 2 and Mic1 material

March 4

Snow cancellation.

Spring Recess

March 18

Mic-1 UART organization and serial port programming and assignment #5 IO

EXAM #1 on ch 2 and Mic1 material to date

Assign #5 handed out (due ~~4/3~~ 4/5)

March 25

Review exam #1, read ch 7 on Linkers, 7.1 – 7.5

Introduction to linkers, ch 7 Symbol Tables, read ch7.6, Linkers, ch 7 Relocation, read 7.7

Assignment #6 code examples, ch 7 Executables, read ch 7.8 - 7.10, Dynamic Linking, ch 7

Shared Libraries, read 7.11

April 1

Review for exam #2, Exceptional Control Flow

Assign #6 handed out (due ~~4/21~~ 4/24)

April 8**EXAM #2 on ch 7 and 8, and Mic1 IO**

Read ch8.1-8.2, Exceptions and Processes, assignment #7 details and examples, ch 8 Processes and Threads, read ch 8.3 – 8.4

April 9 – Withdraw deadline (with a “W”)**April 19 (Class meets on Friday due to Patriot’s Day)**

Review exam #2, Process Attributes and Control, Finish Processes, finish ch 8, Review ch 8 read ch 6, Locality, read ch6.2 – 6.3, Layered Memory, read ch6.4.1 – 6.4.3, read ch 6 Cache Organizations, read ch6.4.4 – 6.4.7

April 22

Memory hierarchy and Cache organizations, review ch 6, read ch6.5 – 6.6

Finish ch 6 and cache models, cache examples,

Assign #7 handed out (due 5/1)

April 29

Wrap up. Last class day, review for final

May 2 Thursday - No Class - Last day of the semester

Last chance to submit assignments #6 and/or #7... assignment submission queues close at midnight (11:59:59 PM) tonight. No exceptions!

May 6 (6:30-9:30 pm)

Final exam - ch 2, 6, 7, 8 and Assign #1 through Assign #7