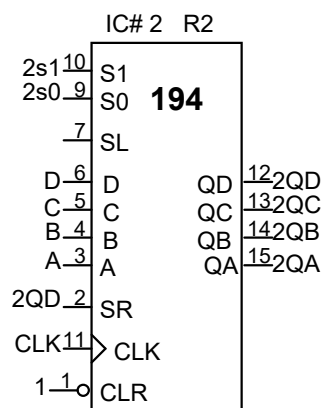
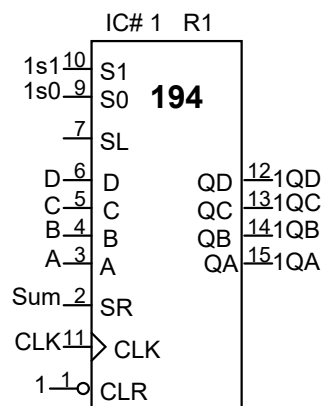
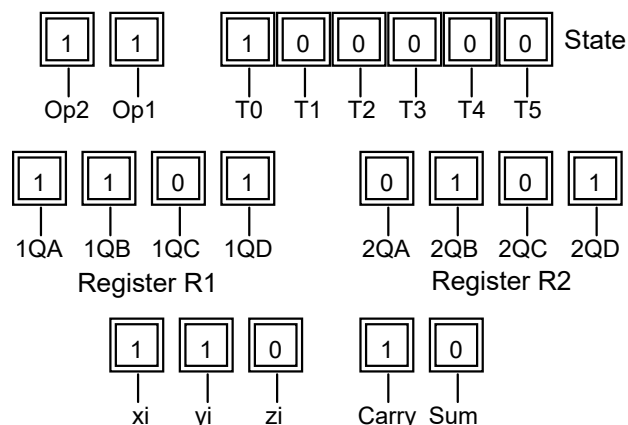
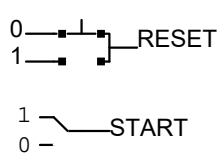
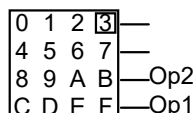


5

16.265 Logic Design
Experiment 5: Arithmetic Processor
Name: Dangnhi Ngo
Student Logic Number: 140
Function Set Number: 12



Use the following signal names for input processor 1QD for ai, 2QD for bi, Op2, Op1, xi, yi, zi

Use the following signal names for control signals
1s1, 1s0, 2s1, 2s0, c0, C2, C1

