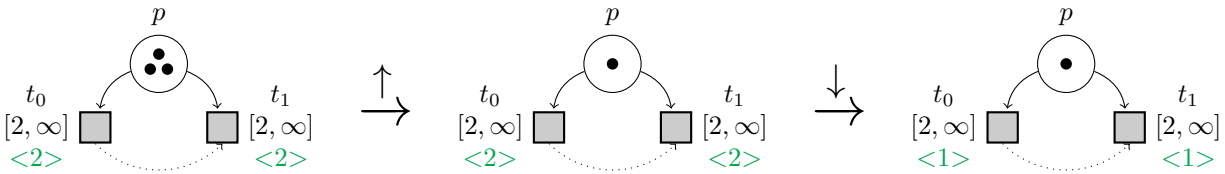

PCI id_p
s_marking = 3
pauths to id_{t_0} = true
pauths to id_{t_1} = true
TCI id_{t_0}
pauths from id_p = true
s_time_counter = 2
fired = true
TCI id_{t_1}
pauths from id_p = true
s_time_counter = 2
fired = true

PCI id_p
s_marking = 1
pauths to id_{t_0} = true
pauths to id_{t_1} = true
TCI id_{t_0}
pauths from id_p = true
s_time_counter = 2
fired = true
TCI id_{t_1}
pauths from id_p = true
s_time_counter = 2
fired = true

PCI id_p
s_marking = 1
pauths to id_{t_0} = true
pauths to id_{t_1} = false
TCI id_{t_0}
pauths from id_p = true
s_time_counter = 2
fired = true
TCI id_{t_1}
pauths from id_p = false
s_time_counter = 2
fired = false

PCI id_p
s_marking = 1
pauths to id_{t_0} = true
pauths to id_{t_1} = false
TCI id_{t_0}
pauths from id_p = true
s_time_counter = 1
fired = true
TCI id_{t_1}
pauths from id_p = false
s_time_counter = 3
fired = false