

UNIVERSITY NAME

DOCTORAL THESIS

Thesis Title

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Research Group Name
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“Thanks to my solid academic training, today I can write hundreds of words on virtually any topic without possessing a shred of information, which is how I got a good job in journalism.”

Dave Barry

UNIVERSITY NAME

Abstract

Faculty Name
Department or School Name

Doctor of Philosophy

Thesis Title

by John SMITH

The Thesis Abstract is written here (and usually kept to just this page). The page is kept centered vertically so can expand into the blank space above the title too...

Acknowledgements

The acknowledgments and the people to thank go here, don't forget to include your project advisor...

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For/Dedicated to/To my...

Chapter 1

Introduction

1.1 Model-based System Engineering and the HILECOP methodology

- present the principles of Model-based System Engineering
- present the context of neuroprostheses and the need for a safe design of digital circuits
- present the HILECOP methodology

1.2 Verifying the HILECOP methodology

- RESEARCH QUESTION (A.K.A THE PROBLEM):

CAN WE PROVE THAT THE HILECOP MODEL-TO-TEXT

TRANSFORMATION IS SEMANTIC-PRESERVING?

- ADDITIONAL QUESTIONS:

- + How do we prove that?
- + What are the similarities with compiler verification?
- + What are the specificities?

- present the task motivation: the creation of safe digital circuits; first step, the behavior of the input model must be preserved in the output VHDL program

- present the model-to-text transformation with more details (but not too much since a whole chapter will be dedicated to it)

→ “inspired by the work on compiler verification, we had an idea about how to prove that the transformation is semantic-preserving, and which steps to follow”.

Then, present the structure of the memoir and the content of the different chapters. The structure of the memoir follows the different steps necessary to establish the proof of semantic preservation. Each chapter corresponds to a step.

Chapter 2

HILECOP high-level models

RESEARCH QUESTION OF THE CHAPTER:

WHICH FORMAL SEMANTICS SHOULD WE CONSIDER
IN THE CASE OF HILECOP HIGH-LEVEL MODELS?

2.1 General facts about Petri nets

- Make a general presentation of Petri nets.
 → maybe put the general presentation of PNs in appendix, and focus here on the PNs semantics.
- Literature review on PNs semantics.
 → don't forget to mention linear logic when talking about PNs semantics
 → CONCLUSION: "we choose to adopt the formal semantics set in previous work".
 → insist on the fact that most of the formalization is not the result of our work, but comes from previous works

2.2 Synchronously executed Petri Nets

- Informal presentation of SITPNs
 → check out what we did for SEFM and PNSE

2.3 Formal definition of SITPNs & semantics

- Present the formal structure of SITPNs, main definitions, and semantics
 → check out SEFM and PNSE, and the last formalization of SITPN semantics (with the new definition of the set of *Fired* transitions)
 → it's better to present the changes made in the semantics in the chapter about the proof
- Present the Coq implementation along the way

CONCLUSION OF THE CHAPTER:

- We will consider the formal semantics defined in previous theses to perform the proof of semantic preservation
- Remind of the contributions added to the formal semantics: simplification, clarification, formalization of conflict resolution and well-defined SITPNs + Coq implementation

Chapter 3

\mathcal{H} -VHDL: a target hardware description language

RESEARCH QUESTION OF THE CHAPTER:

WHICH FORMAL SEMANTICS OF VHDL WILL MOST SUIT OUR NEEDS?

3.1 Presentation of the VHDL language

- Present the main concepts of VHDL
- Present the informal semantics of VHDL, i.e as described in the *Language Reference Manual* (LRM):
 - elaboration
 - simulation
- Present what use is made of VHDL in HILECOP
 - show our simulation cycle
 - present the P and T designs, and maybe a very high-level view of the transformation
 - show a figure of the P and T designs interfaces

→ Don't go in too much details while presenting the use of VHDL in HILECOP. Details of which VHDL constructs are used in our programs will be given in the next section, and details on the transformation will be given in the next chapter.

3.2 Choosing a semantics for VHDL

- Our needs for a formal semantics
 - reminder of the final goal of the thesis
 - Do not describe what the semantics must look like, but rather remind the purpose of the semantics in relation to the PhD goal.
- Research question: which semantics should we consider?
 - express the trade-off between
 1. using existing tools

- 2. creating our own tools
 - Formalization depends on our needs
 - present our needs
 - present the qualifying criterions, and talk about the mandatory hypotheses about stable signals
 - Talking about the mandatory hypotheses first here. Decide the level of precision we want our semantics to have.
 - Presentation of the literature works and relations to our needs

CONLCUSION:

 “We have to set up our own semantics.”

3.3 A natural semantics for \mathcal{H} -VHDL

- Maybe do a quick reminder on abstract syntax and natural semantics before presenting \mathcal{H} -VHDL semantics.
- Take the example of a very simple programming language.

- Abstract syntax
- Semantical domains
 - show implementation choices for Δ and σ
- Elaboration
- Simulation
 - Add examples of derivation of elaboration and simulation rules + P and T design in concrete syntax and abstract syntax, and Coq code.

CONCLUSION OF THE CHAPTER:

Contributions:

- setting a semantics for \mathcal{H} -VHDL, fitting our needs closely, but based on previous formalization works
- implementing the simulation semantics + abs. syntax in Coq

Chapter 4

The HILECOP model-to-text transformation

RESEARCH QUESTION OF THE CHAPTER:

IS THERE A PROPER WAY TO EXPRESS OUR (I.E, HILECOP'S) TRANSFORMATION FUNCTION?

4.1 Expressing transformation functions

- Literature review on compiler verification, model-to-model and model-to-text transformation verification
- Additional questions that the lit. review results try to give an answer to:
 - How to implement the transformation function to ease proof verification?
 - How to implement the transformation function to make it extensible? modular? (thinking about new elements added to the source model)

4.2 Informal presentation of the transformation

- High-level presentation of the transformation
 \rightarrow use the figures of the 2nd CSI
- point out the particularities of HILECOP's transformation function compared to what have been presented in the lit. review.
 \rightarrow don't know if the lit. review will give enough material to do so
 \rightarrow are there cases of model-to-text transformation close enough to our case to enable comparison?

CONCLUSION OF THE SECTION: Hard to express our trans. fun. in a way that facilitate compositional reasoning (if compositional reasoning is at all possible), partly because component instances are connected. However, the lit. review pointed out the necessity to keep a binding between elements of the source models and elements of the target programs (introduction of the γ binder) to enable state comparison, i.e on which relies the proof of semantic preservation.

4.3 The transformation algorithm

→ maybe talk about the fact that the transformation algorithm has been established by observing the behavior of the current HILECOP implementation, and through discussions with the HILECOP designers (a.k.a David Andreu)

- Give the model-to-text transformation algorithm
 - give the alg. as established through our meetings, or maybe use a simpler alg. (without the generate infos part, maybe too much implementation oriented)
- Give the formal specification of the transformation, i.e which structural properties between SITPN elements and \mathcal{H} -VHDL constructs
 - don't know if I'll have the time to do that, but it would be great to have it, and use it in the proof. Each time the proof says "by construction", then the reader can refer to the formal spec
- Present the Coq implementation along the way
 - talk about the state-and-error monad, the γ binder, the use of generic list functions (fold, map, filter, iter...)
- Give a concrete example of generated \mathcal{H} -VHDL code from a simple SITPN

CONCLUSION OF THE CHAPTER: Our transformation function is not really expressible in a compositional way as P and T instances are bound together. It's a specific case of model-to-text transformation. However, as much as the comparison with other works of transformation verification holds, we have been trying to inspire ourselves from the literature to implement the HILECOP model-to-text transformation function in Coq.

Chapter 5

Proving semantic preservation in HILECOP

RESEARCH QUESTION OF THE CHAPTER:

WHAT IS THE STRATEGY TO PROVE THAT THE HILECOP MODEL-TO-TEXT TRANSFORMATION FUNCTION IS SEMANTIC-PRESERVING?

5.1 Reminder on induction principles

- Present all the material that will be used in the proof, and that needs clarifying for people who do not come from the field (e.g, automaticians and electronicians)
 - structural induction
 - induction on relations
 - ...

5.2 Transformation functions and proof strategies

- maybe explain the difference between compiler verification and compiler validation, and stress the fact that we do verification
- maybe close to the first part of X. Leroy's article on CompCert

- Lit. review on proof strategies
 - are there usual proof strategies?
 - do they apply in our case?
- Present the main results (mostly coming from the first part of X. Leroy's article on CompCert):
 - proofs are based on execution steps, and state comparison
 - ...

5.3 Behavior preservation theorem

- preliminary definitions for the proof (+ notations to follow the proof)
→ illustrate the state similarity relation
- definitions of main thms + proofs (proofs refer to lemmas that will be presented in the next section)
- illustrating the main thms (execution steps figure)

5.4 Proving semantic preservation

- Give the proof (the whole proof? maybe a bit long, some part as appendices?)
 1. Initial states
 2. First step
→ not sure it is really relevant, as it uses most of the material coming from the “Initial states” part and the “Rising edge” part
 3. Rising edge
 4. Falling edge
- Illustrate some point of the proof (determine which points) with figures
→ illustrate the part that says “by construction”, linking PN parts to \mathcal{H} -VHDL parts

5.5 A detailed proof: equivalence of fired transitions

→ don't know if this is a separate section or if it must be a part of the previous section

- Informal presentation of the proof
- formal proof
- bug detection (illustrated, show the figure)

CONCLUSION OF THE CHAPTER:

- Remind of the main proof strategy
- Remind of the bug detection, the changes on the SITPN and \mathcal{H} -VHDL semantics that are consequences of the proof
- State of the proof verification. How far am I right now?

Chapter 6

Conclusion

6.1 Contributions

- Contributions to SITPN
- Contributions to VHDL
- Contributions to transformation functions
- Proof
 - Bug detections + changes in semantics
 - How far are we to complete the verification?

6.2 Improvements and perspectives

→ if proof not yet verified, first thing is not complete this job!

- improvements on the implementation of \mathcal{H} -VHDL, get closer to the formal def., more use of dependent types
- proving the semantic preservation thm in its existential version, i.e:
 - the generated design is always elaborable
 - the generated design is always simulable, i.e, it will never produce errors as long as the input model verifies some properties (liveness, boundedness...)
- consider the whole HILECOP high-level model
 - macroplaces
 - GALS

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