

DIC-EE5186-Test-1-4Apr2022

Class Test 1

ee19btech11012@iith.ac.in [Switch account](#)

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Answer the following questions and upload one single PDF.

①		Inputs			Outputs	
Sino		x	y	z	f ₁	f ₂
0		0	0	0	0	0
1		0	0	1	1	0
2		0	1	0	1	0
3		0	1	1	0	1
4		1	0	0	1	0
5		1	0	1	0	1
6		1	1	0	0	1
7		1	1	1	1	1

②		Inputs			Outputs	
Sino		x	y	z	f ₁	f ₂
0		0	0	0	0	0
1		0	0	1	1	1
2		0	1	0	1	1
3		0	1	1	0	1
4		1	0	0	1	0
5		1	0	1	0	0
6		1	1	0	0	0
7		1	1	1	1	1

- Q. (a) Write the logic equations of f_1 and f_2 in standard POS and standard SOP form. (10)
- (b) Use K-Map for minimization and obtain the minimized expressions for SSOP and SPOS forms. (10)
- (c) Draw both of the minimized expressions using AND-OR gates. Identify the circuit containing the least number of AND and least number of OR gates. (10)
- (d) Realize (c) using NOR gates only and draw. (10)
- (e) Realize (c) using NAND gates only and draw. (10)

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