

# Assignment 2

## Theory

1. Propositions to represent state of traffic light at time  $t$  are defined as follows:

$G_t$ : Light is green at time  $t$

$Y_t$ : Light is yellow at time  $t$

$R_t$ : Light is red at time  $t$

$$\text{Rule 1: } (G_t \vee Y_t \vee R_t) \wedge (\neg G_t \vee \neg Y_t) \wedge (\neg G_t \vee \neg R_t) \wedge (\neg Y_t \vee \neg R_t)$$

$$\text{Rule 2: } (G_t \rightarrow Y_{t+1}) \wedge (R_t \rightarrow G_{t+1}) \wedge (Y_t \rightarrow R_{t+1})$$

$$\text{Rule 3: } ((G_t \wedge G_{t+1} \wedge G_{t+2}) \rightarrow \neg G_{t+3}) \wedge ((R_t \wedge R_{t+1} \wedge R_{t+2}) \rightarrow \neg R_{t+3}) \\ \wedge ((Y_t \wedge Y_{t+1} \wedge Y_{t+2}) \rightarrow \neg Y_{t+3})$$

2. Predicates in FOL language

$\text{Node}(x)$ :  $x$  is a node

$\text{Color}(c)$ :  $c$  is a color in  $C: \{c_1, \dots, c_k\}$

$\text{HasColor}(x, c)$ : Node  $x$  has color  $c$

$\text{Edge}(x, y)$ : Directed edge from node  $x$  to node  $y$  exists

$\text{Clique}(x, c)$ : Node  $x$  is part of clique for color  $c$

Axioms

Let  $x, y, z, w, v \in \text{domain of Node}$  and  $c \in \text{domain of Color}$

$$1. \forall x, y (\text{Edge}(x, y) \rightarrow \forall c (\text{HasColor}(x, c) \rightarrow \neg \text{HasColor}(y, c)))$$

$$2. \exists x, y (x \neq y \wedge \text{HasColor}(x, \text{Yellow}) \wedge \text{HasColor}(y, \text{Yellow})) \\ \wedge \forall z (\text{HasColor}(z, \text{Yellow}) \rightarrow (z = x \vee z = y))$$

$$3. \forall x (\text{HasColor}(x, \text{Red}) \rightarrow \exists y (\text{HasColor}(y, \text{Green}) \wedge \text{Edge}(x, y)) \\ \vee \exists y, z (\text{Edge}(x, y) \wedge \text{Edge}(y, z) \wedge \text{HasColor}(z, \text{Green})))$$

$$\vee \exists y, z, w (\text{Edge}(x, y) \wedge \text{Edge}(y, z) \wedge \text{Edge}(z, w) \wedge \text{HasColor}(w, \text{Green}))$$

$$\vee \exists y, z, w, v (\text{Edge}(x, y) \wedge \text{Edge}(y, z) \wedge \text{Edge}(z, w) \wedge \text{HasColor}(v, \text{Green}) \\ \wedge \text{Edge}(w, v))$$

4.

$$4. \forall c (\text{Color}(c) \rightarrow \exists x (\text{HasColor}(x, c) \wedge \text{Node}(x)))$$

$$5. \forall c (\text{Color}(c) \rightarrow \exists x (\text{Node}(x) \wedge \text{HasColor}(x, c) \wedge \forall y (\text{Node}(y) \rightarrow (\text{HasColor}(y, c) \leftrightarrow (\text{Clique}(x, c) \wedge \text{Clique}(y, c) \wedge (x = y \vee (\text{Edge}(x, y) \wedge \text{Edge}(y, x))))))))))$$

3.

### PL representation

1. Cannot be expressed due to inability to express universal statement.
2. Cannot be expressed due to inability to express universal statement.
3. Cannot be expressed due to inability to express existential statement.
4. Cannot be expressed due to inability to express existential statement.
5. Cannot be expressed due to inability to express existential and universal statement.

### FOL Representation

#### Predicates

$\text{Read}(x)$ :  $x$  can read

$\text{Literate}(x)$ :  $x$  is literate

$\text{Intelligent}(x)$ :  $x$  is intelligent

$\text{Dolphin}(x)$ :  $x$  is a dolphin

#### Axioms

1.  $\forall x (\text{Read}(x) \rightarrow \text{Literate}(x))$
2.  $\forall x (\text{Dolphin}(x) \rightarrow \neg \text{Literate}(x))$
3.  $\exists x (\text{Dolphin}(x) \wedge \text{Intelligent}(x))$
4.  $\exists x (\text{Intelligent}(x) \wedge \neg \text{Read}(x))$
5.  $\exists x (\text{Dolphin}(x) \wedge \text{Intelligent}(x) \wedge \text{Read}(x)) \wedge \forall y ((\text{Dolphin}(y) \wedge \text{Intelligent}(y) \wedge \text{Read}(y)) \rightarrow \neg \text{Literate}(y))$



Check satisfiability of fourth sentence

Let  $\alpha$  be axiom 4.

$$\neg \alpha: \neg (\exists x (\text{Intelligent}(x) \wedge \neg \text{Read}(x))) \equiv \forall x (\text{Intelligent}(x) \rightarrow \text{Read}(x))$$

Using axiom 1-3 and  $\neg \alpha$  in our KB, we derive their CNF form

$$S_1: \neg \text{Read}(x) \vee \text{Literate}(x)$$

$$S_2: \neg \text{Dolphin}(x) \vee \neg \text{Literate}(x)$$

$$S_3: \{ \text{Dolphin}(a), \text{Intelligent}(a) \}$$

$$S_4: \neg \text{Intelligent}(x) \vee \text{Read}(x)$$

Using  $S_3$  and  $S_4$  we get  $\{ \text{Read}(a) \}$  ( $\neg \text{Intelligent}(a) \wedge \text{Intelligent}(a)$ ) which is added to  $S_3$ .

$S_3: \{ \text{Dolphin}(a), \text{Intelligent}(a), \text{Read}(a) \}$  leads to contradiction  
Using  $S_1$  and  $S_3$ , we get  $\{ \text{Literate}(a) \}$  which is added to  $S_3$ .

$$S_3: \{ \text{Dolphin}(a), \text{Intelligent}(a), \text{Read}(a), \text{Literate}(a) \}$$

Using  $S_2$  and  $S_3$ , we get  $\{ \neg \text{Literate}(a) \}$  which is added to  $S_3$ .  
But  $\text{Literate}(a)$  and  $\neg \text{Literate}(a)$  leads to contradiction.  
empty clause which is a<sup>3</sup>

Therefore,  $\alpha$  entails KB and  $\alpha$  is satisfiable.

By  $\alpha$  axiom 4

Check satisfiability of fifth sentence

Let  $\beta$  be axiom 5.

$$\neg \beta: \neg (\exists x (\text{Dolphin}(x) \wedge \text{Intelligent}(x) \wedge \text{Read}(x)) \wedge \forall y ((\text{Dolphin}(y) \wedge \text{Intelligent}(y) \wedge \text{Read}(y)) \rightarrow \neg \text{Literate}(y)))$$

$$\equiv \forall x (\neg \text{Dolphin}(x) \vee \neg \text{Intelligent}(x) \vee \neg \text{Read}(x)) \vee$$

$$\exists y (\text{Dolphin}(y) \wedge \text{Intelligent}(y) \wedge \text{Read}(y) \wedge \text{Literate}(y))$$

Using axiom 1-4 and  $\neg \beta$  in our KB, we derive their CNF form

$$C_1: \neg \text{Read}(x) \vee \text{Literate}(x)$$

$$C_2: \neg \text{Dolphin}(x) \vee \neg \text{Literate}(x)$$

$$C_3: \{ \text{Dolphin}(a), \text{Intelligent}(a) \}$$

$$C_4: \{ \text{Intelligent}(b), \neg \text{Read}(b) \}$$

$$C_5: (\neg \text{Dolphin}(x) \vee \neg \text{Intelligent}(x) \vee \neg \text{Read}(x)) \vee$$

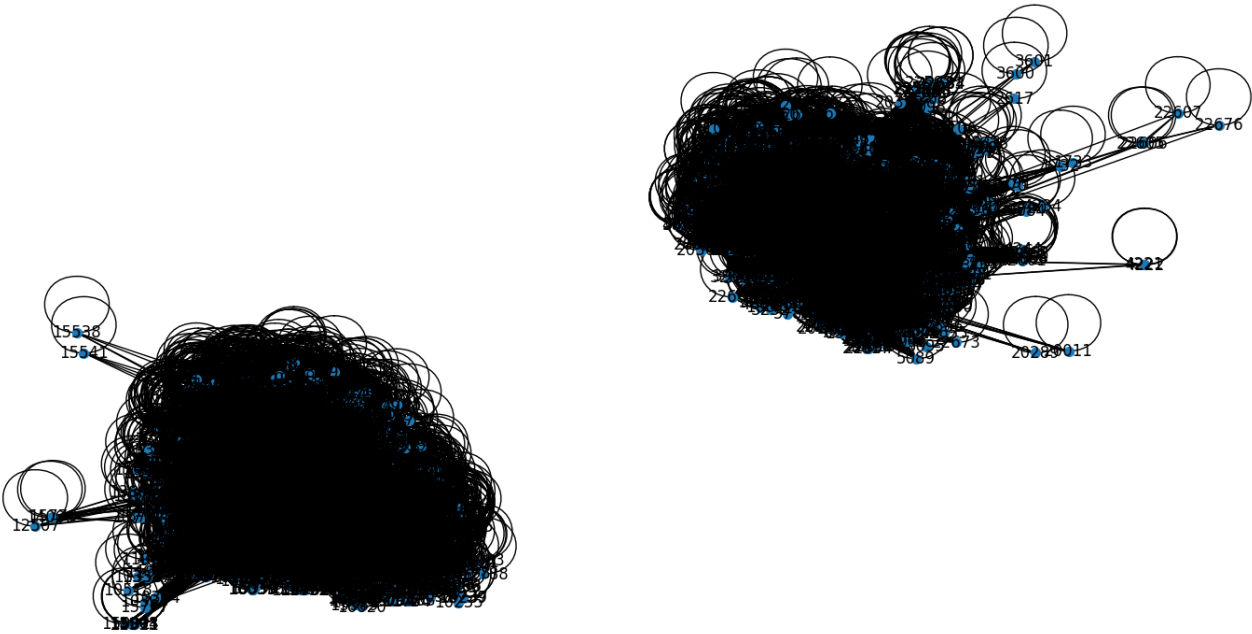
$$\{ \text{Dolphin}(c), \text{Intelligent}(c), \text{Read}(c), \text{Literate}(c) \} \rightarrow C_{51}$$

$C_{52}$



Computational

1.



route\_to\_stops graph

2.

(a), (b), (c)

```
Input to direct_route_brute_force: (2573, 1177)
Execution time of direct_route_brute_force: 0.01598501205444336 s
Memory usage of direct_route_brute_force: 0.000152587890625 MB
Steps taken: 2403

Test direct_route_brute_force (2573, 1177): Pass

Input to direct_route_brute_force: (2001, 2005)
Execution time of direct_route_brute_force: 0.002981901168823242 s
Memory usage of direct_route_brute_force: 0.000152587890625 MB
Steps taken: 2403

Test direct_route_brute_force (2001, 2005): Pass

Input to query_direct_routes: (2573, 1177)
Execution time of query_direct_routes: 0.0067327022552490234 s
Memory usage of query_direct_routes: 0.0348052978515625 MB
Steps taken: 3

Test query_direct_routes (2573, 1177): Pass

Input to query_direct_routes: (2001, 2005)
Execution time of query_direct_routes: 0.002275705337524414 s
Memory usage of query_direct_routes: 0.020275115966796875 MB
Steps taken: 2

Test query_direct_routes (2001, 2005): Pass
```

3.

(a), (b), (c)

```
Test forward_chaining (22540, 2573, 4686, 1): Pass

Input to forward_chaining: (951, 340, 300, 1)
Execution time of forward_chaining: 0.0983114242553711 s
Memory usage of forward_chaining: 0.11587142944335938 MB
Steps taken: 9

Test forward_chaining (951, 340, 300, 1): Pass

Input to backward_chaining: (22540, 2573, 4686, 1)
Execution time of backward_chaining: 0.0 s
Memory usage of backward_chaining: 0.02174854278564453 MB
Steps taken: 1

Test backward_chaining (22540, 2573, 4686, 1): Pass

Input to backward_chaining: (951, 340, 300, 1)
Execution time of backward_chaining: 0.009370088577270508 s
Memory usage of backward_chaining: 0.07877731323242188 MB
Steps taken: 9

Test backward_chaining (951, 340, 300, 1): Pass
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4.

4. 1.

(a), (b), (c)

```
Transfer from route 10153 to route 1407 via stop 4686

Input to pddl_planning: (22540, 2573, 4686, 1)
Execution time of pddl_planning: 0.020138263702392578 s
Memory usage of pddl_planning: 0.07852935791015625 MB
Steps taken: 1

Test pddl_planning (22540, 2573, 4686, 1): Pass
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Transfer from route 1571 to route 712 via stop 300
Transfer from route 49 to route 712 via stop 300
Transfer from route 387 to route 712 via stop 300
Transfer from route 121 to route 712 via stop 300
Transfer from route 10433 to route 712 via stop 300
Transfer from route 1211 to route 712 via stop 300
Transfer from route 37 to route 712 via stop 300
Transfer from route 10453 to route 712 via stop 300
Transfer from route 1038 to route 712 via stop 300

Execution time of pddl_planning: 0.016242027282714844 s
Memory usage of pddl_planning: 0.19697093963623047 MB
Steps taken: 9

Test pddl_planning (951, 340, 300, 1): Pass
```

### Analysis of routes from each algorithm

1. Forward Chaining: It is likely to produce the optimal route because it starts from the initial stop and systematically explores paths. However, it may not always prioritize the most efficient path early, especially when constraints such as transfers and via stops make the problem more complex.
2. Backward Chaining: This method can quickly eliminate non-feasible routes by starting at the goal, but it may return suboptimal paths if there are multiple ways to reach the start stop, especially when paths have multiple valid interchanges.
3. PDDL: If the PDDL problem is well-defined with clear constraints on via stops and maximum transfers, it is likely to find an optimal path. However, the flexibility of PDDL could lead to suboptimal results if constraints are not fine-tuned, or the problem space is not constrained well enough.

### Effect of constraints on optimality

1. Via Stop Constraint: All algorithms produce the same results if this constraint is strictly enforced. However, if an algorithm like PDDL doesn't efficiently model this requirement, it could produce suboptimal routes.
2. Transfer Limits: This constraint can have a significant impact on suboptimality. Both forward and backward chaining can return suboptimal paths if they explore too many interchanges before finding an efficient path. PDDL could also return suboptimal paths if the transfer limit is too high, allowing more complex routes.



## Comparison of algorithms

Algorithm	Optimality
Forward Chaining	Likely to find an optimal route, especially for simple, direct routes. Might explore inefficient paths first if there are multiple valid routes and interchanges.
Backward Chaining	Efficient at finding optimal paths when starting from a clear goal. Could return suboptimal paths when many valid paths exist with multiple interchanges.
PDDL Planning	Flexible and likely to find optimal routes with well-defined constraints. Suboptimal results possible if constraints aren't fine-tuned or if the model is too flexible.

4. 2.

```
Test bfs_route_planner_optimized (22540, 2573, 10, 3): Pass
Test bfs_route_planner_optimized (4012, 4013, 10, 3): Pass
```