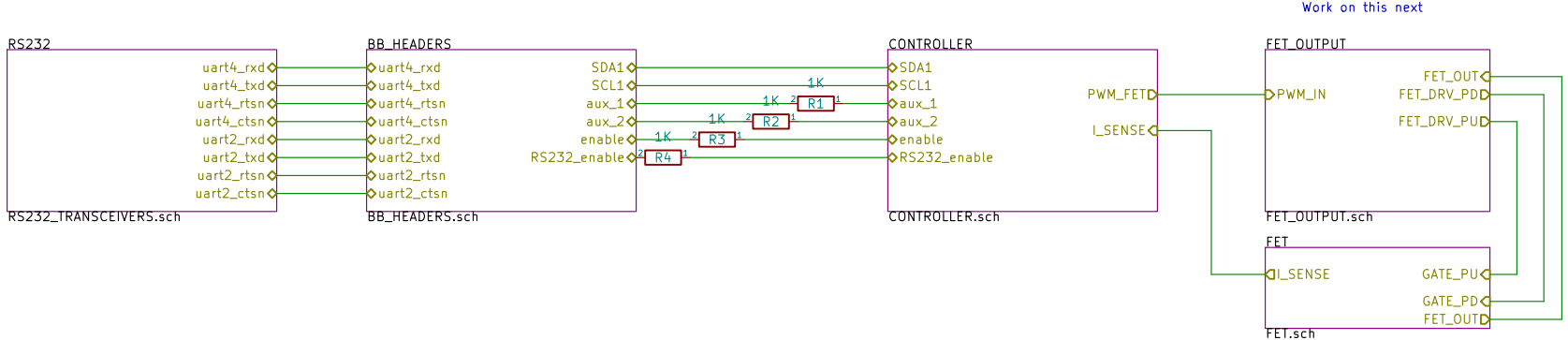
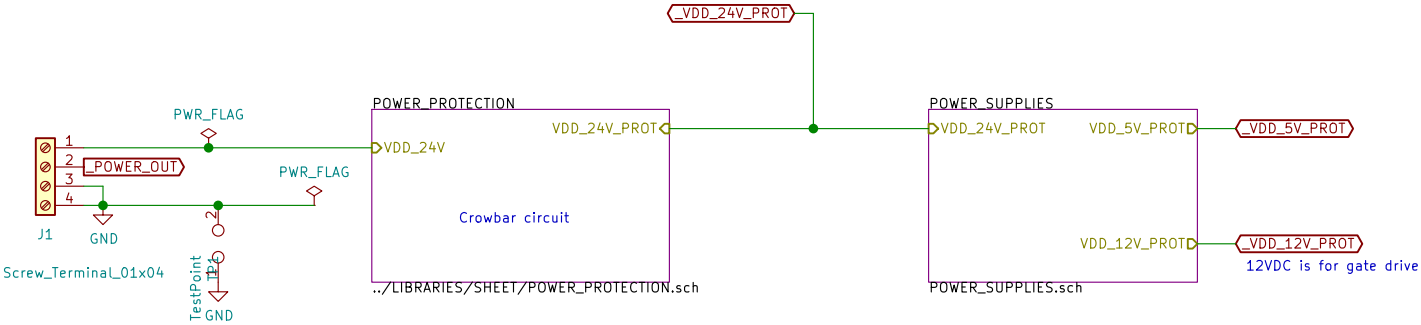


NEXT REV:  
ALL ICs TO SOIC  
INCREASE PRINT SIZE ACROSS THE BOARD  
LABEL POWER TERMINAL ON TOP OF BOARD  
ADD 'VPSB' to the PCB



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Sheet: /

File: BBB\_SHIELD\_V2.sch

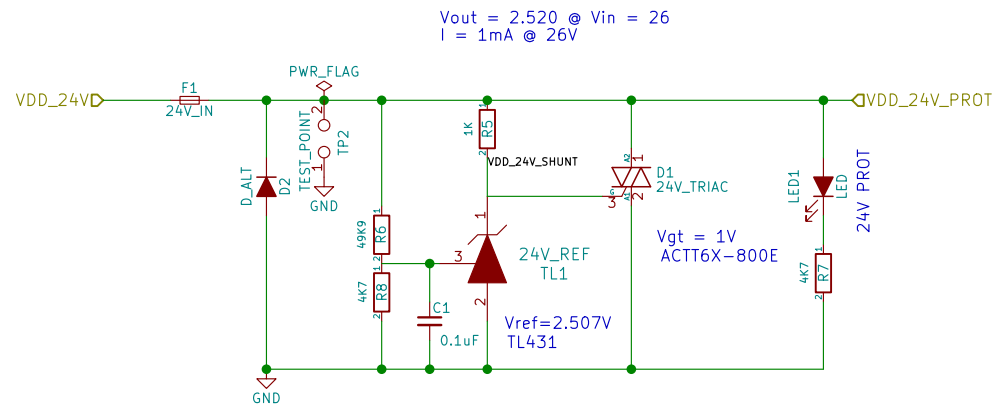
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Size: USLetter Date: 2019-09-03

KiCad E.D.A. kicad 5.0.2-bee76a070ubuntu18.04.1

Rev: V3.0

Id: 1/12



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File: POWER\_PROTECTION.sch

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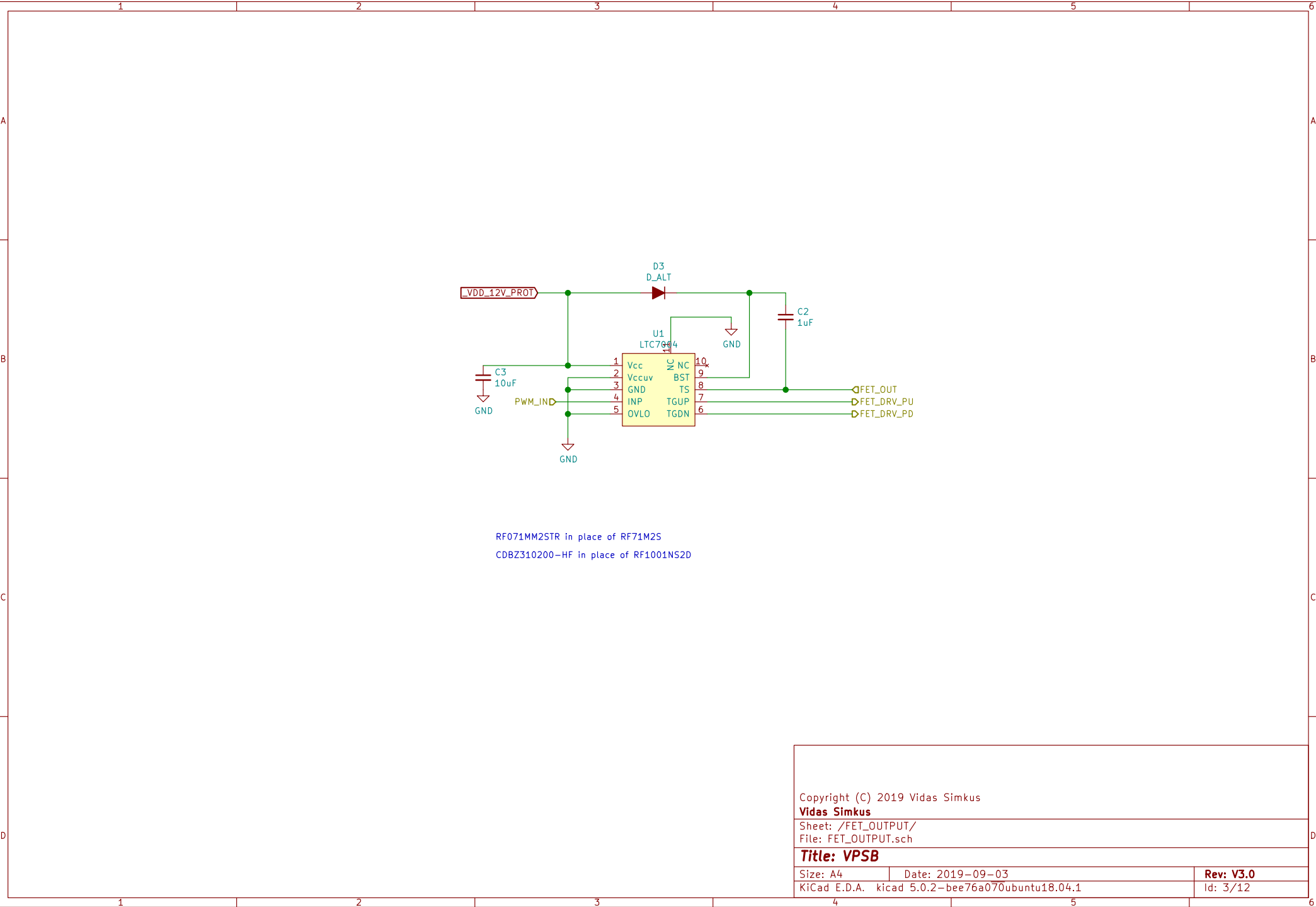
Size: A4

Date: 2019-09-03

**Rev: V3.0**

KiCad E.D.A. kicad 5.0.2-bee76a070ubuntu18.04.1

Id: 2/12



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Sheet: /FET\_OUTPUT/

File: FET\_OUTPUT.sch

Title: VPSB

Size: A4

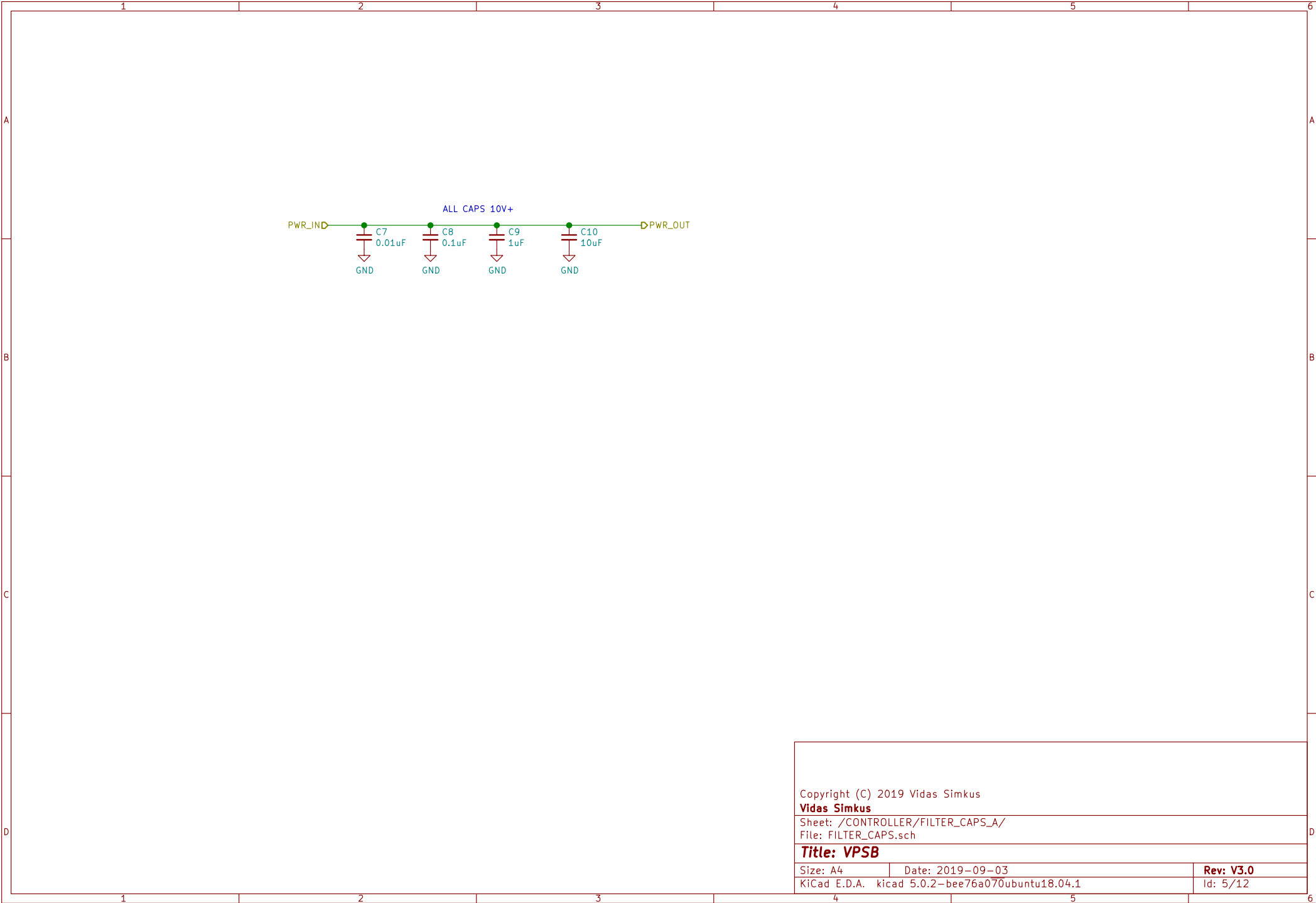
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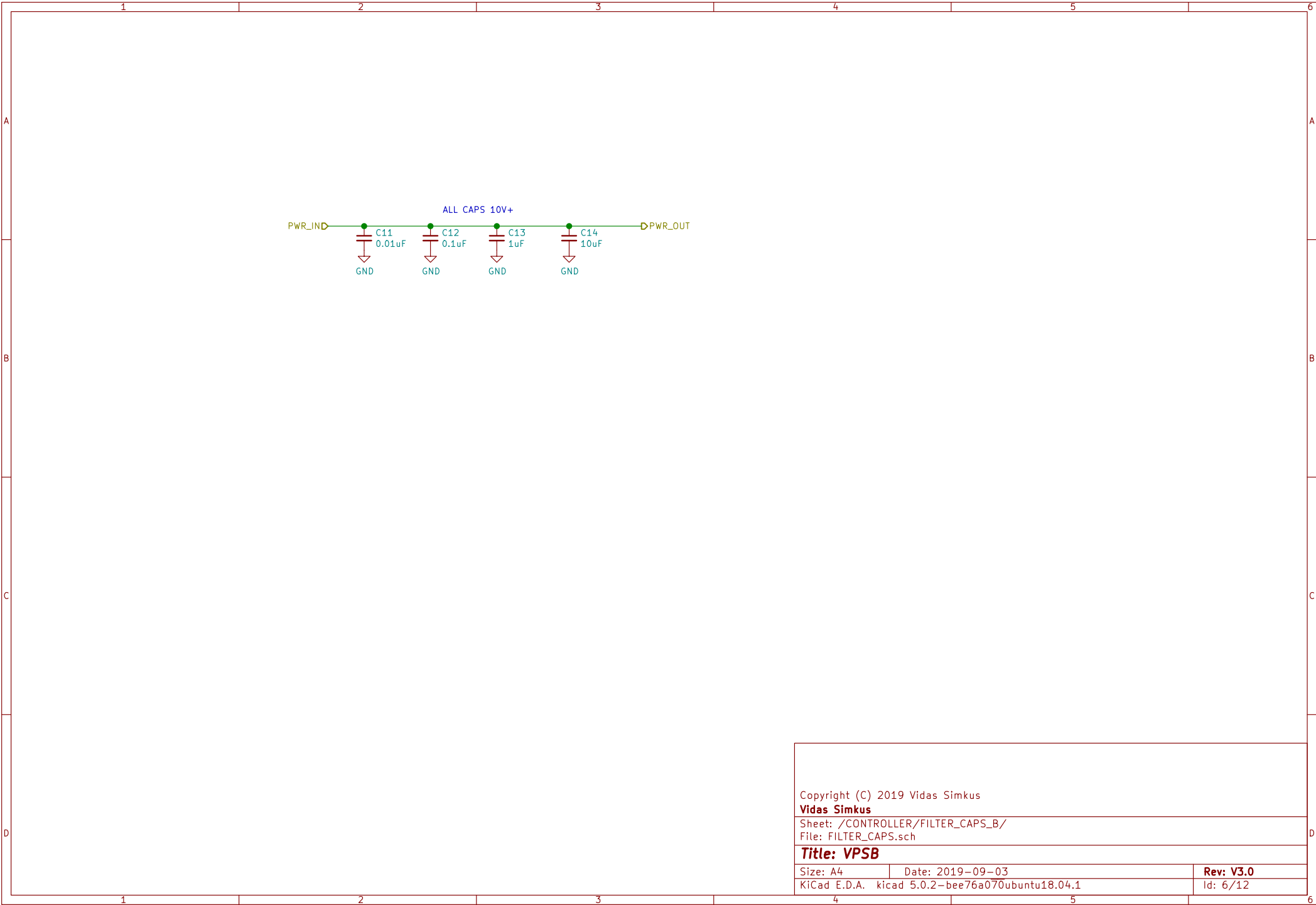
Rev: V3.0

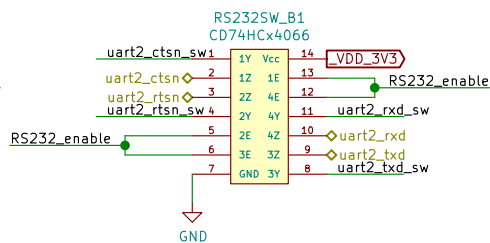
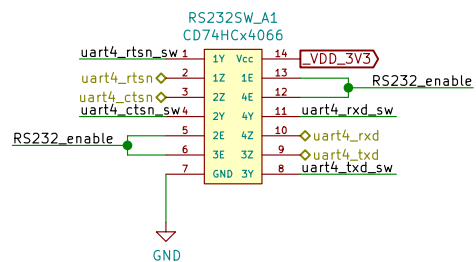
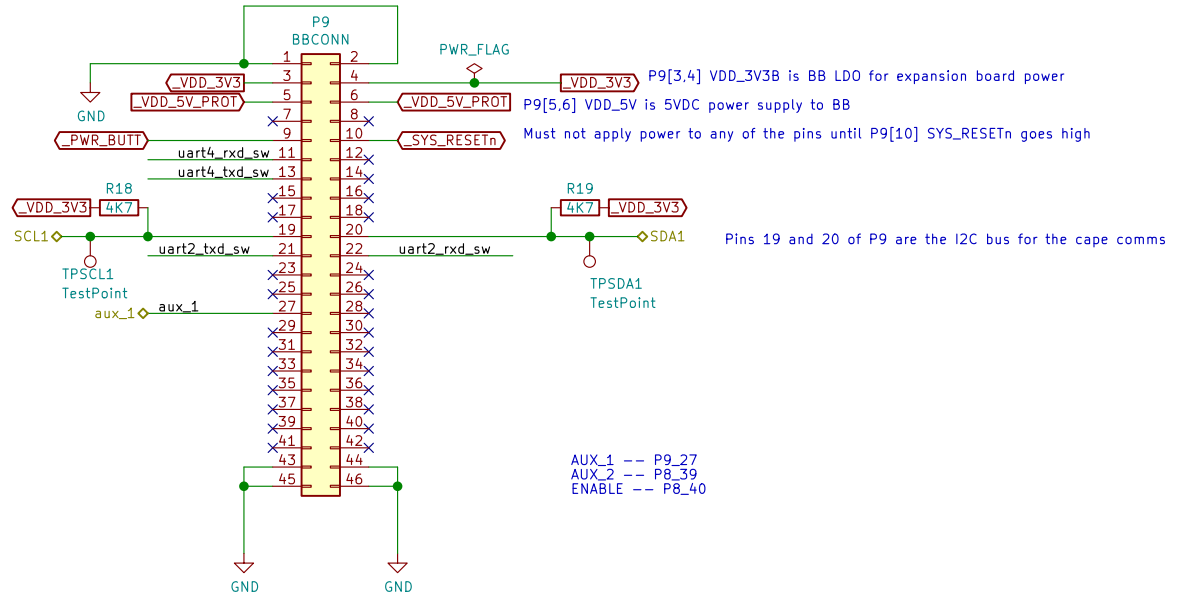
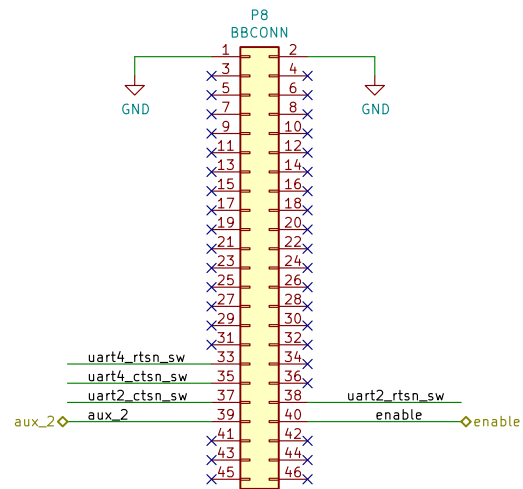
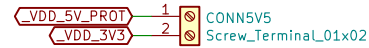
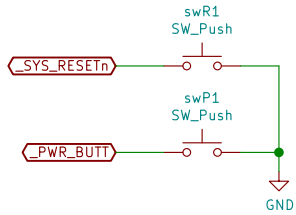
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Id: 3/12









RS232\_enable RS232\_enable

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File: BB\_HEADERS.sch

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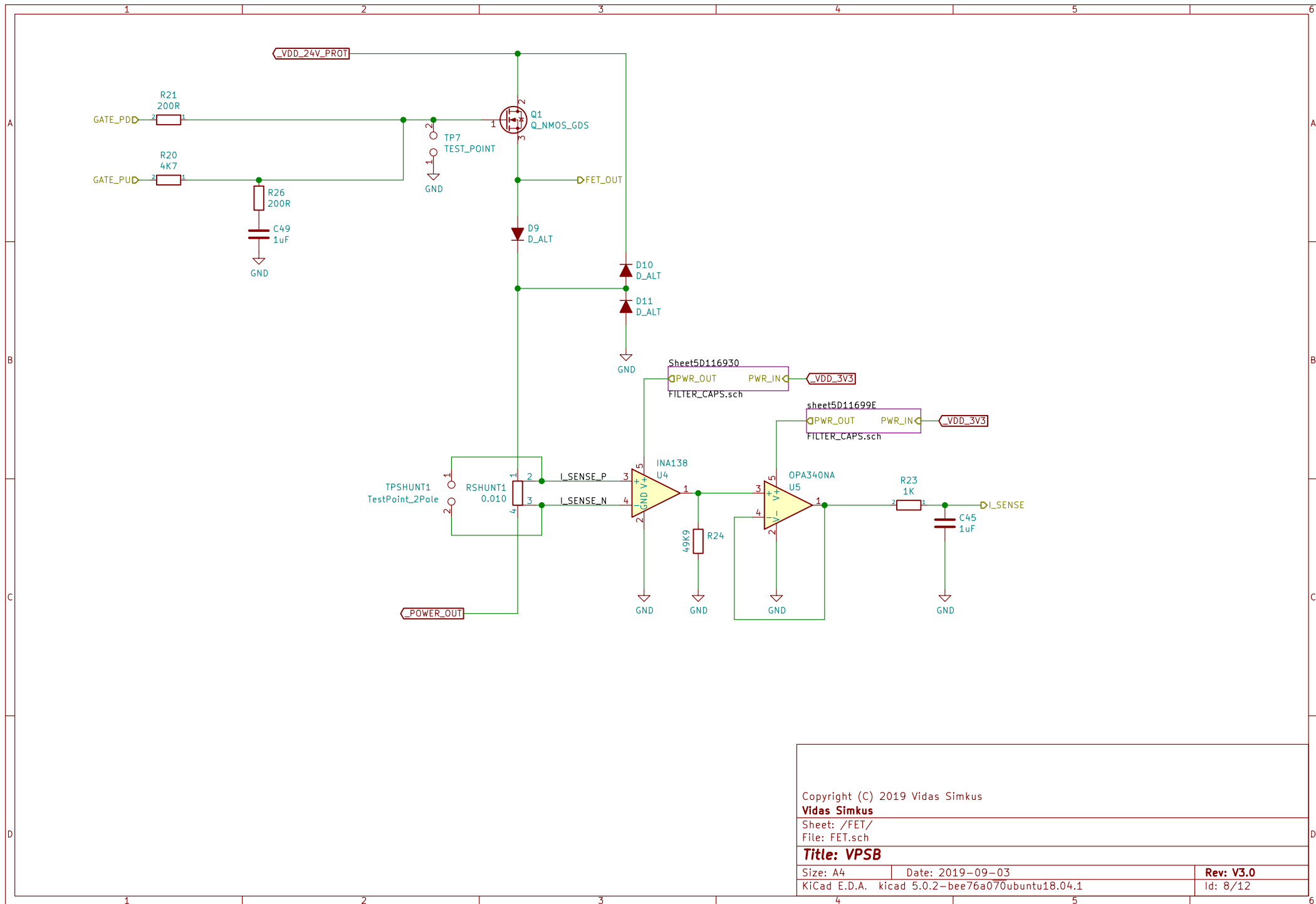
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KiCad E.D.A. kicad 5.0.2-0ubuntu18.04.1

Rev: V3.0

Id: 7/12



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Sheet: /FET/

File: FET.sch

Title: VPSB

Size: A4 Date: 2019-09-03

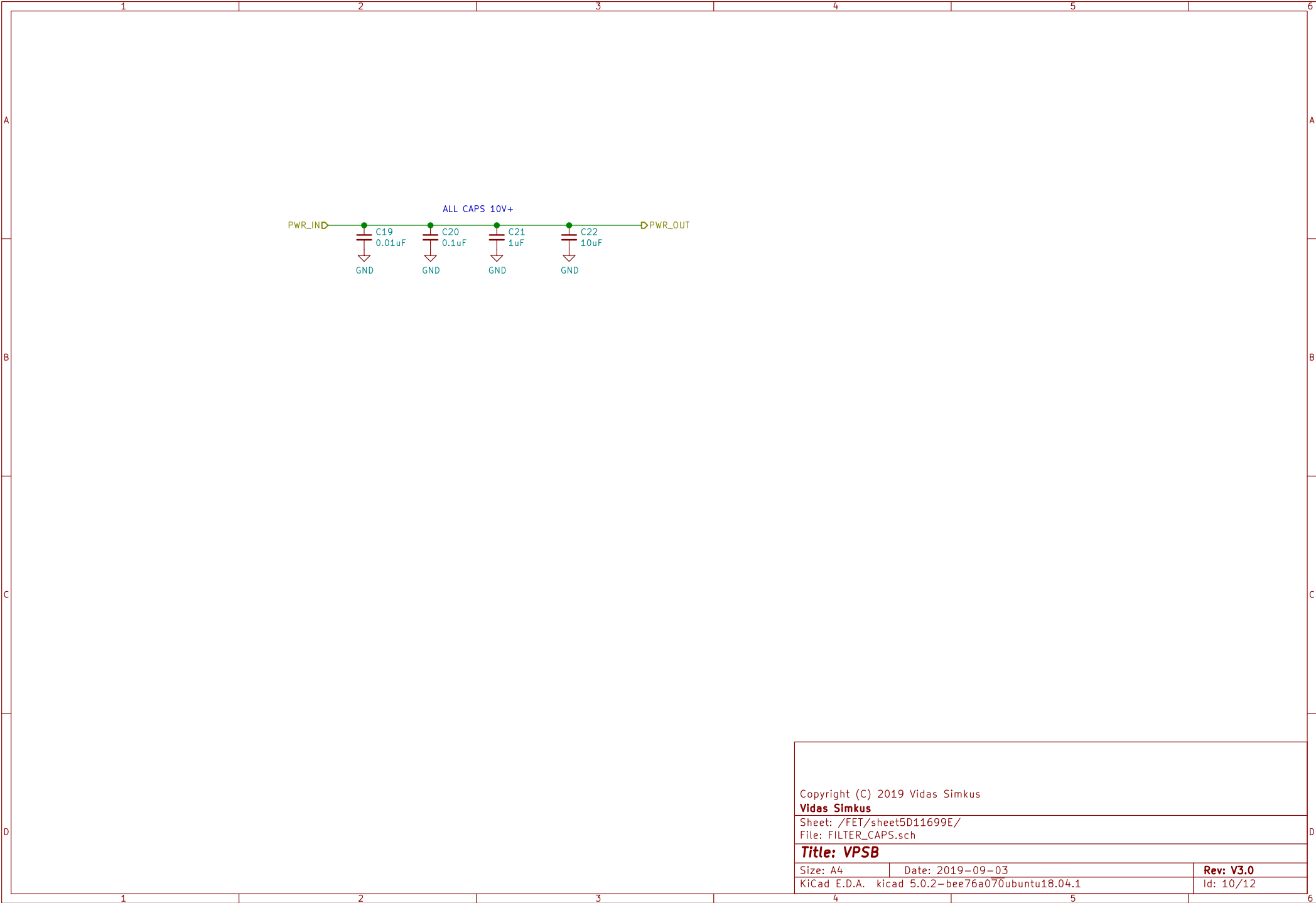
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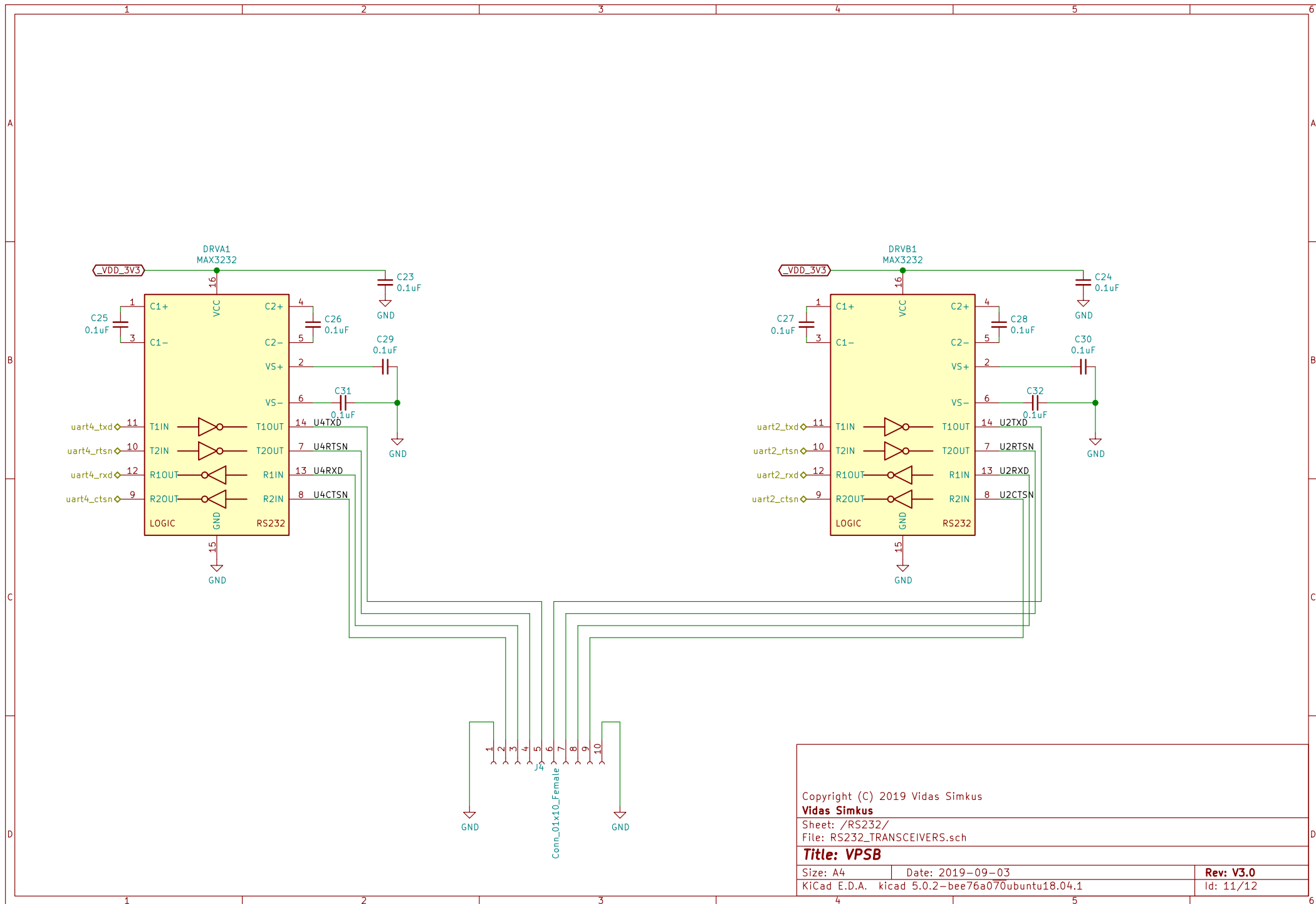
Rev: V3.0

Id: 8/12









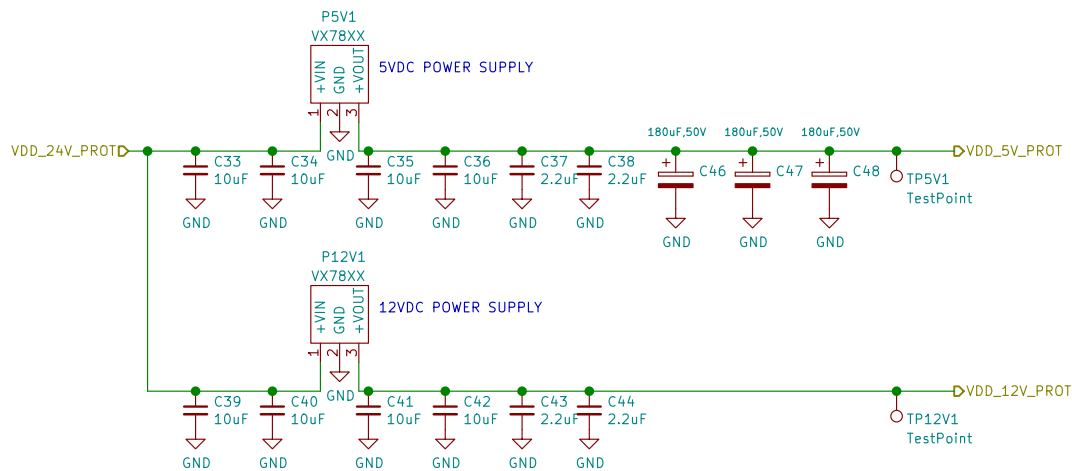
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Date: 2019-09-03  
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**Rev: V3.0**  
Id: 11/12



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Sheet: /POWER\_SUPPLIES/

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**Title: VPSB**

Size: A4

Date: 2019-09-03

**Rev: V3.0**

KiCad E.D.A. kicad 5.0.2-bee76a070ubuntu18.04.1

Id: 12/12