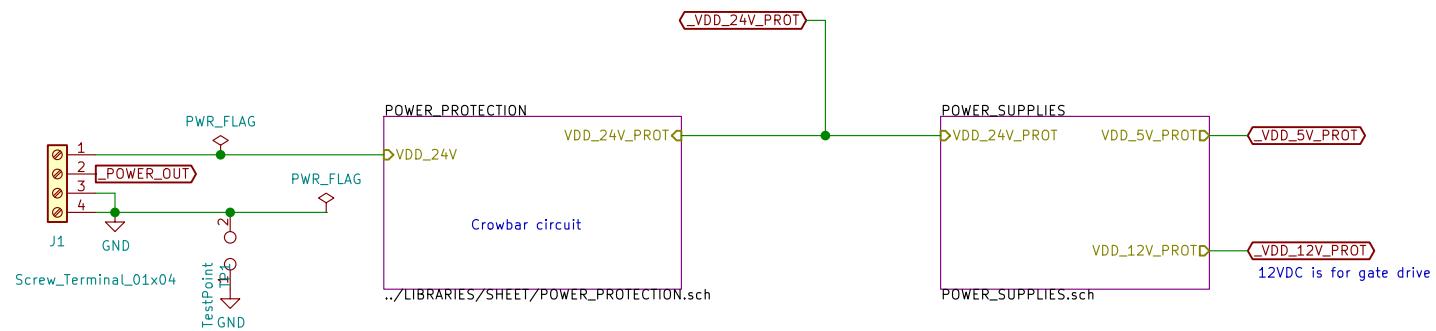


NEXT REV:  
ALL ICs TO SOIC  
INCREASE PRINT SIZE ACROSS THE BOARD  
LABEL POWER TERMINAL ON TOP OF BOARD  
ADD 'VPSB' to the PCB

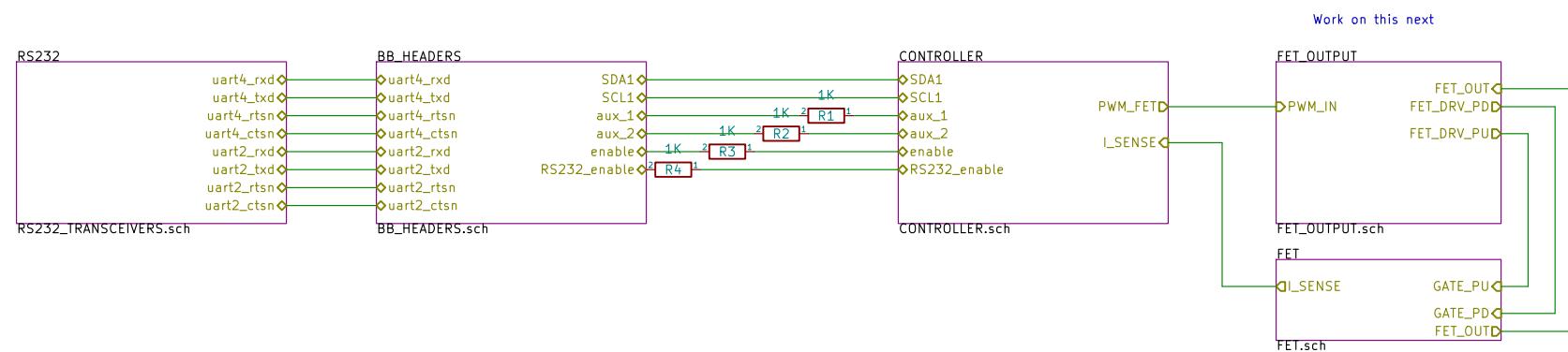
A

A



B

B



D

D

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**Title: VPSB**

Size: USLetter	Date: 2019-09-03
KiCad E.D.A. kicad 5.0.2-bee76a070ubuntu18.04.1	Rev: V3.0

Id: 1/12
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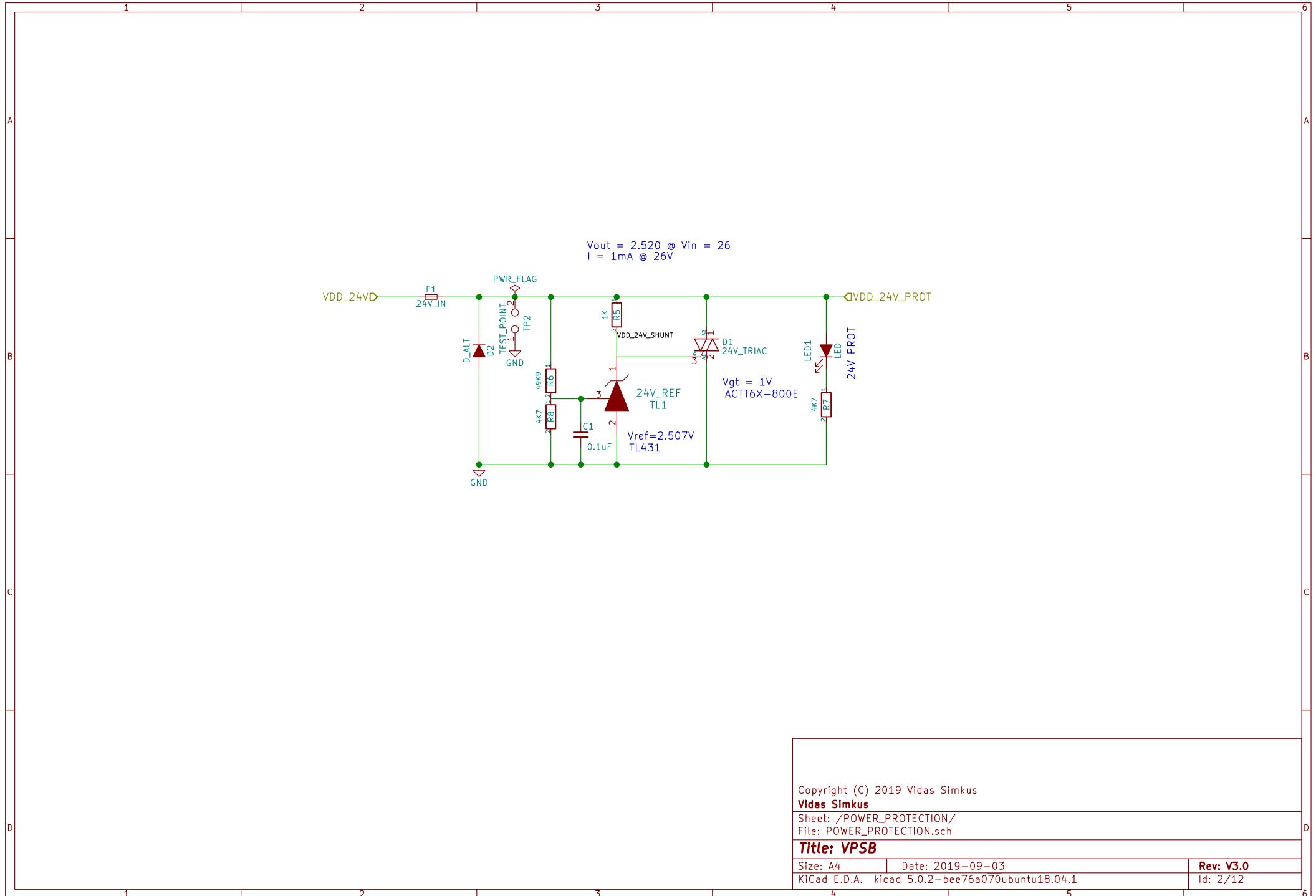
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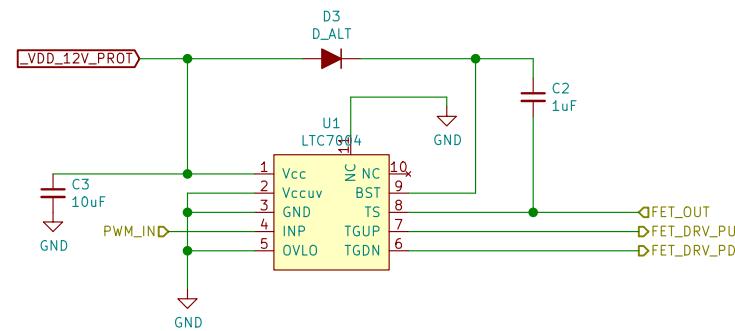
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3

4

5





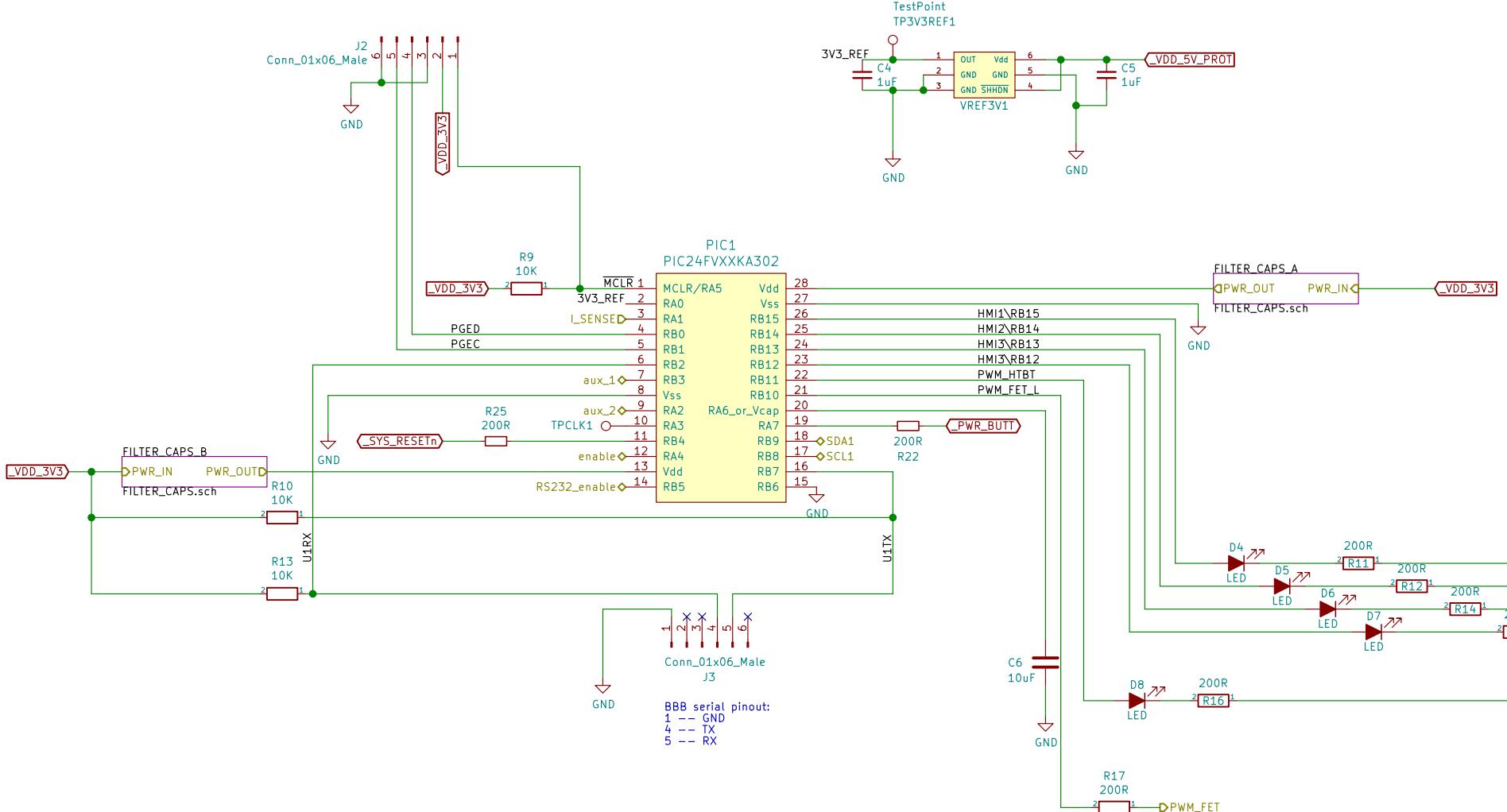
RF071MM2STR in place of RF71M2S  
CDBZ310200-HF in place of RF1001NS2D

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Sheet: /FET\_OUTPUT/  
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**Title: VPSB**

Size: A4	Date: 2019-09-03	Rev: V3.0
KiCad E.D.A. kicad 5.0.2-bee76a070ubuntu18.04.1		Id: 3/12



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Size: A4 Date: 2019-09-03  
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Rev: V3.0  
Id: 4/12

1 2 3 4 5 6

A

A

B

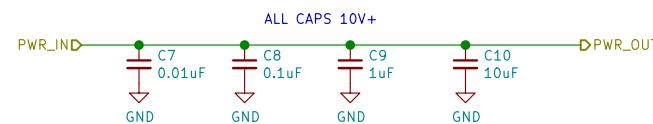
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C

C

D

D



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**Title: VPSB**

Size: A4 Date: 2019-09-03  
KiCad E.D.A. kicad 5.0.2-bee76a070ubuntu18.04.1

**Rev: V3.0**  
Id: 5/12

1 2 3 4 5 6

1 2 3 4 5 6

A

A

B

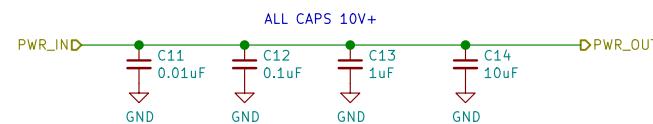
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C

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D



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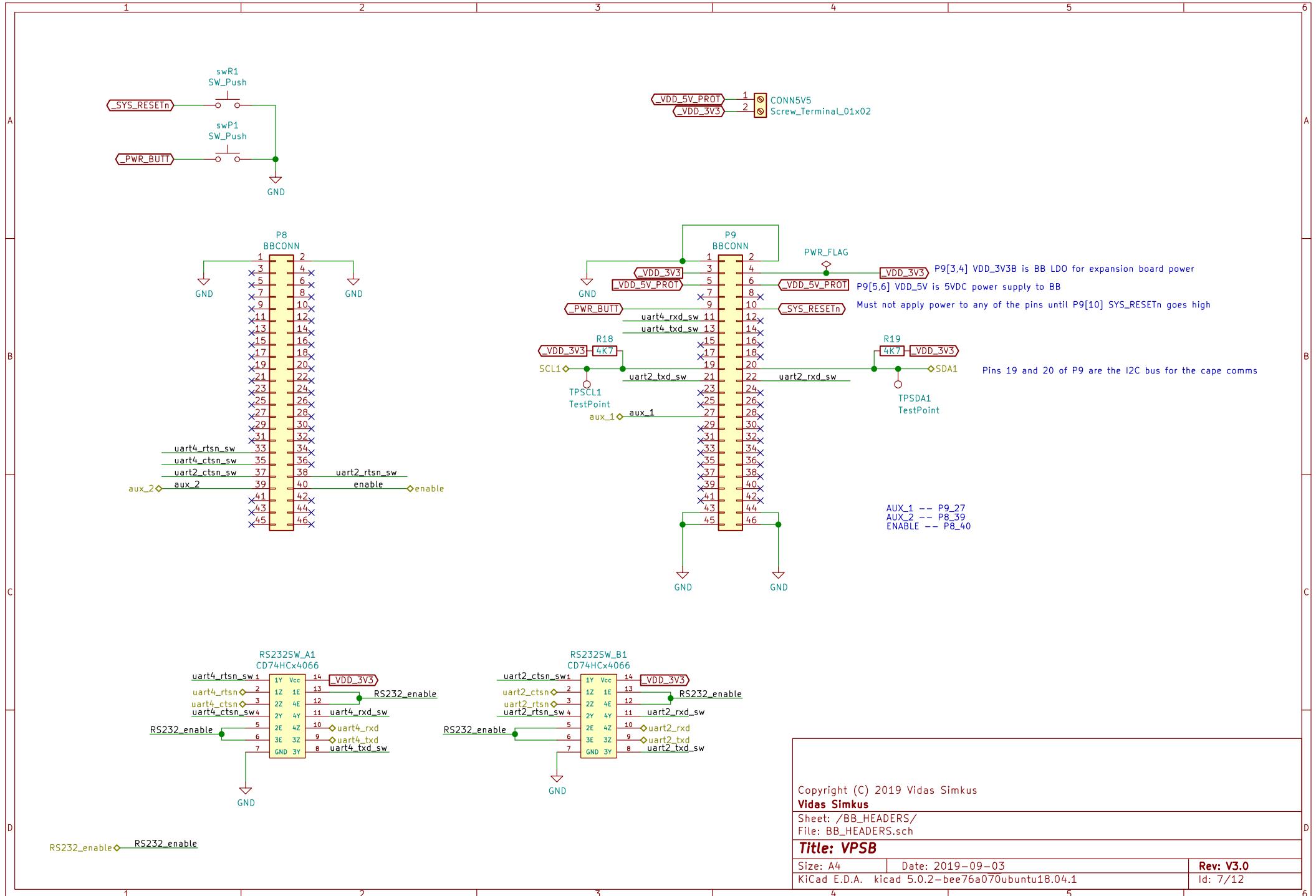
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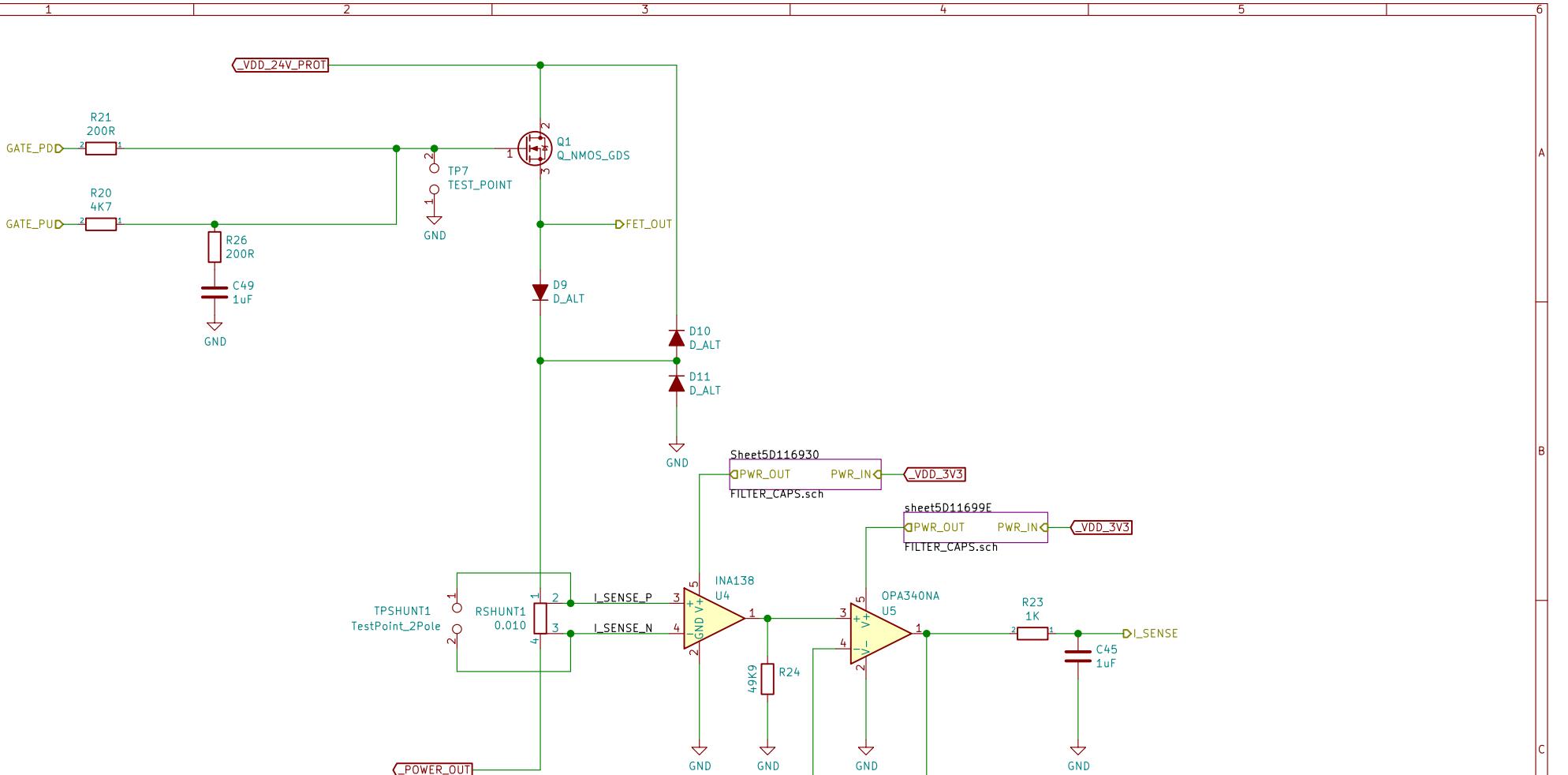
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Size: A4 Date: 2019-09-03  
KiCad E.D.A. kicad 5.0.2-bee76a070ubuntu18.04.1

**Rev: V3.0**  
Id: 6/12

1 2 3 4 5 6





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**Title: VPSB**

Size: A4 Date: 2019-09-03

KiCad E.D.A. kicad 5.0.2-bee76a070ubuntu18.04.1

**Rev: V3.0**

Id: 8/12

1 2 3 4 5 6

A

A

B

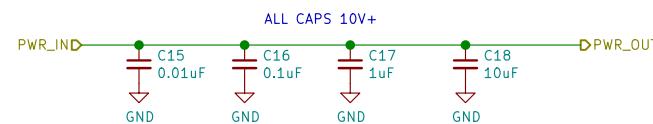
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**Title: VPSB**

Size: A4 Date: 2019-09-03

KiCad E.D.A. kicad 5.0.2-bee76a070ubuntu18.04.1

**Rev: V3.0**

Id: 9/12

1 2 3 4 5 6

1 2 3 4 5 6

A

A

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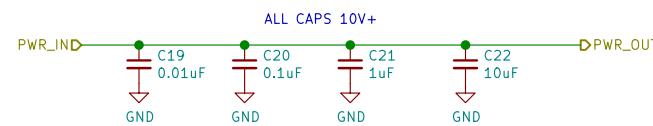
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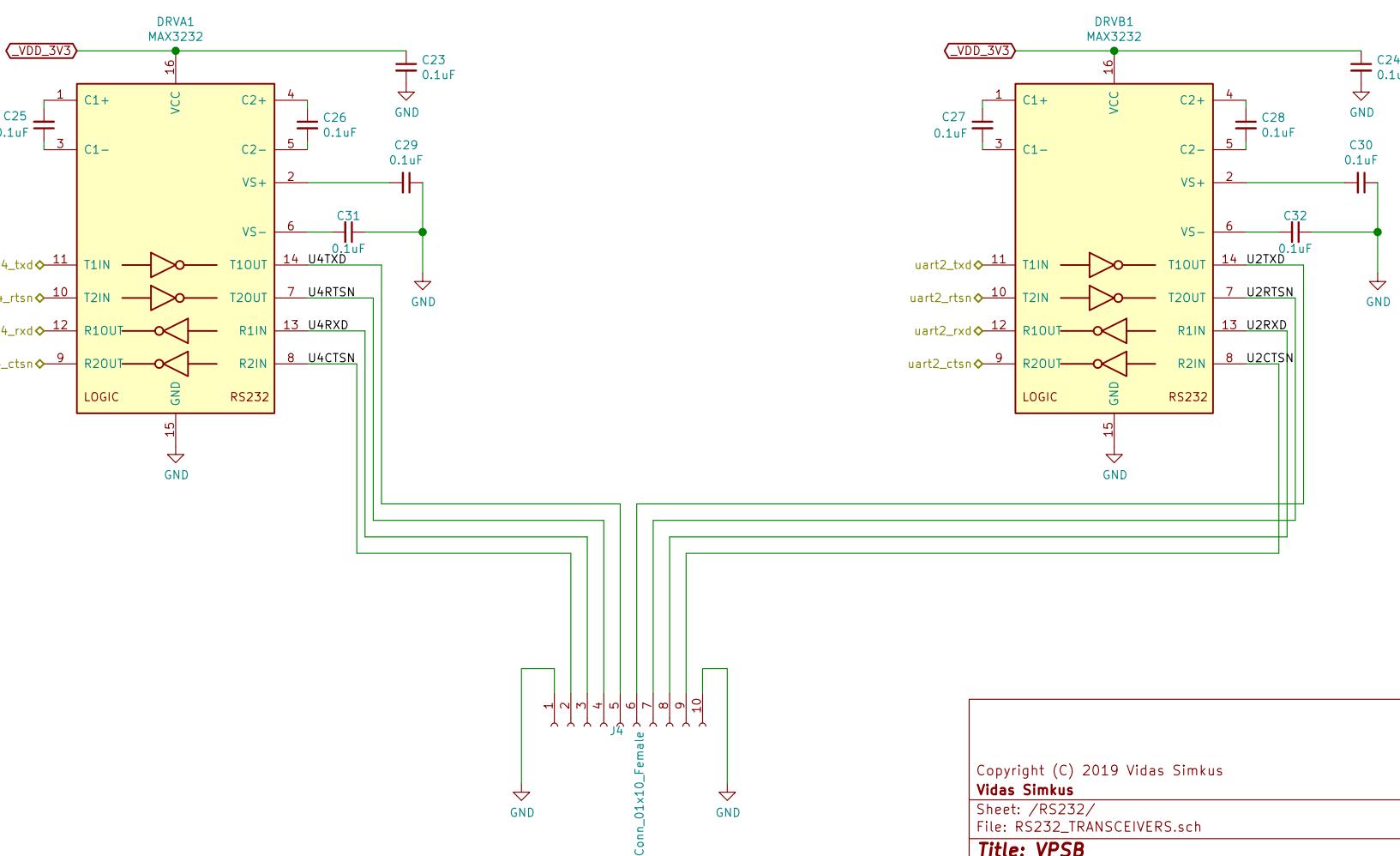
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Rev: V3.0

Id: 10/12

1 2 3 4 5 6



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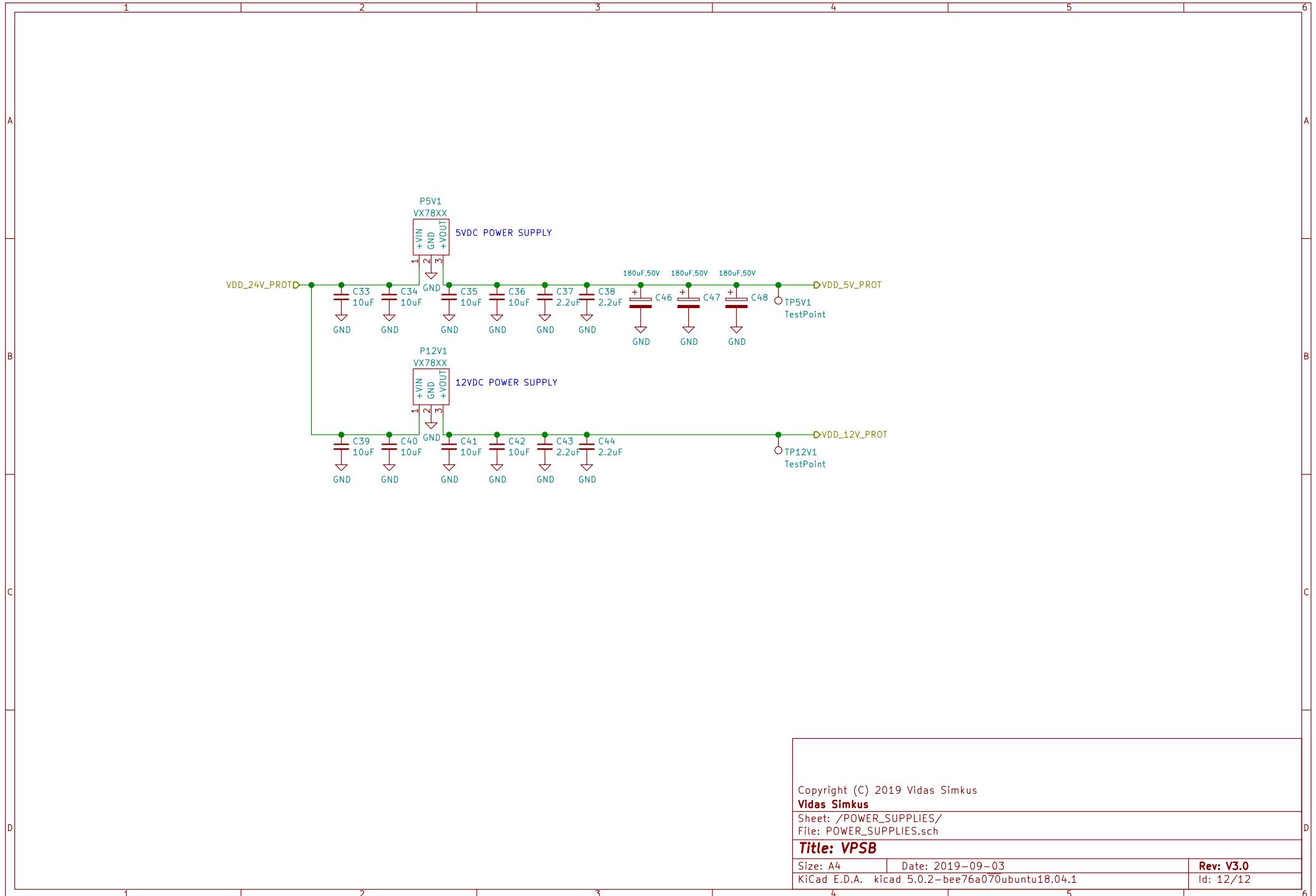
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Size: A4 Date: 2019-09-03  
KiCad E.D.A. kicad 5.0.2-bee76a070ubuntu18.04.1

Rev: V3.0  
Id: 11/12



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Sheet: /POWER\_SUPPLIES/  
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Size: A4	Date: 2019-09-03	Rev: V3.0
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