

# Victor Kariofillis

## CONTACT INFORMATION

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PERSONAL EMAIL: [vickariofillis@gmail.com](mailto:vickariofillis@gmail.com)  
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## RESEARCH INTERESTS

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My research in Computer Architecture focuses on enhancing system performance, energy efficiency, and adaptability. For my PhD, I work on energy-efficient mobile architectures and benchmark characterization. Next, I aim to research Brain-Computer Interface (BCI) workloads.

During my Master's, I developed Precompression for better cache compression.

I am also interested in the social aspects of computing.

## EDUCATION

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JAN. 2020 – JUN. 2025 (Tentative)	University of Toronto, Canada Edward S. Rogers Sr. Department of Electrical & Computer Engineering Doctorate of Philosophy <i>Current CGPA: 3.88 / 4.00</i>
SEPT. 2017 – APRIL. 2020	University of Toronto, Canada Edward S. Rogers Sr. Department of Electrical & Computer Engineering Master of Applied Science <i>CGPA: 3.94 / 4.00</i> Thesis: Precompression: A Prelude to Cache Compression
SEPT. 2010 – JULY. 2017	University of Piraeus, Greece Department of Informatics Bachelor of Science <i>Major: Network and Computing Systems (NCS)</i> <i>GPA: 8.26 / 10.0 — Honours: Very Good</i> <i>Thesis: Performance Measuring of a Multicore Heterogeneous System</i>

## TEACHING EXPERIENCE

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WINTER 2023	<b>ECE1718: Special Topics in Computer Hardware: Socially Responsible Computing</b> University of Toronto, Canada <i>Providing educational support to students. Creating and marking assignments. Marking projects.</i>
SUMMER 2022 – FALL 2023	<b>ECE253: Digital and Computer Systems</b> University of Toronto, Canada <i>Course Development. Updating labs (handouts, code, automarker) to RISC-V assembly. Providing educational support to students. Supervising and marking labs. Creating exam questions and marking exams.</i>

FALL 2021	<b>ECE241: Digital Systems</b> University of Toronto, Canada <i>Providing educational support to students. Supervising and marking labs. Validating exam questions and marking exams.</i>
WINTER 2021	<b>ECE243: Computer Organization</b> University of Toronto, Canada <i>Providing educational support to students. Supervising and marking labs. Creating exam questions and marking exams.</i>
WINTER 2019 – WINTER 2024	<b>ECE342: Computer Hardware</b> University of Toronto, Canada <i>Providing educational support to students. Supervising and marking labs. Creating exam questions and marking exams.</i>
FALL 2018 – FALL 2023	<b>ECE552: Computer Architecture</b> University of Toronto, Canada <i>Providing educational support to students. Teaching tutorials, supervising and marking labs and exams.</i>

## SCHOLARSHIPS

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2022	<a href="#">Onassis Foundation Scholarship</a> — USD 14000
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## PUBLICATIONS

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- [1] **Victor Kariofillis**, Natalie Enright Jerger, “Workload Characterization of Commercial Mobile Benchmark Suites”, *IEEE International Symposium on Performance Analysis of Systems and Software (ISPASS)* 2024.
- [2] **Victor Kariofillis**, Jingyang Liu, Natalie Enright Jerger, “Ethical Considerations of Benchmarking”, *Workshop on Hot Topics in Ethical Computer Systems (HotEthics)* 2024.
- [3] Karthik Ganesan, **Victor Kariofillis**, Julianne Attai, Ahmed Hamoda, Natalie Enright Jerger, “DINAR: Enabling Distribution Agnostic Noise Injection in Machine Learning Hardware”, *Hardware and Architectural Support for Security and Privacy (HASP)* 2023.

## NOTABLE COURSE PROJECTS

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### PHD PROJECTS

FALL 2020	Implementations of i) a Maze Router, ii) an Analytical Placer and iii) a Branch-and-Bounding Partitioning Algorithm tool for the course <i>CAD for Digital Circuit Synthesis and Layout (ECE1387H)</i>
FALL 2020	Implementations of i) Hardware Designs for a 5th Taylor Polynomial , ii) 2D Convolution Engine on an FPGA and iii) a RAM Mapper CAD tool for the course <i>Reconfigurable Computing &amp; FPGA Architecture (ECE1756H)</i>

## MASTER'S PROJECTS

WINTER 2018	XOR Cache Compression Technique (Proof of Concept) for the course <i>Advanced Computer Architecture (ECE1718H)</i>
WINTER 2018	Approximate Decoupled Coherence Protocol for the course <i>Parallel Computer Architecture &amp; Programming (ECE1755H)</i>
FALL 2017	PINocchio: PRAM-based Approach for Parallel Program Inspection for the course <i>Parallel Computer Architecture &amp; Programming (CSC2231H)</i>

## TECHNICAL SKILLS

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Languages: C/C++, R, BASH SCRIPTING  
Tools: GEM5, INTEL PIN, SIMPLESCALAR, R STUDIO, QUARTUS