```
2C
         = 0
EPC
         = 0
                                                          Cause
         = 0
                                                          BadVAddr = 0
                                                          [
         = 3000 ff 10
                                                          Status
                                                          [
ΗI
         = 0
                                                          LO
         = 0
                                                          30
    [r0] = 0
                                                          Γ
R1
    [at] = 0
                                                         Ν
32
                                                         Ν
    [v0] = 0
3
    [v1] = 0
                                                         Ν
₹4
    [a0] = 0
                                                         Ν
R5
                                                         Ν
    [a1] = 0
36
    [a2] = 0
                                                         Ν
                                        3
₹7
    [a3] = 0
                                                         Ν
38
    [t0] = 0
39
    [t1] = 0
R10 [t2] = 0
                                                          R11 [t3] = 0
R12 [t4] = 0
R13 [t5] = 0
                                                        🖳 E
R14 [t6] = 0
k15 [t7] = 0
                                                       Exec
R16 [s0] = 0
R17 [s1] = 0
                                                        Cont
R18 [s2] = 0
R19 [s3] = 0
R20 [s4] = 0
R21 [s5] = 0
R22 [s6] = 0
                                                          R23 [s7] = 0
                                                          R24 [t8] = 0
R25 [t9] = 0
                                                          R26 [k0] = 0
R27 [k1] = 0
                                                          R28 [gp] = 0
R29 [sp] = 7fffeffc
                                                          R30 [s8] = 0
                                                          R31 [ra] = 0
```

```
PC
         = 0
EPC
          = 0
Cause
          = 0
BadVAddr = 0
Status
         = 3000 ff 10
ΗI
          = 0
LO
          = 0
R0
    [r0] = 0
R1
    [at] = 0
R2
    [v0] = 0
R3
    [v1] = 0
    [a0] = 1
R4
R5
    [a1] = 7fffea98
    [a2] = 7fffeaa0
R6
R7
    [a3] = 0
R8
    [t0] = 0
R9
    [t1] = 0
R10 [t2]
         = 0
R11
    [t3]
R12 [t4] = 0
R13 [t5]
         = 0
R14
    [t6]
         = 0
R15 [t7]
         = 0
R16 [s0] = 0
R17 [s1] = 0
R18 [s2]
         = 0
R19 [s3]
         = 0
R20
    [s4]
         = 0
R21 [s5]
         = 0
R22
    [s6]
         = 0
R23 [s7]
         = 0
```

```
R24 [t8] = 0

R25 [t9] = 0

R26 [k0] = 0

R27 [k1] = 0

R28 [gp] = 10008000

R29 [sp] = 7fffea94

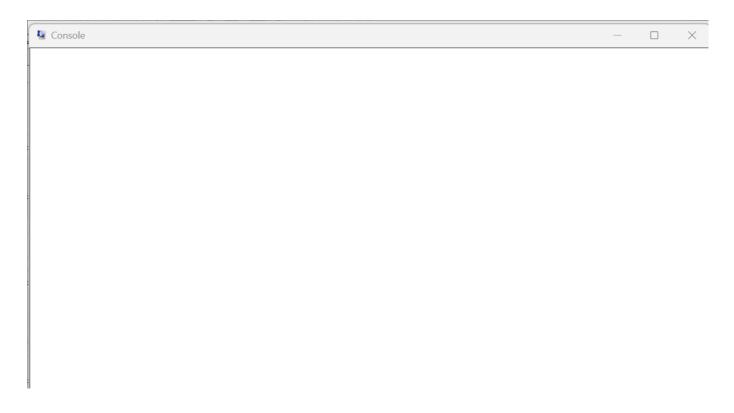
R30 [s8] = 0

R31 [ra] = 0
```

SPIM Version 9.1.24 of August 1, 2023 (final)

```
PC
         = 0
EPC
         = 0
Cause
BadVAddr = 0
Status = 3000ff10
HI
        = 0
LO
         = 0
R0
   [r0] = 0
R1
   [at] = 0
R2
   [\mathbf{v0}] = 0
R3
    [v1] = 0
R4
    [a0] = 0
R5
   [a1] = 0
R6
    [a2] = 0
R7
   [a3] = 0
R8
    [t0] = 0
R9
    [t1] = 0
R10 [t2] = 0
R11 [t3] = 0
R12
    [t4] = 0
R13 [t5]
R14
    [+6]
```

```
R15 [t7] = 0
R16 [s0] = 0
R17 [s1] = 0
R18 [s2] = 0
R19 [s3] = 0
R20 [s4] = 0
R21 [s5] = 0
R22 [s6] = 0
R23 [s7] = 0
R24 [t8] = 0
R25 [t9] = 0
R26 [k0] = 0
R27 [k1] = 0
R28 [gp] = 0
R29 [sp] = 7fffeffc
R30 [s8] = 0
R31 [ra] = 0
SPIM Version 9.1.24 of August 1, 2023 (final)
```



```
(base) PS D:\work\computer_architecture\lab1> gcc -S .\code.c -o code.asm
(base) PS D:\work\computer_architecture\lab1> cat code.asm
          .file
                  "code.c"
          .text
          .globl sumn
          .def
                  sumn;
                          .scl
                                  2;
                                          .type
                                                   32;
                                                           .endef
          .seh_proc
                          sumn
 sumn:
         pushq
                  %rbp
          .seh_pushreg
                          %rbp
                  %rsp, %rbp
         movq
          .seh setframe
                          %rbp, 0
                  $16, %rsp
         subq
          .seh_stackalloc 16
          .seh_endprologue
         movq
                  %rcx, 16(%rbp)
                  %edx, 24(%rbp)
         mov1
                  $0, -4(%rbp)
         mov1
                  $0, -8(%rbp)
         mov1
         jmp
                  .L2
 .L3:
         movl
                  -8(%rbp), %eax
         cltq
         leaq
                  0(,%rax,4), %rdx
                  16(%rbp), %rax
         movq
                  %rdx, %rax
         addq
         mov1
                  (%rax), %eax
         add1
                  %eax, -4(%rbp)
          add1
                  $1, -8(%rbp)
 .L2:
         mov1
                  -8(%rbp), %eax
                  24(%rbp), %eax
         cmpl
         jl
                  .L3
         movl
                  -4(%rbp), %eax
                  $16, %rsp
          addq
                  %rbp
          popq
          ret
          .seh_endproc
          .def
                                                   32;
                                                           .endef
                  __main; .scl
                                           .type
```

