pter 7. Opcode Mnemonics by Opcode

This chapter gives the mapping from Java Virtual Machine instruction opcodes, including the reserved opcodes ($\S6.2$), to the mnemonics for the instructions represented by those opcodes.

Opcode value 186 was not used prior to Java SE 7.

Table 7.1.

Constants				Lo	oads		S	tores
			21 22 23 24	(0x15) (0x16) (0x17) (0x18)	iload lload fload dload	54 55 56 57	(0x36) (0x37) (0x38) (0x39)	istore lstore fstore dstore
			25 26	(0x19) $(0x1a)$	aload iload 0	58 59	(0x3a) (0x3b)	astore istore 0
00 01 02	(0x00) (0x01) (0x02)	nop aconst_null iconst ml	27 28 29	(0x1b) (0x1c) (0x1d)	iload_1 iload_2 iload 3	60 61 62	(0x3c) (0x3d) (0x3e)	istore_1 istore_2 istore 3
03 04	(0x03) $(0x04)$	iconst_0 iconst_1	30 31	(0x1e) (0x1f)	lload_0 lload_1	63 64	(0x3f) (0x40)	lstore_0 lstore_1
05 06 07	(0x05) (0x06) (0x07)	iconst_2 iconst_3 iconst_4	32 33 34	(0x20) (0x21) (0x22)	lload_2 lload_3 fload 0	65 66 67	(0x41) (0x42) (0x43)	lstore_2 lstore_3 fstore 0
08 09 10	(0x08) (0x09) (0x0a)	iconst_5 lconst_0	35 36 37	(0x23) (0x24) (0x25)	fload_1 fload_2 fload_3	68 69 70	(0x44) (0x45) (0x46)	fstore_1 fstore_2
11 12	(0x0b) (0x0c)	lconst_1 fconst_0 fconst_1	38 39	(0x26) $(0x27)$	dload_0 dload_1	71 72	(0x47) $(0x48)$	fstore_3 dstore_0 dstore_1
13 14 15	(0x0d) (0x0e) (0x0f)	fconst_2 dconst_0 dconst_1	40 41 42	(0x28) (0x29) (0x2a)	dload_2 dload_3 aload_0	73 74 75	(0x49) (0x4a) (0x4b)	dstore_2 dstore_3 astore_0
16 17 18	(0x10) (0x11) (0x12)	bipush sipush 1dc	43 44 45	(0x2b) (0x2c) (0x2d)	aload_1 aload_2 aload_3	76 77 78	(0x4c) (0x4d) (0x4e)	astore_1 astore_2 astore_3
19 20	(0x13) $(0x14)$	ldc_w ldc2_w	46 47 48	(0x2e) (0x2f) (0x30)	iaload laload faload	79 80 81	(0x4f) (0x50) (0x51)	iastore lastore fastore
			49 50	(0x31) $(0x32)$	daload aaload	82 83	(0x52) $(0x53)$	dastore aastore
			51 52 53	(0x33) (0x34) (0x35)	baload caload saload	84 85 86	(0x54) (0x55) (0x56)	bastore castore sastore

Table 7.2.

Stack Math Conversions

		Chapter	r. Opcoc	de ivillellionic	s by Opco
	Mat	h		Conversion	ns
96	(0x60)	iadd			
97	(0x61)	ladd			
98	(0x62)	fadd			
99	(0x63)	dadd			
100	(0x64)	isub			
101	(0x65)	lsub			
102	(0x66)	fsub			
103	(0x67)	dsub			
104	(0x68)	imul			
105	(0x69)	lmu1			
106	(0x6a)	fmul			
	(0x6b)			(0x85)	i21
	(0x6c)			(0x86)	i2f
					i2d
					12i
					12f
					12d
					f2i
					f21
					f2d
					d2i
					d21
					d2f
					i2b i2c
					i2s
			147	(0X93)	128
	97 98 99 100 101 102 103 104 105	96 (0x60) 97 (0x61) 98 (0x62) 99 (0x63) 100 (0x64) 101 (0x65) 102 (0x66) 103 (0x67) 104 (0x68) 105 (0x69) 106 (0x6a) 107 (0x6b) 108 (0x6c) 109 (0x6d) 110 (0x6e) 111 (0x6f) 112 (0x70) 113 (0x71) 114 (0x72) 115 (0x73) 116 (0x74) 117 (0x75) 118 (0x76) 119 (0x77) 120 (0x78) 121 (0x79) 122 (0x7a) 123 (0x7b) 124 (0x7c) 125 (0x7d) 126 (0x7e) 127 (0x7f) 128 (0x80) 129 (0x81) 130 (0x82) 131 (0x83)	96 (0x60) iadd 97 (0x61) ladd 98 (0x62) fadd 99 (0x63) dadd 100 (0x64) isub 101 (0x65) lsub 102 (0x66) fsub 103 (0x67) dsub 104 (0x68) imul 105 (0x69) lmul 106 (0x6a) fmul 107 (0x6b) dmul 108 (0x6c) idiv 109 (0x6d) ldiv 110 (0x6e) fdiv 111 (0x6f) ddiv 112 (0x70) irem 113 (0x71) lrem 114 (0x72) frem 115 (0x73) drem 116 (0x74) ineg 117 (0x75) lneg 118 (0x76) fneg 119 (0x77) dneg 120 (0x78) ishl 121 (0x79) lshl 122 (0x70) ishr 123 (0x7b) lshr 124 (0x7c) iushr 125 (0x7d) lushr 126 (0x7e) iand 127 (0x7f) land 128 (0x80) ior 129 (0x81) lor 130 (0x82) ixor 131 (0x83) lxor	Math 96 (0x60) iadd 97 (0x61) ladd 98 (0x62) fadd 99 (0x63) dadd 100 (0x64) isub 101 (0x65) lsub 102 (0x66) fsub 103 (0x67) dsub 104 (0x68) imul 105 (0x69) lmul 106 (0x6a) fmul 107 (0x6b) dmul 108 (0x6c) idiv 110 (0x6e) fdiv 111 (0x6f) ddiv 112 (0x70) irem 113 (0x71) lrem 115 (0x73) drem 116 (0x74) ineg 117 (0x75) lneg 118 (0x76) fneg 119 (0x77) dneg 120 (0x78) ishl 121 (0x79) lshl 122 (0x7a) ishr 123 (0x7b) lshr 124 (0x7c) iushr 125 (0x7d) lushr 126 (0x7e) iand 127 (0x7f) land 128 (0x80) io	96 (0x60) iadd 97 (0x61) ladd 98 (0x62) fadd 99 (0x63) dadd 100 (0x64) isub 101 (0x65) lsub 102 (0x66) fsub 103 (0x67) dsub 104 (0x68) imul 105 (0x69) lmul 106 (0x6a) fmul 107 (0x6b) dmul 109 (0x6d) ldiv 110 (0x6e) fdiv 110 (0x6e) fdiv 111 (0x6f) ddiv 112 (0x70) irem 113 (0x71) lrem 114 (0x72) frem 115 (0x73) drem 116 (0x74) ineg 117 (0x75) lneg 118 (0x76) fneg 119 (0x77) dneg 119 (0x77) dneg 110 (0x78) ishl 121 (0x79) lshl 122 (0x70) ishr 123 (0x70) ishr 124 (0x70) ishr 125 (0x73) drem 141 (0x86) 142 (0x8e) 143 (0x8f) 144 (0x90) 145 (0x91) 146 (0x92) 151 (0x77) dneg 147 (0x93) 158 (0x76) ishr 159 (0x77) dneg 140 (0x8c) 141 (0x90) 142 (0x90) 143 (0x91) 144 (0x90) 145 (0x91) 146 (0x92) 147 (0x93) 148 (0x80) ior 129 (0x81) lor 130 (0x82) ixor 131 (0x83) lxor

Table 7.3.

Table 7.4. Table 7.5.

Comparisons References

Comparisons

148	(0x94)	1cmp
149	(0x95)	fcmpl
150	(0x96)	fcmpg
151	(0x97)	dcmpl
152	(0x98)	dcmpg
153	(0x99)	ifeq
154	(0x9a)	ifne
155	(0x9b)	iflt
156	(0x9c)	ifge
157	(0x9d)	ifgt
158	(0x9e)	ifle
159	(0x9f)	if_icmpeq
160	(0xa0)	if_icmpne
161	(0xa1)	if_icmplt
162	(0xa2)	if_icmpge
163	(0xa3)	if_icmpgt
164	(0xa4)	if_icmple
165	(0xa5)	if_acmpeq
166	(0xa6)	if_acmpne

References

178	(0xb2)	getstatic
179	(0xb3)	putstatic
180	(0xb4)	getfield
181	(0xb5)	putfield
182	(0xb6)	invokevirtual
183	(0xb7)	invokespecial
184	(0xb8)	invokestatic
185	(0xb9)	invokeinterface
186	(0xba)	invokedynamic
187	(0xbb)	new
188	(0xbc)	newarray
189	(0xbd)	anewarray
190	(0xbe)	arraylength
191	(0xbf)	athrow
192	(0xc0)	checkcast
193	(0xc1)	instanceof
194	(0xc2)	monitorenter
195	(0xc3)	monitorexit

Control

167 168 169 170 171 172	(0xa7) (0xa8) (0xa9) (0xaa) (0xab) (0xac)	goto jsr ret tableswitch lookupswitch ireturn
200	(/	100
170	(uxaa)	tableswitch
171	(0xab)	lookupswitch
172	(0xac)	ireturn
173	(0xad)	lreturn
174	(0xae)	freturn
175	(0xaf)	dreturn
176	(0xb0)	areturn
177	(0xb1)	return

Extended

197 198 199 200	(0xc4) (0xc5) (0xc6) (0xc7) (0xc8) (0xc9)	wide multianewarray ifnull ifnonnull goto_w jsr_w
	·/	~ _

Reserved

l	202	(0xca)	breakpoint
ı	254	(0xfe)	impdep1
ı	255	(0xff)	impdep2
l			