

report_area:

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Report : area
Design : tas
Version: L-2016.03-SP2
Date   : Sun May 17 15:39:22 2020
*****
Information: Updating design information... (UID-85)
Library(s) Used:

    saed90nm_typ (File: /nfs/guille/a1/cadlibs/synop_lib/SAED_EDK90nm/Digital_Standard_Cell_Library/synopsys/models/saed90nm_typ_pg.db)

Number of ports:                247
Number of nets:                 493
Number of cells:                248
Number of combinational cells:  154
Number of sequential cells:     85
Number of macros/black boxes:   0
Number of buf/inv:              15
Number of references:           7

Combinational area:             1778.338990
Buf/Inv area:                   82.950003
Noncombinational area:          2263.442022
Macro/Black Box area:           0.000000
Net Interconnect area:          196.606128

Total cell area:                4041.781012
Total area:                     4238.387140
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a. $\text{NAND2X1} = 5.5296 \mu\text{m}^2$

$$\text{Number of cells (gates)} = \frac{\text{Total Area}}{\text{NAND Gate Area}} = \frac{4238.39 \mu\text{m}^2}{5.5296 \mu\text{m}^2} = 767$$