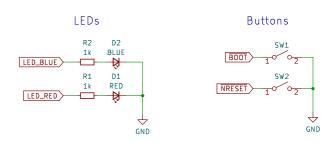
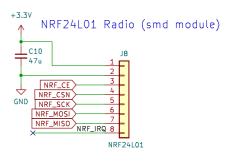
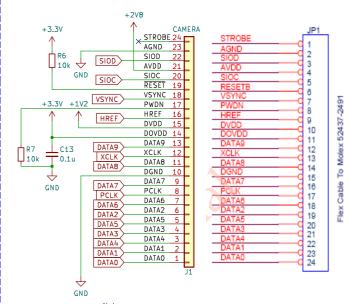


Motor driver (H-bridge) 2.2u +3.3V DRV8833PWP 4 2 C12 GND nSLEEP AISEN 8 MOTOR_NFAULT GND AIN2 Add) AOUT2 BOUT1 BIN2 S BOUT2 GND



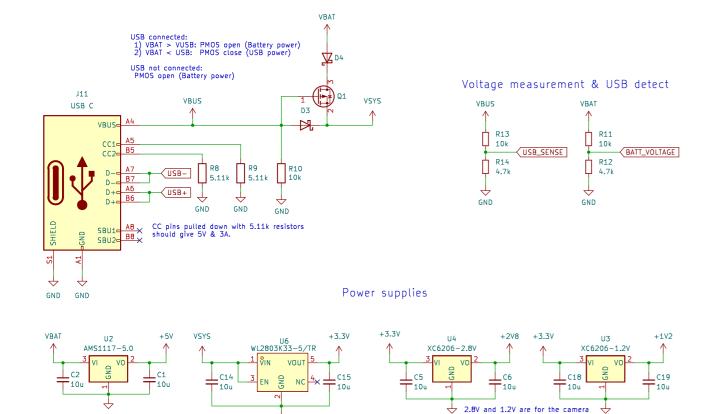


Camera (0V2640)



- Notes:
 Pins are reversed (1-24) due to footprint
 RESET: Active low, internal pull-up
 PWDN (Power down): Active high, internal pull-down,
 So R6 and R7 are probably NOT needed
 SIOC SCCB Input clock
 SIOD SCCB Data
 XCLK System input clock
 VSYNC DVP VSYNC output
 HREF DVP HREF output
 DATAO-9 Data pins

- Voltages: DVDD: 1.2V DOVDD: 3.3V AVVD: 2.8V



GND

GND

GND

5V LDO is connected straight to battery, and requires 2s battery to output 5V