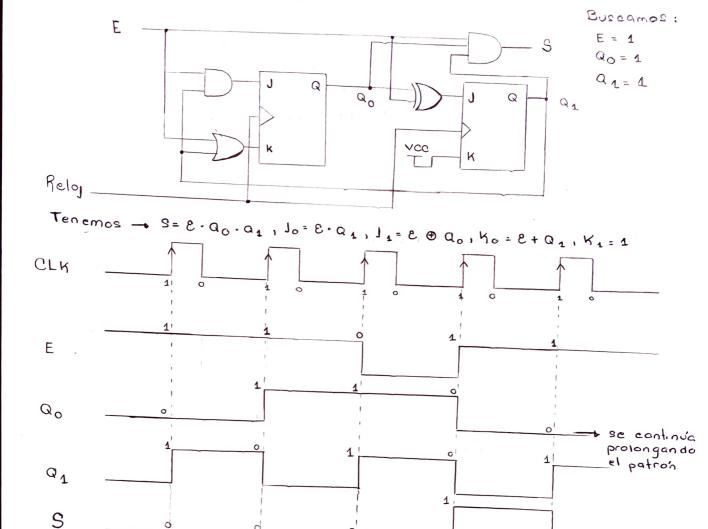
1. Partiendo del estado Qo = Q1 = 0, determine la secuencia de bits de E que activa la salida S. Justifique su respuesta en al diagrama de tiempo. Suponga que la entrada E está sincronizada con la señal de reloj.



- Solo hay dos posibles estados para &: o y 1 . Aplicamos un proceso iterativo para probar ambos resultados:

al estado

original.

Para
$$t = 1 - Q_0 = 0, Q_1 = 1$$
 $E = 0$
 $E = 1$
 $C = 1$
 $C = 1$
 $C = 0.0 = 0$
 $C = 1 + 0 = 1$
 $C = 1$

Para
$$t = 2$$
 $E = 0$
 $S = 1$
 $S = 0$
 $S = 1$
 $S = 0$
 $S = 1$
 $S = 1$

Hay cambio

de estado

de Qo+ = 1.

Para
$$t = 3$$
 $8 = 0$
 $8 = 1$
 $10 = 0.0 = 0$
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opción viable.

Para += 4 $\frac{e=1}{2}$ para complir can $\frac{e=1}{2}$ = $e \cdot Q_0 \cdot Q_1 = 1$: $\frac{1}{2}$ = $\frac{1}{2}$ = $\frac{1}{2}$ = $\frac{1}{2}$

 $Q_0^+ = comple. = \overline{1} = 0$

J1 = 1 @ 1 = 0

K 1 = 1

Q1+= limpig = 0

El estado se reinicia al 8=1, por lo tanto contamos con un patrón que se repite asda 4 ticks, siendo este:

E = 11011101...

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