

# Report Title

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**Abstract**—Description of why we want to develop good prefetching schemes, the chosen scheme and the most important results.

## I. INTRODUCTION

**S**HORT introduction to the problem we are trying to solve (CPU performance vs memory latency). Intruduce our scheme (solution).

## II. BACKGROUND

Introduce general prefetching schemes.

## III. THE SCHEME

Explain our scheme with appropriate figures.

## IV. METHODOLOGY

Here we explain the simulator setup (extensions, parameters, benchmarks).

## V. RESULTS

Here we present the results of our scheme (best performing prefetcher).

## VI. DISCUSSION

Here we discuss other prefetcher implementations tried and their results.

## VII. RELATED WORK

Reference other prefetching schemes and their results. Here we can talk about the example prefetchers that our scheme is compared to in the benchmarking process.

## VIII. CONCLUSION

The conclusion goes here

## APPENDIX A

Appendix one text goes here.

## REFERENCES

- [1] M. Grannæs, *Reducing Memory Latency by Improving Resource Utilization*. Trondheim, Norway: NTNU-trykk, 2010.