Report Title

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***Abstract*—Description of why we want to develop good prefetching schemes, the chosen scheme and the most important results.**

I. Introduction

SHORT introduction to the problem we are trying to solve (CPU performance vs memory latency). Intruduce our scheme (solution).

II. Background

Introduce general prefetching schemes.

III. The Scheme

Explain our scheme with appropriate figures.

IV. Methodology

Here we explain the simulator setup (extensions, parameters, benchmarks).

V. Results

Here we present the results of our scheme (best performing prefetcher).

VI. Discussion

Here we discuss other prefetcher implementations tried and their results.

VII. Related Work

Reference other prefetching schemes and their results. Here we can talk about the example prefetchers that our scheme is compared to in the benchmarking process.

VIII. Conclusion

The conclusion goes here

References

[1] M. Grannæs, *Reducing Memory Latency by Improving Resource Utilization*. Trondheim, Norway: NTNU-trykk, 2010.