CURRICULUM VITAE

VICTOR AUGUSTO DOS SANTOS
Nationality: Brazilian
Date of birth: 10/29/1990
Sex: Male
Address: Rua Antônio Mattos Areas, 104, apto 202, Florianópolis-SC/Brazil

Contact Information   E-mail: viic.santos@gmail.com
                                              Mobile: +55 (48) 8411-9029

ACADEMIC BACKGROUND

2009 – 2015   Electronic Engineer at Federal Institute of Santa Catarina (IF-SC), Brazil.*

*The Federal Institute of Education, Science and Technology of Santa Catarina (IFSC) is a century public education institution in Santa Catarina, being elected for 6 years in a row the best Technological Institute of the country (2008-2013).

PROFESSIONAL BACKGROUND

2015 – Actual   **Federal University of Santa Catarina (Brazil) –** Mechanical Engineering Department
Position: Embedded Systems Developer.

Responsabilities: Development of firmware for AVR and ARM microcontrollers, instrumentation design and control for critical environments, PCB design, R&D.

2011 – 2015   **DIGITRO Intelligence, IT, Telecom (Brazil) -** Hardware Development Department.
Position: Systems Analyst.

Responsabilities: Development of hardware and firmware for embedded system, application of EMC techniques to ANATEL certification process, market research and prospection of new products.

2010 – 2011   **Federal Institute of Santa Catarina (Brazil) -** Electronics Department.
Position: Undergraduate researcher.

Responsabilities: Research and development of a mobile License Plate recognition system based on Beagleboard platform.

2009– 2010   **Federal Institute of Santa Catarina (Brazil) –** Students Admission Department.
Position: Web developer.
Main activities: Developer and maintainer of the Students Admission Department website and responsible for the information management and content update.

LANGUAGE PROFICIENCY

Portuguese: Native   English: Advanced   Spanish: Intermediate   German: Beginner

KNOWLEDGE AREAS

Real Time Operating Systems; Performance and stability improving algorithms; OOP; Modular Programming.

Electromagnetic Compatibility; Circuit Analysis;

Digital Circuits; Microcontrollers; Digital Signal Processors; Programming (Assembly, C, C++, Python, Java, PHP, Shell Script);

Digital Signal Processing for Audio; Digital Image Processing; Computer Vision;

Mobile Development (Android); Classic Control System; Modern Control System; Matlab. Embedded Development; Robotics.

Linux Operational System.

Usability, Ergonomics.

PRODUCTION AND PUBLICATIONS

Events:
5	Participation in events (congress)			
3	Participation in events (workshop)			

Technical Production:
12	Technical Works (project development)
17	Technical Works (technical report)
1	Research report
2	Other technical production

Bibliographic Production:
7	Presentations in Events (Conference or lecture)

Academic Advisories:
1	Academic Advisory - concluded (scientific initiation)

OTHER RELEVAT INFORMATION

Vice President of the Academic Center of Electronic Systems (student's representative entity) – 2012/2013.
Managing Consultant of the Academic Center of Electronic Systems (student's representative entity) – 2010/2011

SELECTED PUBLICATIONS

Presentations in Events:

SANTOS, V. A.; MARCONDES, H. . Semi-Autonomous Tennis Ball Collector Robot. 2014.

SANTOS, V.A . Observation of the cross-talking effect on different PCB layouts. 2014.

SANTOS, V.A. . Kernel with task scheduler and context switching support for AVR 8 bits microcontrollers. 2014.

SANTOS, V.A.; REGIS, S.R. . Microcontrolled PID controller. 2013.

SANTOS, V.A. . Digital Butterworth Low-Pass Filter Design 2013.

SANTOS, V.A .. Static DC-DC Boost Converter. 2013.

SANTOS, V.A. . PID Controller applied to DSP. 2013.

SANTOS, V.A. . Development of Digital Image Processing filters routines in Matlab, C and Assembly to be applied in Digital Signal Processors. 2013.

SANTOS, V.A. . Development of Digital Filters for Audio applications in Matlab, C and Assembly to be applied in Digital Signal Processors. 2013.

SANTOS, V.A. . Color pattern following robot using Android as processing platform. 2013.

SANTOS, V.A, REGIS, S.R. . Analog FM Transmitter and Receiver. 2012.

SANTOS, V.A. . Network communication using sockets in AVR microcontrollers. 2012.

SANTOS, V.A. . Classic Control: Phase-Lead compensator designed using Root Locus method. 2012.

SANTOS, V.A. .VHDL driver for RGB monitors. 2011.

SANTOS, V.A. . Symmetric Adjustable 30V 2A Power Supply with LCD display 2010.